

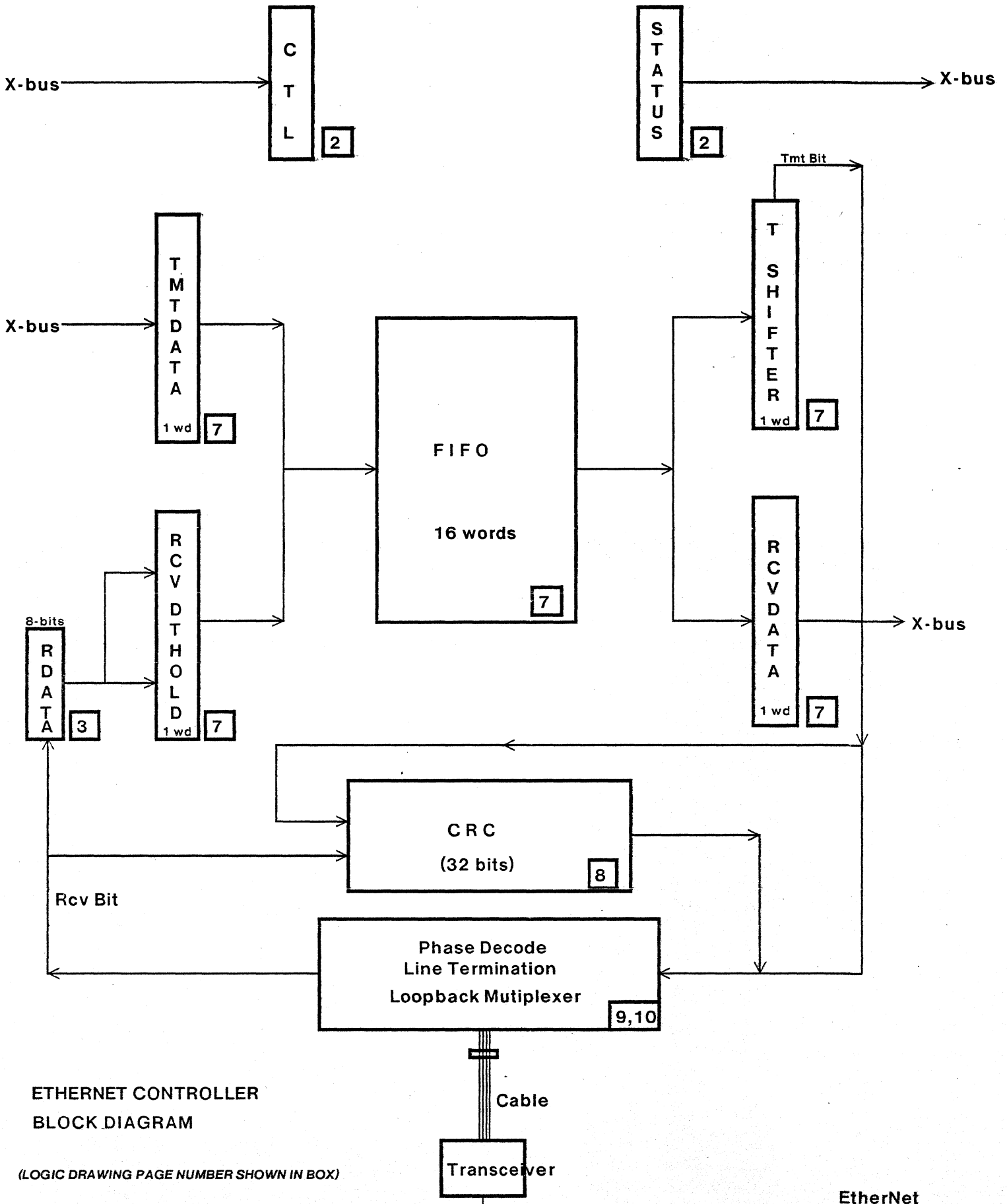
TABLE OF CONTENTS

SHEET

0.4	BLOCK DIAGRAM: ETHERNET CONTROLLER
0.5	BLOCK DIAGRAM: LSEP
0.6	BLOCK DIAGRAM: RS-232-C INTERFACE
1	ETHER CP CLOCKS, BUFFERED X-BUS, TESTABILITY, PULLUPS
2	ETHER CP INTERFACE
3	ETHER RECEIVE DATA / STATES
4	ETHER TRANSMIT DATA / STATES
5	ETHER TRANSMIT COUNTERS
6	ETHER BUFFER CONTROL AND STATUS
7	ETHER HALF DUPLEX BUFFER
8	ETHER CRC
9	ETHER PHASE ENCODER & CABLE INTERFACE
10	ETHER PHASE LOCK LOOP RECEIVER
11	LSEP CP INTERFACE
12	LSEP SHIFTERS
13	LSEP PRINTER CONNECTOR
14	LSEP CONTROL AND STATUS
15	RS232C INTERFACE, Z80A-SIO/2, 8085 CONVERTER
16	RS366 CONTROL & STATUS
17	RS232C/RS366 DRIVERS/RECEIVERS
18	SPARES, REGISTER CLOCKS
19	FUSES & FILTER CAPS
20	FILTER CAPS
21	TEST POINT LISTING, EDGE CONNECTOR LISTING, & SIGNAL LISTING

Note: The issued schematic drawing 156P12028 is made from [Rain]<SDMod>OPT1.dmASIL-D

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET 0.3 OF	

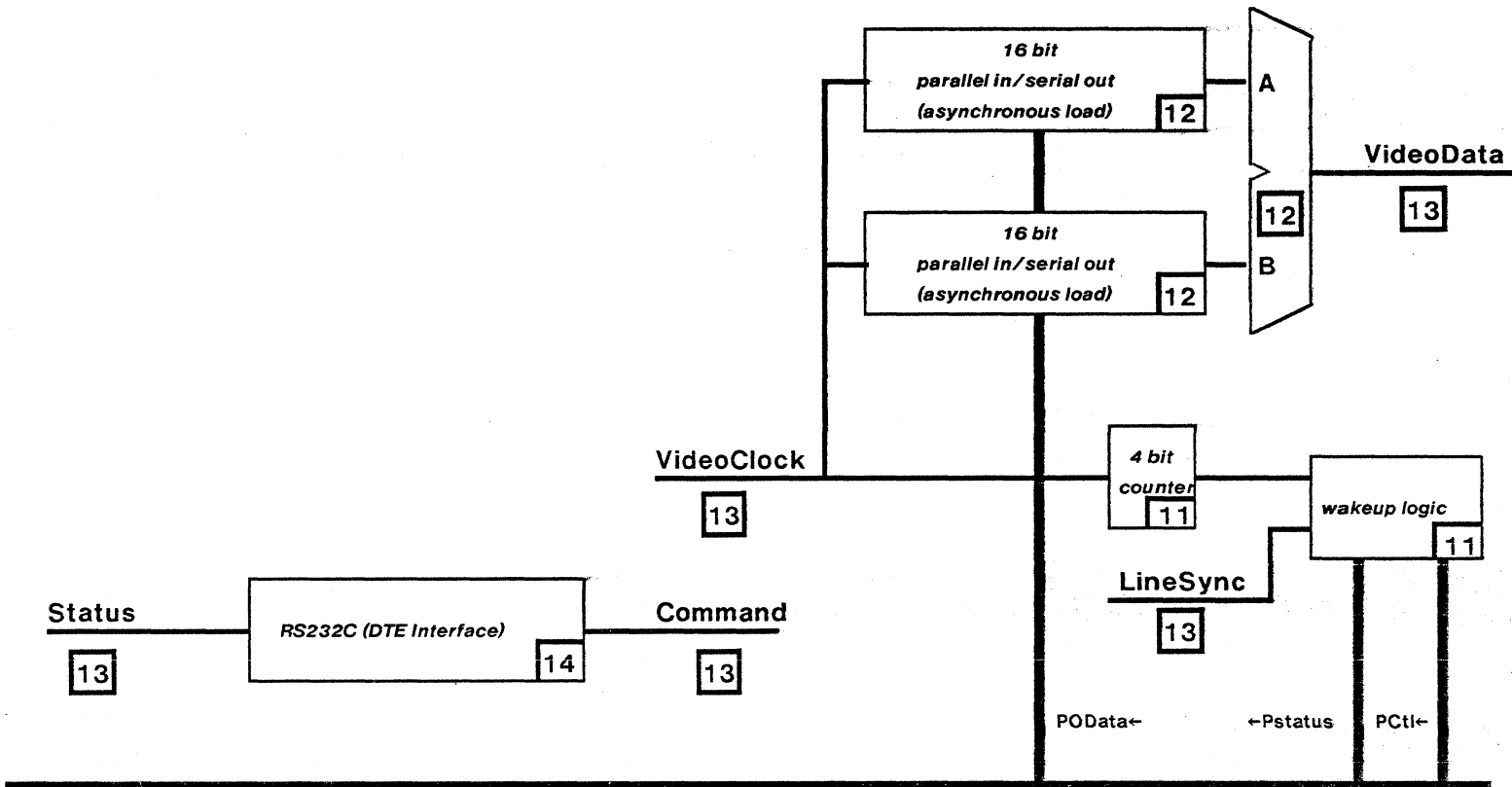


ETHERNET CONTROLLER
BLOCK DIAGRAM

(LOGIC DRAWING PAGE NUMBER SHOWN IN BOX)

EtherNet

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 0.4 OF		



X Bus

Signals from LSEP connector are in the large font.

Control Register Functions:

1. disable wakeup (level)
2. clear buffer (level)
3. test mode (level)
4. end of line (pulse)
5. clear errors (pulse)
6. step (test mode clock)

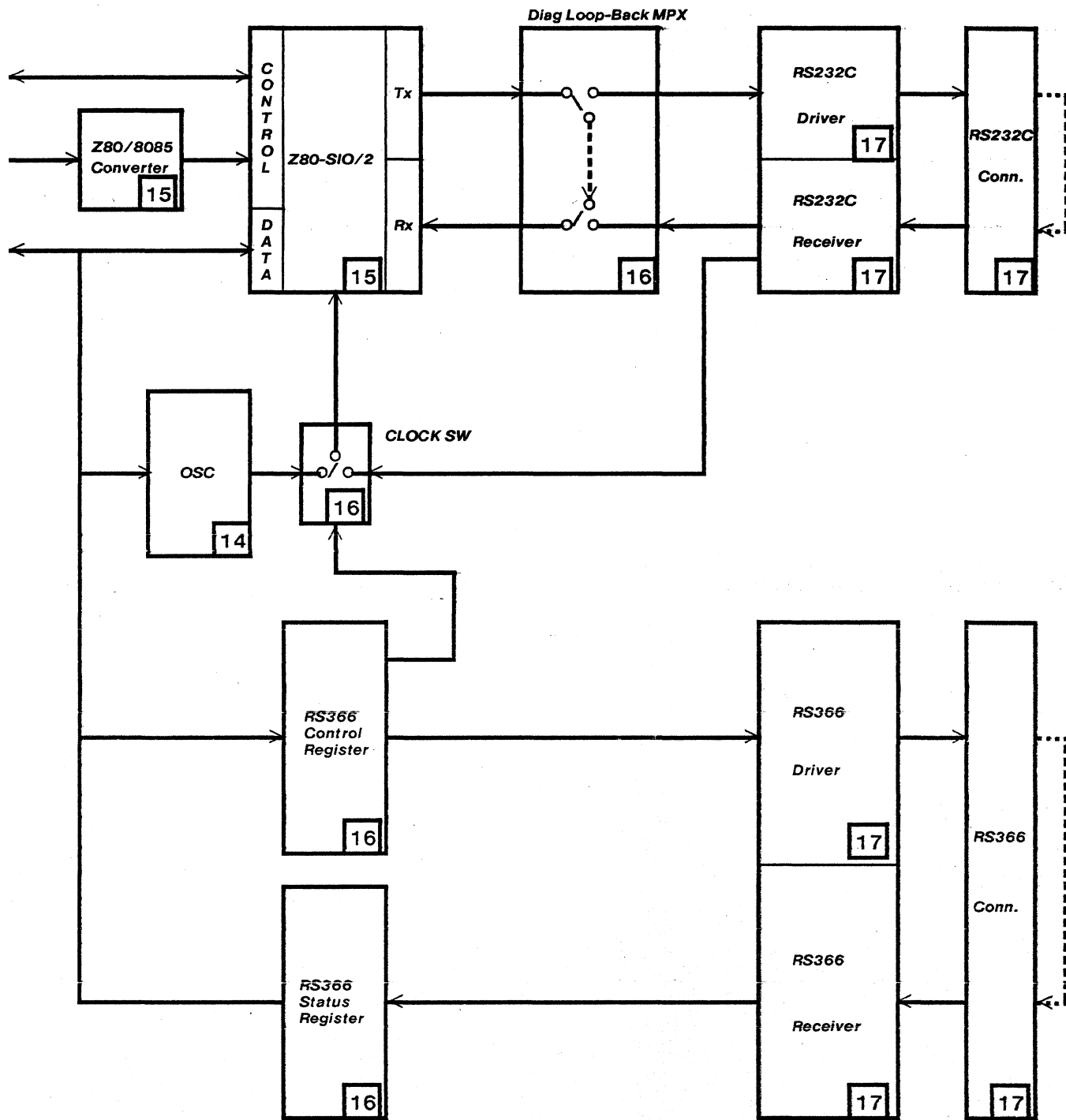
Status Register:

1. data overrun
2. buffer loadable (0 => A, 1 => B)
3. VideoData

LSEP BLOCK DIAGRAM

(LOGIC DRAWING PAGE NUMBER SHOWN IN BOX)

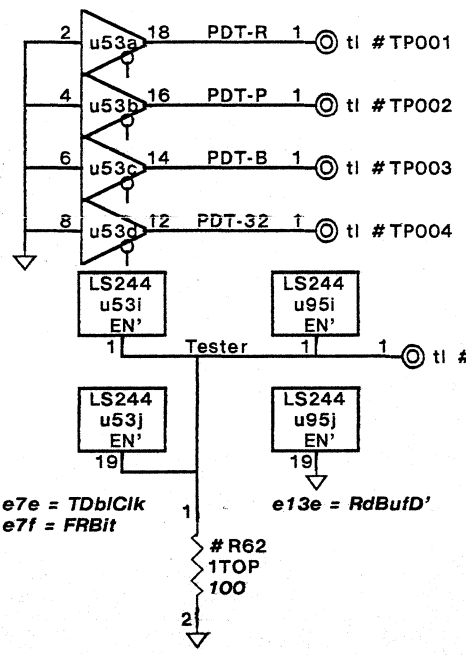
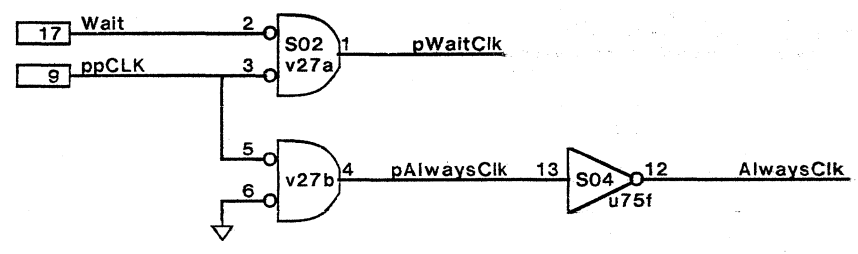
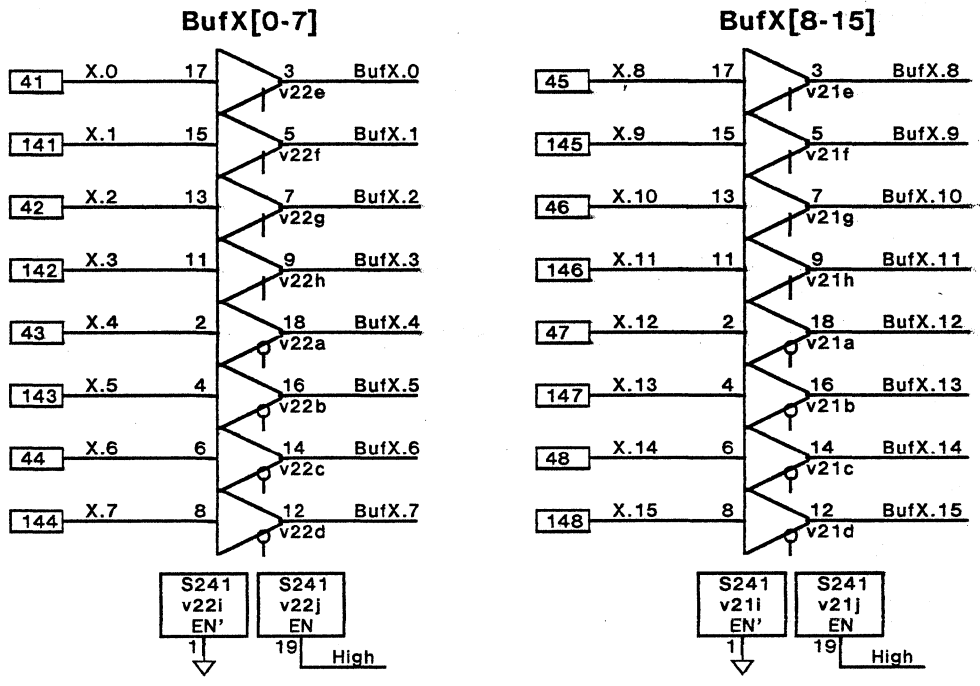
XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 0.5 OF	A



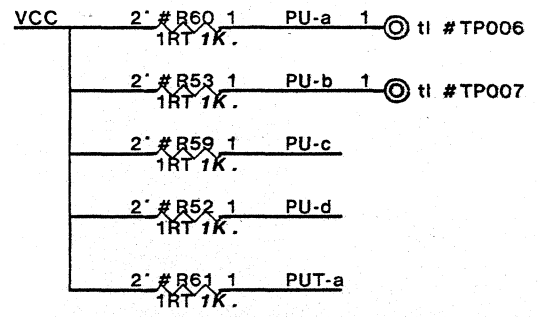
RS-232-C INTERFACE BLOCK DIAGRAM

(LOGIC DRAWING PAGE NUMBER SHOWN IN BOX)

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT		A4	SHEET 0.6	OF	

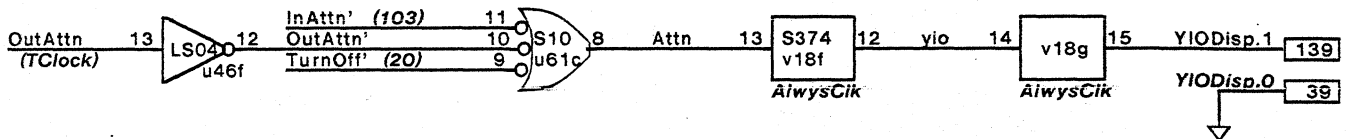
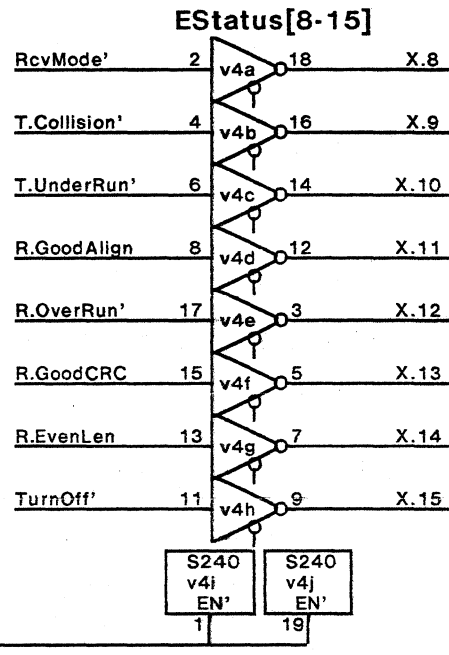
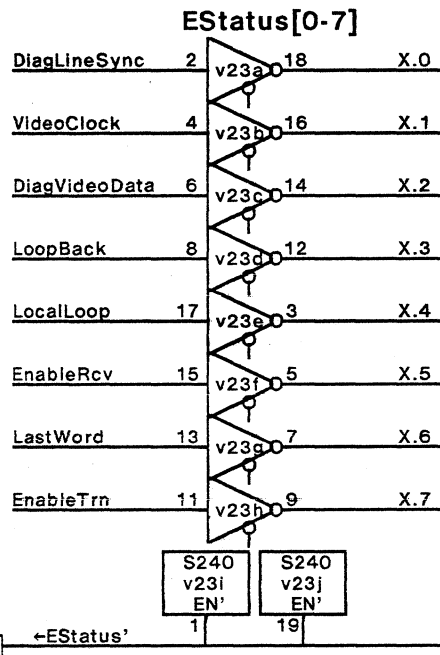
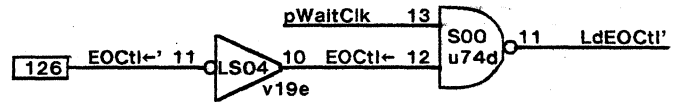
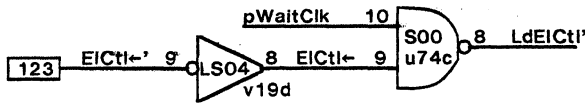
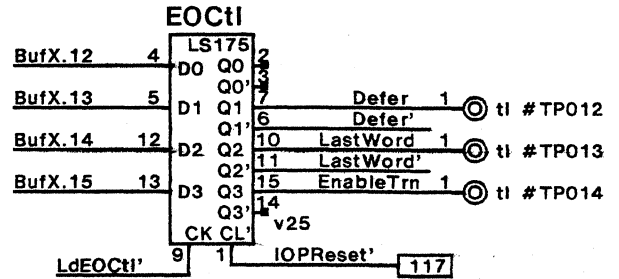
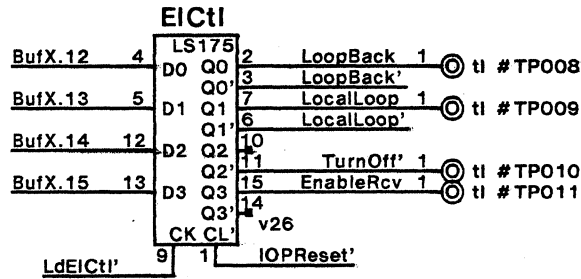


PDT-R is used to disable Register outputs
PDT-P is used to disable PROM outputs
PDT-B is used to disable outputs of FIFO
PDT-32 is used to disable the outputs of the Am26LS32

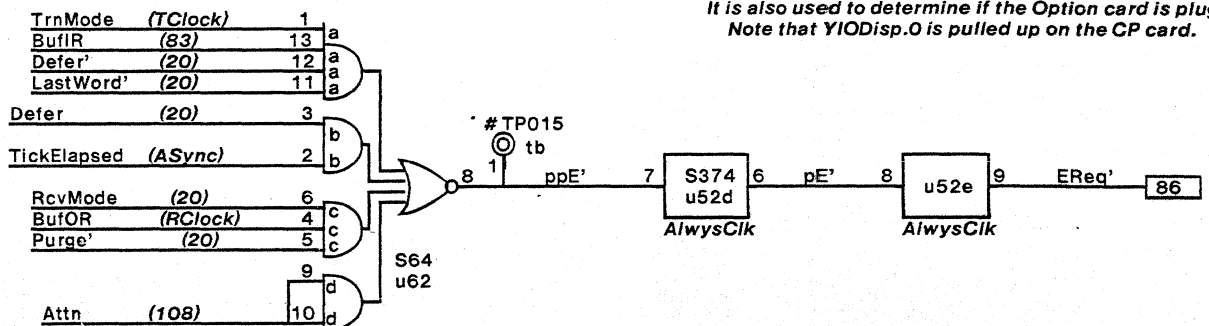


CP Clocks & Buffered X-bus, Testability, Pullups

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT		A4	SHEET 01 OF		

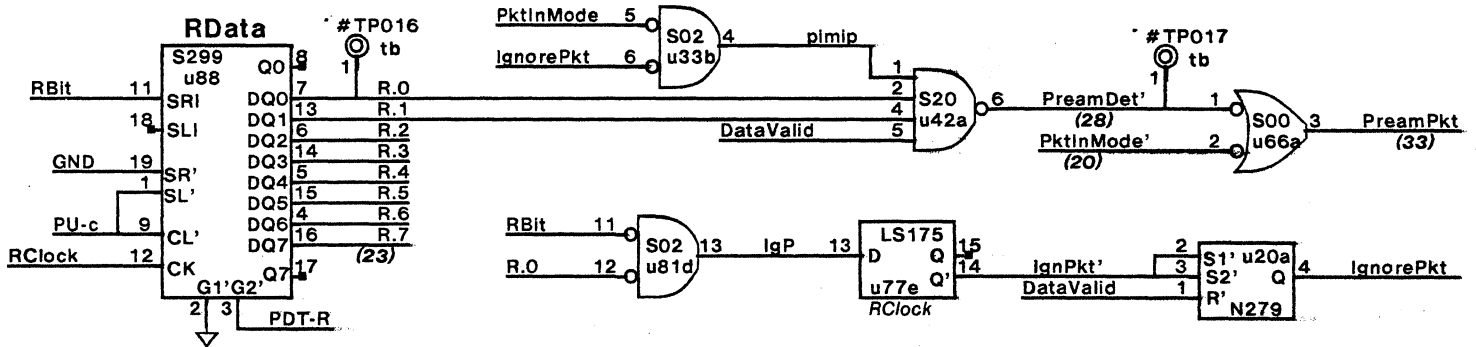


YIODisp.0 must be zero for the Transmitting inner loop uCode. It is also used to determine if the Option card is plugged in. Note that YIODisp.0 is pulled up on the CP card.

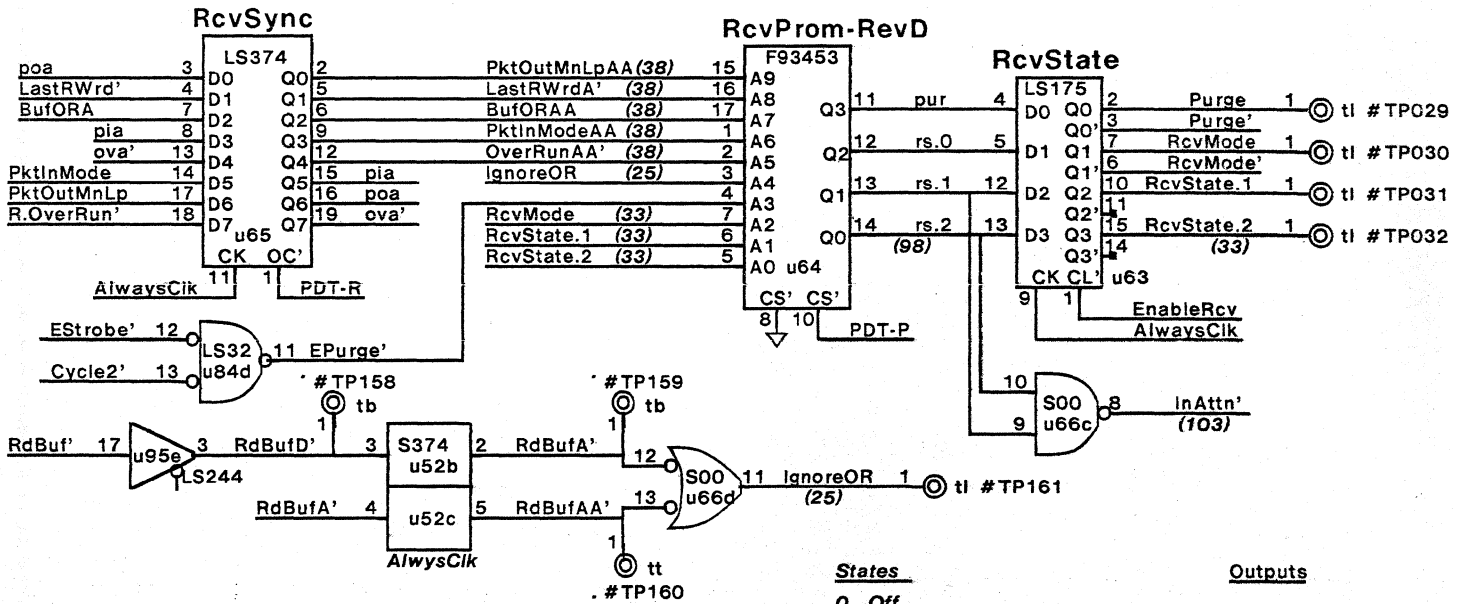
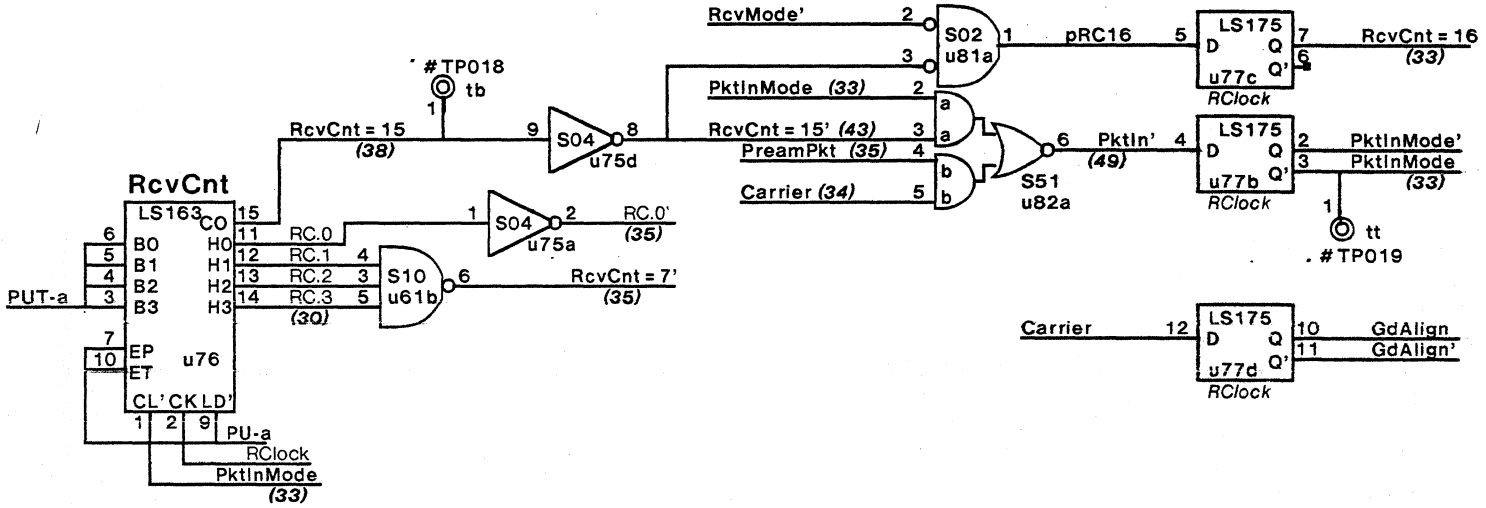


CP Interface

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 02 OF	A



Bits received least bit first
 Last 2 1-bits of Preamble indicate "PreamDet"
 Last 2 0-bits of Preamble indicate "IgnorePacket"

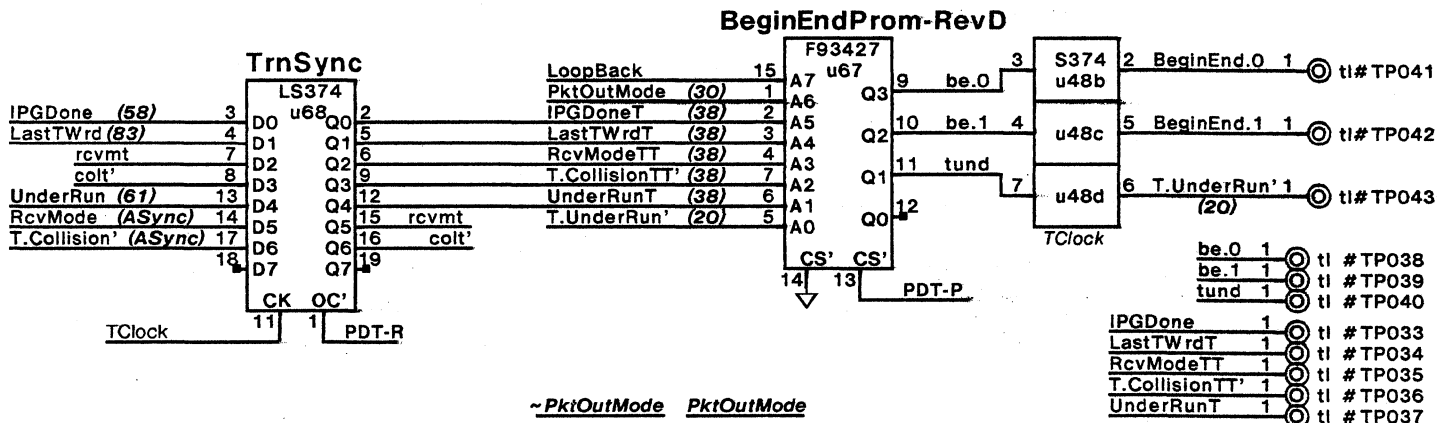


- | | | | |
|----------------|-----------|--------|-----------|
| PktOutMnLpAA 1 | tl #TP020 | pur 1 | tl #TP025 |
| LastRWrDA' 1 | tl #TP021 | rs.0 1 | tl #TP026 |
| BufORAA 1 | tl #TP022 | rs.1 1 | tl #TP027 |
| PktInModeAA 1 | tl #TP023 | rs.2 1 | tl #TP028 |
| OverRunAA' 1 | tl #TP024 | | |

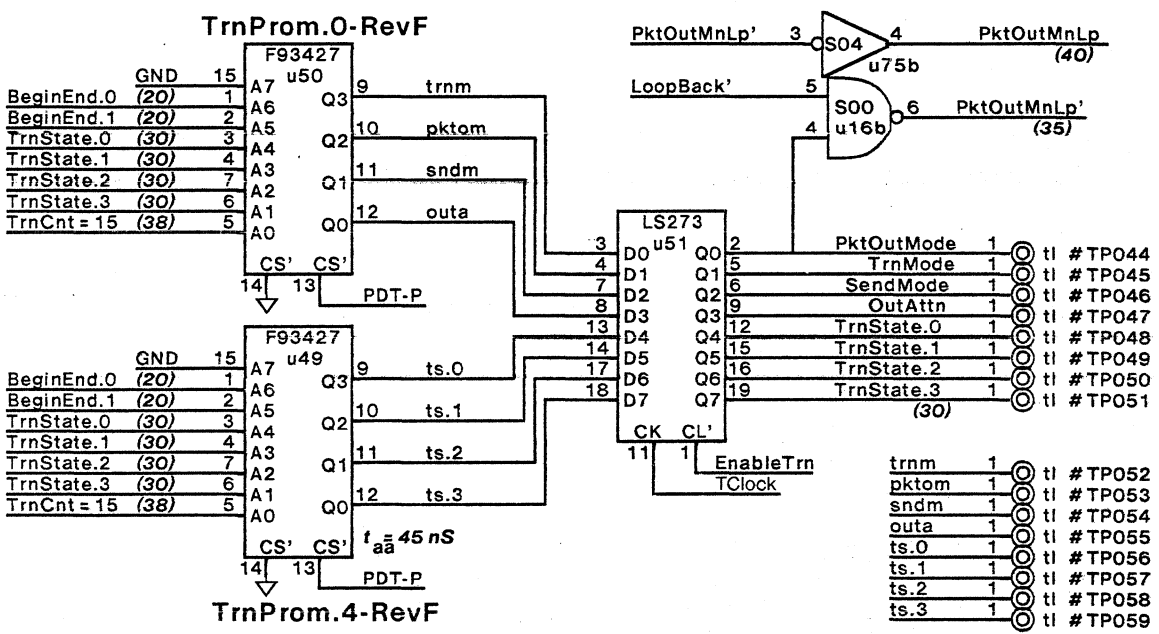
- | States | Outputs |
|--------------------------------|----------------|
| 0 Off | |
| 1 Skip (wait for no PktInMode) | |
| 2 Idle (wait for PktInMode) | |
| 3 unused | |
| 4 BufferEmpty? | RcvMode InAttn |
| 5 ReceiveData | RcvMode |
| 6 Purge | RcvMode Purge |
| 7 Post Status | RcvMode InAttn |

Receive Data/States

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 03 OF		

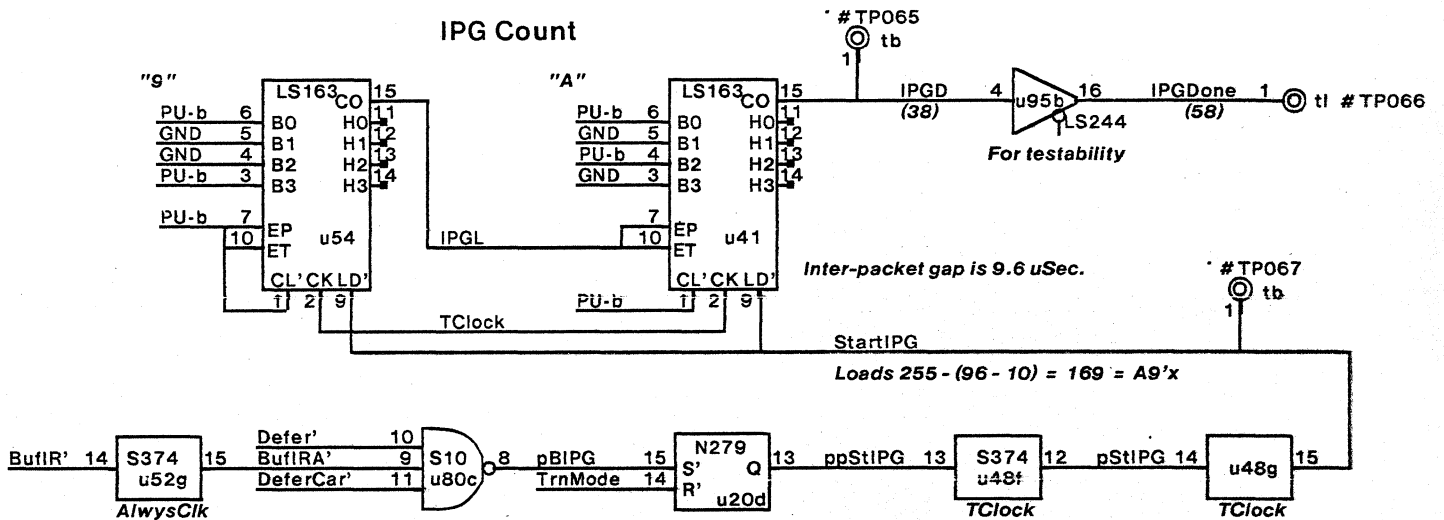
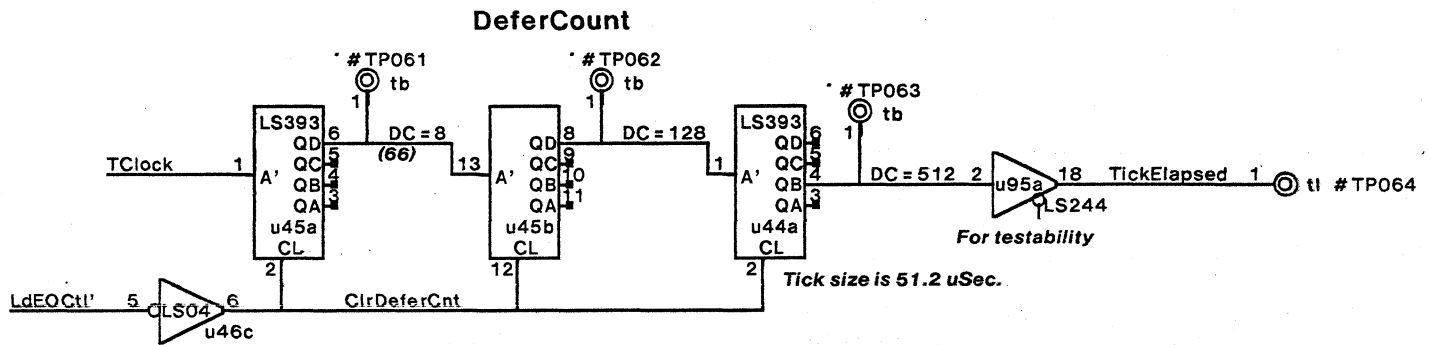
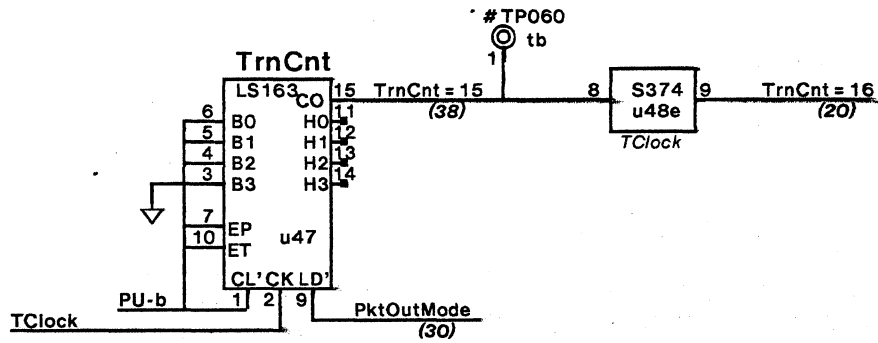


~PktOutMode PktOutMode
 BeginEnd = 0 NoStart NoEnd
 BeginEnd = 1 EndWithJam = T.UnderRun OR T.Collision
 BeginEnd = 2 EndWithoutCRC = Loopback AND LastTWrd
 BeginEnd = 3 StartTrn EndWithCRC = ~Loopback AND LastTWrd
 T.UnderRun 0 T.UnderRun OR UnderRun



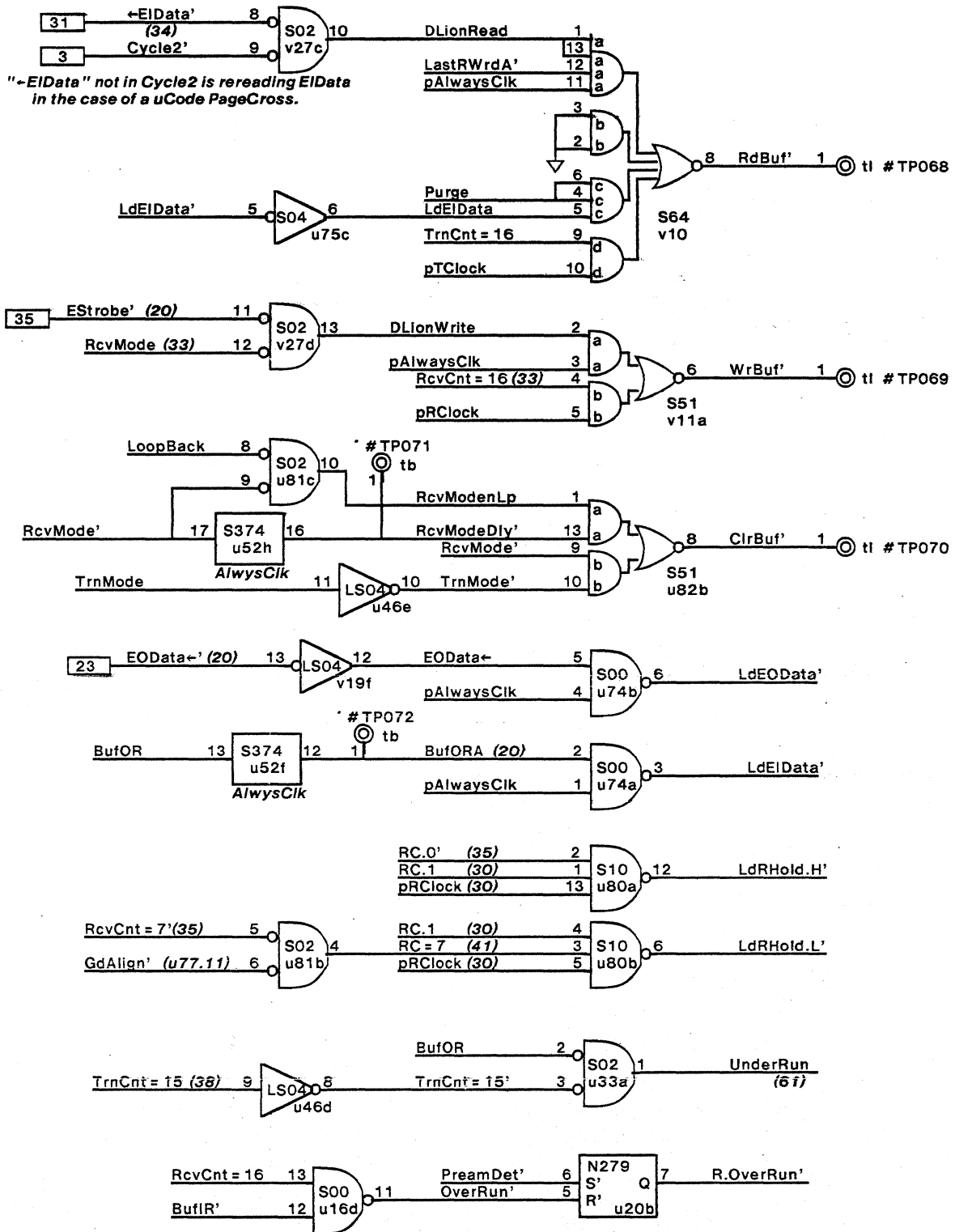
States	Outputs
0 Off	
1 WaitForBuffer	
2 WaitToTran	TrnMode SeICRC
3 PostOutStatus	TrnMode SeICRC OutAttn
4 TrnInitialize	TrnMode PktOutMode
5 Send	TrnMode PktOutMode Send
6 CRC0	TrnMode PktOutMode Send SeICRC
7 CRC1	TrnMode PktOutMode Send SeICRC
8 Pream0	TrnMode PktOutMode Send SetCRC
9 Pream1	TrnMode PktOutMode Send SetCRC
10 Jam0 (Kword)	TrnMode PktOutMode Send SetCRC SeICRC
11 Jam1	TrnMode PktOutMode Send SetCRC SeICRC
12 Pream2	TrnMode PktOutMode Send SetCRC
13 Pream3	TrnMode PktOutMode Send SetCRC
14 Jam2	TrnMode PktOutMode Send SetCRC SeICRC
15 unused	TrnMode PktOutMode Send SetCRC SeICRC

Transmit Data/States



Transmit Counters

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 05 OF	A



BD.0 1 ○ tl # TP073
BD.1 1 ○ tl # TP074
BD.2 1 ○ tl # TP075
BD.3 1 ○ tl # TP076
BD.4 1 ○ tl # TP083
BD.5 1 ○ tl # TP084
BD.6 1 ○ tl # TP085
BD.7 1 ○ tl # TP086
BD.8 1 ○ tl # TP093
BD.9 1 ○ tl # TP094
BD.10 1 ○ tl # TP095

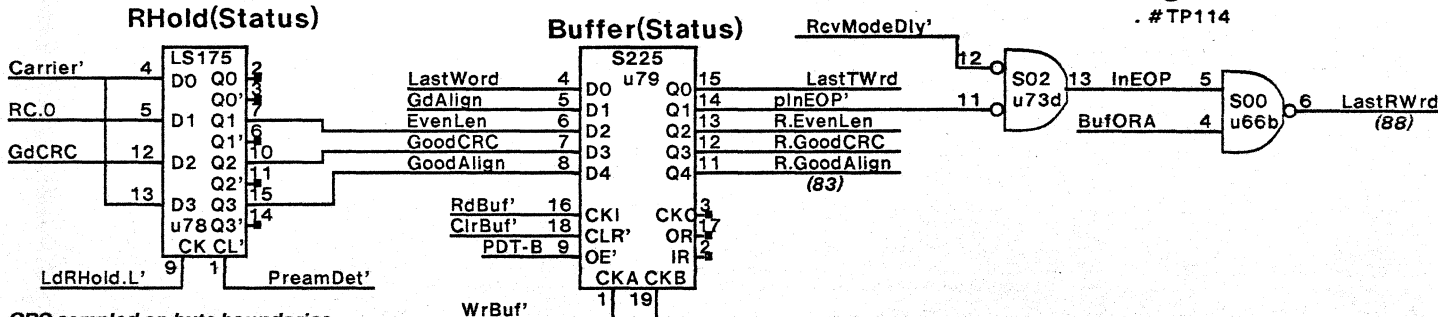
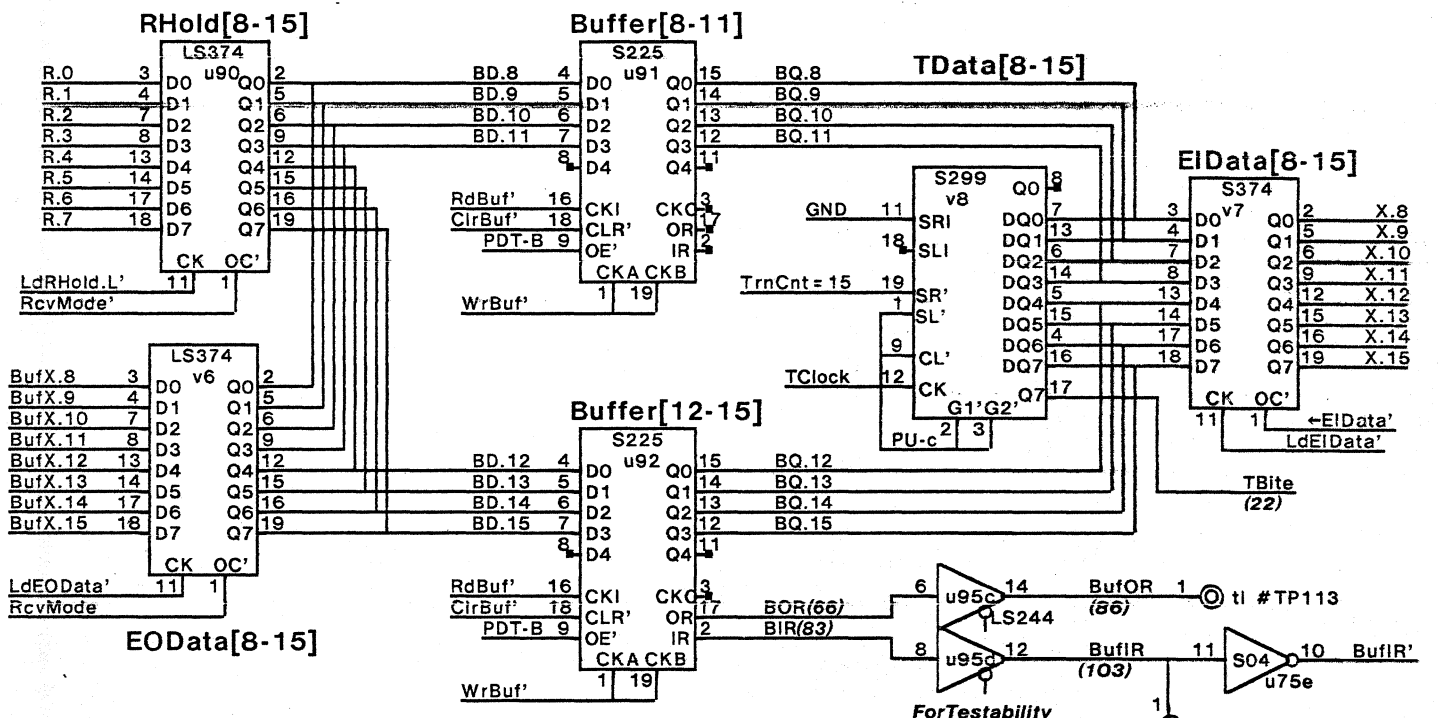
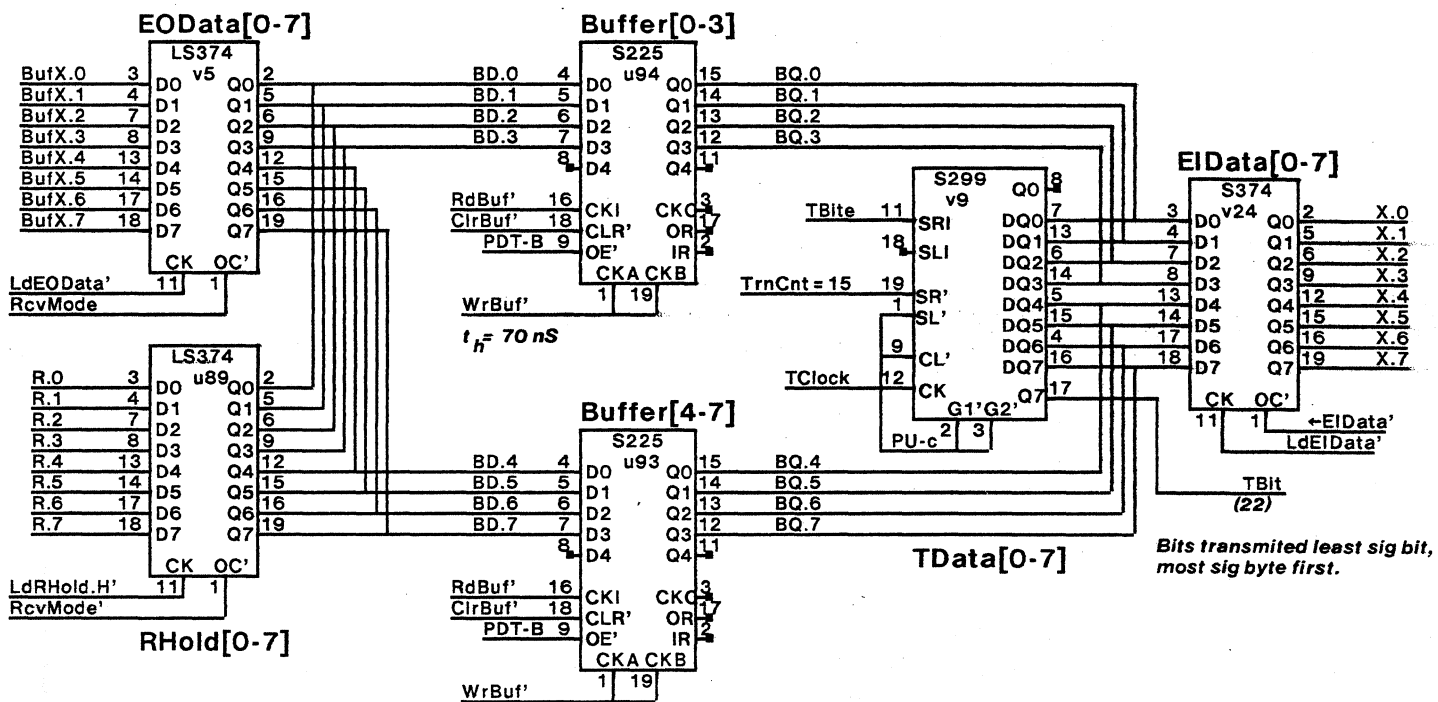
BD.11 1 ○ tl # TP096
BD.12 1 ○ tl # TP103
BD.13 1 ○ tl # TP104
BD.14 1 ○ tl # TP105
BD.15 1 ○ tl # TP106
InEOP 1 ○ tl # TP107
LastWord 1 ○ tl # TP077
GoodAlign 1 ○ tl # TP087
GoodCRC 1 ○ tl # TP162
EvenLen 1 ○ tl # TP097

BQ.0 1 ○ tl # TP078
BQ.1 1 ○ tl # TP079
BQ.2 1 ○ tl # TP080
BQ.3 1 ○ tl # TP081
BQ.4 1 ○ tl # TP088
BQ.5 1 ○ tl # TP089
BQ.6 1 ○ tl # TP090
BQ.7 1 ○ tl # TP091
BQ.8 1 ○ tl # TP098
BQ.9 1 ○ tl # TP099
BQ.10 1 ○ tl # TP100

BQ.11 1 ○ tl # TP101
BQ.12 1 ○ tl # TP108
BQ.13 1 ○ tl # TP109
BQ.14 1 ○ tl # TP110
BQ.15 1 ○ tl # TP111
pLRWrd 1 ○ tl # TP112
LastTWrd 1 ○ tl # TP082
R.GoodAlign 1 ○ tl # TP092
R.GoodCRC 1 ○ tl # TP163
R.EvenLen 1 ○ tl # TP102

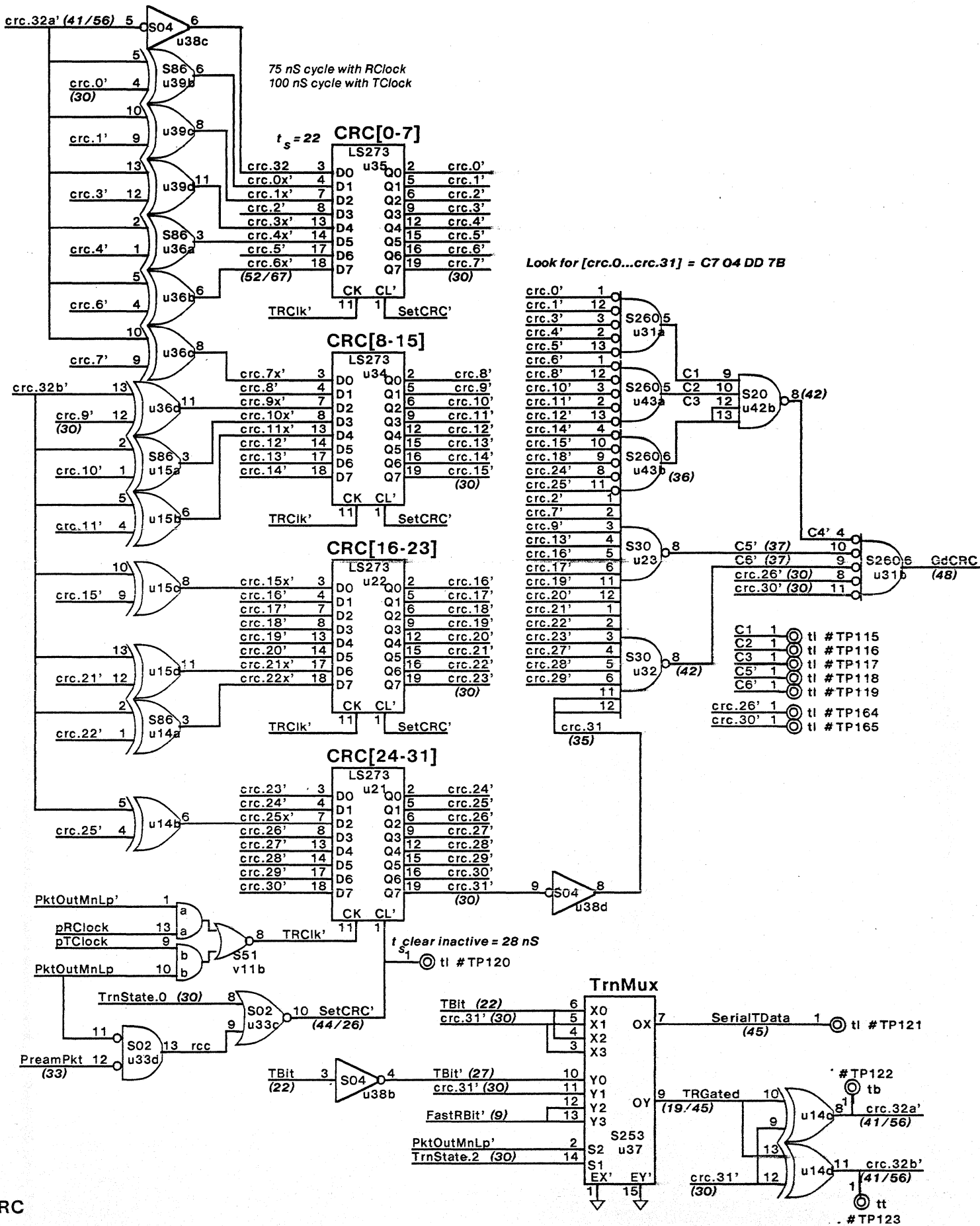
Buffer Control & Status

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS	DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT	A4	SHEET 06 OF	A



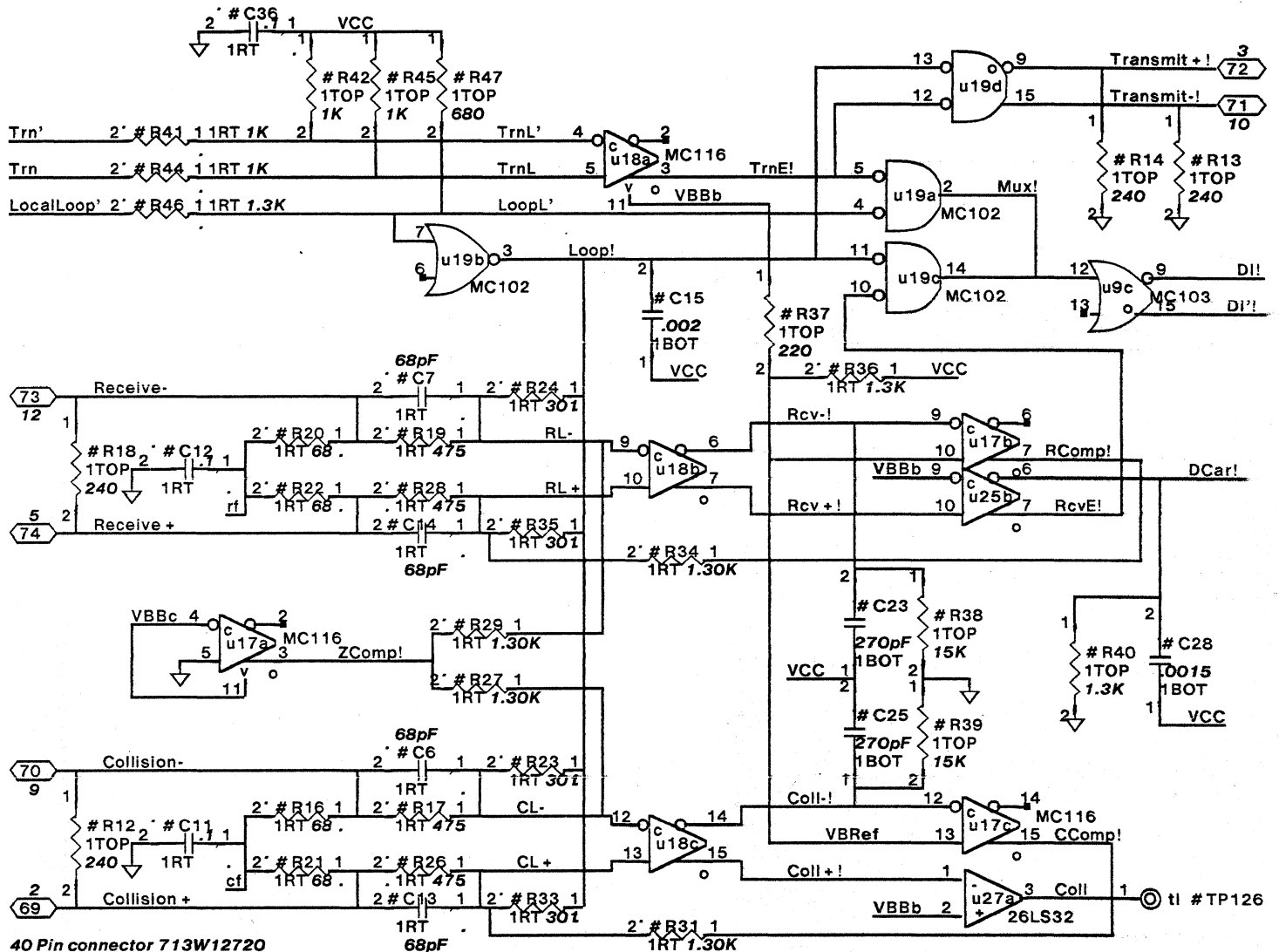
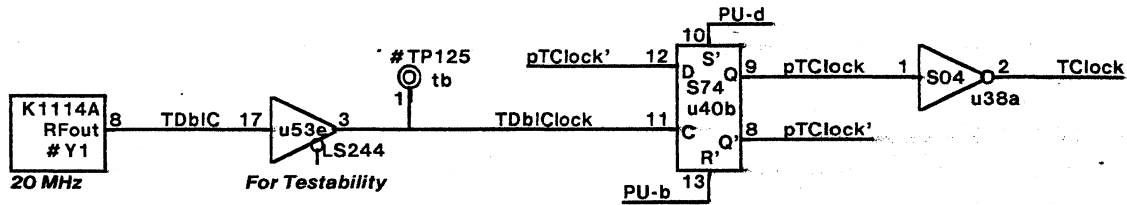
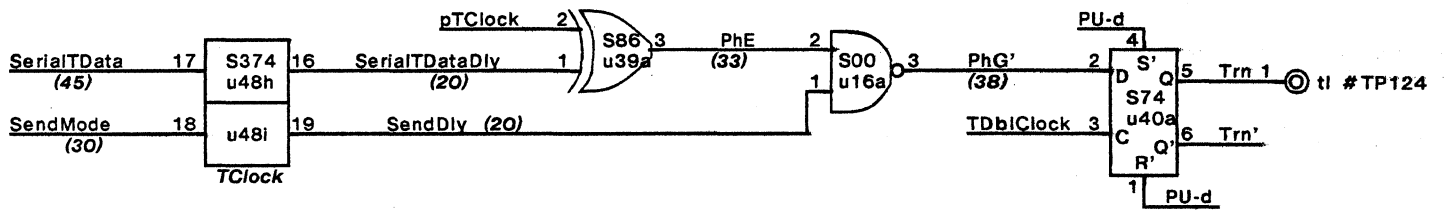
CRC sampled on byte boundaries, i.e., dribbling transceivers allowed.

Half Duplex Buffer



CRC

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028		SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 08 OF		A

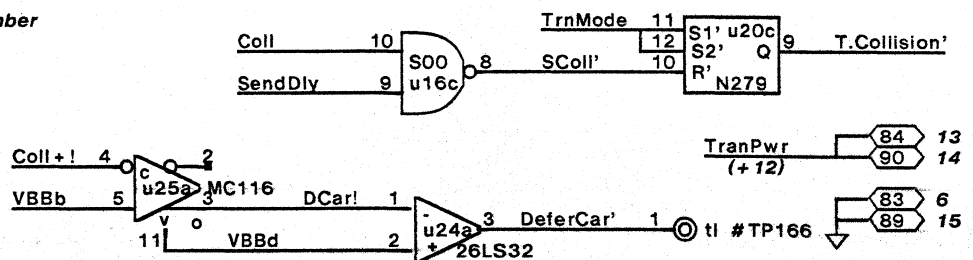


40 Pin connector 713W12720
 Subtract 50 for actual pin number
 Italic number is 15-pin transceiver connector number

ENTerm1

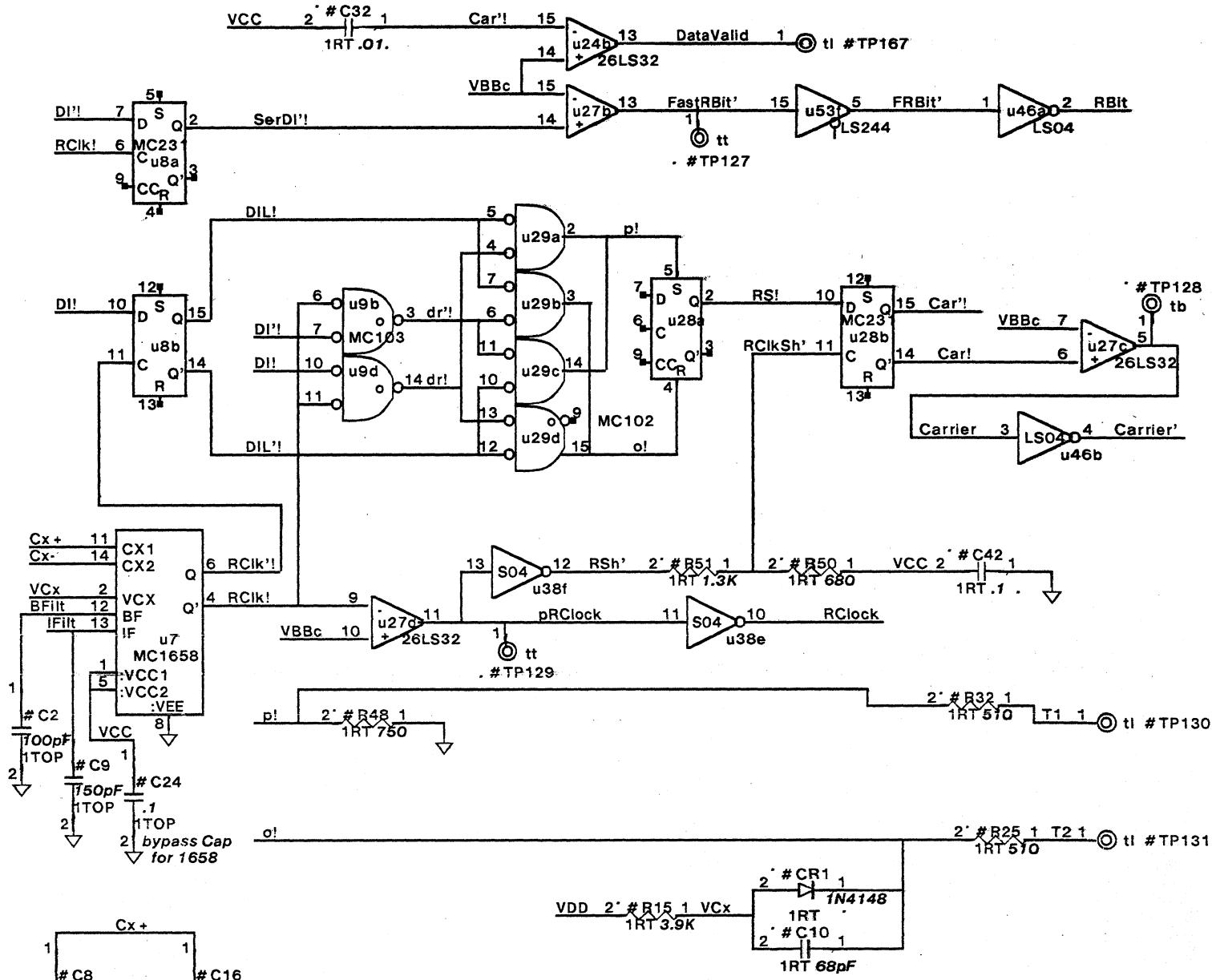
RComp!	1	P1	u26	P16	16	GND
CComp!	2	P2		P15	15	Car!
Coll+!	3	P3		P14	14	SerD!'
RcvE!	4	P4		P13	13	Mux!
TrnE!	5	P5		P12	12	
Rcv+!	6	P6		P11	11	Car!
ZComp!	7	P7		P10	10	
RS!	8	P8	PLAT	P9	9	Loop!

510 ohm



Phase Encoder and Cable Interface

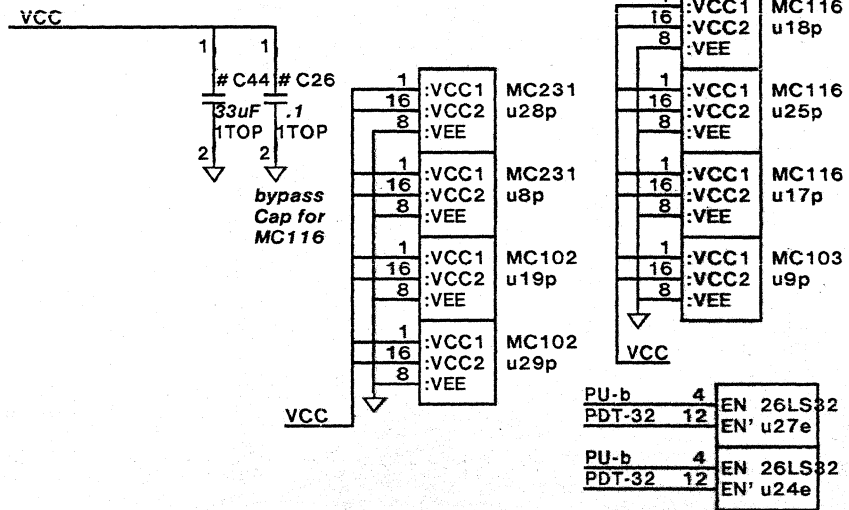
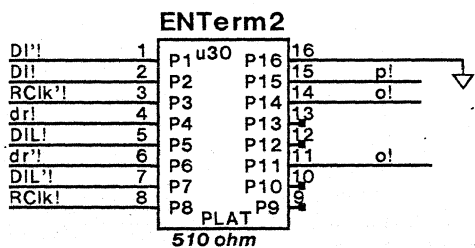
XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 09 OF		



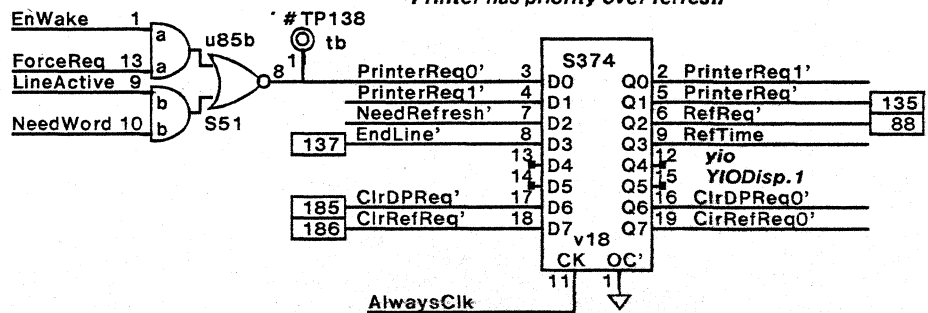
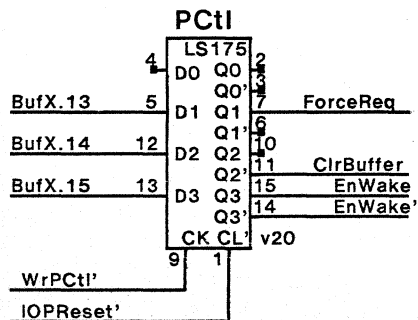
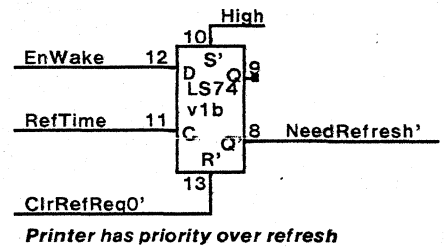
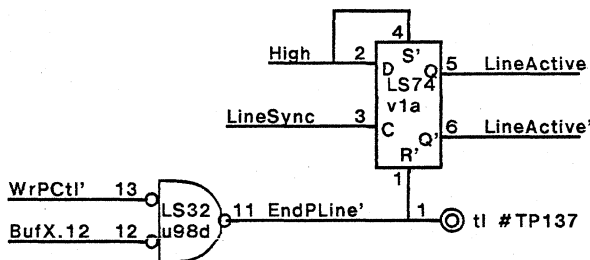
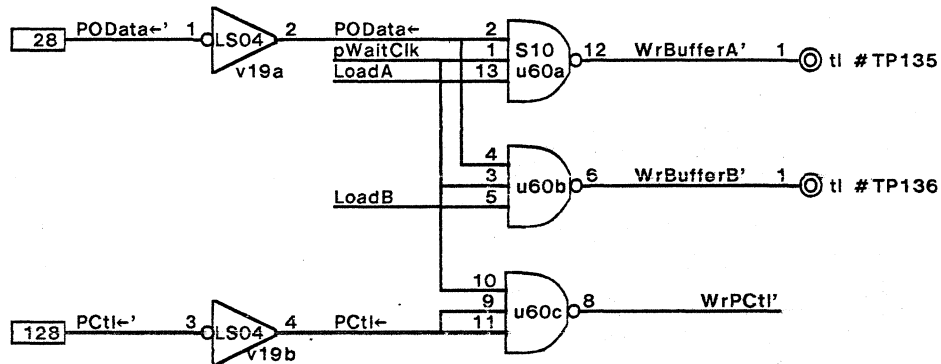
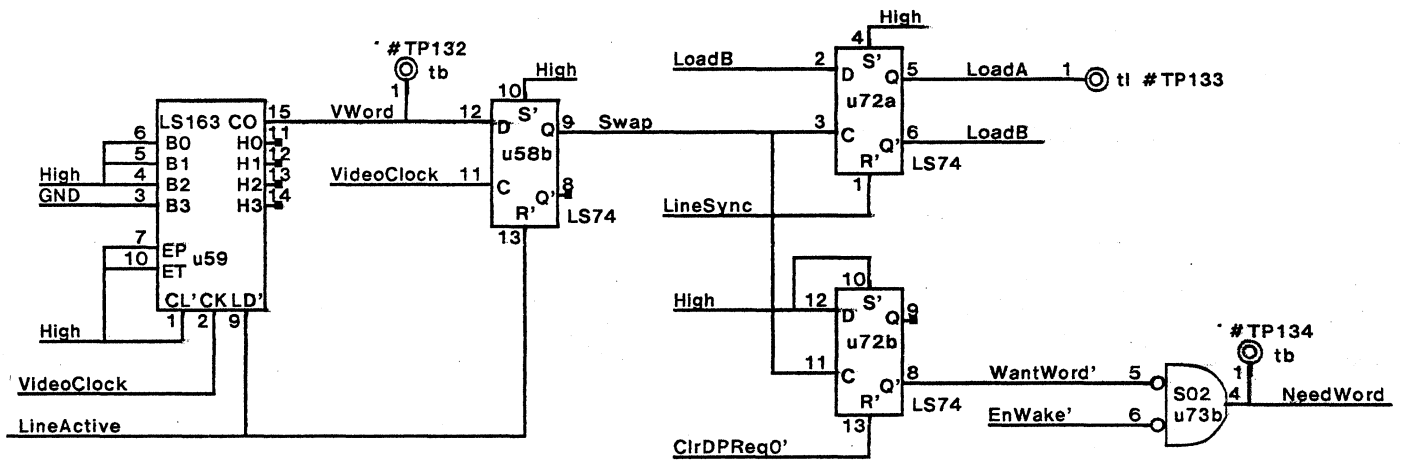
The ECL is operated from 0 to +5V instead of -5.2 to 0V. Thus logic 1 is 4.2V & logic 0 is 3.2V.

The Trim Cap should be adjusted so that the duty cycle of p is the same as o. Both should be high for about 25 nSec.

Equivalently, the Trim Cap should be adjusted to get less than 10mV DC between T1 & T2 (Use meter with greater than 20K ohms/volt Input Impedance)

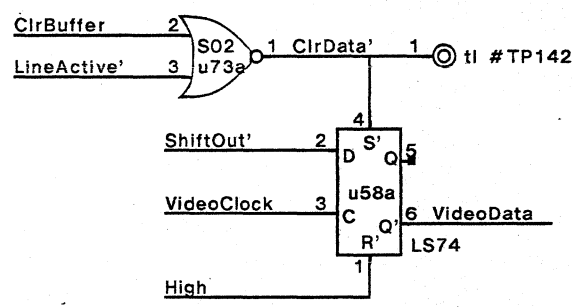
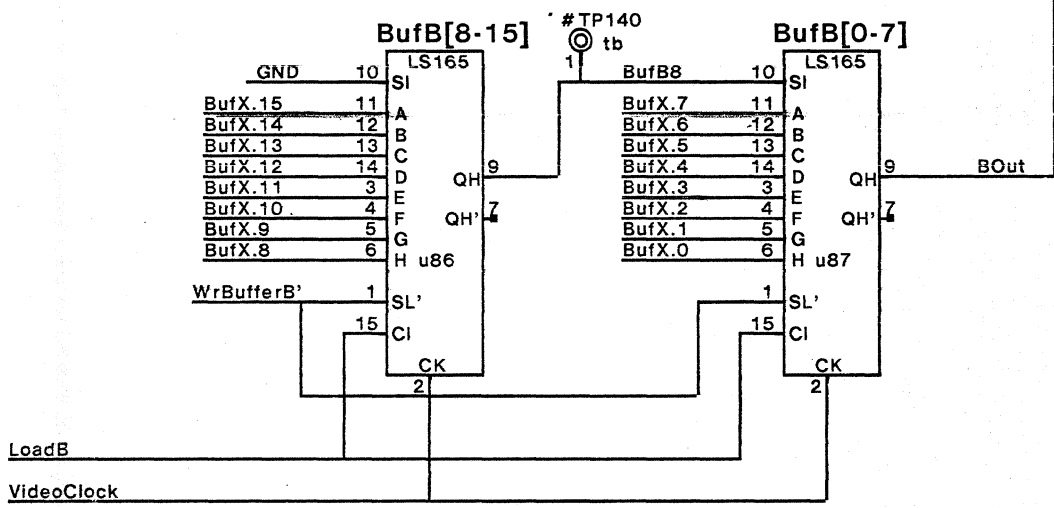
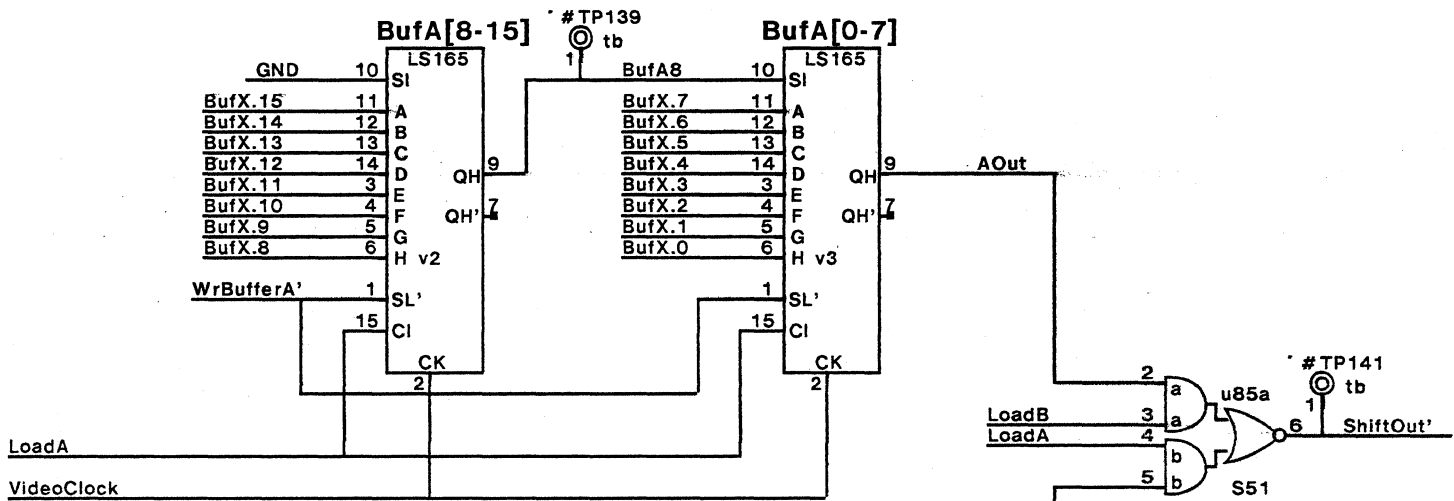


PLL Receiver



LSEP Wakeup Logic

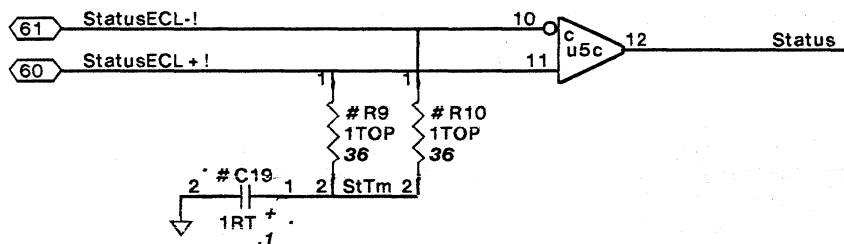
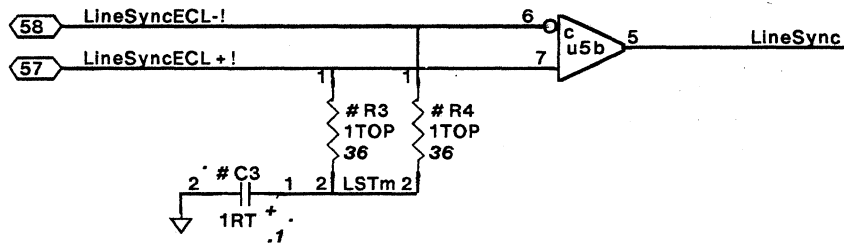
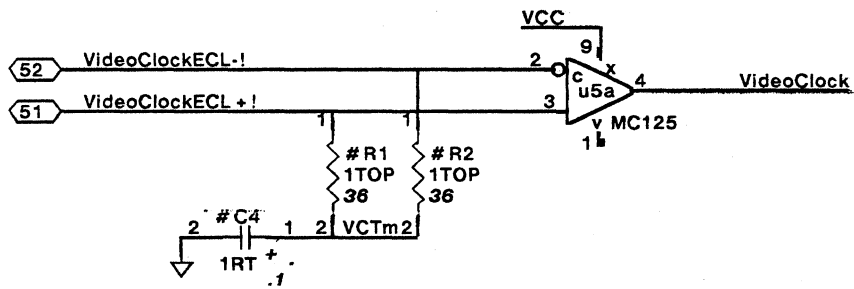
XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 11 OF	A



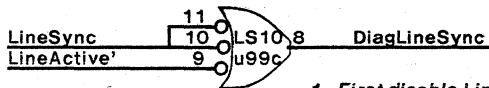
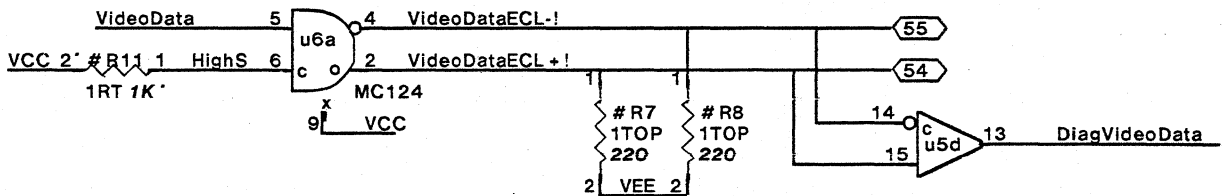
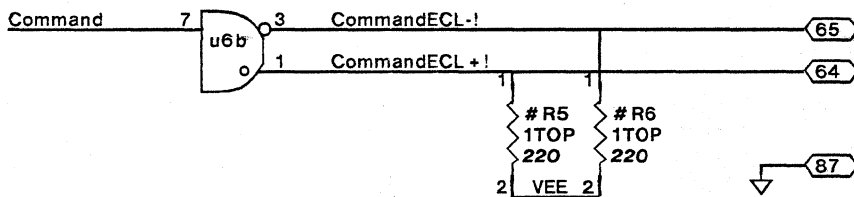
Reclocking the data avoids glitches at word boundaries

LSEP Shifters

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET 12 OF		



40 Pin connector 713W12720
Subtract 50 for actual pin number



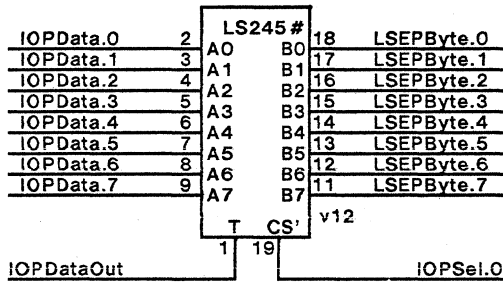
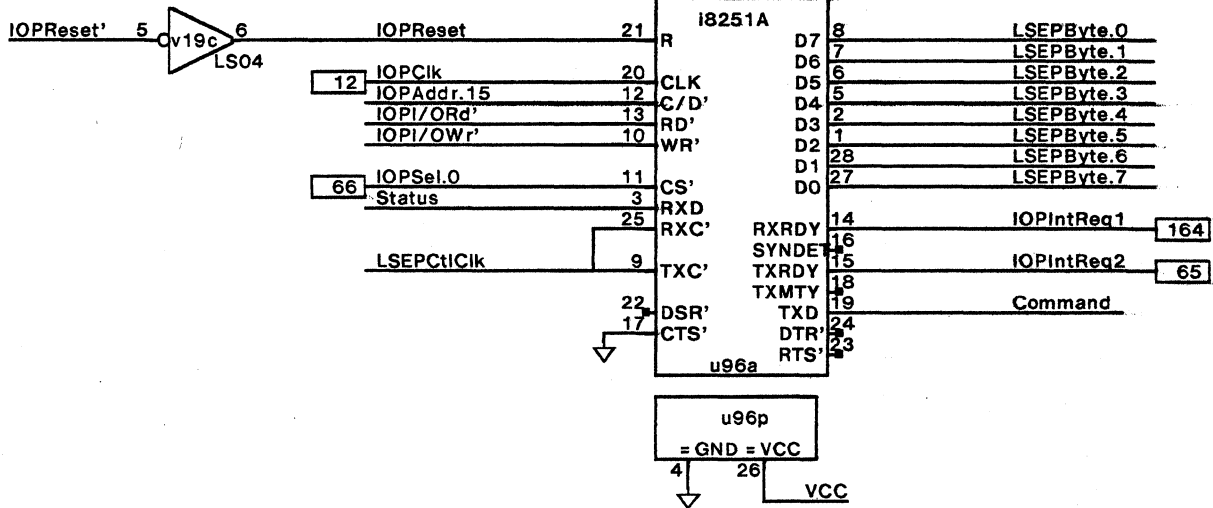
1. First disable LineActive' by setting EndPrinterLine-1.
2. DiagLineSync = 1 if LineSync is stuck 0, = 0 if LineSync is stuck 1.
3. Enable LineActive' by setting EndPrinterLine-0.
4. Sample DiagLineSync: If Line Sync has pulses, this will set LineActive' to 0, causing DiagLineSync = 1. If LineSync is not pulsing DiagLineSync = 0.

High 2' #R63 1 1K VCC
1RT

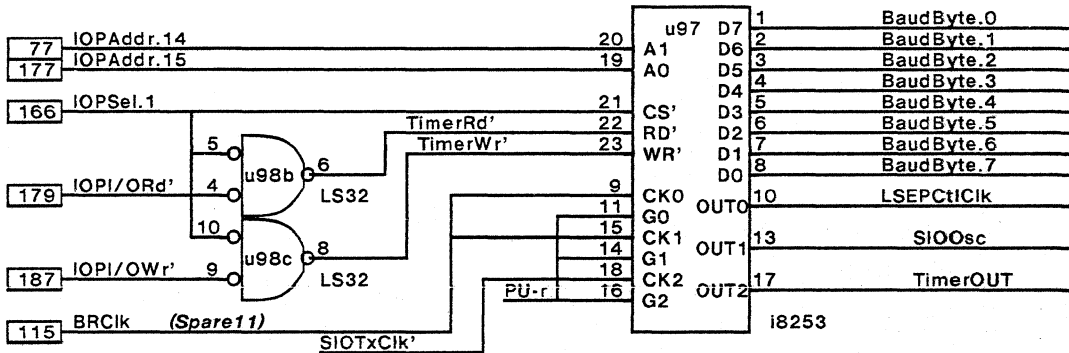
LSEP Printer Connector & Diagnostics

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	13 OF	

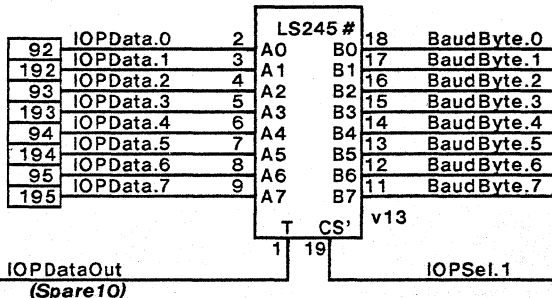
LSEP UART



Baud-rate generator

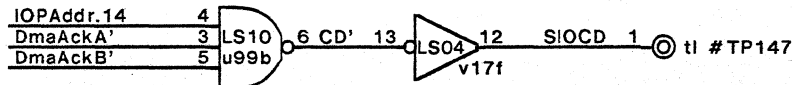
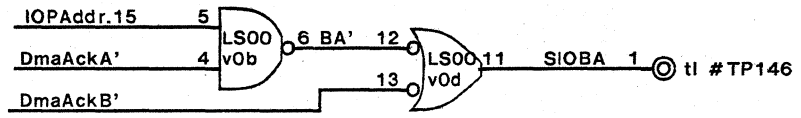
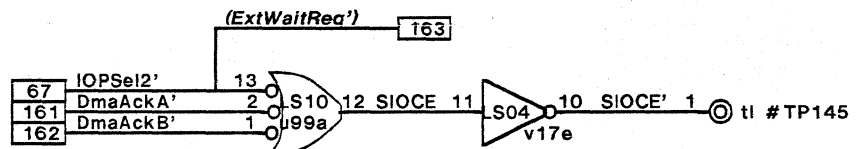
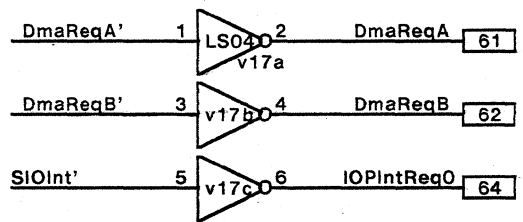
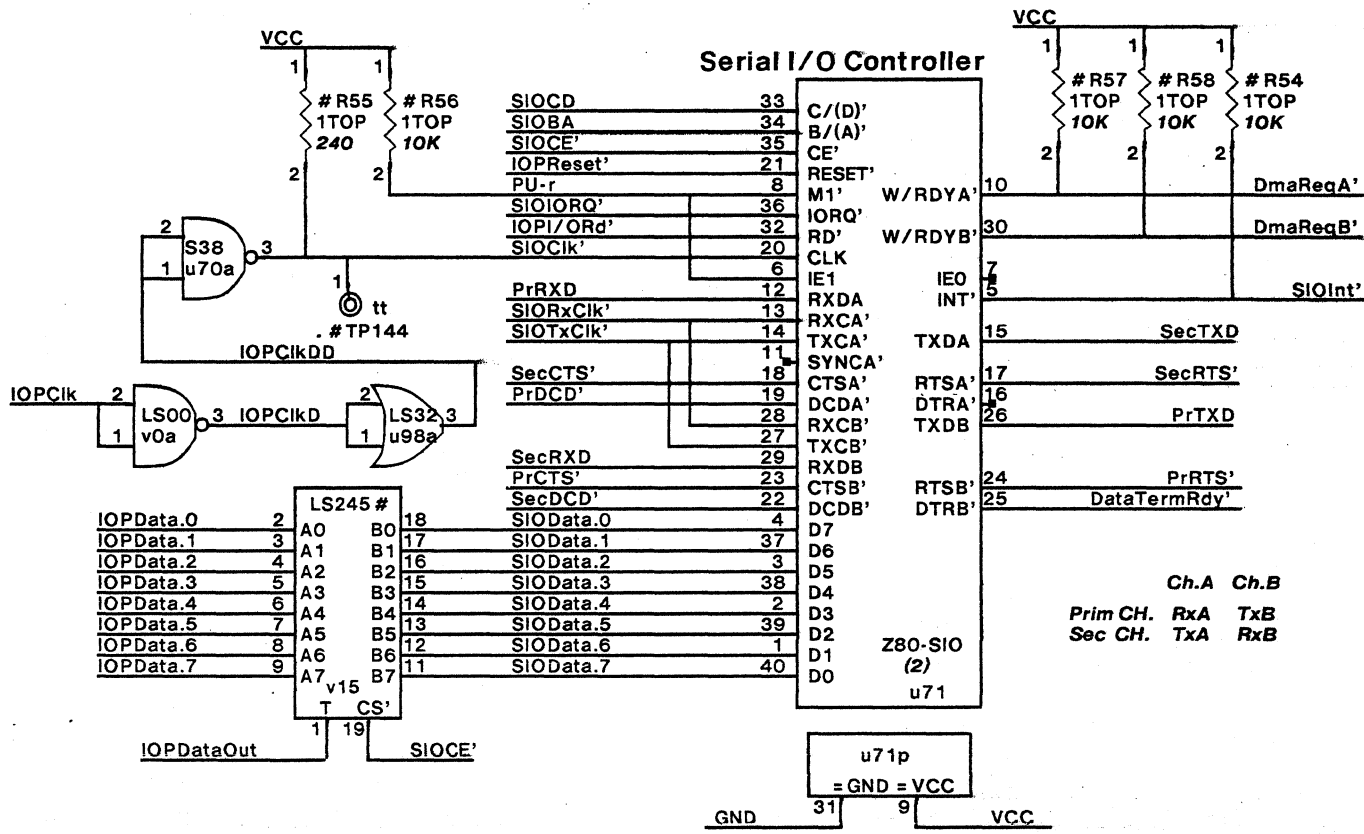


Note: Due to a design shortcoming the RD' and WR' lines of the 8253-5 must be externally qualified.



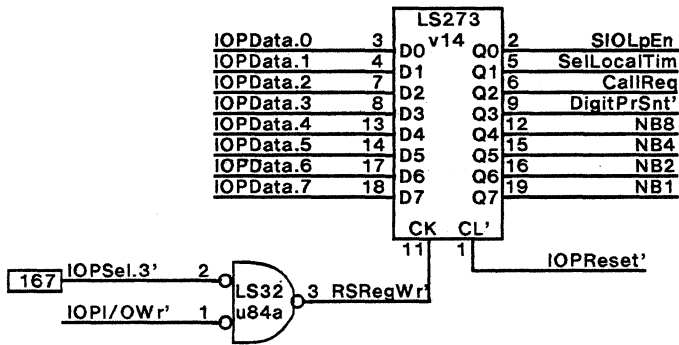
LSEP Control and Status

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 14 OF	A

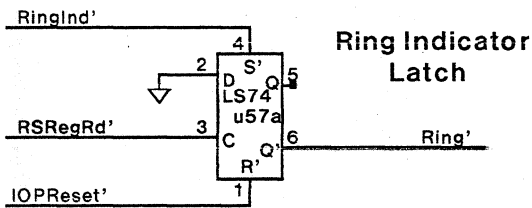
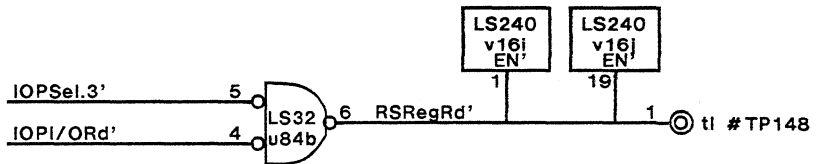
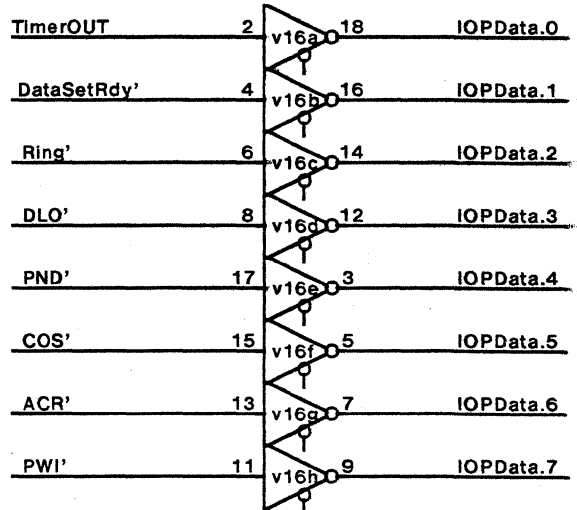


RS-232-C Interface, Z80-SIO/2, 8085 Converter

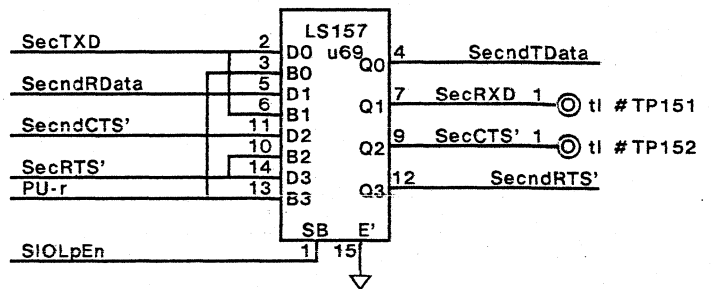
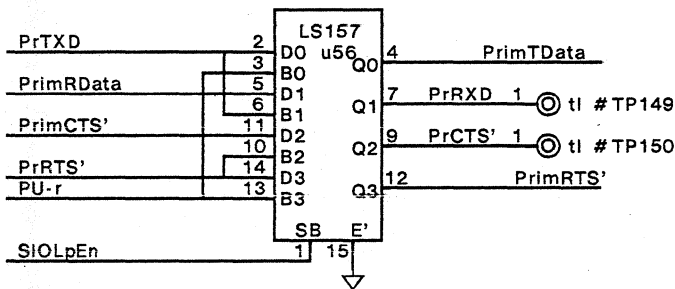
RS366 Control Register



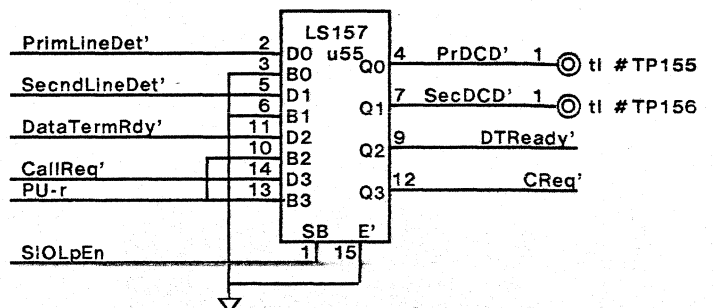
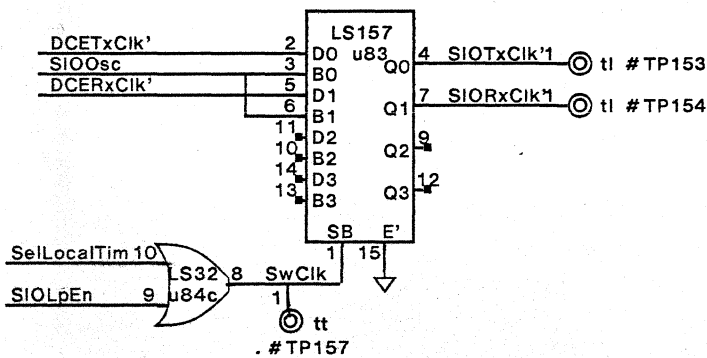
RS366 Status



Diag Loop-Back MPX

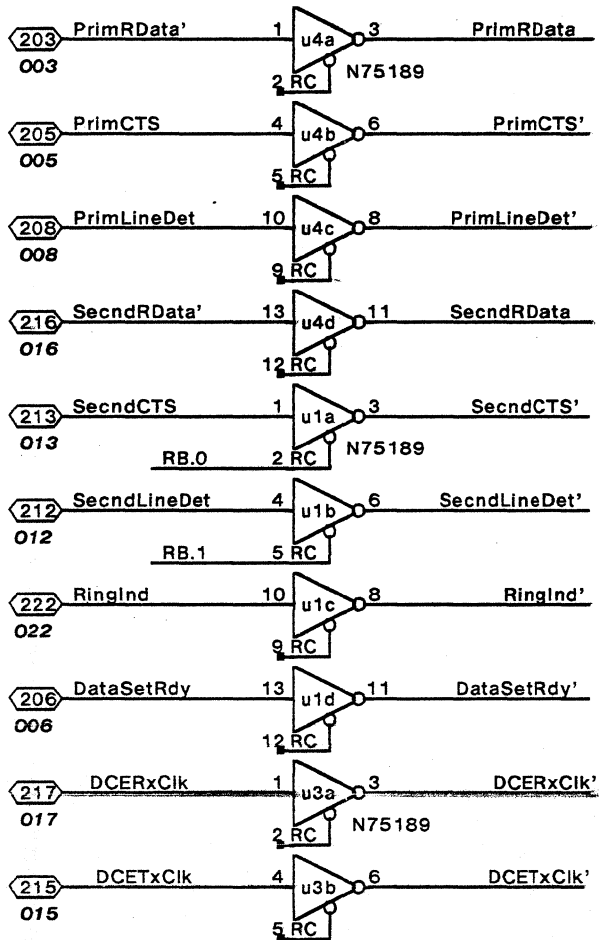


Clock Switch



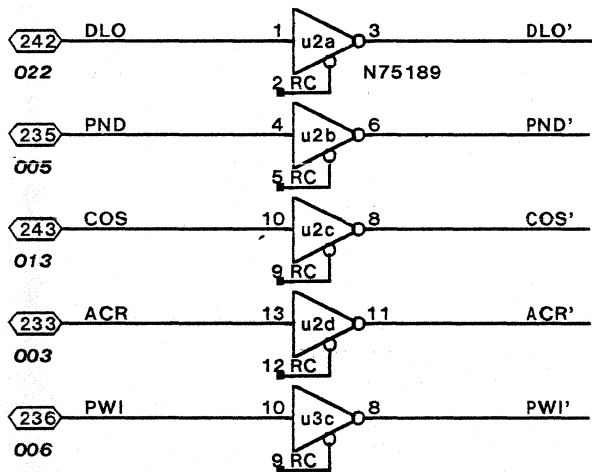
RS366 Control & Status, Diag Loop-Back MPX

RS232C Receivers

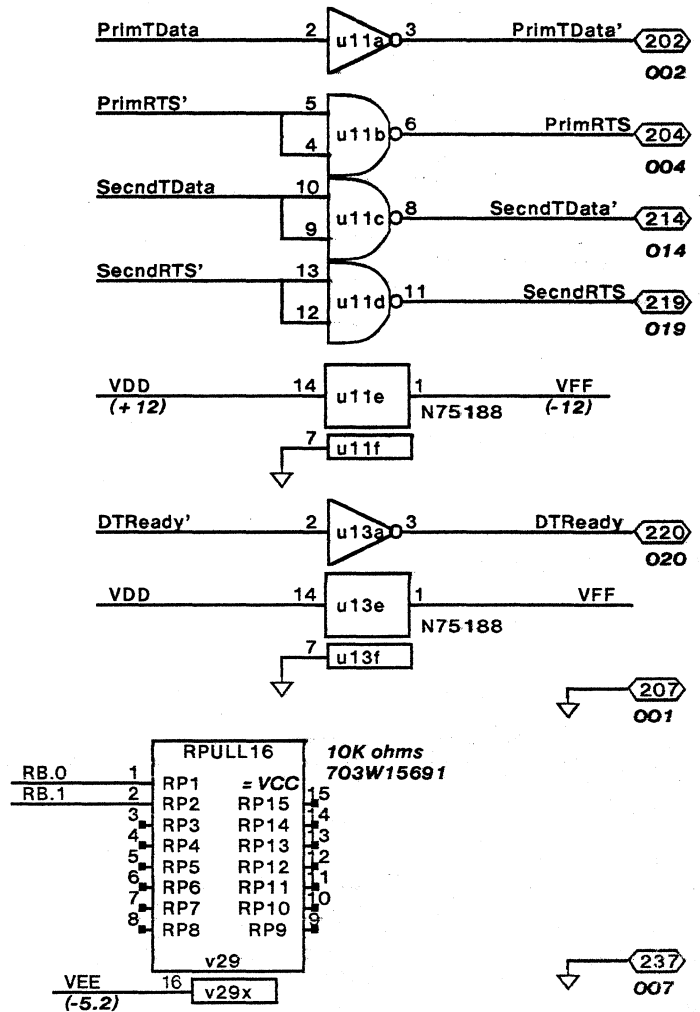


50-Pin Male Connector 713W12820
 Subtract 200 for actual pin number
 Italic pin number is DTE pin number

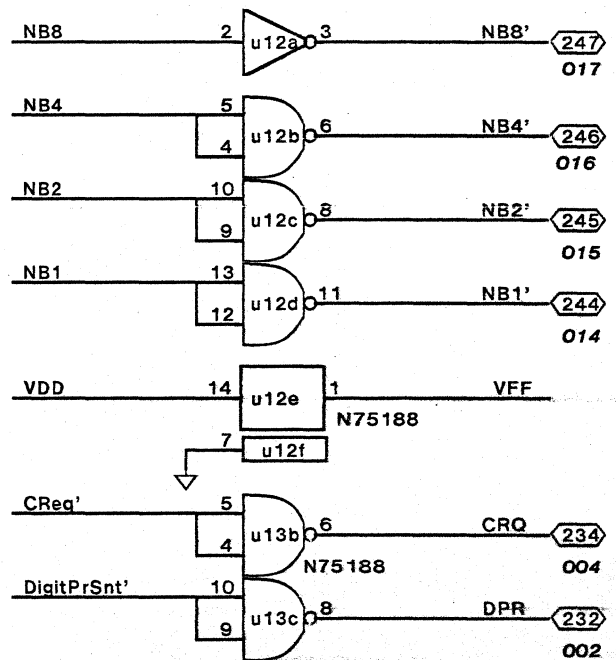
RS366A Receivers



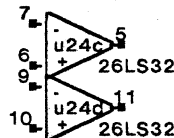
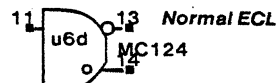
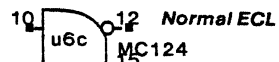
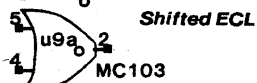
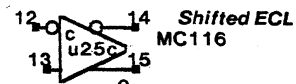
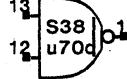
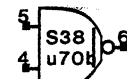
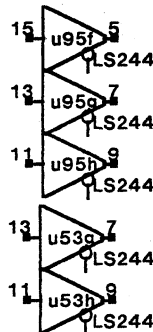
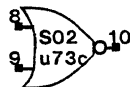
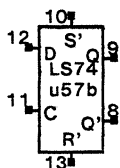
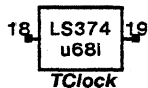
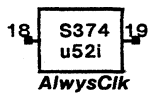
RS232C Drivers



RS366A Drivers



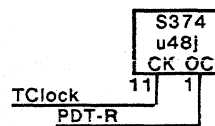
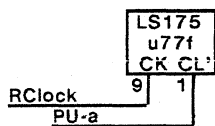
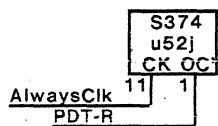
RS-232-C Driver/Receiver, RS366 Driver/Receiver



- b RdBufA'
- c RdBufAA'
- d pE'
- e EReq'
- f BufORA
- g BufIRA'
- h RcvModeDly'
- i ...

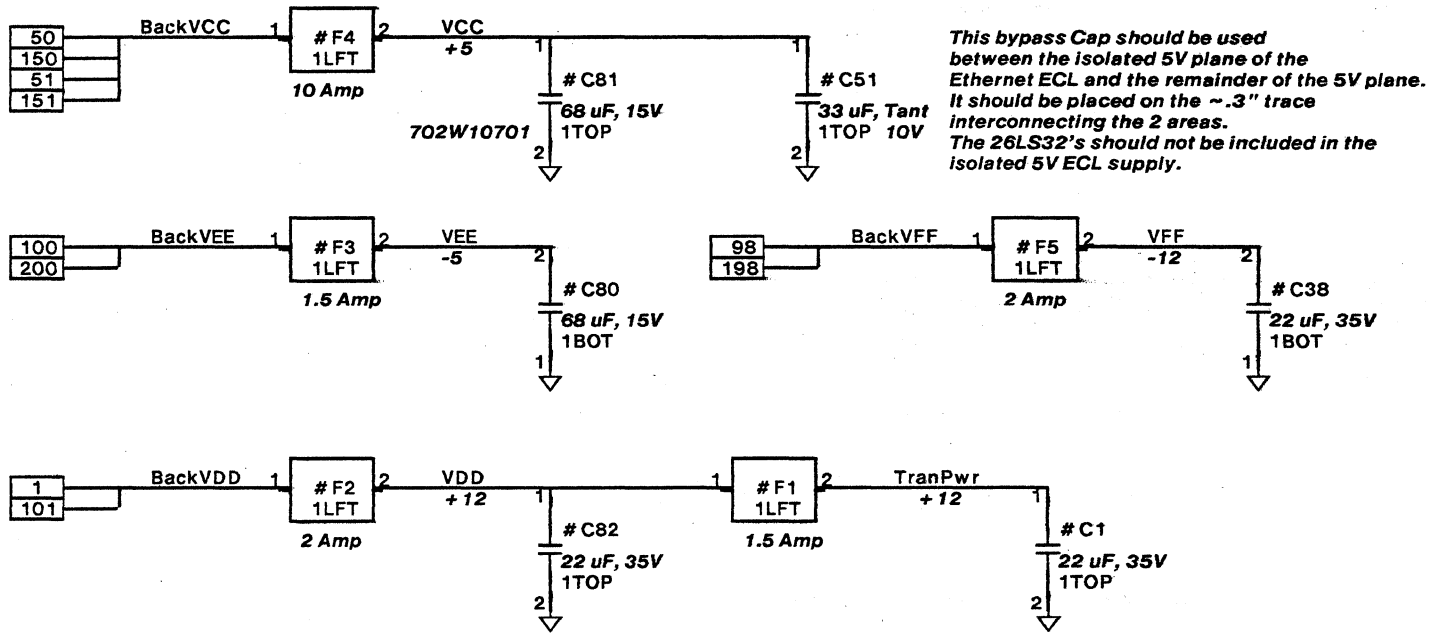
- b PktInMode'
- c RcvCnt = 16
- d GoodAlign
- e IgnorePkt

- b BeginEnd.0
- c BeginEnd.1
- d T.UnderRun'
- e TrnCnt = 16
- f ppStIPG
- g pStIPG
- h SerialTDataDly
- i SendDly



Spares, Regs Clocks,

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 18 OF	A



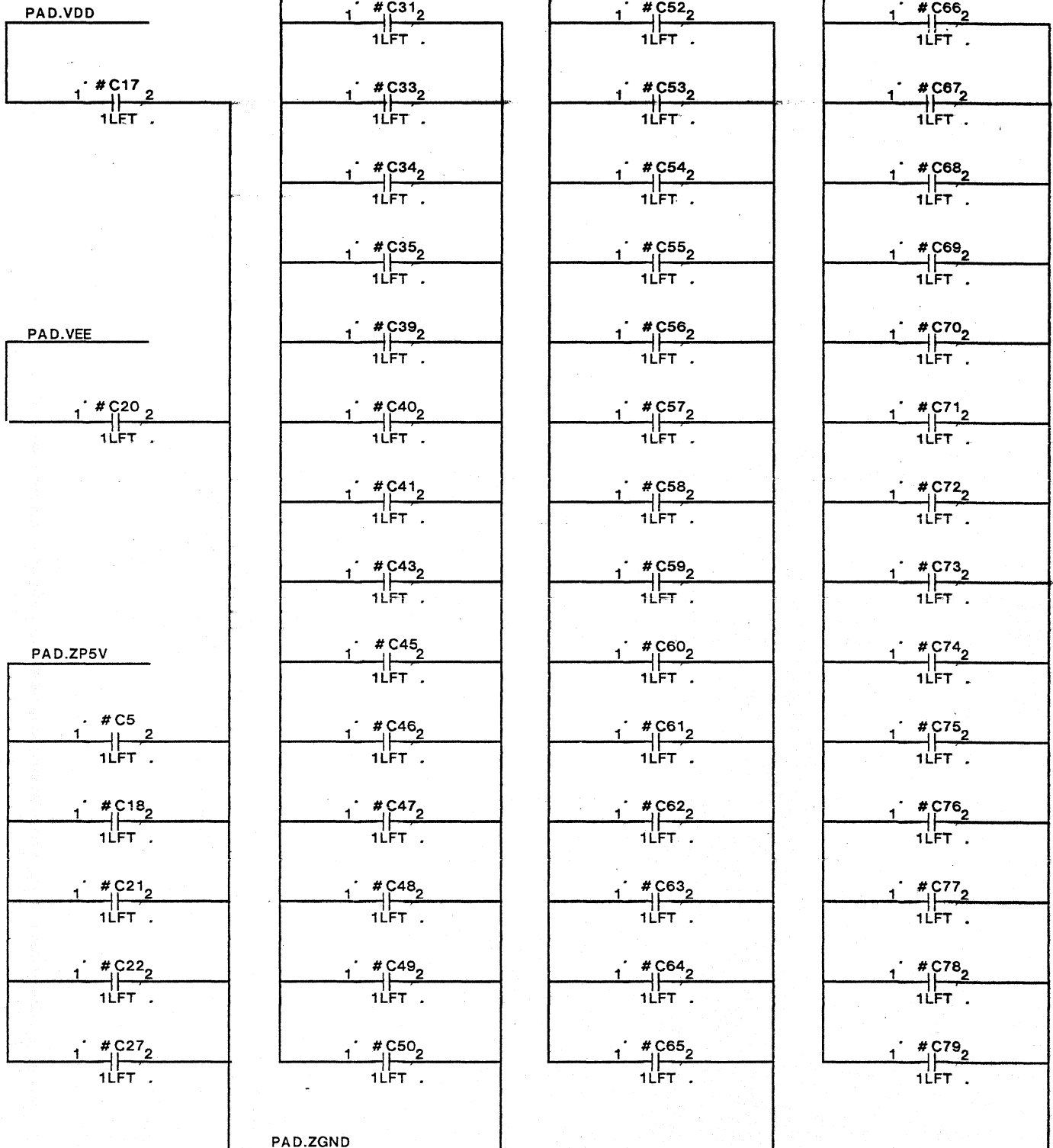
This bypass Cap should be used between the isolated 5V plane of the Ethernet ECL and the remainder of the 5V plane. It should be placed on the ~.3" trace interconnecting the 2 areas. The 26LS32's should not be included in the isolated 5V ECL supply.

F5 & C32 should be located near connector

Fuses, Filter Caps

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	19 OF	

PAD.ZP5V



NOTE: All capacitors on this page are ceramic capacitors, 50V, 0.10uf.

DISCRETE PAGE

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 20 OF	A

Comments:

- 1) Designator notation notes: u1-99 = U1-99, v0-99 = U100-199, w0-99 = U200-299
- 2) The last item on lines below, preceeded by a semicolon (;), is the schematic page number on which the test point, connector or signal information originates.
- 3) Line with no page number was a continuation of the previous line.

#TP001	.1i	PDT-R	;01	#TP052	.1i	trnm	;04
#TP002	.1i	PDT-P	;01	#TP053	.1i	pktom	;04
#TP003	.1i	PDT-B	;01	#TP054	.1i	sndm	;04
#TP004	.1i	PDT-32	;01	#TP055	.1i	outa	;04
#TP005	.1i	Tester	;01	#TP056	.1i	ts.0	;04
#TP006	.1i	PU-a	;01	#TP057	.1i	ts.1	;04
#TP007	.1i	PU-b	;01	#TP058	.1i	ts.2	;04
#TP008	.1i	LoopBack	;02	#TP059	.1i	ts.3	;04
#TP009	.1i	LocalLoop	;02	#TP060	.1i	TrnCnt=15	;05
#TP010	.1i	TurnOff'	;02	#TP061	.1i	DC=8	;05
#TP011	.1i	EnableRcv	;02	#TP062	.1i	DC=128	;05
#TP012	.1i	Defer	;02	#TP063	.1i	DC=512	;05
#TP013	.1i	LastWord	;02	#TP064	.1i	TickElapsed	;05
#TP014	.1i	EnableTrn	;02	#TP065	.1i	IPGD	;05
#TP015	.1i	ppE'	;02	#TP066	.1i	IPGDone	;05
#TP016	.1i	R.0	;03	#TP067	.1i	StartIPG	;05
#TP017	.1i	PreamDet'	;03	#TP068	.1i	RdBuf'	;06
#TP018	.1i	RcvCnt=15	;03	#TP069	.1i	WrBuf'	;06
#TP019	.1i	PktInMode	;03	#TP070	.1i	CTrBuf'	;06
#TP020	.1i	PktOutMnLpAA	;03	#TP071	.1i	RcvModeDly'	;06
#TP021	.1i	LastWrdaA'	;03	#TP072	.1i	BufORA	;06
#TP022	.1i	BufORAA	;03	#TP073	.1i	BD.0	;06
#TP023	.1i	PktInModeAA	;03	#TP074	.1i	BD.1	;06
#TP024	.1i	OverRunAA'	;03	#TP075	.1i	BD.2	;06
#TP025	.1i	pur	;03	#TP076	.1i	BD.3	;06
#TP026	.1i	rs.0	;03	#TP077	.1i	LastWord	;06
#TP027	.1i	rs.1	;03	#TP078	.1i	BQ.0	;06
#TP028	.1i	rs.2	;03	#TP079	.1i	BQ.1	;06
#TP029	.1i	Purge	;03	#TP080	.1i	BQ.2	;06
#TP030	.1i	RcvMode	;03	#TP081	.1i	BQ.3	;06
#TP031	.1i	RcvState.1	;03	#TP082	.1i	LastTWrd	;06
#TP032	.1i	RcvState.2	;03	#TP083	.1i	BD.4	;06
#TP033	.1i	IPGDone	;04	#TP084	.1i	BD.5	;06
#TP034	.1i	LastTWrdT	;04	#TP085	.1i	BD.6	;06
#TP035	.1i	RcvModeTT	;04	#TP086	.1i	BD.7	;06
#TP036	.1i	T.CollisionTT'	;04	#TP087	.1i	GoodAlign	;06
#TP037	.1i	UnderRunT	;04	#TP088	.1i	BQ.4	;06
#TP038	.1i	be.0	;04	#TP089	.1i	BQ.5	;06
#TP039	.1i	be.1	;04	#TP090	.1i	BQ.6	;06
#TP040	.1i	tund	;04	#TP091	.1i	BQ.7	;06
#TP041	.1i	BeginEnd.0	;04	#TP092	.1i	R.GoodAlign	;06
#TP042	.1i	BeginEnd.1	;04	#TP093	.1i	BD.8	;06
#TP043	.1i	T.UnderRun'	;04	#TP094	.1i	BD.9	;06
#TP044	.1i	PktOutMode	;04	#TP095	.1i	BD.10	;06
#TP045	.1i	TrnMode	;04	#TP096	.1i	BD.11	;06
#TP046	.1i	SendMode	;04	#TP097	.1i	EvenLen	;06
#TP047	.1i	OutAttn	;04	#TP098	.1i	BQ.8	;06
#TP048	.1i	TrnState.0	;04	#TP099	.1i	BQ.9	;06
#TP049	.1i	TrnState.1	;04	#TP100	.1i	BQ.10	;06
#TP050	.1i	TrnState.2	;04	#TP101	.1i	BQ.11	;06
#TP051	.1i	TrnState.3	;04	#TP102	.1i	R.EvenLen	;06

#TP103	.1i	BD.12	;06	#TP162	.1i	GoodCRC	;06
#TP104	.1i	BD.13	;06	#TP163	.1i	R.GoodCRC	;06
#TP105	.1i	BD.14	;06	#TP164	.1i	crc.26'	;08
#TP106	.1i	BD.15	;06	#TP165	.1i	crc.30'	;08
#TP107	.1i	InEOP	;06	#TP166	.1i	DeferCar'	;09
#TP108	.1i	BQ.12	;06	#TP167	.1i	DataValid	;10
#TP109	.1i	BQ.13	;06				
#TP110	.1i	BQ.14	;06	C051		VideoClockECL+	;13
#TP111	.1i	BQ.15	;06	C052		VideoClockECL-	;13
#TP112	.1i	pLRWrd	;06	C054		VideoDataECL+	;13
#TP113	.1i	BufOR	;07	C055		VideoDataECL-	;13
#TP114	.1i	BufIR	;07	C057		LineSyncECL+	;13
#TP115	.1i	C1	;08	C058		LineSyncECL-	;13
#TP116	.1i	C2	;08	C060		StatusECL+	;13
#TP117	.1i	C3	;08	C061		StatusECL-	;13
#TP118	.1i	C5'	;08	C064		CommandECL+	;13
#TP119	.1i	C6'	;08	C065		CommandECL-	;13
#TP120	.1i	SetCRC'	;08	C069		Collision+	;09
#TP121	.1i	SerialTData	;08	C070		Collision-	;09
#TP122	.1i	crc.32a'	;08	C071		Transmit-	;09
#TP123	.1i	crc.32b'	;08	C072		Transmit+	;09
#TP124	.1i	Trn	;09	C073		Receive-	;09
#TP125	.1i	TDb1Clock	;09	C074		Receive+	;09
#TP126	.1i	Coll	;09	C083		GND	;09
#TP127	.1i	FastRBit'	;10	C084		TranPwr	;09
#TP128	.1i	Carrier	;10	C087		GND	;13
#TP129	.1i	pRClock	;10	C089		GND	;09
#TP130	.1i	T1	;10	C090		TranPwr	;09
#TP131	.1i	T2	;10	C202		PrimTData'	;17
#TP132	.1i	VWord	;11	C203		PrimRData'	;17
#TP133	.1i	LoadA	;11	C204		PrimRTS	;17
#TP134	.1i	NeedWord	;11	C205		PrimCTS	;17
#TP135	.1i	WrBufferA'	;11	C206		DataSetRdy	;17
#TP136	.1i	WrBufferB'	;11	C207		GND	;17
#TP137	.1i	EndPLine'	;11	C208		PrimLineDet	;17
#TP138	.1i	PrinterReq0'	;11	C212		SecndLineDet	;17
#TP139	.1i	BufA8	;12	C213		SecndCTS	;17
#TP140	.1i	BufB8	;12	C214		SecndTData'	;17
#TP141	.1i	ShiftOut'	;12	C215		DCETxC1k	;17
#TP142	.1i	ClrData'	;12	C216		SecndRData'	;17
#TP143	.1i	SIOIORQ'	;15	C217		DCERxC1k	;17
#TP144	.1i	SIOC1k'	;15	C219		SecndRTS	;17
#TP145	.1i	SIOCE'	;15	C220		DTReady	;17
#TP146	.1i	SIOBA	;15	C222		RingInd	;17
#TP147	.1i	SIOCD	;15	C232		DPR	;17
#TP148	.1i	RSRegRd'	;16	C233		ACR	;17
#TP149	.1i	PrRXD	;16	C234		CRQ	;17
#TP150	.1i	PrCTS'	;16	C235		PND	;17
#TP151	.1i	SecRXD	;16	C236		PWI	;17
#TP152	.1i	SecCTS'	;16	C237		GND	;17
#TP153	.1i	SIOTxC1k'	;16	C242		DLO	;17
#TP154	.1i	SIORxC1k'	;16	C243		COS	;17
#TP155	.1i	PrDCD'	;16	C244		NB1'	;17
#TP156	.1i	SecDCD'	;16	C245		NB2'	;17
#TP157	.1i	SwC1k	;16	C246		NB4'	;17
#TP158	.1i	RdBufD'	;03	C247		NB8'	;17
#TP159	.1i	RdBufA'	;03				
#TP160	.1i	RdBufAA'	;03	E001		BackVDD	;19
#TP161	.1i	IgnoreOR	;03	E003		Cycle2'	;06

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	22 OF	

E009	ppCLK	:01	E167	IOPSe1.3'	:16
E012	IOPC1k	:14	E177	IOPAddr.15	:14
E015	IOPDataOut	:14	E179	IOPI/ORd'	:14
E017	Wait	:01	E185	ClrDPReq'	:11
E023	EOData←'	:06	E186	ClrRefReq'	:11
E028	POData←'	:11	E187	IOPI/OWr'	:14
E031	←EIData'	:06	E192	IOPData.1	:14
E035	ESTrobe'	:06	E193	IOPData.3	:14
E039	GND	:02	E194	IOPData.5	:14
E041	X.0	:01	E195	IOPData.7	:14
E042	X.2	:01	E198	BackVFF	:19
E043	X.4	:01	E200	BackVEE	:19
E044	X.6	:01			
E045	X.8	:01			
E046	X.10	:01	ACR': v16.13i	:16	
E047	X.12	:01	ACR': u02.11o	:17	
E048	X.14	:01			
E050	BackVCC	:19	ACR: C233, u02.13i	:17	
E051	BackVCC	:19			
E061	DmaReqA	:15	AlwaysC1k: u75.12o	:01	
E062	DmaReqB	:15	AlwaysC1k: u63.9i	:03	
E064	IOPIntReq0	:15	AlwaysC1k: u65.11i	:03	
E065	IOPIntReq2	:14	AlwaysC1k: v18.11i	:11	
E066	IOPSe1.0	:14	AlwaysC1k: u52.11i	:18	
E067	IOPSe12'	:15			
E077	IOPAddr.14	:14	AOut: v03.9o, u85.2i	:12	
E086	EReq'	:02			
E088	RefReq'	:11	Attn: u61.8o, v18.13i	:02	
E092	IOPData.0	:14	Attn: u62.9i, u62.10i	:02	
E093	IOPData.2	:14			
E094	IOPData.4	:14	BA': v00.6o, v00.12i	:15	
E095	IOPData.6	:14			
E098	BackVFF	:19	BackVCC: E150, E51, E151, E50	:19	
E100	BackVEE	:19	BackVCC: #F4.1i		
E101	BackVDD	:19			
E115	BRC1k	:14	BackVDD: E101, E1, #F2.1i	:19	
E117	IOPReset'	:02			
E123	EICt1←'	:02	BackVEE: E200, E100, #F3.1i	:19	
E126	EOCt1←'	:02			
E128	PCT1←'	:11	BackVFF: E198, E98, #F5.1i	:19	
E131	←EStatus'	:02			
E135	PrinterReq'	:11	BaudByte.0: v13.18o	:14	
E137	EndLine'	:11	BaudByte.0: u97.1o	:14	
E139	YIODisp.1	:02			
E141	X.1	:01	BaudByte.1: v13.17o	:14	
E142	X.3	:01	BaudByte.1: u97.2o	:14	
E143	X.5	:01			
E144	X.7	:01	BaudByte.2: v13.16o	:14	
E145	X.9	:01	BaudByte.2: u97.3o	:14	
E146	X.11	:01			
E147	X.13	:01	BaudByte.3: v13.15o	:14	
E148	X.15	:01	BaudByte.3: u97.4o	:14	
E150	BackVCC	:19			
E151	BackVCC	:19	BaudByte.4: v13.14o	:14	
E161	DmaAckA'	:15	BaudByte.4: u97.5o	:14	
E162	DmaAckB'	:15			
E163	IOPSe12'	:15	BaudByte.5: v13.13o	:14	
E164	IOPIntReq1	:14	BaudByte.5: u97.6o	:14	
E166	IOPSe1.1	:14			

BaudByte.6: v13.12o ;14
 BaudByte.6: u97.7o ;14

 BaudByte.7: v13.11o ;14
 BaudByte.7: u97.8o ;14

 BD.0: #TP073.1i ;06
 BD.0: u89.2o, v05.2o, u94.4i ;07

 BD.10: #TP095.1i ;06
 BD.10: v06.6o, u90.6o, u91.6i ;07

 BD.11: #TP096.1i ;06
 BD.11: v06.9o, u90.9o, u91.7i ;07

 BD.12: #TP103.1i ;06
 BD.12: u90.12o, v06.12o, u92.4i ;07

 BD.13: #TP104.1i ;06
 BD.13: u90.15o, v06.15o, u92.5i ;07

 BD.14: #TP105.1i ;06
 BD.14: u90.16o, v06.16o, u92.6i ;07

 BD.15: #TP106.1i ;06
 BD.15: u90.19o, v06.19o, u92.7i ;07

 BD.1: #TP074.1i ;06
 BD.1: u89.5o, v05.5o, u94.5i ;07

 BD.2: #TP075.1i ;06
 BD.2: u89.6o, v05.6o, u94.6i ;07

 BD.3: #TP076.1i ;06
 BD.3: u89.9o, v05.9o, u94.7i ;07

 BD.4: #TP083.1i ;06
 BD.4: v05.12o, u89.12o, u93.4i ;07

 BD.5: #TP084.1i ;06
 BD.5: v05.15o, u89.15o, u93.5i ;07

 BD.6: #TP085.1i ;06
 BD.6: v05.16o, u89.16o, u93.6i ;07

 BD.7: #TP086.1i ;06
 BD.7: v05.19o, u89.19o, u93.7i ;07

 BD.8: #TP093.1i ;06
 BD.8: v06.2o, u90.2o, u91.4i ;07

 BD.9: #TP094.1i ;06
 BD.9: v06.5o, u90.5o, u91.5i ;07

 be.0: u67.9o, u48.3i ;04
 be.0: #TP038.1i ;04

 be.1: u67.10o, u48.4i ;04
 be.1: #TP039.1i ;04

BeginEnd.0: u48.2o, #TP041.1i ;04
 BeginEnd.0: u49.1i ;04
 BeginEnd.0: u50.1i ;04

 BeginEnd.1: u48.5o, #TP042.1i ;04
 BeginEnd.1: u49.2i ;04
 BeginEnd.1: u50.2i ;04

 Bfilt: #C2.1i, u07.12i ;10

 BIR: u92.2o, u95.8i ;07

 BOR: u92.17o, u95.6i ;07

 BOut: u87.9o, u85.5i ;12

 BQ.0: #TP078.1i ;06
 BQ.0: u94.15o, v24.3i, v09.7o ;07

 BQ.10: #TP100.1i ;06
 BQ.10: u91.13o, v07.7i, v08.6o ;07

 BQ.11: #TP101.1i ;06
 BQ.11: u91.12o, v07.8i, v08.14o ;07

 BQ.12: #TP108.1i ;06
 BQ.12: u92.15o, v07.13i, v08.5o ;07

 BQ.13: #TP109.1i ;06
 BQ.13: u92.14o, v07.14i, v08.15o ;07

 BQ.14: #TP110.1i ;06
 BQ.14: u92.13o, v07.17i, v08.4o ;07

 BQ.15: #TP111.1i ;06
 BQ.15: u92.12o, v07.18i, v08.16o ;07

 BQ.1: #TP079.1i ;06
 BQ.1: u94.14o, v24.4i, v09.13o ;07

 BQ.2: #TP080.1i ;06
 BQ.2: u94.13o, v24.7i, v09.6o ;07

 BQ.3: #TP081.1i ;06
 BQ.3: u94.12o, v24.8i, v09.14o ;07

 BQ.4: #TP088.1i ;06
 BQ.4: u93.15o, v24.13i, v09.5o ;07

 BQ.5: #TP089.1i ;06
 BQ.5: u93.14o, v24.14i, v09.15o ;07

 BQ.6: #TP090.1i ;06
 BQ.6: u93.13o, v24.17i, v09.4o ;07

 BQ.7: #TP091.1i ;06
 BQ.7: u93.12o, v24.18i, v09.16o ;07

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	24 OF	

BQ.8: #TP098.1i ;06
 BQ.8: u91.15o, v07.3i, v08.7o ;07

 BQ.9: #TP099.1i ;06
 BQ.9: u91.14o, v07.4i, v08.13o ;07

 BRC1k: E115, u97.15i, u97.9i ;14

 BufA8: #TP139.1i, v02.9o, v03.10i ;12

 BufB8: #TP140.1i, u86.9o, u87.10i ;12

 BufIR': u52.14i ;05
 BufIR': u16.12i ;06
 BufIR': u75.10o ;07

 BufIR: u62.13i ;02
 BufIR: #TP114.1i, u95.12o, u75.11i ;07

 BufIRA': u52.15o, u80.9i ;05

 BufOR: u62.4i ;02
 BufOR: u52.13i ;06
 BufOR: u33.2i ;06
 BufOR: u95.14o, #TP113.1i ;07

 BufORA: u65.7i ;03
 BufORA: #TP072.1i, u52.12o, u74.2i ;06
 BufORA: u66.4i ;07

 BufORAA: u65.6o, u64.17i ;03
 BufORAA: #TP022.1i ;03

 BufX.0: v22.3o ;01
 BufX.0: v05.3i ;07
 BufX.0: v03.6i ;12
 BufX.0: u87.6i ;12

 BufX.10: v21.7o ;01
 BufX.10: v06.7i ;07
 BufX.10: v02.4i ;12
 BufX.10: u86.4i ;12

 BufX.11: v21.9o ;01
 BufX.11: v06.8i ;07
 BufX.11: v02.3i ;12
 BufX.11: u86.3i ;12

 BufX.12: v21.18o ;01
 BufX.12: v26.4i ;02
 BufX.12: v25.4i ;02
 BufX.12: v06.13i ;07
 BufX.12: u98.12i ;11
 BufX.12: v02.14i ;12
 BufX.12: u86.14i ;12

 BufX.13: v21.16o ;01
 BufX.13: v26.5i ;02
 BufX.13: v25.5i ;02

BufX.13: v06.14i ;07
 BufX.13: v20.5i ;11
 BufX.13: v02.13i ;12
 BufX.13: u86.13i ;12

 BufX.14: v21.14o ;01
 BufX.14: v26.12i ;02
 BufX.14: v25.12i ;02
 BufX.14: v06.17i ;07
 BufX.14: v20.12i ;11
 BufX.14: v02.12i ;12
 BufX.14: u86.12i ;12

 BufX.15: v21.12o ;01
 BufX.15: v26.13i ;02
 BufX.15: v25.13i ;02
 BufX.15: v06.18i ;07
 BufX.15: v20.13i ;11
 BufX.15: v02.11i ;12
 BufX.15: u86.11i ;12

 BufX.1: v22.5o ;01
 BufX.1: v05.4i ;07
 BufX.1: v03.5i ;12
 BufX.1: u87.5i ;12

 BufX.2: v22.7o ;01
 BufX.2: v05.7i ;07
 BufX.2: v03.4i ;12
 BufX.2: u87.4i ;12

 BufX.3: v22.9o ;01
 BufX.3: v05.8i ;07
 BufX.3: v03.3i ;12
 BufX.3: u87.3i ;12

 BufX.4: v22.18o ;01
 BufX.4: v05.13i ;07
 BufX.4: v03.14i ;12
 BufX.4: u87.14i ;12

 BufX.5: v22.16o ;01
 BufX.5: v05.14i ;07
 BufX.5: v03.13i ;12
 BufX.5: u87.13i ;12

 BufX.6: v22.14o ;01
 BufX.6: v05.17i ;07
 BufX.6: v03.12i ;12
 BufX.6: u87.12i ;12

 BufX.7: v22.12o ;01
 BufX.7: v05.18i ;07
 BufX.7: v03.11i ;12
 BufX.7: u87.11i ;12

 BufX.8: v21.3o ;01
 BufX.8: v06.3i ;07
 BufX.8: v02.6i ;12

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 25 OF		

BufX.8: u86.6i ;12
 BufX.9: v21.5o ;01
 BufX.9: v06.4i ;07
 BufX.9: v02.5i ;12
 BufX.9: u86.5i ;12
 C1: u31.5o, u42.9i ;08
 C1: #TP115.1i ;08
 C2: u43.5o, u42.10i ;08
 C2: #TP116.1i ;08
 C3: u42.12i, u43.6o, u42.13i ;08
 C3: #TP117.1i ;08
 C4': u42.8o, u31.4i ;08
 C5': u23.8o, u31.10i ;08
 C5': #TP118.1i ;08
 C6': u32.8o, u31.9i ;08
 C6': #TP119.1i ;08
 CallReq': v14.6o ;16
 CallReq': u55.14i ;16
 Car': u26.15o ;09
 Car': u28.15o ;10
 Car': #C32.1o, u24.15i ;10
 Car: u26.11o ;09
 Car: u28.14o, u27.6i ;10
 Carrier': u78.13i, u78.4i ;07
 Carrier': u46.4o ;10
 Carrier: u82.5i ;03
 Carrier: u77.12i ;03
 Carrier: #TP128.1i, u27.5o, u46.3i ;10
 CComp: u17.15o, #R31.1o ;09
 CComp: u26.2i ;09
 CD': u99.6o, v17.13i ;15
 cf: #R21.2i, #C11.1o, #R16.2i ;09
 CL+: #R31.2i, #R33.2i, #C13.1o ;09
 CL+: #R26.1o, u18.13i
 CL-: #R27.1o, #R23.2i, #C6.1o ;09
 CL-: #R17.1o, u18.12i
 ClrBuf': u82.8o, #TP070.1i ;06
 ClrBuf': u92.18i ;07
 ClrBuf': u91.18i ;07
 ClrBuf': u93.18i ;07
 ClrBuf': u94.18i ;07
 ClrBuf': u79.18i ;07
 ClrBuffer: v20.11o ;11
 ClrBuffer: u73.2i ;12
 ClrData': u58.4i, u73.1o ;12
 ClrData': #TP142.1i
 ClrDeferCnt: u45.12i, u45.2i ;05
 ClrDeferCnt: u46.6o, u44.2i
 ClrDPReq': E185, v18.17i ;11
 ClrDPReq0': v18.16o ;11
 ClrDPReq0': u72.13i ;11
 ClrRefReq': E186, v18.18i ;11
 ClrRefReq0': v18.19o ;11
 ClrRefReq0': v01.13i ;11
 Coll+: u18.15o, u27.1i ;09
 Coll+: u25.4i ;09
 Coll+: u26.3i ;09
 Coll-: #R39.2i, #C25.1i, u18.14o ;09
 Coll-: u17.12i
 Coll: u27.3o, #TP126.1i ;09
 Coll: u16.10i ;09
 Collision+: #R26.2i, #R21.1o ;09
 Collision+: #R12.2i, C69, #C13.2i
 Collision-: #R17.2i, #R16.1o ;09
 Collision-: #R12.1i, C70, #C6.2i
 colt': u68.16o ;04
 colt': u68.8i ;04
 Command: u06.7i ;13
 Command: u96.19o ;14
 CommandECL+: #R5.1i, u06.1o, C64 ;13
 CommandECL-: #R6.1i, u06.3o, C65 ;13
 COS': v16.15i ;16
 COS': u02.8o ;17
 COS: C243, u02.10i ;17
 crc.0': u35.2o ;08
 crc.0': u39.4i ;08
 crc.0': u31.1i ;08
 crc.0x': u39.6o, u35.4i ;08
 crc.1': u35.5o ;08

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT		SHEET 26	OF	

```

crc.1': u39.9i ;08
crc.1': u31.12i ;08

crc.10': u34.6o ;08
crc.10': u15.1i ;08
crc.10': u43.3i ;08

crc.10x': u15.3o, u34.8i ;08

crc.11': u34.9o ;08
crc.11': u15.4i ;08
crc.11': u43.2i ;08

crc.11x': u15.6o, u34.13i ;08

crc.12': u34.12o ;08
crc.12': u34.14i ;08
crc.12': u43.13i ;08

crc.13': u34.15o ;08
crc.13': u34.17i ;08
crc.13': u23.4i ;08

crc.14': u34.16o ;08
crc.14': u34.18i ;08
crc.14': u43.4i ;08

crc.15': u34.19o ;08
crc.15': u15.9i ;08
crc.15': u43.10i ;08

crc.15x': u15.8o, u22.3i ;08

crc.16': u22.2o ;08
crc.16': u22.4i ;08
crc.16': u23.5i ;08

crc.17': u22.5o ;08
crc.17': u22.7i ;08
crc.17': u23.6i ;08

crc.18': u22.6o ;08
crc.18': u22.8i ;08
crc.18': u43.9i ;08

crc.19': u22.9o ;08
crc.19': u22.13i ;08
crc.19': u23.11i ;08

crc.1x': u39.8o, u35.7i ;08

crc.2': u35.6o ;08
crc.2': u35.8i ;08
crc.2': u23.1i ;08

crc.20': u22.12o ;08
crc.20': u22.14i ;08
crc.20': u23.12i ;08

crc.21': u22.15o ;08
crc.21': u15.12i ;08
crc.21': u32.1i ;08

crc.21x': u15.11o, u22.17i ;08

crc.22': u22.16o ;08
crc.22': u14.1i ;08
crc.22': u32.2i ;08

crc.22x': u14.3o, u22.18i ;08

crc.23': u22.19o ;08
crc.23': u21.3i ;08
crc.23': u32.3i ;08

crc.24': u21.2o ;08
crc.24': u21.4i ;08
crc.24': u43.8i ;08

crc.25': u21.5o ;08
crc.25': u14.4i ;08
crc.25': u43.11i ;08

crc.25x': u14.6o, u21.7i ;08

crc.26': u21.6o ;08
crc.26': u21.8i ;08
crc.26': u31.8i ;08
crc.26': #TP164.1i ;08

crc.27': u21.9o ;08
crc.27': u21.13i ;08
crc.27': u32.4i ;08

crc.28': u21.12o ;08
crc.28': u21.14i ;08
crc.28': u32.5i ;08

crc.29': u21.15o ;08
crc.29': u21.17i ;08
crc.29': u32.6i ;08

crc.3': u35.9o ;08
crc.3': u39.12i ;08
crc.3': u31.3i ;08

crc.30': u21.16o ;08
crc.30': u21.18i ;08
crc.30': u31.11i ;08
crc.30': #TP165.1i ;08

crc.31': u21.19o, u38.9i ;08
crc.31': u14.9i, u14.12i ;08
crc.31': u37.11i ;08
crc.31': u37.3i, u37.5i ;08

crc.31: u32.12i, u32.11i, u38.8o ;08

```

crc.32: u38.6o, u35.3i ;08
 crc.32a': #TP122.1i, u14.8o ;08
 crc.32a': u39.5i, u39.10i, u39.13i ;08
 crc.32a': u36.2i, u36.5i, u36.10i
 crc.32a': u38.5i

 crc.32b': #TP123.1i, u14.11o ;08
 crc.32b': u15.2i, u15.5i, u15.10i ;08
 crc.32b': u15.13i, u14.2i, u14.5i
 crc.32b': u36.13i

 crc.3x': u39.11o, u35.13i ;08

 crc.4': u35.12o ;08
 crc.4': u36.1i ;08
 crc.4': u31.2i ;08

 crc.4x': u36.3o, u35.14i ;08

 crc.5': u35.15o ;08
 crc.5': u35.17i ;08
 crc.5': u31.13i ;08

 crc.6': u35.16o ;08
 crc.6': u36.4i ;08
 crc.6': u43.1i ;08

 crc.6x': u36.6o, u35.18i ;08

 crc.7': u35.19o ;08
 crc.7': u36.9i ;08
 crc.7': u23.2i ;08

 crc.7x': u36.8o, u34.3i ;08

 crc.8': u34.2o ;08
 crc.8': u34.4i ;08
 crc.8': u43.12i ;08

 crc.9': u34.5o ;08
 crc.9': u36.12i ;08
 crc.9': u23.3i ;08

 crc.9x': u36.11o, u34.7i ;08

 CReq': u55.12o ;16
 CReq': u13.4i, u13.5i ;17

 CRQ: u13.6o, C234 ;17

 Cx+: u07.11i ;10
 Cx+: #C8.1i, #C16.1i ;10

 Cx-: u07.14i ;10
 Cx-: #C8.2i, #C16.2i ;10

 Cycle2': u84.13i ;03
 Cycle2': E3, v27.9i ;06

DataSetRdy': v16.4i ;16
 DataSetRdy': u01.11o ;17

 DataSetRdy: C206, u01.13i ;17

 DataTermRdy': u71.25o ;15
 DataTermRdy': u55.11i ;16

 DataValid: u42.5i ;03
 DataValid: u20.1i ;03
 DataValid: u24.13o, #TP167.1i ;10

 DC=128: #TP062.1i, u45.8o, u44.1i ;05
 DC=512: #TP063.1i, u44.4o, u95.2i ;05
 DC=8: #TP061.1i, u45.6o, u45.13i ;05
 DCar: #R40.1i, #C28.2i, u25.6o ;09
 DCar: u25.3o, u24.1i ;09

 DCERxC1k': u83.5i ;16
 DCERxC1k': u03.3o ;17

 DCERxC1k: C217, u03.1i ;17

 DCETxC1k': u83.2i ;16
 DCETxC1k': u03.6o ;17

 DCETxC1k: C215, u03.4i ;17

 Defer': v25.6o ;02
 Defer': u62.12i ;02
 Defer': u80.10i ;05

 Defer: v25.7o, #TP012.1i ;02
 Defer: u62.3i ;02

 DeferCar': u80.11i ;05
 DeferCar': u24.3o, #TP166.1i ;09

 DI': u09.15o ;09
 DI': u09.7i ;10
 DI': u08.7i ;10
 DI': u30.1i ;10

 DI: u09.9o ;09
 DI: u08.10i ;10
 DI: u09.10i ;10
 DI: u30.2i ;10

 DiagLineSync: v23.2i ;02
 DiagLineSync: u99.8o ;13

 DiagVideoData: v23.6i ;02
 DiagVideoData: u05.13o ;13

 DigitPrSnt': v14.9o ;16

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	28 OF	

DigitPrSnt': u13.9i, u13.10i ;17
 DIL': u29.10i, u08.14o, u29.12i ;10
 DIL': u30.7i ;10
 DIL: u29.7i, u08.15o, u29.5i ;10
 DIL: u30.5i ;10
 DLionRead: v10.13i, v27.10o ;06
 DLionRead: v10.1i
 DLionWrite: v27.13o, v11.2i ;06
 DLO': v16.8i ;16
 DLO': u02.3o ;17
 DLO: C242, u02.1i ;17
 DmaAckA': u99.3i ;15
 DmaAckA': E161, u99.2i ;15
 DmaAckA': v00.4i ;15
 DmaAckB': v00.13i ;15
 DmaAckB': u99.5i ;15
 DmaAckB': E162, u99.1i ;15
 DmaReqA': #R57.2i, u71.10o ;15
 DmaReqA': v17.1i ;15
 DmaReqA: v17.2o, E61 ;15
 DmaReqB': #R58.2i, u71.30o ;15
 DmaReqB': v17.3i ;15
 DmaReqB: v17.4o, E62 ;15
 DPR: u13.8o, C232 ;17
 dr': u29.11i, u09.3o, u29.6i ;10
 dr': u30.6i ;10
 dr: u09.14o, u29.13i, u29.4i ;10
 dr: u30.4i ;10
 DTReady': u55.9o ;16
 DTReady': u13.2i ;17
 DTReady: u13.3o, C220 ;17
 EICtl←': E123, v19.9i ;02
 EICtl←: v19.8o, u74.9i ;02
 EnableRcv: v26.15o, #TP011.1i ;02
 EnableRcv: v23.15i ;02
 EnableRcv: u63.1i ;03
 EnableTrn: v25.15o, #TP014.1i ;02
 EnableTrn: v23.11i ;02

EnableTrn: u51.1i ;04
 EndLine': E137, v18.8i ;11
 EndPLine': v01.1i, u98.11o ;11
 EndPLine': #TP137.1i
 EnWake': v20.14o ;11
 EnWake': u73.6i ;11
 EnWake: v20.15o ;11
 EnWake: u85.1i ;11
 EnWake: v01.12i ;11
 EOctl←': E126, v19.11i ;02
 EOctl←: v19.10o, u74.12i ;02
 EOData←': E23, v19.13i ;06
 EOData←: v19.12o, u74.5i ;06
 EPurge': u84.11o, u64.4i ;03
 EReq': u52.9o, E86 ;02
 EStrobe': u84.12i ;03
 EStrobe': E35, v27.11i ;06
 EvenLen: #TP097.1i ;06
 EvenLen: u78.7o, u79.6i ;07
 FastRBit': u37.12i, u37.13i ;08
 FastRBit': #TP127.1i, u27.13o ;10
 FastRBit': u53.15i
 ForceReq: v20.7o ;11
 ForceReq: u85.13i ;11
 FRBit': u53.5o, u46.1i ;10
 GdAlign': u77.11o ;03
 GdAlign': u81.6i ;06
 GdAlign: u77.10o ;03
 GdAlign: u79.5i ;07
 GdCRC: u78.12i ;07
 GdCRC: u31.6o ;08
 GND: v27.6i ;01
 GND: v22.1i ;01
 GND: v21.1i ;01
 GND: u53.8i, u53.6i, u53.4i ;01
 GND: u53.2i
 GND: u95.19i ;01
 GND: #R62.2i ;01
 GND: E39 ;02
 GND: u88.19i ;03

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 29 OF		

GND: u88.2i ;03
 GND: u64.8i ;03
 GND: u49.15i ;04
 GND: u50.15i ;04
 GND: u67.14i ;04
 GND: u49.14i ;04
 GND: u50.14i ;04
 GND: u41.5i ;05
 GND: u41.3i ;05
 GND: u54.5i ;05
 GND: u54.4i ;05
 GND: u47.3i ;05
 GND: v10.2i, v10.3i ;06
 GND: v08.11i ;07
 GND: u37.1i ;08
 GND: u37.15i ;08
 GND: u26.16o ;09
 GND: u17.5i ;09
 GND: #C36.2i ;09
 GND: #C12.2i ;09
 GND: #C11.2i ;09
 GND: #R14.2i ;09
 GND: #R13.2i ;09
 GND: #R39.1i, #R38.2i ;09
 GND: C89, C83 ;09
 GND: #R40.2i ;09
 GND: u30.16o ;10
 GND: u07.8i ;10
 GND: u09.8i, u17.8i, u25.8i ;10
 GND: u18.8i ;10
 GND: u29.8i, u19.8i, u08.8i ;10
 GND: u28.8i ;10
 GND: #C42.1o ;10
 GND: #C2.2i ;10
 GND: #C9.2i ;10
 GND: #C24.2i ;10
 GND: #C44.2i ;10
 GND: #C26.2i ;10
 GND: #R48.1o ;10
 GND: u59.3i ;11
 GND: v18.1i ;11
 GND: u86.10i ;12
 GND: v02.10i ;12
 GND: C87 ;13
 GND: #C4.2i ;13
 GND: #C3.2i ;13
 GND: #C19.2i ;13
 GND: u96.17i ;14
 GND: u96.4i ;14
 GND: u71.31i ;15
 GND: u57.2i ;16
 GND: u83.15i ;16
 GND: u55.15i, u55.6i, u55.3i ;16
 GND: u56.15i ;16
 GND: u69.15i ;16
 GND: C207 ;17
 GND: C237 ;17
 GND: u11.7i ;17
 GND: u13.7i ;17

GND: u12.7i ;17
 GND: #C81.2i ;19
 GND: #C82.2i ;19
 GND: #C80.1i ;19
 GND: #C38.1i ;19
 GND: #C1.2i ;19
 GND: #C51.2i ;19

GoodAlign: #TP087.1i ;06
 GoodAlign: u78.15o, u79.8i ;07

GoodCRC: #TP162.1i ;06
 GoodCRC: u78.10o, u79.7i ;07

High: v22.19i ;01
 High: v21.19i ;01
 High: u59.10i, u59.7i, u59.1i ;11
 High: u72.4i ;11
 High: u58.10i ;11
 High: v01.10i ;11
 High: v01.4i, v01.2i ;11
 High: u72.10i, u72.12i ;11
 High: u59.5i, u59.6i, u59.4i ;11
 High: u58.1i ;12
 High: #R63.2i ;13

HighS: #R11.1o, u06.6i ;13

IFilt: #C9.1i, u07.13i ;10

IgnoreOR: u64.3i ;03
 IgnoreOR: u66.11o, #TP161.1i ;03

IgnorePkt: u33.6i ;03
 IgnorePkt: u20.4o ;03

IgnPkt': u20.2i, u77.14o, u20.3i ;03

IgP: u81.13o, u77.13i ;03

InAttn': u61.11i ;02
 InAttn': u66.8o ;03

InEOP: #TP107.1i ;06
 InEOP: u73.13o, u66.5i ;07

IOPAddr.14: E77, u97.20i ;14
 IOPAddr.14: u99.4i ;15

IOPAddr.15: E177, u97.19i ;14
 IOPAddr.15: u96.12i ;14
 IOPAddr.15: v00.5i ;15

IOPClk: E12, u96.20i ;14
 IOPClk: v00.1i, v00.2i ;15

IOPClkD: v00.3o, u98.1i, u98.2i ;15

IOPClkDD: u70.1i, u70.2i, u98.3o ;15

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	30 OF	

IOPData.0: v12.2i ;14
 IOPData.0: E92, v13.2i ;14
 IOPData.0: v15.2i ;15
 IOPData.0: v14.3i ;16
 IOPData.0: v16.18o ;16

 IOPData.1: v12.3i ;14
 IOPData.1: E192, v13.3i ;14
 IOPData.1: v15.3i ;15
 IOPData.1: v14.4i ;16
 IOPData.1: v16.16o ;16

 IOPData.2: v12.4i ;14
 IOPData.2: E93, v13.4i ;14
 IOPData.2: v15.4i ;15
 IOPData.2: v14.7i ;16
 IOPData.2: v16.14o ;16

 IOPData.3: v12.5i ;14
 IOPData.3: E193, v13.5i ;14
 IOPData.3: v15.5i ;15
 IOPData.3: v14.8i ;16
 IOPData.3: v16.12o ;16

 IOPData.4: v12.6i ;14
 IOPData.4: E94, v13.6i ;14
 IOPData.4: v15.6i ;15
 IOPData.4: v14.13i ;16
 IOPData.4: v16.3o ;16

 IOPData.5: v12.7i ;14
 IOPData.5: E194, v13.7i ;14
 IOPData.5: v15.7i ;15
 IOPData.5: v14.14i ;16
 IOPData.5: v16.5o ;16

 IOPData.6: v12.8i ;14
 IOPData.6: E95, v13.8i ;14
 IOPData.6: v15.8i ;15
 IOPData.6: v14.17i ;16
 IOPData.6: v16.7o ;16

 IOPData.7: v12.9i ;14
 IOPData.7: E195, v13.9i ;14
 IOPData.7: v15.9i ;15
 IOPData.7: v14.18i ;16
 IOPData.7: v16.9o ;16

 IOPDataOut: v12.1i ;14
 IOPDataOut: E15, v13.1i ;14
 IOPDataOut: v15.1i ;15

 IOPI/ORD': E179, u98.4i ;14
 IOPI/ORD': u96.13i ;14
 IOPI/ORD': v00.9i ;15
 IOPI/ORD': u71.32i ;15
 IOPI/ORD': u84.4i ;16

IOPI/OWr': E187, u98.9i ;14
 IOPI/OWr': u96.10i ;14
 IOPI/OWr': v00.10i ;15
 IOPI/OWr': u84.1i ;16

 IOPIntReq0: v17.6o, E64 ;15

 IOPIntReq1: u96.14o, E164 ;14

 IOPIntReq2: u96.15o, E65 ;14

 IOPReset': v26.1i ;02
 IOPReset': v25.1i, E117 ;02
 IOPReset': v20.1i ;11
 IOPReset': v19.5i ;14
 IOPReset': u71.21i ;15
 IOPReset': v14.1i ;16
 IOPReset': u57.1i ;16

 IOPReset: v19.6o, u96.21i ;14

 IOPSel.0: E66, u96.11i ;14
 IOPSel.0: v12.19i ;14

 IOPSel.1: u98.5i, u98.10i, E166 ;14
 IOPSel.1: u97.21i
 IOPSel.1: v13.19i ;14

 IOPSel.3': E167, u84.2i ;16
 IOPSel.3': u84.5i ;16

 IOPSel2': E163, E67, u99.13i ;15

 IORQ: v00.8o, v17.9i ;15

 IPGD: #TP065.1i, u41.15o, u95.4i ;05

 IPGDone: u68.3i ;04
 IPGDone: #TP033.1i ;04
 IPGDone: u95.16o, #TP066.1i ;05

 IPGDoneT: u68.2o, u67.2i ;04

 IPGL: u41.7i, u54.15o, u41.10i ;05

 LastRWrD': u65.4i ;03
 LastRWrD': u66.6o ;07

 LastRWrDA': u65.5o, u64.16i ;03
 LastRWrDA': #TP021.1i ;03
 LastRWrDA': v10.12i ;06

 LastTWrd: u68.4i ;04
 LastTWrd: #TP082.1i ;06
 LastTWrd: u79.15o ;07

 LastTWrdT: u68.5o, u67.3i ;04
 LastTWrdT: #TP034.1i ;04

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 31	OF	

LastWord': v25.11o ;02
 LastWord': u62.11i ;02

 LastWord: v23.13i ;02
 LastWord: v25.10o, #TP013.1i ;02
 LastWord: #TP077.1i ;06
 LastWord: u79.4i ;07

 LdEICt1': u74.8o ;02
 LdEICt1': v26.9i ;02

 LdEIData': u74.3o ;06
 LdEIData': u75.5i ;06
 LdEIData': v24.11i ;07
 LdEIData': v07.11i ;07

 LdEIData: u75.6o, v10.5i ;06

 LdEOct1': v25.9i ;02
 LdEOct1': u74.11o ;02
 LdEOct1': u46.5i ;05

 LdEOData': u74.6o ;06
 LdEOData': v05.11i ;07
 LdEOData': v06.11i ;07

 LdRHold.H': u80.12o ;06
 LdRHold.H': u89.11i ;07

 LdRHold.L': u80.6o ;06
 LdRHold.L': u90.11i ;07
 LdRHold.L': u78.9i ;07

 LineActive': v01.6o ;11
 LineActive': u73.3i ;12
 LineActive': u99.9i ;13

 LineActive: v01.5o ;11
 LineActive: u85.9i ;11
 LineActive: u59.9i, u58.13i ;11

 LineSync: u72.1i ;11
 LineSync: v01.3i ;11
 LineSync: u99.11i, u99.10i ;13
 LineSync: u05.5o ;13

 LineSyncECL+: #R3.1i, C57, u05.7i ;13

 LineSyncECL-: #R4.1i, C58, u05.6i ;13

 LoadA: u72.5o, #TP133.1i ;11
 LoadA: u60.13i ;11
 LoadA: v02.15i, v03.15i ;12
 LoadA: u85.4i ;12

 LoadB: u72.6o ;11
 LoadB: u72.2i ;11
 LoadB: u60.5i ;11
 LoadB: u85.3i ;12

LoadB: u86.15i, u87.15i ;12

 LocalLoop': v26.6o ;02
 LocalLoop': #R46.2i ;09

 LocalLoop: v23.17i ;02
 LocalLoop: v26.7o, #TP009.1i ;02

 Loop: u26.10o, u26.9o ;09
 Loop: u19.13i, #C15.2i, #R24.1o ;09
 Loop: #R35.1o, #R23.1o, #R33.1o
 Loop: u19.3o, u19.11i

 LoopBack': v26.3o ;02
 LoopBack': u16.5i ;04

 LoopBack: v26.2o, #TP008.1i ;02
 LoopBack: v23.8i ;02
 LoopBack: u67.15i ;04
 LoopBack: u81.8i ;06

 LoopL': #R47.2i, u19.7i, #R46.1o ;09
 LoopL': u19.4i

 LSEPByte.0: u96.8o ;14
 LSEPByte.0: v12.18o ;14

 LSEPByte.1: u96.7o ;14
 LSEPByte.1: v12.17o ;14

 LSEPByte.2: u96.6o ;14
 LSEPByte.2: v12.16o ;14

 LSEPByte.3: u96.5o ;14
 LSEPByte.3: v12.15o ;14

 LSEPByte.4: u96.2o ;14
 LSEPByte.4: v12.14o ;14

 LSEPByte.5: u96.1o ;14
 LSEPByte.5: v12.13o ;14

 LSEPByte.6: u96.28o ;14
 LSEPByte.6: v12.12o ;14

 LSEPByte.7: u96.27o ;14
 LSEPByte.7: v12.11o ;14

 LSEPct1Clk: u96.25i, u96.9i ;14
 LSEPct1Clk: u97.10o ;14

 LSTm: #R3.2i, #C3.1o, #R4.2i ;13

 Mux: u19.2o, u09.12i, u19.14o ;09
 Mux: u26.13o ;09

 NB1': u12.11o, C244 ;17

 NB1: v14.19o ;16

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 32 OF	

NB1: u12.12i, u12.13i ;17
 NB2': u12.8o, C245 ;17
 NB2: v14.16o ;16
 NB2: u12.9i, u12.10i ;17
 NB4': u12.6o, C246 ;17
 NB4: v14.15o ;16
 NB4: u12.4i, u12.5i ;17
 NB8': u12.3o, C247 ;17
 NB8: v14.12o ;16
 NB8: u12.2i ;17
 NeedRefresh': v01.8o ;11
 NeedRefresh': v18.7i ;11
 NeedWord: #TP134.1i, u73.4o ;11
 NeedWord: u85.10i ;11
 o: u29.3o, u29.15o, u28.4i ;10
 o: u30.14o ;10
 o: u30.11o ;10
 o: #CR1.1o, #C10.1o, #R25.2i ;10
 outa: u50.12o, u51.8i ;04
 outa: #TP055.1i ;04
 OutAttn': u46.12o, u61.10i ;02
 OutAttn: u46.13i ;02
 OutAttn: u51.9o, #TP047.1i ;04
 ova': u65.19o ;03
 ova': u65.13i ;03
 OverRun': u16.11o, u20.5i ;06
 OverRunAA': u65.12o, u64.2i ;03
 OverRunAA': #TP024.1i ;03
 p: u29.14o, u29.2o, u28.5i ;10
 p: u30.15o ;10
 p: #R32.2i, #R48.2i ;10
 PAD.VDD: #C17.1i ;20
 PAD.VEE: #C20.1i ;20
 PAD.ZGND: #C65.2o, #C64.2o ;20
 PAD.ZGND: #C63.2o, #C62.2o
 PAD.ZGND: #C61.2o, #C60.2o
 PAD.ZGND: #C59.2o, #C58.2o
 PAD.ZGND: #C57.2o, #C56.2o
 PAD.ZGND: #C55.2o, #C54.2o
 PAD.ZGND: #C53.2o, #C52.2o
 PAD.ZGND: #C50.2o, #C49.2o
 PAD.ZGND: #C48.2o, #C47.2o
 PAD.ZGND: #C46.2o, #C45.2o
 PAD.ZGND: #C43.2o, #C41.2o
 PAD.ZGND: #C40.2o, #C39.2o
 PAD.ZGND: #C35.2o, #C34.2o
 PAD.ZGND: #C33.2o, #C31.2o
 PAD.ZGND: #C27.2o, #C22.2o
 PAD.ZGND: #C21.2o, #C18.2o, #C5.2o
 PAD.ZGND: #C20.2o, #C17.2o
 PAD.ZGND: #C79.2o, #C78.2o
 PAD.ZGND: #C77.2o, #C76.2o
 PAD.ZGND: #C75.2o, #C74.2o
 PAD.ZGND: #C73.2o, #C72.2o
 PAD.ZGND: #C71.2o, #C70.2o
 PAD.ZGND: #C69.2o, #C68.2o
 PAD.ZGND: #C67.2o, #C66.2o
 PAD.ZP5V: #C52.1i, #C53.1i ;20
 PAD.ZP5V: #C54.1i, #C55.1i
 PAD.ZP5V: #C56.1i, #C57.1i
 PAD.ZP5V: #C58.1i, #C59.1i
 PAD.ZP5V: #C60.1i, #C61.1i
 PAD.ZP5V: #C62.1i, #C63.1i
 PAD.ZP5V: #C64.1i, #C65.1i
 PAD.ZP5V: #C31.1i, #C33.1i
 PAD.ZP5V: #C34.1i, #C35.1i
 PAD.ZP5V: #C39.1i, #C40.1i
 PAD.ZP5V: #C41.1i, #C43.1i
 PAD.ZP5V: #C45.1i, #C46.1i
 PAD.ZP5V: #C47.1i, #C48.1i
 PAD.ZP5V: #C49.1i, #C50.1i
 PAD.ZP5V: #C66.1i, #C67.1i
 PAD.ZP5V: #C68.1i, #C69.1i
 PAD.ZP5V: #C70.1i, #C71.1i
 PAD.ZP5V: #C72.1i, #C73.1i
 PAD.ZP5V: #C74.1i, #C75.1i
 PAD.ZP5V: #C76.1i, #C77.1i
 PAD.ZP5V: #C78.1i, #C79.1i
 PAD.ZP5V: #C5.1i, #C18.1i, #C21.1i ;20
 PAD.ZP5V: #C22.1i, #C27.1i
 pAlwaysClk: v27.4o, u75.13i ;01
 pAlwaysClk: u74.1i ;06
 pAlwaysClk: u74.4i ;06
 pAlwaysClk: v10.11i ;06
 pAlwaysClk: v11.3i ;06
 pBIPG: u80.8o, u20.15i ;05
 Pctl←': E128, v19.3i ;11
 Pctl←: u60.9i, v19.4o, u60.11i ;11
 PDT-32: u53.12o, #TP004.1i ;01
 PDT-32: u27.12i ;10
 PDT-32: u24.12i ;10
 PDT-B: u53.14o, #TP003.1i ;01

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT		SHEET 33 OF		

PDT-B: u94.9i ;07
 PDT-B: u93.9i ;07
 PDT-B: u91.9i ;07
 PDT-B: u92.9i ;07
 PDT-B: u79.9i ;07

 PDT-P: u53.16o, #TP002.1i ;01
 PDT-P: u64.10i ;03
 PDT-P: u67.13i ;04
 PDT-P: u49.13i ;04
 PDT-P: u50.13i ;04

 PDT-R: u53.18o, #TP001.1i ;01
 PDT-R: u88.3i ;03
 PDT-R: u65.1i ;03
 PDT-R: u68.1i ;04
 PDT-R: u48.1i ;18
 PDT-R: u52.1i ;18

 pE': u52.6o, u52.8i ;02

 PhE: u39.3o, u16.2i ;09

 PhG': u16.3o, u40.2i ;09

 pia: u65.8i ;03
 pia: u65.15o ;03

 pimip: u33.4o, u42.1i ;03

 pInEOP': u79.14o, u73.11i ;07

 PktIn': u82.6o, u77.4i ;03

 PktInMode': u66.2i ;03
 PktInMode': u77.2o ;03

 PktInMode: u33.5i ;03
 PktInMode: u82.2i ;03
 PktInMode: #TP019.1i, u77.3o ;03
 PktInMode: u76.1i ;03
 PktInMode: u65.14i ;03

 PktInModeAA: u65.9o, u64.1i ;03
 PktInModeAA: #TP023.1i ;03

 pktom: u50.10o, u51.4i ;04
 pktom: #TP053.1i ;04

 PktOutMnLp': u16.6o ;04
 PktOutMnLp': u75.3i ;04
 PktOutMnLp': u37.2i ;08
 PktOutMnLp': v11.1i ;08

 PktOutMnLp: u65.17i ;03
 PktOutMnLp: u75.4o ;04
 PktOutMnLp: u33.11i, v11.10i ;08

 PktOutMnLpAA: u65.2o, u64.15i ;03

PktOutMnLpAA: #TP020.1i ;03

 PktOutMode: u67.1i ;04
 PktOutMode: u16.4i, u51.2o ;04
 PktOutMode: #TP044.1i
 PktOutMode: u47.9i ;05

 pLRWrd: #TP112.1i ;06

 PND': v16.17i ;16
 PND': u02.6o ;17

 PND: C235, u02.4i ;17

 poa: u65.3i ;03
 poa: u65.16o ;03

 POData←': E28, v19.1i ;11

 POData←: u60.4i, v19.2o, u60.2i ;11

 ppCLK: v27.5i, E9, v27.3i ;01

 ppE': #TP015.1i, u62.8o, u52.7i ;02

 ppStIPG: u20.13o, u48.13i ;05

 pRC16: u81.1o, u77.5i ;03

 pRClock: v11.5i ;06
 pRClock: u80.5i ;06
 pRClock: u80.13i ;06
 pRClock: v11.13i ;08
 pRClock: #TP129.1i, u38.13i ;10
 pRClock: u27.11o, u38.11i

 PrCTS': u71.23i ;15
 PrCTS': u56.9o, #TP150.1i ;16

 PrDCD': u71.19i ;15
 PrDCD': u55.4o, #TP155.1i ;16

 PreamDet': #TP017.1i, u42.6o ;03
 PreamDet': u66.1i
 PreamDet': u20.6i ;06
 PreamDet': u78.1i ;07

 PreamPkt: u66.3o ;03
 PreamPkt: u82.4i ;03
 PreamPkt: u33.12i ;08

 PrimCTS': u56.11i ;16
 PrimCTS': u04.6o ;17

 PrimCTS: C205, u04.4i ;17

 PrimLineDet': u55.2i ;16
 PrimLineDet': u04.8o ;17

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 34 OF	

PrimLineDet: C208, u04.10i ;17
 PrimRData': C203, u04.1i ;17
 PrimRData: u56.5i ;16
 PrimRData: u04.3o ;17
 PrimRTS': u56.12o ;16
 PrimRTS': u11.4i, u11.5i ;17
 PrimRTS: u11.6o, C204 ;17
 PrimTData': u11.3o, C202 ;17
 PrimTData: u56.4o ;16
 PrimTData: u11.2i ;17
 PrinterReq': v18.5o, E135 ;11
 PrinterReq0': #TP138.1i, u85.8o ;11
 PrinterReq0': v18.3i
 PrinterReq1': v18.4i ;11
 PrinterReq1': v18.2o ;11
 PrRTS': u71.24o ;15
 PrRTS': u56.10i, u56.14i ;16
 PrRXD: u71.12i ;15
 PrRXD: u56.7o, #TP149.1i ;16
 PrTXD: u71.26o ;15
 PrTXD: u56.6i, u56.2i ;16
 pStIPG: u48.12o, u48.14i ;05
 pTClOCK': u40.8o ;09
 pTClOCK': u40.12i ;09
 pTClOCK: v10.10i ;06
 pTClOCK: v11.9i ;08
 pTClOCK: u40.9o, u38.1i ;09
 pTClOCK: u39.2i ;09
 PU-a: #R60.1o, #TP006.1i ;01
 PU-a: u76.9i, u76.10i, u76.7i ;03
 PU-a: u77.1i ;18
 PU-b: #R53.1o, #TP007.1i ;01
 PU-b: u41.6i ;05
 PU-b: u41.4i ;05
 PU-b: u41.1i ;05
 PU-b: u54.10i, u54.1i, u54.7i ;05
 PU-b: u54.6i ;05
 PU-b: u54.3i ;05
 PU-b: u47.10i, u47.7i, u47.4i ;05
 PU-b: u47.5i, u47.6i, u47.1i
 PU-b: u40.13i ;09
 PU-b: u27.4i ;10

PU-b: u24.4i ;10
 PU-c: #R59.1o ;01
 PU-c: u88.1i, u88.9i ;03
 PU-c: v09.2i, v09.9i, v09.1i ;07
 PU-c: v09.3i
 PU-c: v08.2i, v08.9i, v08.1i ;07
 PU-c: v08.3i
 PU-d: #R52.1o ;01
 PU-d: u40.4i ;09
 PU-d: u40.10i ;09
 PU-d: u40.1i ;09
 PU-r: u97.14i, u97.11i, u97.16i ;14
 PU-r: u71.6i, #R56.2i, u71.8i ;15
 PU-r: u56.3i, u56.13i ;16
 PU-r: u55.10i, u55.13i ;16
 PU-r: u69.3i, u69.13i ;16
 pur: u64.11o, u63.4i ;03
 pur: #TP025.1i ;03
 Purge': u62.5i ;02
 Purge': u63.3o ;03
 Purge: u63.2o, #TP029.1i ;03
 Purge: v10.6i, v10.4i ;06
 PUT-a: #R61.1o ;01
 PUT-a: u76.4i, u76.5i, u76.6i ;03
 PUT-a: u76.3i
 pWaitClk: v27.1o ;01
 pWaitClk: u74.10i ;02
 pWaitClk: u74.13i ;02
 pWaitClk: u60.3i, u60.10i, u60.1i ;11
 PWI': v16.11i ;16
 PWI': u03.8o ;17
 PWI: C236, u03.10i ;17
 R.0: #TP016.1i, u88.7o, u42.2i ;03
 R.0: u81.12i ;03
 R.0: u89.3i ;07
 R.0: u90.3i ;07
 R.1: u88.13o, u42.4i ;03
 R.1: u89.4i ;07
 R.1: u90.4i ;07
 R.2: u88.6o ;03
 R.2: u89.7i ;07
 R.2: u90.7i ;07
 R.3: u88.14o ;03
 R.3: u89.8i ;07
 R.3: u90.8i ;07

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 35 OF	

R.4: u88.5o ;03
 R.4: u89.13i ;07
 R.4: u90.13i ;07

 R.5: u88.15o ;03
 R.5: u89.14i ;07
 R.5: u90.14i ;07

 R.6: u88.4o ;03
 R.6: u89.17i ;07
 R.6: u90.17i ;07

 R.7: u88.16o ;03
 R.7: u89.18i ;07
 R.7: u90.18i ;07

 R.EvenLen: v04.13i ;02
 R.EvenLen: #TP102.1i ;06
 R.EvenLen: u79.13o ;07

 R.GoodAlign: v04.8i ;02
 R.GoodAlign: #TP092.1i ;06
 R.GoodAlign: u79.11o ;07

 R.GoodCRC: v04.15i ;02
 R.GoodCRC: #TP163.1i ;06
 R.GoodCRC: u79.12o ;07

 R.OverRun': v04.17i ;02
 R.OverRun': u65.18i ;03
 R.OverRun': u20.7o ;06

 RB.0: v29.1i ;17
 RB.0: u01.2i ;17

 RB.1: v29.2i ;17
 RB.1: u01.5i ;17

 RBit: u88.11i ;03
 RBit: u81.11i ;03
 RBit: u46.2o ;10

 RC.0': u75.2o ;03
 RC.0': u80.2i ;06

 RC.0: u76.11o, u75.1i ;03
 RC.0: u78.5i ;07

 RC.1: u76.12o, u61.4i ;03
 RC.1: u80.4i ;06
 RC.1: u80.1i ;06

 RC.2: u76.13o, u61.3i ;03

 RC.3: u76.14o, u61.5i ;03

 RC=7: u81.4o, u80.3i ;06

rcc: u33.13o, u33.9i ;08

 RC1k': u30.3i ;10
 RC1k': u07.6o, u08.11i ;10

 RC1k: u09.11i, u09.6i, u07.4o ;10
 RC1k: u27.9i
 RC1k: u30.8i ;10
 RC1k: u08.6i ;10

 RC1kSh': #R50.2i, #R51.1o, u28.11i ;10

 RClock: u88.12i ;03
 RClock: u76.2i ;03
 RClock: u38.10o ;10
 RClock: u77.9i ;18

 RComp: u17.7o, #R34.1o ;09
 RComp: u26.1i ;09

 Rcv+: u18.7o, u25.10i ;09
 Rcv+: u26.6i ;09

 Rcv-: #R38.1i, #C23.2i, u18.6o ;09
 Rcv-: u17.9i

 RcvCnt=15': u81.3i, u75.8o, u82.3i ;03

 RcvCnt=15: #TP018.1i, u76.15o ;03
 RcvCnt=15: u75.9i

 RcvCnt=16: u77.7o ;03
 RcvCnt=16: v11.4i ;06
 RcvCnt=16: u16.13i ;06

 RcvCnt=7': u61.6o ;03
 RcvCnt=7': u81.5i ;06

 RcvE: u25.7o, u19.10i ;09
 RcvE: u26.4i ;09

 RcvMode': v04.2i ;02
 RcvMode': u63.6o ;03
 RcvMode': u81.2i ;03
 RcvMode': u82.9i ;06
 RcvMode': u81.9i, u52.17i ;06
 RcvMode': u89.1i ;07
 RcvMode': u90.1i ;07

 RcvMode: u62.6i ;02
 RcvMode: u63.7o, #TP030.1i ;03
 RcvMode: u64.7i ;03
 RcvMode: u68.14i ;04
 RcvMode: v27.12i ;06
 RcvMode: v05.1i ;07
 RcvMode: v06.1i ;07

 RcvModeDly': #TP071.1i, u52.16o ;06
 RcvModeDly': u82.13i

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT		SHEET 36	OF	

RcvModeDly': u73.12i ;07
RcvModenLp: u81.10o, u82.1i ;06
RcvModeTT: u68.6o, u67.4i ;04
RcvModeTT: #TP035.1i ;04
rcvmt: u68.15o ;04
rcvmt: u68.7i ;04
RcvState.1: u64.6i ;03
RcvState.1: u63.10o, #TP031.1i ;03
RcvState.2: u64.5i ;03
RcvState.2: u63.15o, #TP032.1i ;03
RdBuf': u95.17i ;03
RdBuf': v10.8o, #TP068.1i ;06
RdBuf': u92.16i ;07
RdBuf': u91.16i ;07
RdBuf': u93.16i ;07
RdBuf': u94.16i ;07
RdBuf': u79.16i ;07
RdBufA': u52.2o, u66.12i ;03
RdBufA': #TP159.1i
RdBufA': u52.4i ;03
RdBufAA': u52.5o, #TP160.1i ;03
RdBufAA': u66.13i
RdBufD': #TP158.1i, u95.3o, u52.3i ;03
Receive+: #R28.2i, #R22.1o ;09
Receive+: #R18.2i, C74, #C14.2i
Receive-: #R19.2i, #R20.1o ;09
Receive-: #R18.1i, C73, #C7.2i
RefReq': v18.6o, E88 ;11
RefTime: v18.9o ;11
RefTime: v01.11i ;11
rf: #R22.2i, #C12.1o, #R20.2i ;09
Ring': v16.6i ;16
Ring': u57.6o ;16
RingInd': u57.4i ;16
RingInd': u01.8o ;17
RingInd: C222, u01.10i ;17
RL+: #C14.1o, #R34.2i, #R35.2i ;09
RL+: #R28.1o, u18.10i
RL-: #R29.1o, #R24.2i, #C7.1o ;09
RL-: #R19.1o, u18.9i
rs.0: u64.12o, u63.5i ;03
rs.0: #TP026.1i ;03
rs.1: u66.9i, u64.13o, u63.12i ;03
rs.1: #TP027.1i ;03
rs.2: u66.10i, u64.14o, u63.13i ;03
rs.2: #TP028.1i ;03
RS: u26.8i ;09
RS: u28.2o, u28.10i ;10
RSh': u38.12o, #R51.2i ;10
RSRegRd': v16.19i, v16.1i, u84.6o ;16
RSRegRd': #TP148.1i
RSRegRd': u57.3i ;16
RSRegWr': u84.3o, v14.11i ;16
SColl': u16.8o, u20.10i ;09
SecCTS': u71.18i ;15
SecCTS': u69.9o, #TP152.1i ;16
SecDCD': u71.22i ;15
SecDCD': u55.7o, #TP156.1i ;16
SecndCTS': u69.11i ;16
SecndCTS': u01.3o ;17
SecndCTS: C213, u01.1i ;17
SecndLineDet': u55.5i ;16
SecndLineDet': u01.6o ;17
SecndLineDet: C212, u01.4i ;17
SecndRData': C216, u04.13i ;17
SecndRData: u69.5i ;16
SecndRData: u04.11o ;17
SecndRTS': u69.12o ;16
SecndRTS': u11.12i, u11.13i ;17
SecndRTS: u11.11o, C219 ;17
SecndTData': u11.8o, C214 ;17
SecndTData: u69.4o ;16
SecndTData: u11.9i, u11.10i ;17
SecRTS': u71.17o ;15
SecRTS': u69.10i, u69.14i ;16
SecRXD: u71.29i ;15
SecRXD: u69.7o, #TP151.1i ;16

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT			SHEET 37 OF	

SecTXD: u71.15o ;15
 SecTXD: u69.6i, u69.2i ;16
 SelLocalTim: v14.5o ;16
 SelLocalTim: u84.10i ;16
 SendDly: u48.19o, u16.1i ;09
 SendDly: u16.9i ;09
 SendMode: u51.6o, #TP046.1i ;04
 SendMode: u48.18i ;09
 SerDI': u26.14o ;09
 SerDI': u08.2o, u27.14i ;10
 SerialTData: u37.7o, #TP121.1i ;08
 SerialTData: u48.17i ;09
 SerialTDataDly: u48.16o, u39.1i ;09
 SetCRC': u33.10o, #TP120.1i ;08
 SetCRC': u21.1i
 SetCRC': u35.1i ;08
 SetCRC': u34.1i ;08
 SetCRC': u22.1i ;08
 ShiftOut': #TP141.1i, u85.6o ;12
 ShiftOut': u58.2i ;12
 SIOBA: v00.11o, #TP146.1i ;15
 SIOBA: u71.34i ;15
 SIOCD: v17.12o, #TP147.1i ;15
 SIOCD: u71.33i ;15
 SIOCE': v17.10o, #TP145.1i ;15
 SIOCE': u71.35i ;15
 SIOCE': v15.19i ;15
 SIOCE: u99.12o, v17.11i ;15
 SIOC1k': #TP144.1i, #R55.2i ;15
 SIOC1k': u70.3o, u71.20i
 SIODData.0: v15.18o, u71.4i ;15
 SIODData.1: v15.17o, u71.37i ;15
 SIODData.2: v15.16o, u71.3i ;15
 SIODData.3: v15.15o, u71.38i ;15
 SIODData.4: v15.14o, u71.2i ;15
 SIODData.5: v15.13o, u71.39i ;15
 SIODData.6: v15.12o, u71.1i ;15
 SIODData.7: v15.11o, u71.40i ;15
 SIOInt': v17.5i ;15
 SIOInt': #R54.2i, u71.5o ;15
 SIOIORQ': v17.8o, #TP143.1i ;15
 SIOIORQ': u71.36i ;15
 SIOlpEn: v14.2o ;16
 SIOlpEn: u56.1i ;16
 SIOlpEn: u69.1i ;16
 SIOlpEn: u55.1i ;16
 SIOlpEn: u84.9i ;16
 SIO0sc: u97.13o ;14
 SIO0sc: u83.6i, u83.3i ;16
 SIORxClk': u71.28i, u71.13i ;15
 SIORxClk': u83.7o, #TP154.1i ;16
 SIOTxClk': u97.18i ;14
 SIOTxClk': u71.27i, u71.14i ;15
 SIOTxClk': u83.4o, #TP153.1i ;16
 sndm: u50.11o, u51.7i ;04
 sndm: #TP054.1i ;04
 StartIPG: #TP067.1i, u41.9i ;05
 StartIPG: u54.9i, u48.15o
 Status: u05.12o ;13
 Status: u96.3i ;14
 StatusECL+: #R9.1i, C60, u05.11i ;13
 StatusECL-: #R10.1i, C61, u05.10i ;13
 StTm: #R9.2i, #C19.1o, #R10.2i ;13
 Swap: u72.11i, u58.9o, u72.3i ;11
 SwClk: #TP157.1i, u84.8o, u83.1i ;16
 T.Collision': v04.4i ;02
 T.Collision': u68.17i ;04
 T.Collision': u20.9o ;09
 T.CollisionTT': u68.9o, u67.7i ;04
 T.CollisionTT': #TP036.1i ;04
 T.UnderRun': v04.6i ;02
 T.UnderRun': u48.6o, #TP043.1i ;04
 T.UnderRun': u67.5i ;04
 T1: #R32.1o, #TP130.1i ;10
 T2: #R25.1o, #TP131.1i ;10
 TBit': u38.4o, u37.10i ;08

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028	SHEET REV. A
	TITLE SCHEMATIC, OPT		SHEET 38 OF		

TBit: v09.17o ;07
 TBit: u38.3i ;08
 TBit: u37.4i, u37.6i ;08

 TBite: v08.17o ;07
 TBite: v09.11i ;07

 TClock: u68.11i ;04
 TClock: u51.11i ;04
 TClock: u54.2i, u41.2i ;05
 TClock: u47.2i ;05
 TClock: u45.1i ;05
 TClock: v09.12i ;07
 TClock: v08.12i ;07
 TClock: u38.2o ;09
 TClock: u48.11i ;18

 TDb1C: #Y1.8o, u53.17i ;09

 TDb1Clock: #TP125.1i, u53.3o ;09
 TDb1Clock: u40.11i
 TDb1Clock: u40.3i ;09

 Tester: u95.1i, u53.19i, #R62.1i ;01
 Tester: u53.1i, #TP005.1i

 TickElapsed: u62.2i ;02
 TickElapsed: u95.18o, #TP064.1i ;05

 TimerOUT: u97.17o ;14
 TimerOUT: v16.2i ;16

 TimerRd': u98.6o, u97.22i ;14

 TimerWr': u98.8o, u97.23i ;14

 TranPwr: C84, C90 ;09
 TranPwr: #F1.2o, #C1.1i ;19

 Transmit+: #R14.1i, u19.9o, C72 ;09

 Transmit-: #R13.1i, u19.15o, C71 ;09

 TRClk': v11.8o, u21.11i ;08
 TRClk': u35.11i ;08
 TRClk': u34.11i ;08
 TRClk': u22.11i ;08

 TRGated: u14.13i, u37.9o, u14.10i ;08

 Trn': u40.6o ;09
 Trn': #R41.2i ;09

 Trn: u40.5o, #TP124.1i ;09
 Trn: #R44.2i ;09

 TrnCt=15': u46.8o, u33.3i ;06

TrnCt=15: u49.5i ;04
 TrnCt=15: u50.5i ;04
 TrnCt=15: #TP060.1i, u47.15o ;05
 TrnCt=15: u48.8i
 TrnCt=15: u46.9i ;06
 TrnCt=15: v08.19i ;07
 TrnCt=15: v09.19i ;07

 TrnCt=16: u48.9o ;05
 TrnCt=16: v10.9i ;06

 TrnE: u19.12i, u18.3o, u19.5i ;09
 TrnE: u26.5i ;09

 TrnL': #R42.2i, #R41.1o, u18.4i ;09

 TrnL: #R45.2i, #R44.1o, u18.5i ;09

 trnm: u50.9o, u51.3i ;04
 trnm: #TP052.1i ;04

 TrnMode': u46.10o, u82.10i ;06

 TrnMode: u62.1i ;02
 TrnMode: u51.5o, #TP045.1i ;04
 TrnMode: u20.14i ;05
 TrnMode: u46.11i ;06
 TrnMode: u20.12i, u20.11i ;09

 TrnState.0: u49.3i ;04
 TrnState.0: u50.3i ;04
 TrnState.0: u51.12o, #TP048.1i ;04
 TrnState.0: u33.8i ;08

 TrnState.1: u49.4i ;04
 TrnState.1: u50.4i ;04
 TrnState.1: u51.15o, #TP049.1i ;04

 TrnState.2: u49.7i ;04
 TrnState.2: u50.7i ;04
 TrnState.2: u51.16o, #TP050.1i ;04
 TrnState.2: u37.14i ;08

 TrnState.3: u49.6i ;04
 TrnState.3: u50.6i ;04
 TrnState.3: u51.19o, #TP051.1i ;04

 ts.0: u49.9o, u51.13i ;04
 ts.0: #TP056.1i ;04

 ts.1: u49.10o, u51.14i ;04
 ts.1: #TP057.1i ;04

 ts.2: u49.11o, u51.17i ;04
 ts.2: #TP058.1i ;04

 ts.3: u49.12o, u51.18i ;04
 ts.3: #TP059.1i ;04

tund: u67.11o, u48.7i ;04
tund: #TP040.1i ;04
TurnOff': v26.11o, #TP010.1i ;02
TurnOff': v04.11i ;02
TurnOff': u61.9i ;02
UnderRun: u68.13i ;04
UnderRun: u33.1o ;06
UnderRunT: u68.12o, u67.6i ;04
UnderRunT: #TP037.1i ;04
VBBb: u18.11i, #R37.1i ;09
VBBb: u25.9i ;09
VBBb: u27.2i ;09
VBBb: u25.5i ;09
VBBc: u17.11i, u17.4i ;09
VBBc: u24.14i, u27.15i ;10
VBBc: u27.7i ;10
VBBc: u27.10i ;10
VBBd: u25.11i, u24.2i ;09
VBRef: u17.10i, #R36.2i, #R37.2i ;09
VBRef: u17.13i
VCC: #R53.2i, #R59.2i, #R52.2i ;01
VCC: #R61.2i, #R60.2i
VCC: #R45.1i, #R42.1i, #C36.1o ;09
VCC: #R47.1i
VCC: #R36.1o ;09
VCC: #C25.2i, #C23.1i ;09
VCC: #C15.1i ;09
VCC: #C28.1i ;09
VCC: u29.16i, u29.1i, u19.16i ;10
VCC: u19.1i, u08.16i, u08.1i
VCC: u28.16i, u28.1i
VCC: u09.16i, u09.1i, u17.16i ;10
VCC: u17.1i, u25.16i, u25.1i
VCC: u18.16i, u18.1i
VCC: #C32.2i ;10
VCC: #R50.1o, #C42.2i ;10
VCC: #C44.1i, #C26.1i ;10
VCC: u07.5i, u07.1i, #C24.1i ;10
VCC: u06.9i ;13
VCC: u05.9i ;13
VCC: #R63.1o ;13
VCC: #R11.2i ;13
VCC: u96.26i ;14
VCC: u71.9i ;15
VCC: #R55.1i, #R56.1i ;15
VCC: #R58.1i, #R57.1i, #R54.1i ;15
VCC: #C81.1i, #F4.2o, #C51.1i ;19
VCTm: #R1.2i, #C4.1o, #R2.2i ;13
VCx: u07.2i ;10
VCx: #R15.1o, #C10.2i, #CR1.2i ;10
VDD: #R15.2i ;10
VDD: u12.14i ;17
VDD: u11.14i ;17
VDD: u13.14i ;17
VDD: #C82.1i, #F2.2o, #F1.1i ;19
VEE: #R5.2i, #R6.2i ;13
VEE: #R7.2i, #R8.2i ;13
VEE: v29.16i ;17
VEE: #F3.2o, #C80.2i ;19
VFF: u12.1o ;17
VFF: u13.1o ;17
VFF: u11.1o ;17
VFF: #F5.2o, #C38.2i ;19
VideoClock: v23.4i ;02
VideoClock: u59.2i ;11
VideoClock: u58.11i ;11
VideoClock: v02.2i, v03.2i ;12
VideoClock: u86.2i, u87.2i ;12
VideoClock: u58.3i ;12
VideoClock: u05.4o ;13
VideoClockECL+: #R1.1i, C51 ;13
VideoClockECL+: u05.3i
VideoClockECL-: #R2.1i, C52 ;13
VideoClockECL-: u05.2i
VideoData: u58.6o ;12
VideoData: u06.5i ;13
VideoDataECL+: u05.15i, #R7.1i ;13
VideoDataECL+: u06.2o, C54
VideoDataECL-: u05.14i, #R8.1i ;13
VideoDataECL-: u06.4o, C55
VWord: #TP132.1i, u59.15o, u58.12i ;11
Wait: E17, v27.2i ;01
WantWord': u72.8o, u73.5i ;11
WrBuf': v11.6o, #TP069.1i ;06
WrBuf': u92.1i, u92.19i ;07
WrBuf': u91.1i, u91.19i ;07
WrBuf': u93.1i, u93.19i ;07
WrBuf': u94.1i, u94.19i ;07
WrBuf': u79.1i, u79.19i ;07
WrBufferA': u60.12o, #TP135.1i ;11
WrBufferA': v03.1i, v02.1i ;12
WrBufferB': u60.6o, #TP136.1i ;11
WrBufferB': u87.1i, u86.1i ;12

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P12028		SHEET REV. A
	TITLE	SCHEMATIC, OPT		SHEET	40 OF	

WrPCtl': u60.8o ;11
 WrPCtl': u98.13i ;11
 WrPCtl': v20.9i ;11

 X.0: E41, v22.17i ;01
 X.0: v23.18o ;02
 X.0: v24.2o ;07

 X.10: E46, v21.13i ;01
 X.10: v04.14o ;02
 X.10: v07.6o ;07

 X.11: E146, v21.11i ;01
 X.11: v04.12o ;02
 X.11: v07.9o ;07

 X.12: E47, v21.2i ;01
 X.12: v04.3o ;02
 X.12: v07.12o ;07

 X.13: E147, v21.4i ;01
 X.13: v04.5o ;02
 X.13: v07.15o ;07

 X.14: E48, v21.6i ;01
 X.14: v04.7o ;02
 X.14: v07.16o ;07

 X.15: E148, v21.8i ;01
 X.15: v04.9o ;02
 X.15: v07.19o ;07

 X.1: E141, v22.15i ;01
 X.1: v23.16o ;02
 X.1: v24.5o ;07

 X.2: E42, v22.13i ;01
 X.2: v23.14o ;02
 X.2: v24.6o ;07

 X.3: E142, v22.11i ;01
 X.3: v23.12o ;02
 X.3: v24.9o ;07

 X.4: E43, v22.2i ;01
 X.4: v23.3o ;02
 X.4: v24.12o ;07

 X.5: E143, v22.4i ;01
 X.5: v23.5o ;02
 X.5: v24.15o ;07

 X.6: E44, v22.6i ;01
 X.6: v23.7o ;02
 X.6: v24.16o ;07

 X.7: E144, v22.8i ;01
 X.7: v23.9o ;02

X.7: v24.19o ;07

 X.8: E45, v21.17i ;01
 X.8: v04.18o ;02
 X.8: v07.2o ;07

 X.9: E145, v21.15i ;01
 X.9: v04.16o ;02
 X.9: v07.5o ;07

 yio: v18.12o, v18.14i ;02

 YIODisp.1: v18.15o, E139 ;02

 ZComp: u26.7i ;09
 ZComp: u17.3o, #R27.2i, #R29.2i ;09

 ←EIData': E31, v27.8i ;06
 ←EIData': v07.1i ;07
 ←EIData': v24.1i ;07

 ←EStatus': v04.1i, v23.19i, v23.1i ;02
 ←EStatus': E131, v04.19i

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P12028	SHEET REV.
	TITLE SCHEMATIC, OPT		A4	SHEET 41 OF	A