

**VARIAN 620/L-100
MAINTENANCE MANUAL**

Specifications Subject to Change Without Notice



varian data machines/a varian subsidiary

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CUSTOMER: VID
 SALES ORDER: 37-16986
 DATE: _____
 FROM: C. M. FUJIOKA

END USER: VID

BRIEF DESCRIPTION & NOTES

This 620/L-103 has been modified per VID document #998712-01.

The memory timing and control board (slot 6) has a bootstrap loader protect option (DM-395A) protecting addresses 0 to 77₈ and the upper 400₈ locations.

APPROVED BY: _____
 PROD. MGR.
 DATE: _____

A
DO NOT SCALE DRAWING

DESCRIPTION OF CHANGE	DRAWN	DATE	APPROVED	DATE	CODE
	CHECKED	DATE	APPROVED	DATE	CLASS
SYSTEM MEMO					
ED	NOT OTHERWISE SPEC: FRAC ± ANG ±				SCALE
DFT	FIN. ✓ DEC .X ± .XX ± .XXX ±				
CHK	varian		A		
DATE			DIVISION	SIZE	DRAWING NO.
RIV					

VENDOR: VDM IRVINE

VDM MODEL E-2861 B

This consists of:

- 1) 1 620L-103 16K CPU
- 2) 1 E 2847 BOOTSTRAP PROTECT (03-998 116)
- 3) LESS THE FOLLOWING:

PRIORITY INTERRUPT MODULE (PIM)


REAL TIME CLOCK

FRONT PANEL, LATCH & HINGE BRACKETS

DOCUMENTATION & TAPES (See Note 2)

- NOTES:
- 1) Test unit with front panel, then remove panel. VDM/PA will install adapter board to permit operation without front panel.
 - 2) Does not include program tapes or listings. Only documentation required is Maintenance Manual, Volume 2.

SPEC CONTROL DRAWING

DESCRIPTION OF CHANGE	WAS CHANGED TO #2	DRAWN	DATE	APPROVED	DATE	CODE
			R. Anderson	11/1/72		
		CHECKED	DATE	APPROVED	DATE	CLASS
				<i>[Signature]</i>	11/1/72	A
620 L CPU for VID (GATOR)						
		 varian	NOT OTHERWISE SPEC: FRAC ±		ANG ±	SCALE
DFT	4/1		FIN. ✓	DEC. X ±	.XX ±	.XXX ±
CHK	JIT	VDM/Palo Alto	A	03-998114		B
DATE		DIVISION	SIZE	DRAWING NO.		REV
REV						

03-998114
A

DO NOT SCALE DRAWING

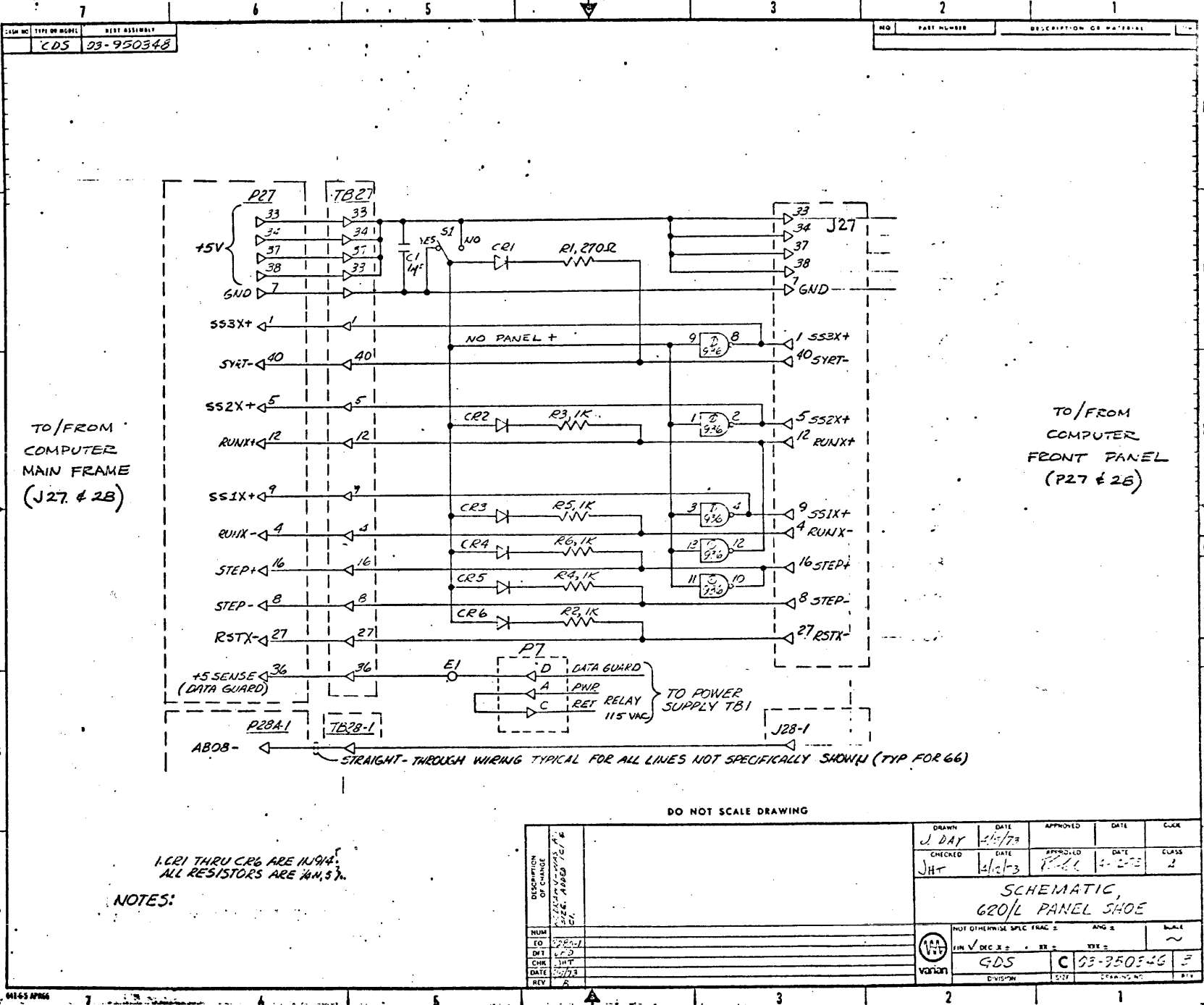
ADDENDUM

620/i and 620/L Power Failure/Restart
Manuals

98 A 9902 442

February 1972

Not only must the PF/R threshold be readjusted when the power supply and/or the PF/R card is changed, it must also be readjusted whenever a change from 50 to 60 Hz (or vice versa) operation is made, or whenever there is an increase in the load on the +5V dc supply.



TO/FROM
COMPUTER
MAIN FRAME
(J27 & 28)

TO/FROM
COMPUTER
FRONT PANEL
(P27 & 28)

DO NOT SCALE DRAWING

1. CR1 THRU CR6 ARE 1N914.
ALL RESISTORS ARE 1/4W, 5%.
NOTES:

NOTES:

DISPOSITION OF CHANGE SIZE: ADDED 1/2, 1/2 DT:	NUM TO: 620/L DWT: 620/L CHK: JHT DATE: 10/73 REV: A	DRAWN: J DAY CHECKED: JHT DATE: 10/73	APPROVED: P. G. C. DATE: 1-2-73	CLASS: 2	
	SCHEMATIC, 620/L PANEL SHOE				
	NOT OTHERWISE SPEC. FRAC. 2		ANG. 2	SHALE	FIN. V. DEC. 2
	DIVISION: GDS		PART NUMBER: C 93-950348		CLASS: 2

VENDOR: VDM IRVINE

VDM MODEL E-2847 Loader Protect

This is Loader-protect option for 620L-100 (Model 620L-115) with following modification:

Locations 0 to 77₈, inclusive, are protected in the same manner as the upper 400₈ locations.

Any attempt to write in the locations specified above will be converted to a read cycle. No HALT or interrupt will be generated.

The purpose of the additional protection is to preserve the Power-on interrupt locations and the GATOR manual-interrupt location (0).

03-998

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DESCRIPTION OF CHANGE	2023-1 VMS 0005 100	DRAWN	DATE	APPROVED	DATE	CODE
		R. Anderson	11/10/72			
		CHECKED	DATE	APPROVED	DATE	CLASS
				<i>FLK</i>	11-10-72	A
SPEC CONTROL DIAG: E-2847 LOADER PROTECT						
		NOT OTHERWISE SPEC: FRAC ±			ANG ±	SCALE
DIT	110	FIN. ✓	DEC. X ±	.XX ±	.XXX ±	—
CHK	JAT	GDS	A	03-998116		A
DATE	11/10/72	DIVISION		SIZE	DRAWING NO.	REV
REV	1	varian				

DWG NO
98A0935

REVISIONS

SYM	DESCRIPTION	APPROVED	DATE
A	PRODUCTION RELEASE PER EN 81815	<i>MG/L</i>	3/28/73

DR	K. Ellinor	2/27/73
CHK		
DSGN		
ENGR	<i>Walt</i>	3/28/73
APPD	<i>A. White</i>	3/28/73
APPD		



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2722 michelson drive / irvine / california / 92664

TITLE

ENGINEERING DESCRIPTION
BOOTSTRAP LOADER PROTECT
(E-2847)

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CODE IDENT NO.	SIZE	DWG NO.	REV
21101	A	98A0935	A
SCALE	SHEET 1 OF 10		

0153-0006

ENGINEERING DATA FORM


OPTION _____ Bootstrap Loader Protect
MODEL _____
NO. OF LOGIC CARDS REQ'D. _____ Assembled on the memory timing and control
NO. OF CARD SLOTS REQ'D. _____ 1
LOCATION OF SLOTS (NUMBERING) _____ CPU Card Slot 6
CONNECTORS REQ'D. (EXCLUDING I/O) _____ N/A
KEYING _____
ST'D. DEVICE ADDRESS _____ N/A
WIRELIST NUMBER _____ N/A (PC Board)
MANUAL PUBLICATIONS NUMBER _____ This document
PERIPHERAL EQUIPT. REQ'D _____ For test only: BIC and paper tape system
MFG'R. _____
MODEL _____
GEN'L. SPECS _____

NOTES:

Drawings:

 Top Assembly 44P0671
 Logic Diagram 91D0436
 Bracket Assembly 04C0656

 Software and test procedures are part of this drawing.

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	PREPARED BY	APPR.	SHT 2 OF 10

SECTION 1
GENERAL DESCRIPTION

The loader protect feature consists of additional logic located on the memory timing and control board plus additional backplane wiring to a switch. Drawing describes the modification procedure.

The purpose of the loader protect is to prevent writing into the last 400₈ locations of a selected 4K memory increment and locations 0 through 77₈ of the first 4K memory increment.



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21101

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SECTION 2
FUNCTIONAL DESCRIPTION

The loader protect feature prevents writing into memory locations which are used by the bootstrap and binary load/dump routines. These locations are normally the last 400 octal addresses of the core memory. The loader protect circuitry is located on the timing and control card located in CPU card slot 6. The card contains jumper pads for the three most significant address terms (L12X+, L13X+, L14X+). Jumpers are installed at the factory or in the field when the system memory capacity exceeds 4K. Address may be adjusted to protect any memory increment from 4 to 32K.

NOTE: If no jumpers are installed, the last 400 octal locations of each 4K section is controlled by the loader protect circuitry. (See Table 1 for jumper configuration).

There is a switch mounted on the chassis inside the front panel that enables and disables the option.

There are no additional instructions or control terms for this option. All addresses for write type operation are compared for error. An error occurs whenever a write type operation is attempted in a protected area with the option enabled.



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SECTION 3 THEORY OF OPERATION

All addresses being accessed in memory are checked by the loader protect option. When the address being accessed is equal to that of a protected address, and LPEX+ (loader protect enable from switch) is true, and H2XX+ (decode for a store instruction) is true, then the WRTX+ (write control to memory) is forced to ground. This causes the memory cycle to be a read/restore cycle.

After an error, the WRTX+ signal will be forced to ground causing a read cycle, and the computer will continue to cycle, changing all write cycles in a protected area. During a trap-in operation, the read/write command (WRTX+) and the memory start pulse (MSPX+) are forced to the low state and the CPU continues to cycle.

3.1 TRAP-IN OPERATION

When executing a trap-in request with the option enabled, if the memory address provided by the trapping device is in the protected area, the following events occur. The read/write command (WRTX+) and the memory start pulse (MSPX+) are forced to the low state.

3.2 LOADER PROTECT ENABLE/DISABLE SWITCH

A toggle switch located on the chassis inside the front panel is used to enable the loader protect circuitry. When the switch is in the disable position, all memory locations are available for storage. However, when the switch is in the enable position, the loader protect feature prevents writing into the memory locations X7400 through X7777 and locations 0 through 77g.

*X = 4K core segment

3.3 ADDRESS SELECTION

Table 1 shows jumper placement on the memory timing and control board for address selection. Typically, the system memo will specify the proper jumpering.



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TABLE 1

MEMORY SIZE	JUMPERS REQ.		
	A-A	B-B	C-C
4K	No	No	No
8K	No	No	Yes
12K	No	Yes	No
16K	No	Yes	Yes
20K	Yes	No	No
24K	Yes	No	Yes
28K	Yes	Yes	No
32K	Yes	Yes	Yes

A-A, B-B and C-C are jumper pads for the three most significant address terms and are located on the memory timing control board.



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SECTION 4
MNEMONIC LIST

H2XX+	Store instruction decode
ICLX-	Inhibit clock
L02X+ through L14X+	Memory address register lines
LPEX+	Loader protect enable from switch
MSPX+	Memory start pulse
TPIX-I	Trap-in
WRTX+	Read/write command



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SECTION 5
TEST DESCRIPTION

5.1 PROGRAM MODE TEST

Using Aid II program with loader protect disabled write a fixed data pattern in locations 0 through 77 and X7400 through X7777.

i.e.

0, 77, 177777, ..	X = 0-7 for
X7400, X7777, 177777, ..	4 → 32K
	1 ^E 4K = 0
	8K = 1

Enable loader protect then write a different fixed pattern in locations 0 through 77 and locations X7400 through X7777.

i.e.

0, 77, 0.
X7400, X777, 0, ..

Now verify that the original pattern did not get altered by reading locations 0 77 and X7400 X7777 and comparing it with the original pattern by using the search function of Aid II program.

i.e.

S 0, 77, 177777, N
S X7400, X7777, 177777, N

If any errors occurred, they will be listed as follows: There should be no errors.

i.e.

Address	Contents
000043	(000000)

5.2 TRAP-IN TEST

Using the Aid II program load the following program into core starting at location 100.



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100	100021	Initialize BIC
101	006010	LDAI
102	0	
103	006020	LDBI
104	-77	
105	101537	Sen Buff Ready
106	000111	
107	001000	JMP
110	000105	
111	103120	OAR BIC Initial Reg.
112	103221	OBR BIC Final Reg.
113	100020	Activate BIC
114	100537	Start Reader
115	005000	NOP
116	101020	Sen BIC Not Busy
117	000122	
120	001000	JMP
121	000115	
122	100021	Initialize BIC
123	006010	LDAI
124	0X7400	
125	006020	LDBI
126	0X7777	
127	101537	Sen Buff Ready
130	000133	
131	001000	
132	000127	
133	103120	OAR BIC Initial Reg.
134	103221	OBR BIC Final Reg.
135	100020	Activate BIC
136	100537	Start Reader
137	005000	NOP
140	101020	Sen BIC Not Busy
141	0X6000	
142	001000	JMP
143	000137	



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Place a test tape in the reader, ~~press~~ then run the program starting at location 100. When it is complete, it will return to the Aid II program.

Verify that none of the protected locations of core get altered by using the search function of the Aid II program.

i.e.

S 0, 77, 177777, N

S X7400, X7777, 177777, N

Any errors will be listed as in previous test. There should be no errors.



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DOCUMENTATION RECORD

CUSTOMER VARIAN QADS
 MODEL NO. 620/L-100
 SYS. SERIAL NO. 81

JOB ORDER NO. 73284-29
 SHIP DATE _____
 CHECKED BY _____

UNIT	DOCUMENTATION NO.	REV	ART REV	S/N	DESCRIPTION	INSP.
	01E1035	-	-	-	FRAME ASSY	
	01D1036	-	-	-	FRONT PANEL ASSY	
	93D0275	-	-	-	POWER SUPPLY INSTALLATION	
	3238441	-	-	-	CORE STACK	
DM288	44P0506	P		11047	SENSE INHIBIT	
DM288	44P0506	P		11050	SENSE INHIBIT	
DM288	44P0506	P		8987	SENSE INHIBIT	
DM288	44P0506	P		9018	SENSE INHIBIT	
DM288	44P0506				SENSE INHIBIT	
DM288	44P0506				SENSE INHIBIT	
DM295	44P0515				DISPLAY BOARD	
DM327	44P0578	G		3075	DRIVE/SINK SW	
DM327	44P0578	G		3142	DRIVE/SINK SW	
DM327	44P0578				DRIVE/SINK SW	
DM336	44P0592	D		3684	REGISTER CARD	
DM336	44P0592	D		3688	REGISTER CARD	
DM336	44P0592	D		3675	REGISTER CARD	
DM336	44P0592				REGISTER CARD	
DM336	44P0592				REGISTER CARD	
DM337	44P0593	N		1152	PROCESSOR CONT. #4	
DM337	44P0593				PROCESSOR CONT. #4	
DM338	44P0594	D		1435	HM/D & F.A.	
DM338	44P0594				HM/D & F.A.	
DM339	44P0595	B		1547	PROCESSOR CONT #1	
DM339	44P0595				PROCESSOR CONT #1	
DM340	44P0596	F		1314	PROCESSOR CONT. #2	
DM340	44P0596				PROCESSOR CONT. #2	
DM341	44P0597	E		1153	PROCESSOR CONT. #3	
DM341	44P0597				PROCESSOR CONT. #3	
DM342	44P0598	E		1380	DMA	
DM343	44P0671	F		092	MEMORY T AND CONT.	
DM343	44P0599				MEMORY T AND CONT.	

967 -0008

PREPARED BY Ma

DATE 5-30-74

NOTE:



PARTS LIST FOR 620/L AND 620/L-100 CIRCUIT CARDS

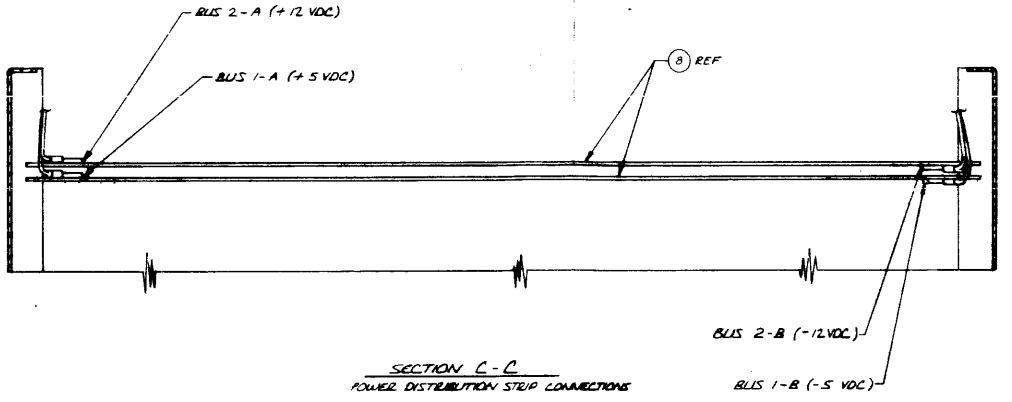
The following is a composite parts list for the 620/L and 620/L-100. It is divided into three sections. Section 1 contains the standard circuit cards, section 2 the power supply, and section 3 the controller cards. The parts for each circuit card are listed in numerical order according to Varian part numbers. The reference designations in the parts list also appear on the assembly drawings and logic diagrams.

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
1. STANDARD LOGIC CARDS			
44P0172 *	49A0002-000	Tex Inst SN7473N	IC 16,33,39,44,49
Priority	49A0004-000	Tex Inst SN7440N	IC 41
Interrupt	49A0007-000	Tex Inst SN7400N	IC 23,24
Module	49A0008-000	Tex Inst SN15846N	IC 6,10,12,18,19,25,26, 30,32,38,43,45,46,48,50
	49A0010-000	Fairchild SL18162	IC 4,5,11,28,29,36
	49A0011-000	Tex Inst SN15830N	IC 1,7,13,17,20,21,27,35,51
	49A0012-000	Tex Inst SN7474N	IC 2,3,9,15,31,37,42,47
	49A0014-000	Tex Inst SN15850N	IC 22,34
	49A0016-000	Tex Inst SN15833N	IC 8,14,40
44P0185	49A0002-000	Tex Inst SN7473N	IC 3,13,19,20,26,27,33
Power Failure/ Restart and Real-Time Clock	49A0004-000	Tex Inst SN7440N	IC 22,23,32
	49A0007-000	Tex Inst SN7400N	IC 8,9,12,31
	49A0008-000	Tex Inst SN15846N	IC 1,2,4,5,6,11,21,24,25, 34,35,36,37,39,40,41
	49A0010-000	Fairchild SL18162	IC 14,15,16,17,18,28,30,42
	49A0011-000	Tex Inst SN15830N	IC 7,10
	49A0518-000	Motorola MC851L	IC 29,38
	76A2369-000	2N2369	Q 3,5,6,8,9,10,11,15,16,17,18
	76A3009-000	2N3009	Q 12,19
	76A4034-000	2N4034	Q 1,2,4,7,13,14
	76N4916-000	Fairchild 2N4916	Q 1,2,4,7,13,14
	76S1002-000	Motorola 2N3019	Q 20
	76S1046-000	Fairchild 2N3646	Q 12,19
	77N0753-000	IN753	CR 3,10
	77S1017-000	Fairchild EDN400	CR 1,2,4,5,6,7,9

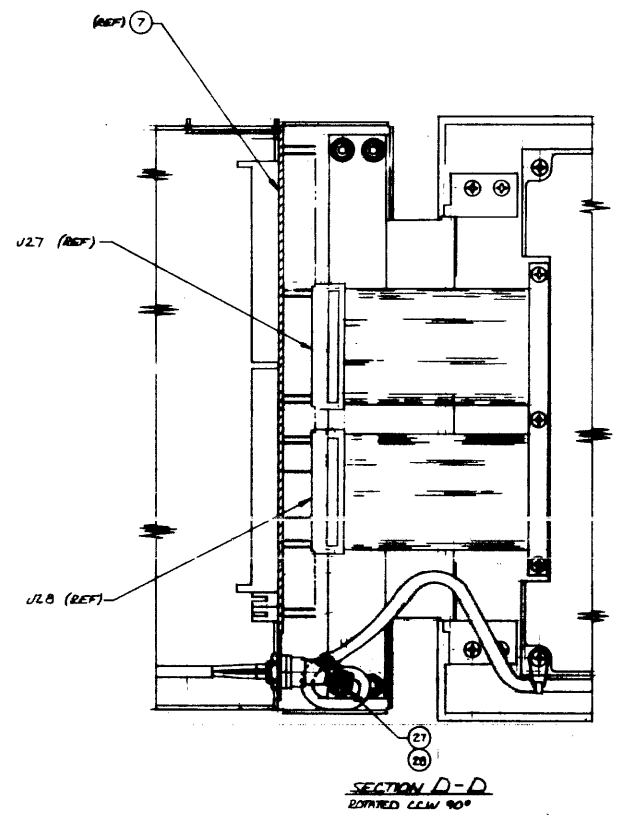
* See page 6 for another version of Priority Interrupt Module.

01E1035

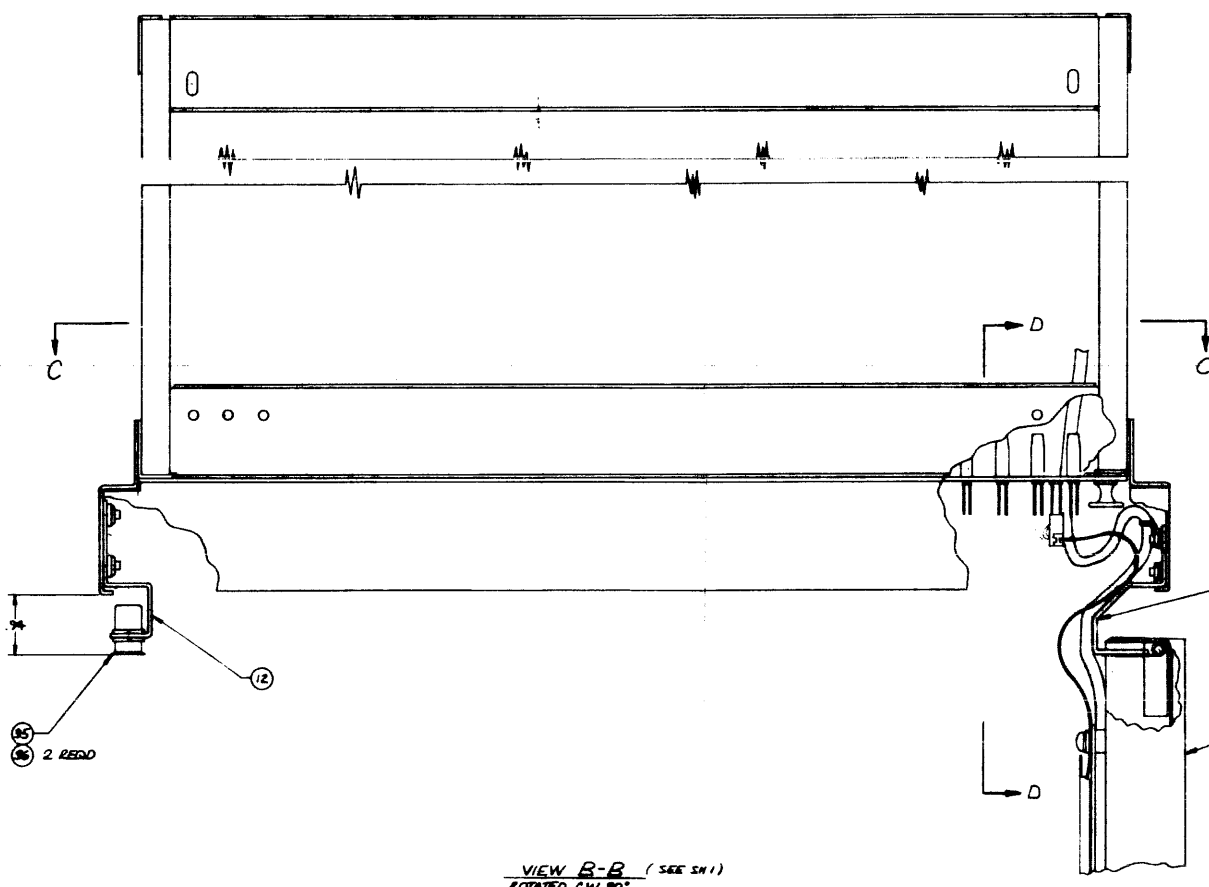
REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	



SECTION C-C
POWER DISTRIBUTION STRIP CONNECTIONS



SECTION D-D
ROTATED 60°



VIEW R-R (SEE SH 1)
ROTATED 90°

01E1035
J

NOTE: UNLESS OTHERWISE SPECIFIED

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0506 Sense/Inhibit	48A0002-001	Varian	A 101,201,401,501,701, 801,1001,1101
	48A0003-001	Varian	A 303,603,903,1203
	49A0022-000	Tex Inst SN74H11N	IC 4,5,7,9,10,12
	49A0032-000	Tex Inst SN7402N	IC 2
	49A0042-000	Tex Inst SN74H01N	IC 1,3,6,8,11
	49A0080-000	Tex Inst SN7524N	IC 101,201,401,501,701, 801,1001,1101
	49A0119-000	Motorola MH05859	A 302,602,902,1202
	76A2369-000	2N2369	O 301 through 304, 601 through 604, 901 through 904, 1201 through 1204
	77S1017-000	Fairchild EDN400	CR 101,102,103,104 through 904,1201,1202,1203,1204, 305,605,905,1205
	44P0515 Display Board	49A0004-000	Tex Inst SN7440N
49A0040-000		Tex Inst SN7404N	IC 3,7,11,13
49A0110-000		Tex Inst SN75451BP	IC 1,2,4,5,6,8,9,10,12,14
44P0521 Memory Buffer	49A0023-000	Tex Inst SN74H04N	IC 2,4,7
	49A0025-000	Varian	A 1,2
	49A0042-000	Tex Inst SN74H01N	IC 1,3,5,6,8
	49A0124-000	Tex Inst SN7407N	IC 13,14,15
	49A0125-000	Tex Inst SN7408N	IC 9,10,11,12
44P0578 Driver/Sink Switch	49A0032-000	Tex Inst SN7402N	IC 1,3
	49A0041-000	Tex Inst SN74H51N	IC 4,7,8
	49A0044-000	Tex Inst SN7442N	IC 2,5,9,11
	49A0118-000	Tex Inst SN7427N	IC 6,10
	49A0119-000	Motorola MH05859	A 102,104,202,204,302,304,402 404,502,504,602,604,702,704 802,804
	76A0002-000	2N3725A	O 25
	76A2369-000	2N2369	O 1,2,5,9,12,13,14,17,18
	76A2904-000	2N2904	O 23
	76A2907-000	Motorola 2N2907	O 3,4,6,7,8,10,11,15,16,19,20
	76N3640-000	2N3640	O 21,24
76S1046-000	Fairchild 2N3646	O 22,26	
77N0751-000	Tex Inst IN751A	CR 7,10,11,12,13,14	
77S1017-000	Fairchild EDN400	CR 1 through 6,8,9,15, 101 through 116, 201 through 216, 301 through 316, 401 through 416, 501 through 524, 601 through 624, 701 through 724, 801 through 824	

Card P/N
and Name

Varian P/N

Manufacturer
and P/N

Reference
Designation

44P0592
Register Card

49A0000-000	Tex Inst SN7475N	IC 9,21,33
49A0007-000	Tex Inst SN7400N	IC 13,24
49A0010-000	Fairchild SL18162	IC 2,6,7
49A0012-000	Tex Inst SN7474N	IC 4,8,14
49A0023-000	Tex Inst SN74H04N	IC 12,18,41
49A0040-000	Tex Inst SN7404N	IC 1
49A0095-000	Tex Inst SN74H52N	IC 15
49A0096-000	Tex Inst SN74181N	IC 19,25
49A0102-000	Tex Inst SN74182N	IC 3
49A0104-000	Motorola MC3001P	IC 26
49A0106-000	Tex Inst SN74H53N	IC 5,10,11,16,17,22,23,27, 28,29,30,31,32,34,35,40, 42,43,44
49A0141-000	Tex Inst SN74174N	IC 36,37,38,39
49A0554-001	Tex Inst SN74H10N	IC 20
77S1011-000	Varian	CR 1 through 6

44P0593
Processor
Control
Number 4

49A0000-000	Tex Inst SN7475N	IC 40
49A0002-000	Tex Inst SN7473N	IC 4,18,19,43
49A0004-000	Tex Inst SN7440N	IC 2
49A0005-000	Tex Inst SN7410N	IC 10,13,44
49A0006-000	Tex Inst SN7420N	IC 21
49A0007-000	Tex Inst SN7400N	IC 5,12,42
49A0010-000	Fairchild SL18162	IC 1,29
49A0019-000	Tex Inst SN74H40N	IC 6,7,32
49A0020-000	Tex Inst SN74H72N	IC 8
49A0023-000	Tex Inst SN74H04N	IC 14,17,22
49A0025-000	Varian	A 1,2
49A0036-000	Tex Inst SN74H73N	IC 28
49A0038-000	Tex Inst SN74H22N	IC 15
49A0039-000	Tex Inst SN74H00N	IC 9,39,41,45
49A0042-000	Tex Inst SN74H01N	IC 3,27
49A0056-000	Tex Inst SN74H20N	IC 31,33,38
49A0077-000	Tex Inst SN74H60N	IC 24
49A0104-000	Motorola MC3001P	IC 16,25
49A0106-000	Tex Inst SN74H53N	IC 26
49A0127-000	Tex Inst SN74161N	IC 23
49A0138-000	Tex Inst SN7437N	IC 11,30,37
49A0146-000	Tex Inst SN74122N	IC 46
49A0554-001	Tex Inst SN74H10N	IC 20,34,35,36

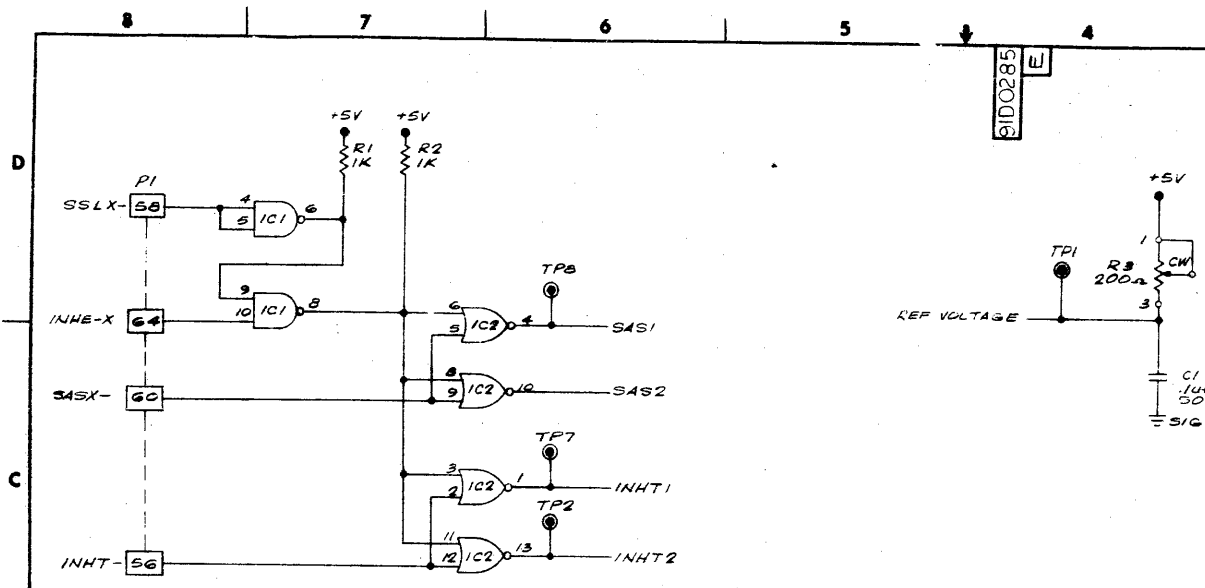
<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>	
44P0594 Multiply/ Divide, Extended Address	49A0000-000	Tex Inst SN7475N	IC 16	
	49A0002-000	Tex Inst SN7473N	IC 2,29	
	49A0006-000	Tex Inst SN7420N	IC 27	
	49A0007-000	Tex Inst SN7400N	IC 21,22	
	49A0008-000	Tex Inst SN15846N	IC 6,8,9,24,25	
	49A0009-000	Tex Inst SN15862N	IC 19	
	49A0010-000	Fairchild SL18162	IC 7	
	49A0011-000	Tex Inst SN15830N	IC 10,14,28	
	49A0036-000	Tex Inst SN74H73N	IC 15	
	49A0038-000	Tex Inst SN74H22N	IC 1,4,5,13,18	
	49A0039-000	Tex Inst SN74H00N	IC 17,32	
	49A0042-000	Tex Inst SN74H01N	IC 3,20	
	49A0128-001	Tex Inst SN7438N	IC 11,12,26,30,31	
	49A0142-000	Tex Inst SN7412N	IC 23	
	44P0595 Processor Control Number 1	49A0000-000	Tex Inst SN7475N	IC 18,19,26,27
		49A0002-000	Tex Inst SN7473N	IC 1,4,8,34
49A0004-000		Tex Inst SN7440N	IC 37	
49A0005-000		Tex Inst SN7410N	IC 36	
49A0007-000		Tex Inst SN7400N	IC 7,10	
49A0008-000		Tex Inst SN15846N	IC 2,5,12,13,15,16,20,24, 25,28,29,30,31,33	
49A0009-000		Tex Inst SN15862N	IC 11	
49A0011-000		Tex Inst SN15830N	IC 3,6	
49A0019-000		Tex Inst SN74H40N	IC 32	
49A0021-000		Tex Inst SN7401N	IC 14,21,22,23	
49A0039-000		Tex Inst SN74H00N	IC 9	
49A0042-000		Tex Inst SN74H01N	IC 17	
76N3055-000		2N3055	Q 1	
77N4730-000		IN4730A	CR 1	
77S1011-000		Varian	CR 2	
44P0596 Processor Control Number 2		49A0000-000	Tex Inst SN7475N	IC 41
	49A0002-000	Tex Inst SN7473N	IC 9,20,27	
	49A0004-000	Tex Inst SN7440N	IC 18,40,43	
	49A0005-000	Tex Inst SN7410N	IC 10,11,30,48	
	49A0006-000	Tex Inst SN7420N	IC 22,23,26,28,34	
	49A0007-000	Tex Inst SN7400N	IC 4,5,7,12,19,37	
	49A0008-000	Tex Inst SN15846N	IC 15,17,31,42	
	49A0009-000	Tex Inst SN15862N	IC 1,6,29	
	49A0010-000	Fairchild SL18162	IC 13,24,47	
	49A0011-000	Tex Inst SN15830N	IC 3,21,25	
	49A0019-000	Tex Inst SN74H40N	IC 32	
	49A0038-000	Tex Inst SN74H22N	IC 8,14,38	
	49A0039-000	Tex Inst SN74H00N	IC 36	
	49A0042-000	Tex Inst SN74H01N	IC 2,16,35,39,45	
	49A0056-000	Tex Inst SN74H20N	IC 33	
	77S1011-000	Varian	CR 1	

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>	
44P0597 Processor Control Number 3	49A0002-000	Tex Inst SN7473N	IC 2,12	
	49A0004-000	Tex Inst SN7440N	IC 1,5,9,15,18,20,24,29	
	49A0005-000	Tex Inst SN7410N	IC 4	
	49A0006-000	Tex Inst SN7420N	IC 10,14,43	
	49A0007-000	Tex Inst SN7400N	IC 13,42	
	49A0008-000	Tex Inst SN15846N	IC 11,23,27,28,32,33	
	49A0009-000	Tex Inst SN15862N	IC 6,31	
	49A0019-000	Tex Inst SN74H40N	IC 26,34,41	
	49A0038-000	Tex Inst SN74H22N	IC 37	
	49A0039-000	Tex Inst SN74H00N	IC 19,38	
	49A0042-000	Tex Inst SN74H01N	IC 7,16,21,22,30,36,39	
	49A0128-001	Tex Inst SN7438N	IC 3,8	
	49A0142-000	Tex Inst SN7412N	IC 25,35,40	
	49A0554-000	Tex Inst SN74H10N	IC 17,44	
	77S1011-000	Varian	CR 1,2	
	44P0598 Interrupt Trap	49A0002-000	Tex Inst SN7473N	IC 1,9
		49A0004-000	Tex Inst SN7440N	IC 24,28,32
49A0005-000		Tex Inst SN7410N	IC 40	
49A0007-000		Tex Inst SN7400N	IC 10,15	
49A0008-000		Tex Inst SN15846N	IC 3,6,12,14,16,20,23,27, 35,37,38	
49A0010-000		Fairchild SL18162	IC 2,8	
49A0011-000		Tex Inst SN15830N	IC 36	
49A0021-000		Tex Inst SN7401N	IC 39	
49A0036-000		Tex Inst SN74H73N	IC 19	
49A0038-000		Tex Inst SN74H22N	IC 7	
49A0039-000		Tex Inst SN74H00N	IC 18,22,26,30,31,34	
49A0042-000		Tex Inst SN74H01N	IC 13,17,21,25,29,33	
49A0056-000		Tex Inst SN74H20N	IC 4,11	
49A0142-000		Tex Inst SN7412N	IC 5	
77S1011-000		Varian	CR 1,2	
44P0599 Memory Timing and Control		49A0003-000	Tex Inst SN7472N	IC 9
		49A0019-000	Tex Inst SN74H40N	IC 17
	49A0023-000	Tex Inst SN74H04N	IC 7,13	
	49A0039-000	Tex Inst SN74H00N	IC 6,11	
	49A0041-000	Tex Inst SN74H51N	IC 16	
	49A0056-000	Tex Inst SN74H20N	IC 12	
	49A0079-000	Tex Inst SN74H54N	IC 8,15	
	49A0146-000	Tex Inst SN74122N	IC 14	
	49A0554-001	Tex Inst SN74H10N	IC 10	
	77S1017-000	Fairchild EDN400	CR 1	

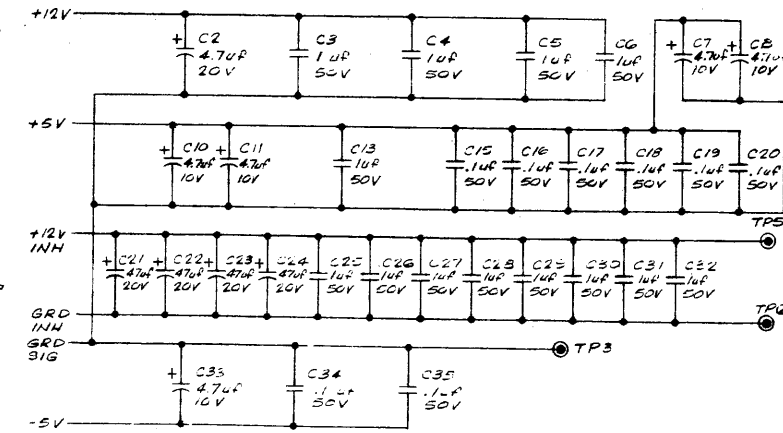
<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0640 Memory Timing and Control	49A0003-000	Tex Inst SN7472N	IC 9
	49A0019-000	Tex Inst SN74H40N	IC 17
	49A0023-000	Tex Inst SN74H04N	IC 7,13
	49A0039-000	Tex Inst SN74H00N	IC 6,11
	49A0041-000	Tex Inst SN74H51N	IC 16
	49A0056-000	Tex Inst SN74H20N	IC 12
	49A0079-000	Tex Inst SN74H54N	IC 8,15
	49A0146-000	Tex Inst SN74122N	IC 14
	49A0554-001	Tex Inst SN74H10N	IC 10
	77S1017-000	Fairchild EDN400	CR 1
44P0683 Priority Interrupt Module	49A0002-000	Tex Inst SN7473N	C 6,7; D 6,7
	49A0012-000	Tex Inst SN7474N	B 3; C 2,3,4
	49A0022-000	Tex Inst SN74H11N	D 4
	49A0023-000	Tex Inst SN74H04N	C 1; E 3
	49A0036-000	Tex Inst SN74H73N	F 2
	49A0039-000	Tex Inst SN74H00N	E 1,6,7; F 3
	49A0040-000	Tex Inst SN7404N	A 1; B 1,2,3; E 5
	49A0056-000	Tex Inst SN74H20N	A 4,5; C 5; D 1,5
	49A0082-001	Tex Inst SN74H74N	A 6,7; B 6,7; F 1
	49A0093-001	Tex Inst SN74H50N	D 2
	49A0094-001	Tex Inst SN74H21N	E 4
	49A0104-000	Motorcla MC3001P	D 3; F 6,7
	49A0128-001	Tex Inst SN7438N	A 2,3
	49A0138-000	Tex Inst SN7437N	E 2
	49A0554-001	Tex Inst SN74H10N	B 4

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
2. POWER SUPPLY			
44P0518 Heat Sink Board	76N3055-000	2N3005	Q 1,2
44P0526 Power Supply Board	76A0009-002 77N4001-004 77N4003-000 82A0030-001	Motorola MR751 IN4005 IN4003 Potter Brumfield KUP14AE6-115 VAC	CR 5,6,7,8,9,10,11,13 CR 15,16 CR 12,14 K 1
44P0528 Regulator Board	49A0103-001 76A0007-000 76A2904-000 77A0004-000 77N0751-000 77N4003-000	Fairchild U5R7723393 2N3054 2N2904 IN746A Tex Inst IN751A IN4003	IC 1,2,3,4 Q 1,2,3,5 Q 4 CR 6 CR 3 CR 4,5
83P0035 Power Supply	76A0008-000 77A0005-000	Motorola MR1121 Motorola MCR3935-2	CR 1,2,3,4 Q 1
3. CONTROLLER CARDS			
44P0013 Teletype Controller	49A0002-000 49A0004-000 49A0007-000 49A0008-000 49A0009-000 49A0010-000 49A0012-000 76A2369-000 76S1072-000 77S1017-000 82A0006-000 82A0006-003	Tex Inst SN7473N Tex Inst SN7440N Tex Inst SN7400N Tex Inst SN15846N Tex Inst SN15862N Fairchild SL18162 Tex Inst SN7474N 2N2369 Varian Fairchild EDN400 Aztec 20229 Aztec 20213	IC 6,10,11,22,23,27,28, 30,31,36,41 IC 25,40 IC 2,3,42 IC 1,4,13,14,15,17,19,20, 24,29,34,35,37,43 IC 5,38 IC 7,8,9,12,21,39,44 IC 16,18,26,32,33 Q 1 through 4 Q 1 through 4 CR 1 through 10 K 2 K 1

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0026 Buffer Interlace Controller	49A0000-000	Tex Inst SN7475N	IC 14,28,42,56
	49A0002-000	Tex Inst SN7473N	IC 57
	49A0004-000	Tex Inst SN7440N	IC 15,17,37
	49A0005-000	Tex Inst SN7410N	IC 31
	49A0007-000	Tex Inst SN7400N	IC 4,5,16,38
	49A0008-000	Tex Inst SN15846N	IC 2,6,7,13,20,21,23,27,34, 35,41,43,48,49,55,62,63
	49A0010-000	Fairchild SL18162	IC 1,18,19,29,32,33,46,47, 50,51,58,60,61
	49A0011-000	Tex Inst SN15830N	IC 3
	49A0012-000	Tex Inst SN7474N	IC 11,12,25,26,39,40,53,54
	49A0014-000	Tex Inst SN15850N	IC 8,9,10,22,24,30,36,44, 45,52,59
44P0689 Buffer Interlace Controller	49A0000-000	Tex Inst SN7475N	A 7; B 7; C 7; D 7
	49A0002-000	Tex Inst SN7473N	E 6; F 2; K 4
	49A0012-000	Tex Inst SN7474N	F 6; H 7; K 3
	49A0022-000	Tex Inst SN74H11N	E 4; H 2
	49A0023-000	Tex Inst SN74H04N	A 4; C 4; D 5; E 5; K 5
	49A0039-000	Tex Inst SN74H00N	K 5
	49A0040-000	Tex Inst SN7404N	A 1; B 1; C 1; D 3
	49A0042-000	Tex Inst SN74H01N	A 6; B 6; C 6; D 6
	49A0082-001	Tex Inst SN74H74N	E 2
	49A0093-001	Tex Inst SN74H50N	E 3; F 5,7
	49A0094-001	Tex Inst SN74H21N	H 3
	49A0104-000	Motorola MC3001P	F 1,4; H 6; K 6
	49A0127-000	Tex Inst SN74161N	A 3; B 3; D 4
	49A0128-001	Tex Inst SN7438N	A 2; B 2; C 2; D 1,2; E 1; H
49A0178-000	Tex Inst SN74175N	H 1	
49A0554-001	Tex Inst SN74H10N	F 3	
44P0176 Paper Tape Controller	49A0000-000	Tex Inst SN7475N	IC 8,14
	49A0002-000	Tex Inst SN7473N	IC 52
	49A0004-000	Tex Inst SN7440N	IC 16,25,31,37,40,42, 45,48,51,55
	49A0008-000	Tex Inst SN15846N	IC 1,6,7,10,12,13,15, 18,19,24,27,29,30, 34,36,38,47,49,50,57
	49A0009-000	Tex Inst SN15862N	IC 21,41,53
	49A0010-000	Fairchild SL18162	IC 5,11,17,23,26,32,43
	49A0011-000	Tex Inst SN15830N	IC 35
	49A0014-000	Tex Inst SN15850N	IC 4,28,33,39,44
	49A0018-000	Tex Inst SN15851N	IC 22
	49A0042-000	Tex Inst SN74H01N	IC 2,3,9,20
49A0146-000	Tex Inst SN74122N	IC 46,56	
44P0601 Bootstrap Loader	49A0007-000	Tex Inst SN7400N	IC 2
	49A0039-000	Tex Inst SN74H00N	IC 18
	49A0508-000	Tex Inst SN15846J	IC 4
	49A0510-000	Fairchild SL13016	IC 1,5
	49A0516-000	Motorola MC833L	IC 3



REV	DATE	DESCRIPTION	APPROVED	BY
X		PROTOTYPE RELEASE		
X		PROTOTYPE RELEASE; REVISED ALL CIRCUITS		
A	1/10	PRODUCTION RELEASE PER 5310		
B	1/14	TPI WAS ON SIGNAL SAS2 EN 5305		
C	3/11	REMOVED RESISTOR R1 1K 5% 2.2K 5% WAS 2.2K; ADDED NOTE FOR PLE IN BOARD		
D	5/11	REVISED DHT 4 PER EN 51270		
E	5/11	.0025UF WAS .0029UF 9 14.5K 1% 3W WAT 12 D 1% 3W. REVISED PER 531307		



5. VALUE FOR INDICATED RESISTORS IS: FOR 44P0506-000 ASSY 2.2K OHMS FOR 44P0506-001 ASSY 4.3 OHMS
- POWER DISTRIBUTION TO IC1 THRU IC12: PIN 7 - GRD PIN 14 - +5V
 - PREFIX COMPONENT REF DESIGN NO. WITH CIRCUIT NUMBER EXAMPLE: CKT 1 - R101, R102 CKT 2 - R201, R202.
 - COMPONENT VALUE TO BE DETERMINED BY FINAL TEST.
 - ALL RESISTOR VALUES ARE IN OHMS, 1/4 W, 5%.
- NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DESIGNATIONS	
LAST USED	NOT USED
C35, C1104	C9, 12, 14
R21, R1217	
CR1205	
IC12, IC1101	
Q1204	
A1203	
TP8	
P1	
J1	

REFERENCE DRAWINGS	
40D0454	P.W. BOARD
44E0506	ASSEMBLY
44P0506	PARTS LIST
97D0533	ARTWORK
97D0555	SILKSCREEN
97D0556	SOLDER MASK

620/L
44E0506

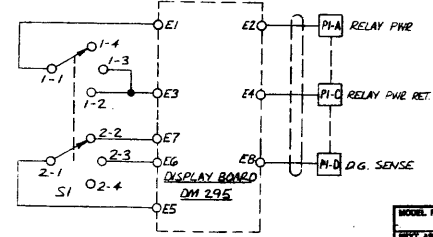
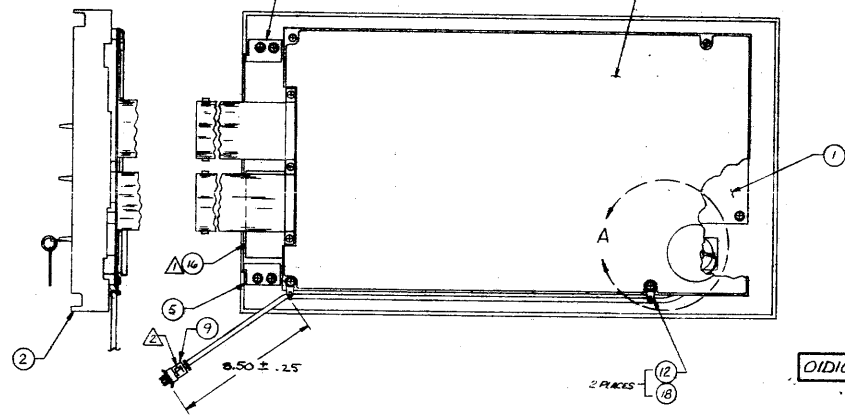
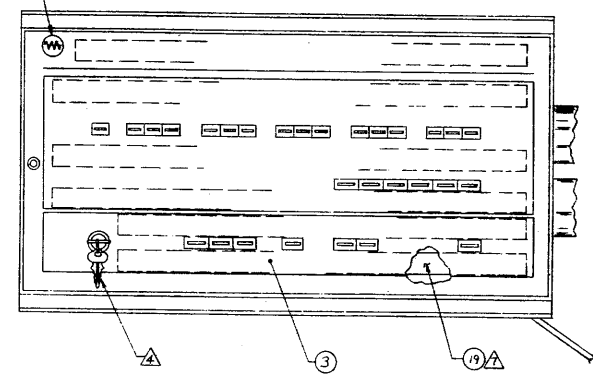
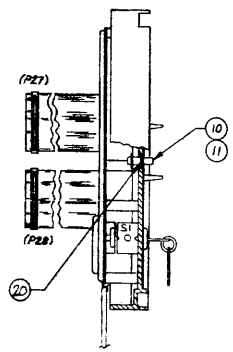
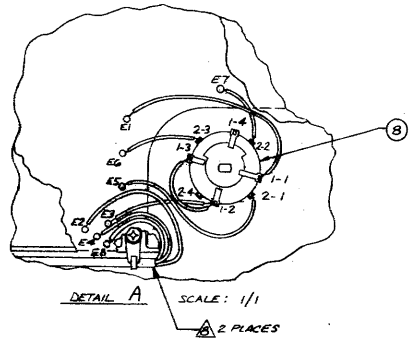
LOGIC DIAG
SEN: INH
DM288

21101 D 91D0285

91D0285

WIRE LIST			
FROM	TO	FIND NO.	COLOR
E5	2-1	15	WHY
E6	2-3	15	WHY
E8	1-2	15	WHY
E9	1-2	15	WHY
E10	PI-A	17	RED
E11	PI-C	17	BLEN
E12	PI-D	17	ORNG
E13	2-E	15	WHY
E14	1-1	15	WHY

REVISIONS			
SYM	DATE	DESCRIPTION	APPROVED
A		PRODUCTION RELEASE PER EN 5324	
B	JM	ADDED TO WIRE LIST PER EN 6550	
B	JM	8.522.25 WAS 4.752.25 PER EN 5884	
C	JM	ADDED FIND NO. 20 TO FIND ZONE C7 PER EN 60184	



- ▲ CUT SHIELD BACK FLUSH WITH INSULATION, TYP BOTH ENDS
 - ▲ APPLY TAPE (FIND NO 19) BETWEEN FIND NOS. 2 & 3 AS READ.
 - ▲ SPOT TIE AS REQUIRED.
 - ▲ ROUTING OF WIRES IS SHOWN FOR REFERENCE ONLY.
 - ▲ BAG KEYS (PART OF F/N B 1) AND ATTACH SECURELY TO ASSEMBLY.
 - ▲ USE HARDWARE IN ACCORDANCE WITH 98A0536.
 - ▲ "PI" REFERENCE DESIGNATION SHOWN FOR INFORMATION ONLY AND DOES NOT APPEAR ON PART
 - ▲ MAKE PART NO. 0101036-000 & THE REV. LETTER OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED A SERIAL NO. OF ASSEMBLY IN .12 HIGH CHARACTERS ON FIND NO. 16; ATTACH TO FIND NO. 2 APPROX WHERE SHOWN.
- NOTE: UNLESS OTHERWISE SPECIFIED

FOR PARTS LIST SEE 0101036

MODEL NO. 620/L	DIMENSIONS ARE IN INCHES AND AFTER FINISHING	DR. KLUSIS	DATE 1/17/61
NEXT ASSY 0101035	TOLERANCES UNLESS OTHERWISE SPECIFIED	CHK. ZAD	DATE 1/17/61
MATERIAL	X ± .1	ENGR. MARCH	DATE
FINISH	XX ± .05	APPD. STANLEY	DATE
	XXX ± .010	APPD. STANLEY	DATE
	ANGLES ± 0.5°	THIS DOCUMENT MUST CONTAIN PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT WITHOUT WRITTEN PERMISSION FROM YDM	
	BREAK ALL SHARP EDGES AND R APPROX	CODE IDENT NO. 842	DATE 0101036
	DO NOT SCALE DRAWING	SCALE 1/2	SHEET 1 OF 1

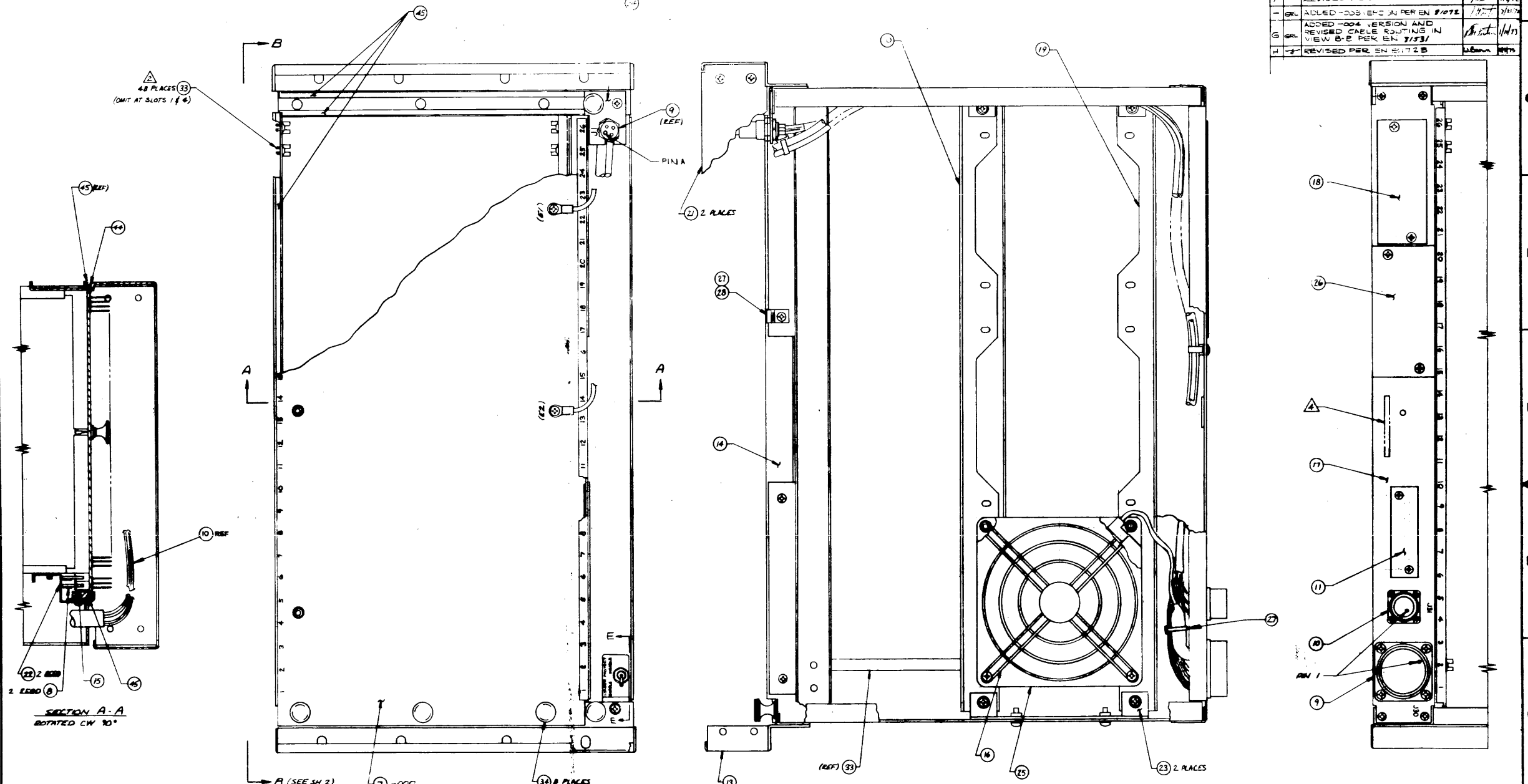
FOR PARTS LIST SEE 0101036

FRONT PANEL ASSY

11101 D 0101036

WIRE LIST		
FROM	TO	COLOR
J31-1	J17-113	PURPLE
J31-2	J17-112	GREEN
J31-3	J17-114	YELLOW

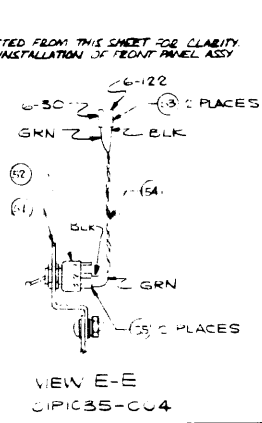
REVISIONS			
REV. NO.	DESCRIPTION	APPROVED	DATE
A	PRODUCTION RELEASE PER EN 5547	W/S	1/27/73
B	REVISED PER EN 5547	W/S	1/27/73
C	ADDED CABLE CLAMP AND SERVICE LOOP PER E.N. 5584	W/S	1/27/73
D	2 INCH DEPTH-LEADED WIRE BUNDLE PINS TO BE USED PER EN 5584	W/S	1/27/73
E	REVISED PER EN 5547	W/S	1/27/73
F	REVISED PER EN 5547	W/S	1/27/73
G	ADDED -004 VERSION AND REVISED CABLE ROUTING IN VIEW E-E PER EN 5178	W/S	1/27/73
H	REVISED PER EN 5178	W/S	1/27/73



SECTION A-A
ROTATED CW 90°

1 - 000
3 - 002, 003 & 004

NOTE: FRONT PANEL ASSY OMITTED FROM THIS SHEET FOR CLARITY. SEE SHEET 2 FOR INSTALLATION OF FRONT PANEL ASSY.



VIEW E-E
OIP1035-CU4

TABULATION BLOCK		
VDM PART NO.	DESCRIPTION	NEXT ASSY
OIP1035-000	FRAME ASSY, 16 BIT	OIP1035-001
OIP1035-001	FRAME ASSY, 16 BIT	OIP1035-002
OIP1035-002	FRAME ASSY, 16 BIT	OIP1035-003
OIP1035-003	FRAME ASSY, 16 BIT	OIP1035-004
OIP1035-004	FRAME ASSY, 16 BIT WITHOUT FRONT PANEL	

1. MODIFY PIN 3 WIRING FOR OIP1035-004 VERSION ONLY PER WIRE LIST 75W0985 USING PIN 56.

△ CABLE CLAMP (PART NO. 5) IS TO BE INSTALLED ON WIRE WITH END OF WIRE TO WIRING IN WHICH INSTALLED BY FRONT PANEL ASSY (REFER TO WIRE PART NO. OIP1035-APPENDIX 1 FOR LTR. OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED APPROX. WHERE SHOWN IDENTIFICATION TO BE 2, HIGH CHARACTERS & PERMANENT.

△ TERMINATE J17 END OF WIRE WITH ITEM 32. COVER WITH ITEM #2 APPROX. .75 LONG AFTER INSTALLATION TO CONNECTOR PLATE.

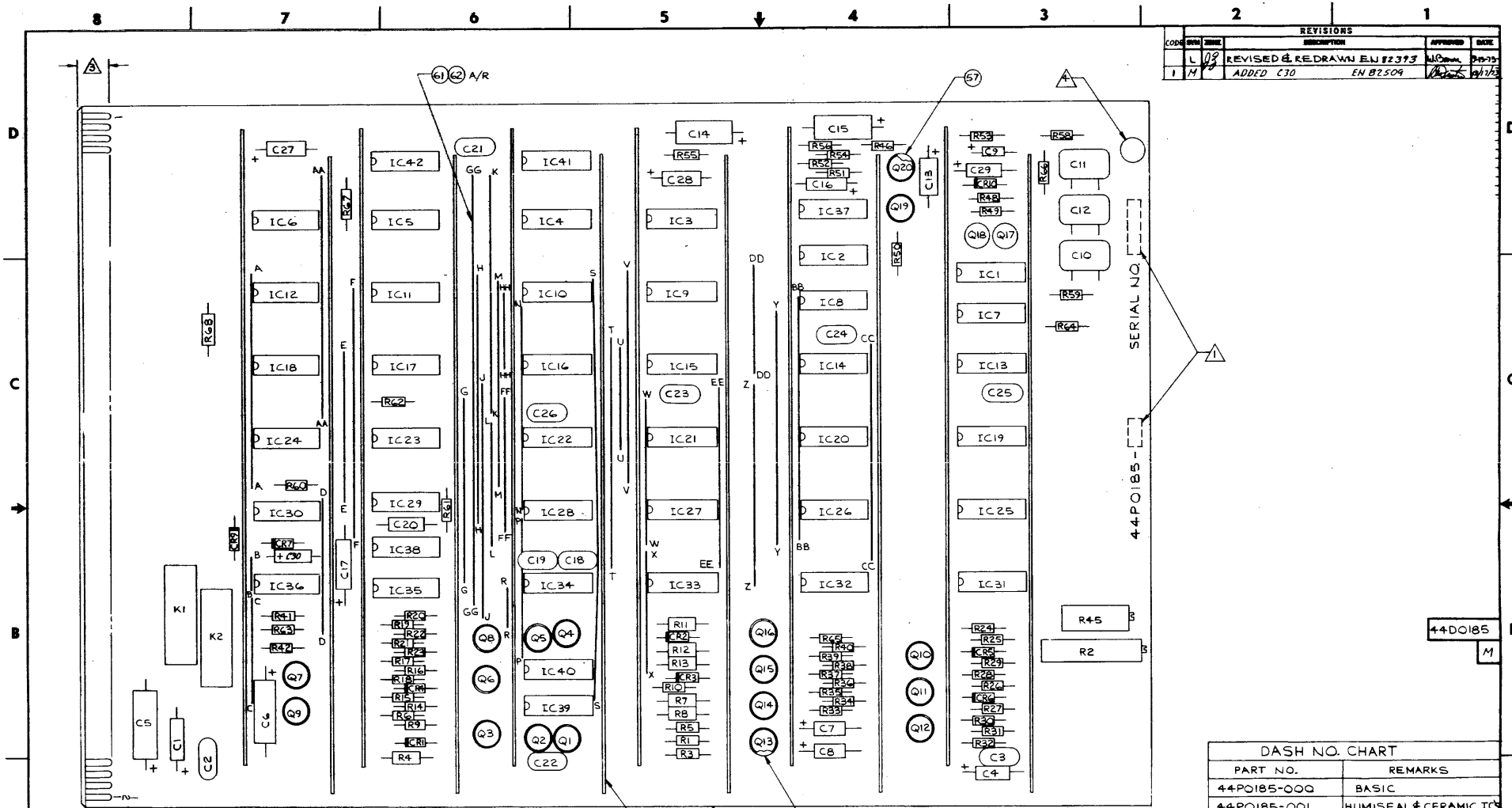
△ INSTALL CARDBRIDGES (ITEM 33) TO MATCH CONNECTORS ON CONNECTOR PLATE (ITEM 7) PRIOR TO INSTALLATION OF CONNECTOR PLATE.

1. USE HARDWARE IN ACCORDANCE WITH 75W0536

NOTE: UNLESS OTHERWISE SPECIFIED

FOR PARTS LIST, SEE OIP1035

FORM NO. 6204/6204-100	REV. 1/73	DATE	11/27/73
101234	01A1277		
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND IS UNCLASSIFIED FOR ANY PURPOSE OR USED TO PROMOTE THE SELLER OR SUBJECT WITHOUT WRITTEN PERMISSION FROM VDM.		TITLE FRAME ASSEMBLY	CODE IDENT NO. 21101 E PART NO. OIE1035
SCALE 1/1		SHEET 1 OF 2	



REVISONS			
CODE	DATE	DESCRIPTION	APPROVED
L	1/19	REVISED & REDRAWN EU 82373	W.C. ...
M		ADDED C30	EN 82504

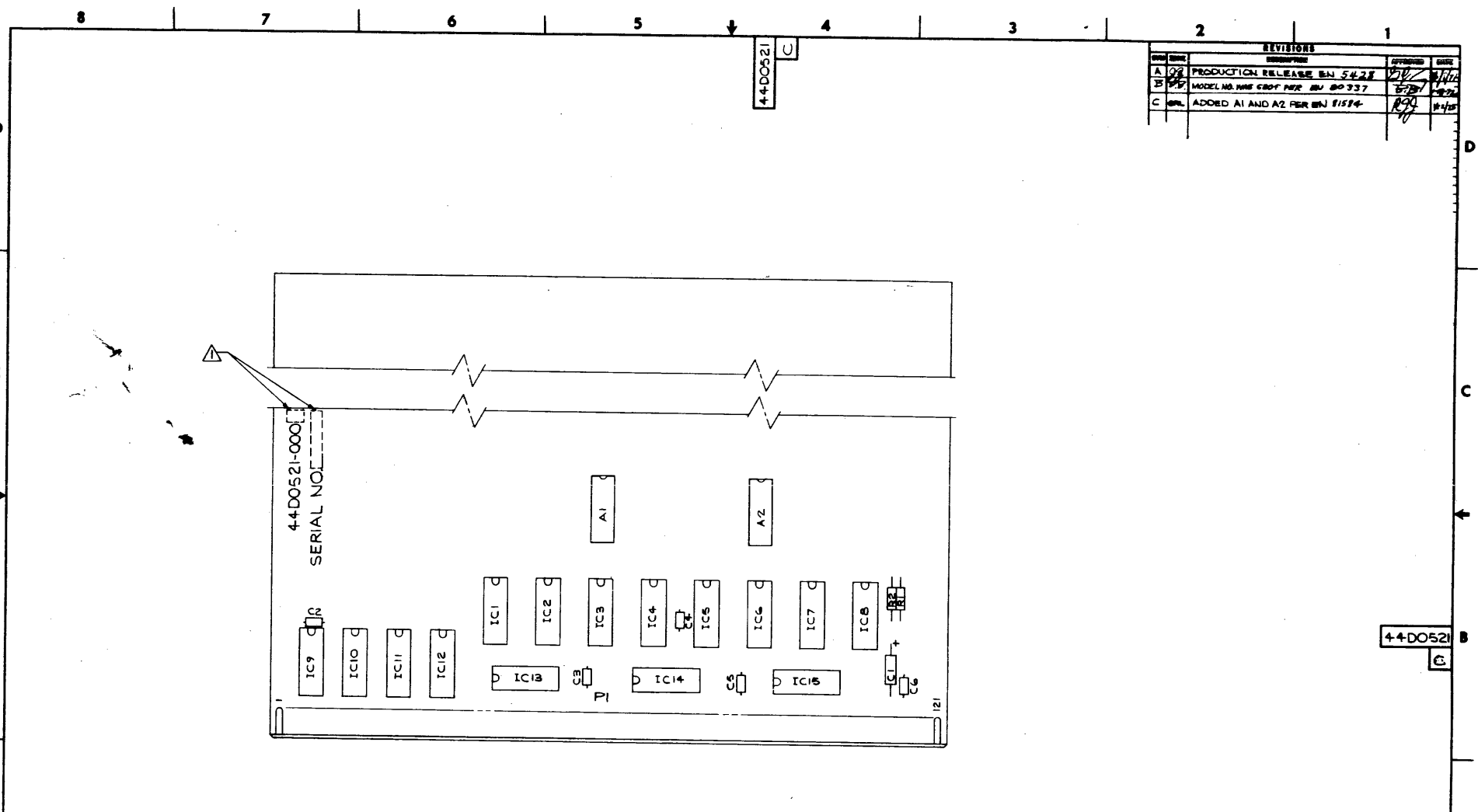
DASH NO. CHART	
PART NO.	REMARKS
44-PO185-000	BASIC
44-PO185-001	HUMISEAL & CERAMIC IC'S

FOR PARTS LIST SEE 44-PO185

- MAXIMUM COMPONENT HEIGHT TO BE .45
 - PERMANENTLY APPLY 3 YELLOW DOT (-001 ONLY) TO BOARD APPROX WHERE SHOWN.
 - AFTER TEST AND PRIOR TO ACCEPTANCE TEST (-001 ONLY) MASK OFF CONNECTOR CONTACT AREA ON BOTH SIDES OF P/N 1. TOTALLY COAT BOTH SIDES OF ASSY WITH F/N 74.
 - FOR ENGR DESCR OF REAL TIME CLOCK OPTION (6202-13) SEE DWG 98A010G.
 - MARK PART WITH REVISION LETTER OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED, AND SERIAL NUMBER, APPROXIMATELY WHERE SHOWN. IDENTIFICATION TO BE .12 HIGH CHARACTERS PERMANENT & LEGIBLE.
- NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 97D0642 SILKSCREEN
 97D0641 SOLDER MASK
 44-PO185 PARTS LIST
 40D0201 BOARD DETAIL
 97D0134 PHOTOMASTER
 95D0135 LOGIC DIAGRAM

MODEL NO. 6202-13/14	DESIGNER DR. J. NAUGHTON	DATE 7/22/68	VERSION DATA 1/1
TEST UNIT	CHECKER C.H. JEVERS	DATE 7/22/68	REVISION 1
MATERIAL	DESIGN APPROVED DR. J. NAUGHTON	DATE 7/11/68	
	ENGR. APPROVED EMMETT E. HANSON	DATE 7/11/68	
	APPROVED APPO		
	THIS DOCUMENT MAY CONTAIN PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO REPRODUCE THE ARTICLE OR SUBJECT, WITHOUT WRITTEN PERMISSION FROM VON		
	CODE IDENT NO. 21101	SIZE D	DWG NO. 44-DO185
	SCALE		SHEET 1 OF



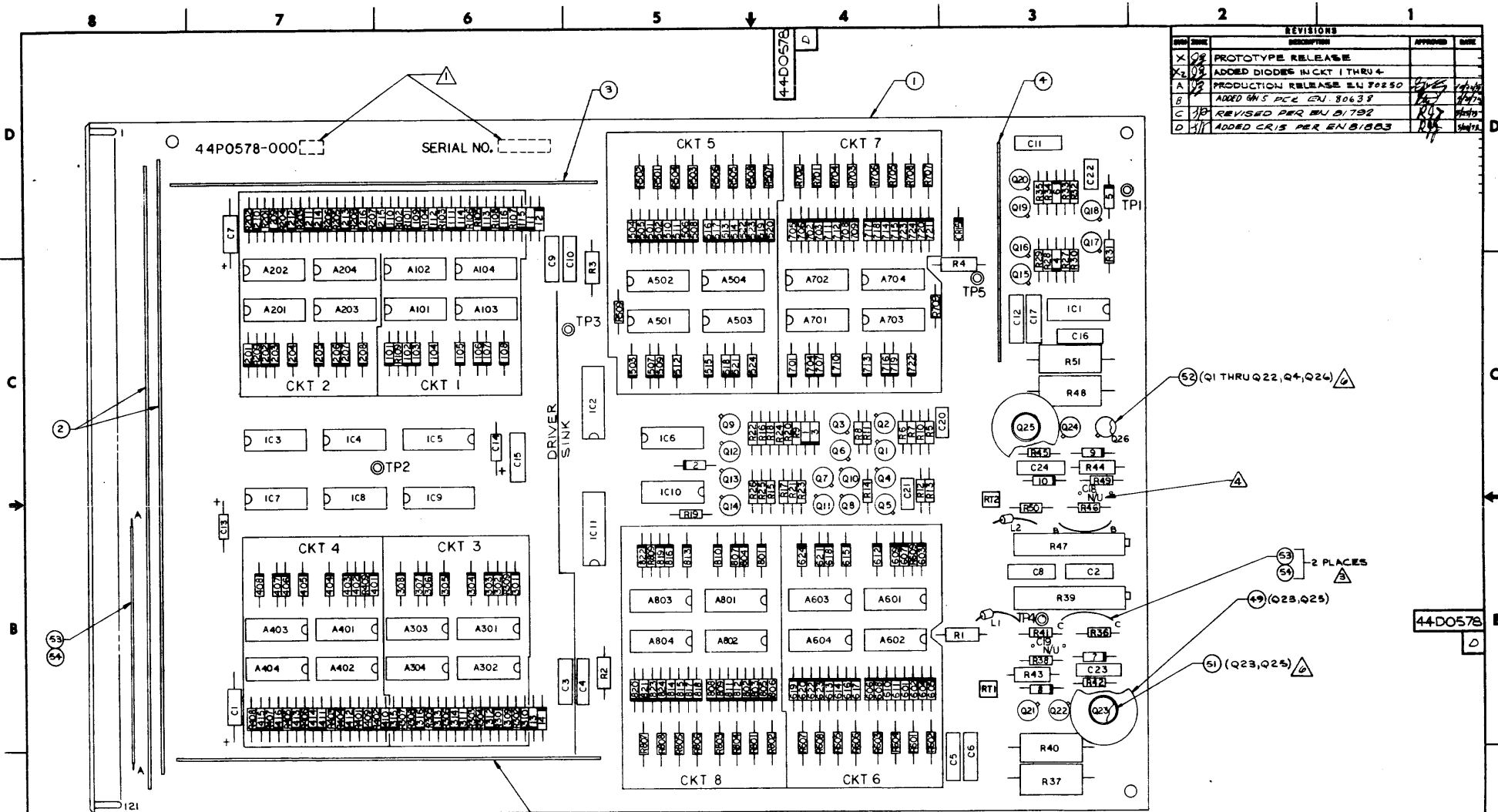
REVISIONS				
REV	DATE	DESCRIPTION	APPROVED	DATE
A	08	PRODUCTION RELEASE ENJ 5428	[Signature]	11/72
B	07	MODEL NO. HAS CHG ^d PER INJ 80337	[Signature]	10-72
C	08	ADDED A1 AND A2 PER INJ 81594	[Signature]	11-72

FOR PARTS LIST SEE 44P0521

- REFERENCE DRAWINGS
- 44D0469 BOARD DETAIL
 - 91D0297 LOGIC DIAGRAM
 - 97D0566 ARTWORK
 - 97D0567 SILKSCREEN
 - 97D0568 SOLDER MASK

DRAWING NO. 620 L REV. NO. 01P1101 DATE DESIGNED BY CHECKED BY APPROVED BY IN CHARGE	DIMENSIONS ARE TO BE USED AND AFTER FINISHING TOLERANCES UNLESS OTHERWISE SPECIFIED X .1 .25 .18 .38 .18 .48 .18 .58 .18 .68 .18 .78 .18 .88 .18 .98 .18 .10 .18 .11 .18 .12 .18 .13 .18 .14 .18 .15 .18 .16 .18 .17 .18 .18 .18 .19 .18 .20 .18 .21 .18 .22 .18 .23 .18 .24 .18 .25 .18 .26 .18 .27 .18 .28 .18 .29 .18 .30 .18 .31 .18 .32 .18 .33 .18 .34 .18 .35 .18 .36 .18 .37 .18 .38 .18 .39 .18 .40 .18 .41 .18 .42 .18 .43 .18 .44 .18 .45 .18 .46 .18 .47 .18 .48 .18 .49 .18 .50 .18 .51 .18 .52 .18 .53 .18 .54 .18 .55 .18 .56 .18 .57 .18 .58 .18 .59 .18 .60 .18 .61 .18 .62 .18 .63 .18 .64 .18 .65 .18 .66 .18 .67 .18 .68 .18 .69 .18 .70 .18 .71 .18 .72 .18 .73 .18 .74 .18 .75 .18 .76 .18 .77 .18 .78 .18 .79 .18 .80 .18 .81 .18 .82 .18 .83 .18 .84 .18 .85 .18 .86 .18 .87 .18 .88 .18 .89 .18 .90 .18 .91 .18 .92 .18 .93 .18 .94 .18 .95 .18 .96 .18 .97 .18 .98 .18 .99 .18 .100 .18	THIS DOCUMENT IS THE PROPERTY OF THE COMPANY AND SUCH INFORMATION SHOULD NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO REPRODUCE THE ARTICLE OR SUBJECT WITHOUT WRITTEN PERMISSION FROM YOU BREAK ALL SHARP EDGES AND R APPEAR DO NOT SCALE DIMENSIONS	TITLE MEMORY BUFFER BOARD ASSY DRAWING NO. 21101 D REV. NO. 44D0521 C SCALE 2/1 SHEET 1 OF 1
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△ MARK REVISION LETTER OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED AND THE SERIAL NUMBER OF THE ASSEMBLY APPROX WHERE SHOWN. IDENTIFICATION TO BE .12 HIGH CHARACTERS AND PERMANENT.
 NOTE: UNLESS OTHERWISE SPECIFIED



REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
X	9/8	PROTOTYPE RELEASE	
X	9/8	ADDED DIODES IN CKT 1 THRU 4	
A	9/8	PRODUCTION RELEASE EN 70250	
B		ADDED 4N5 PER EN 80639	
C	10/8	REVISED PER BU 81792	
D	11/1	ADDED CR13 PER EN 81883	

5. INSTALL FREON SENSITIVE ITEMS (R39, R47) AFTER NORMAL CLEANING PROCESS
- ▲ N/U DENOTES COMPONENT NOT USED
- ▲ JUMPERS BETWEEN POINTS "B" & "C" TO BE 2.5 ± .25 LONG
2. PREFIX DIODE REFERENCE DESIGNATIONS WITH "CR" AS SHOWN. EXAMPLE: ~~101~~ IS CR101
- ▲ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED AND SERIAL NO. USING .12 HIGH PERMANENT CHARACTERS APPROX WHERE SHOWN.
- NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 40D0497 BOARD DETAIL
 91C0346 LOGIC DIAGRAM
 97D0673 ARTWORK
 97D0672 SILK SCREEN
 97D0671 SOLDERMASK

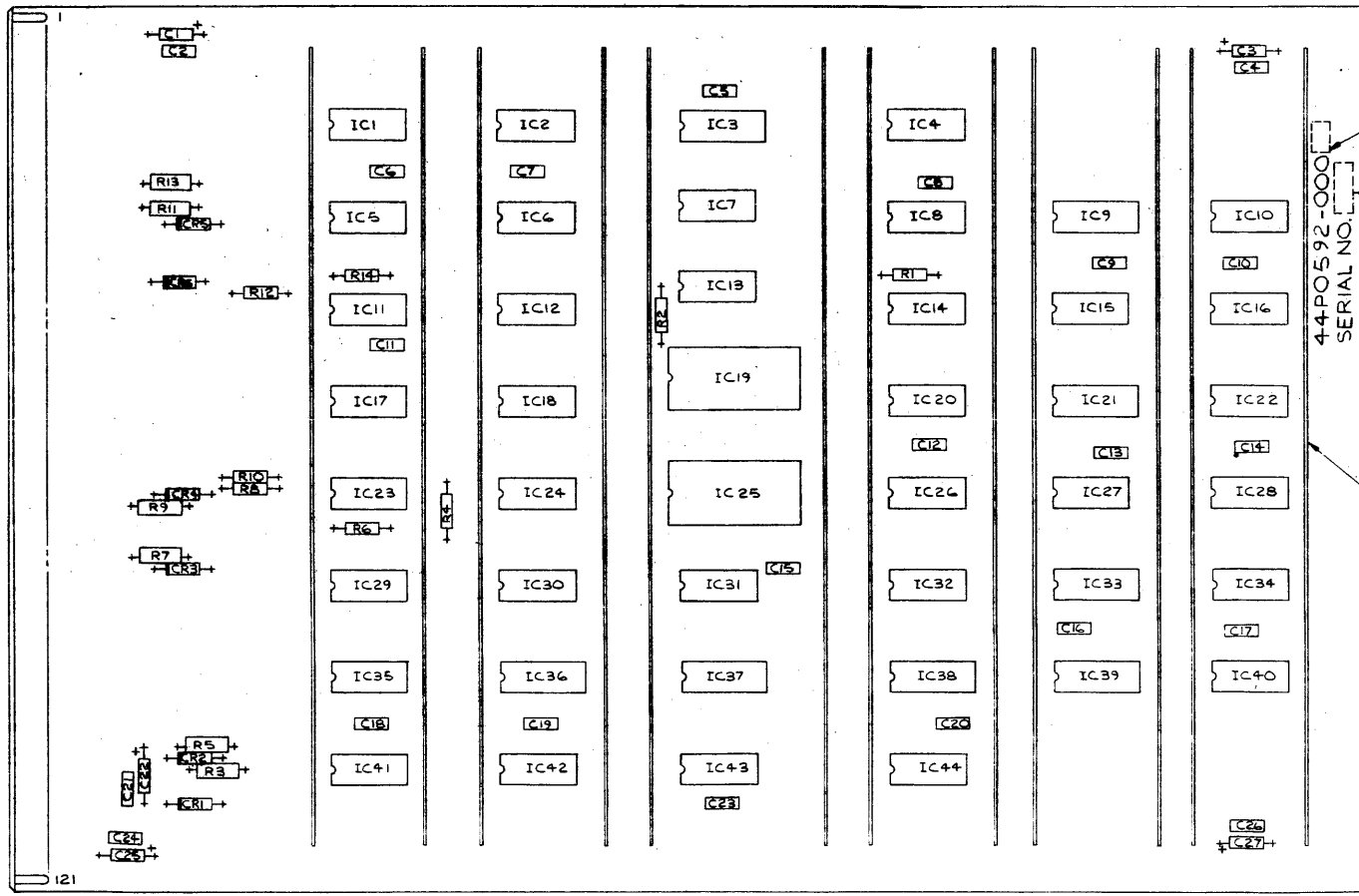
▲ USE OF TRANSISTOR MOUNTING PADS OPTIONAL

FOR PARTS LIST SEE 44P0578

SERIAL NO. G20/E-100 CHECK MARK OPI247	DIMENSIONS ARE IN INCHES AND DECIMALS THEREAFTER UNLESS OTHERWISE SPECIFIED TOLERANCES UNLESS OTHERWISE SPECIFIED X .1 XX .05 XXX .01 FINISHES AS SHOWN	DR. <i>[Signature]</i> 11/21/51 ENGR. <i>[Signature]</i> 11/21/51 APPR. <i>[Signature]</i> 11/21/51 APPR. <i>[Signature]</i> 11/21/51	TITLE DRIVER/SINK SWITCH ASSY
THIS DOCUMENT AND ANY PROPRIETARY INFORMATION AND DATA CONTAINED HEREIN ARE UNCLASSIFIED EXCEPT WHERE SHOWN OTHERWISE.		COGN. CONT. NO. 21101 SHEET D OF 1	PART NO. 44DO578 SCALE 2/1 SHEET 1 OF 1

44DO592
A

REVISIONS				
REV	DATE	DESCRIPTION	APPROVED	DATE
X1	2/2	PROTOTYPE RELEASE		
X2	CW	REVISED NOTE 1 & ADDED NOTE 2	T. E. H.	11-18-71
X3	CW	MOVED CR6	T. E. H.	1-18-72
A		PRODUCTION RELEASE EN 22391	JH	2/4/72



MARK SERIAL NO. WHERE SHOWN USING .12 HIGH PERMANENT CHARACTERS.

MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED APPROX. WHERE SHOWN. IDENTIFICATION TO BE .12 HIGH CHARACTERS AND PERMANENT.

NOTE: UNLESS OTHERWISE SPECIFIED

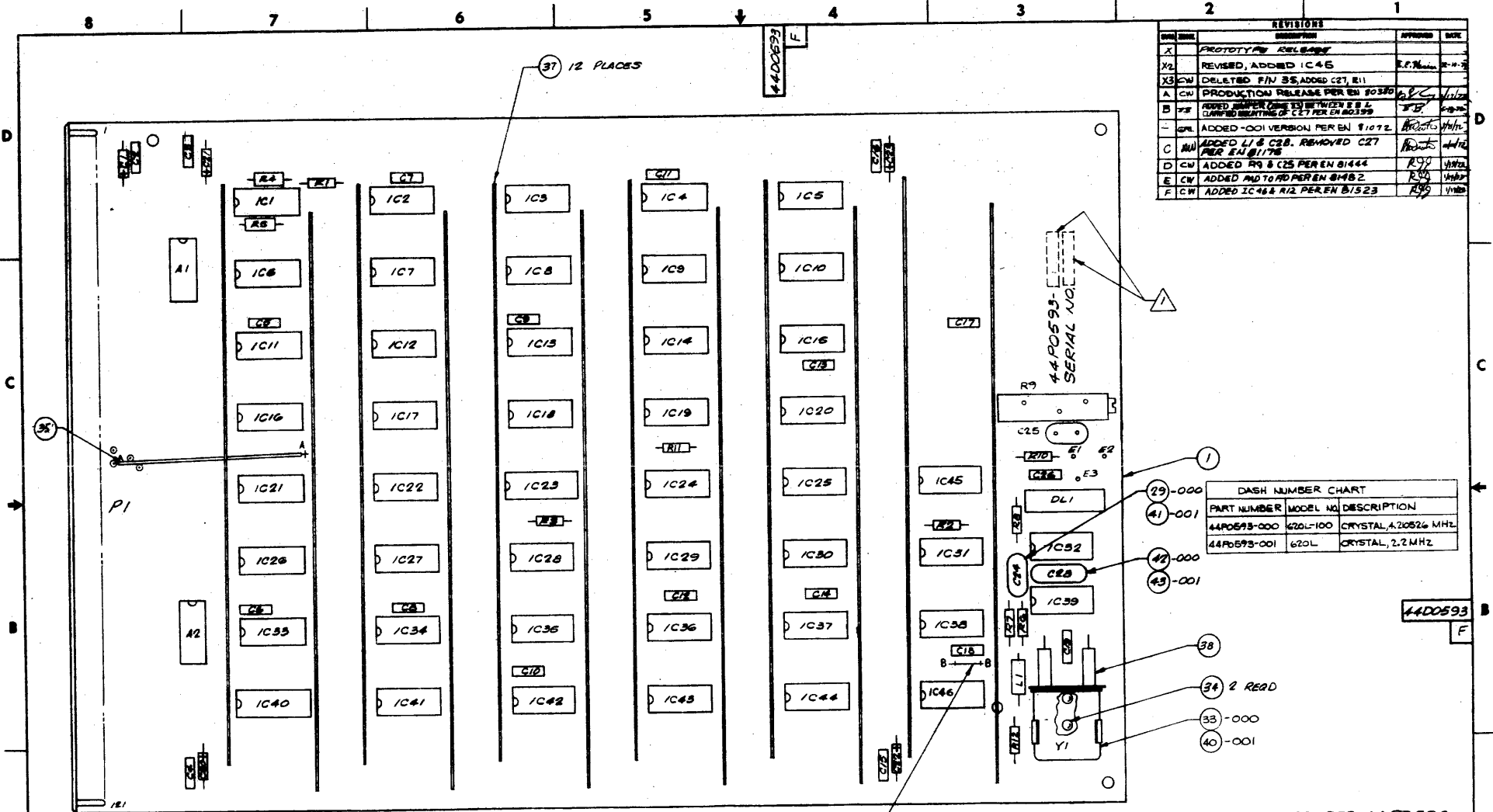
REFERENCE DRAWINGS
 40DQ505 PW BOARD
 97D0696 SOLDER MASK
 97D0495 SILKSCREEN
 97D0694 ARTWORK
 91C0356 LOGIC DIAGRAM

MODEL NO. 620/L-100	DATE 2/2/72	DESIGNER T. E. H.	DRW NO. 11/10
MFG. PART NO. CIP 1277	DATE 11/12/71	DESIGN T. E. H.	DRW NO. 11/10
MATERIAL	APPROVED T. E. H.	DATE 1-12-72	DRW NO. 11/10
FINISH	APPROVED T. E. H.	DATE 1-12-72	DRW NO. 11/10
DO NOT SCALE DRAWING	DIMENSIONS ARE IN INCHES AND AFTER FINISHING UNLESS OTHERWISE SPECIFIED TOLERANCES ANGLES OTHERWISE SPECIFIED X .1 XX .05 XXX .010 ANGLES ± .5° BREAK ALL SHARP EDGES AND R APPROX. THIS DOCUMENT MAY CONTAIN PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT WITHOUT WRITTEN PERMISSION FROM VOM.		
REGISTER CARD ASSY DM336		CODE IDENT NO. 21101	SIZE D
SCALE 2/1		SHEET OF 4	44DO592

FOR PARTS LIST SEE 44PO592

44DO592
A

**CAUTION: EN 82268
AFFECTS THIS DWG.**



REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
X1		PROTOTYPE RELEASE	
X2		REVISED, ADDED IC46	R.F. M...
X3		DELETED P/N 35, ADDED C27, R11	
A		PRODUCTION RELEASE PER EN 80380	
B		ADDED JUMPER (SEE TEST) BETWEEN P1 & 2 CLAMPED BEHIND C27 PER EN 80339	
C		ADDED -001 VERSION PER EN 81072	
D		ADDED L1 & C28. REMOVED C27 PER EN 81178	
E		ADDED R9 & C25 PER EN 81444	
F		ADDED R10 TO R12 PER EN 81482	
G		ADDED IC46 & R12 PER EN 81523	

DASH NUMBER CHART			
PART NUMBER	MODEL NO.	DESCRIPTION	
44P0593-000	620L-100	CRYSTAL, 4.20526 MHZ	
44P0593-001	620L	CRYSTAL, 2.2 MHZ	

- ①
- ②9-000
- ④1-001
- ④2-000
- ④3-001
- ③8
- ③4 2 REAR
- ③3-000
- ④0-001

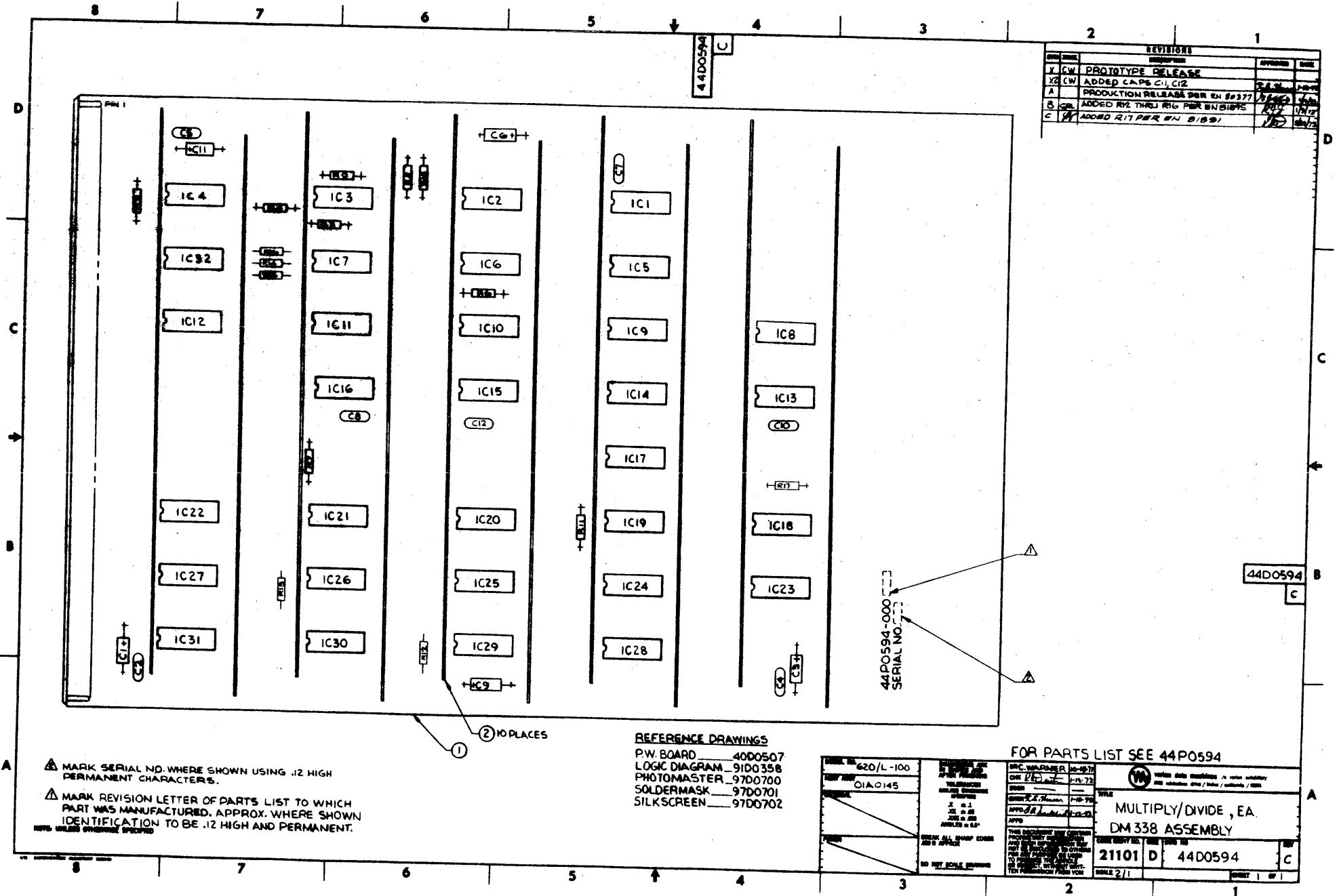
JUMPER TO BE ADDED AT TEST

FOR PARTS LIST SEE 44P0593

REFERENCE DRAWINGS
 4000696 — PW BOARD
 9100357 — LOGIC DIAGRAM
 9700657 — ARTWORK
 9700698 — SILKSCREEN
 9700699 — SOLDER MASK

MODEL NO. 620L-100	REV. NO. 01A1277	DATE 11/77	BY J.M.D.
TITLE PROCESSOR CONTROL NO. 4 ASSY DM357		CHECKED BY 21101 D	DRAWN BY 4400593 F
PART NO. 4400593		SCALE 1/1	SHEET 1 OF 1

② NOTATION: X11 DENOTES COMPONENT NOT USED
 MARK APPROPRIATE DASH NUMBER AND THE REVISION LETTER OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED AND SERIAL NO. APPROXIMATELY WHERE SHOWN, IDENTIFICATION TO BE .12 HIGH CHARACTERS, PERMANENT AND LEGIBLE.
 NOTE: UNLESS OTHERWISE SPECIFIED



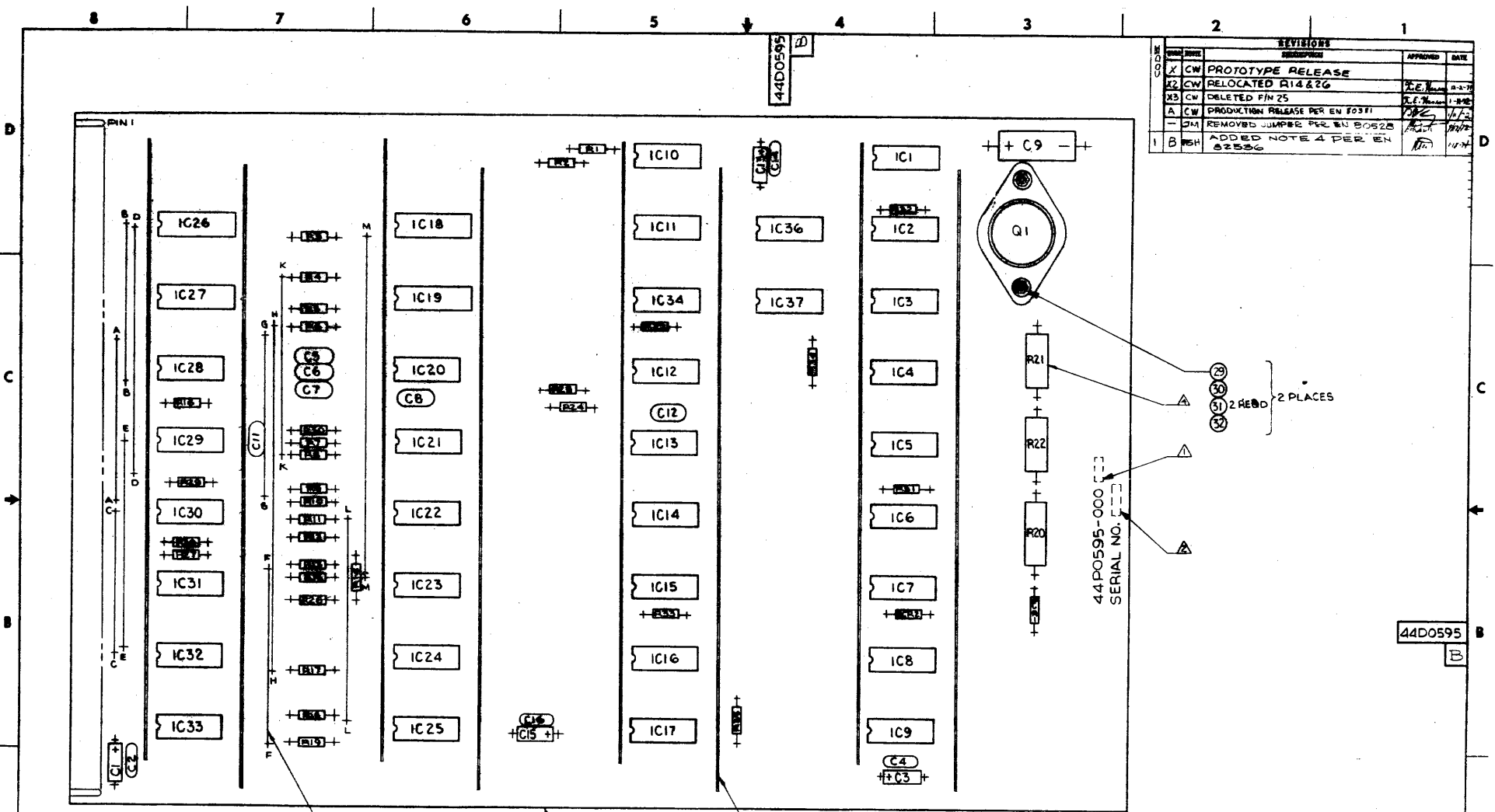
REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
Y	1/27/72	PROTOTYPE RELEASE	
X	1/27/72	ADDED CAPS C1, C2	
A	1/27/72	PRODUCTION RELEASE PER EN 80377	
B	1/27/72	ADDED R12, TRAIL RIG PER EN 81895	
C	1/27/72	ADDED R17 PER EN 81895	

▲ MARK SERIAL NO. WHERE SHOWN USING .12 HIGH PERMANENT CHARACTERS.
 ▲ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED. APPROX. WHERE SHOWN IDENTIFICATION TO BE .12 HIGH AND PERMANENT.
 NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 P.W. BOARD 40D0507
 LOGIC DIAGRAM 91D0358
 PHOTOMASTER 97D0700
 SOLDERMASK 97D0701
 SILKSCREEN 97D0702

FOR PARTS LIST SEE 44P0594

PART NO. 620/L-100 REV. 01A0145	THIS DRAWING IS THE PROPERTY OF THE COMPANY AND IS NOT TO BE REPRODUCED OR COPIED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF THE COMPANY. ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED. DIMENSIONS IN PARENTHESES ARE FOR INFORMATION ONLY. DIMENSIONS IN PARENTHESES ARE FOR INFORMATION ONLY. DIMENSIONS IN PARENTHESES ARE FOR INFORMATION ONLY.	TITLE MULTIPLY/DIVIDE, EA. DM 338 ASSEMBLY
QUANTITY 21101	PART NO. 44D0594	SCALE 2/1



REVISIONS			
NO.	DATE	DESCRIPTION	APPROVED
1		PROTOTYPE RELEASE	
2		RELOCATED R14 & R20	
3		DELETED F/N 25	
4		PRODUCTION RELEASE PER EN 80371	
5		REMOVED JUMPER PER EN 80528	
6		ADDED NOTE 4 PER EN 82556	

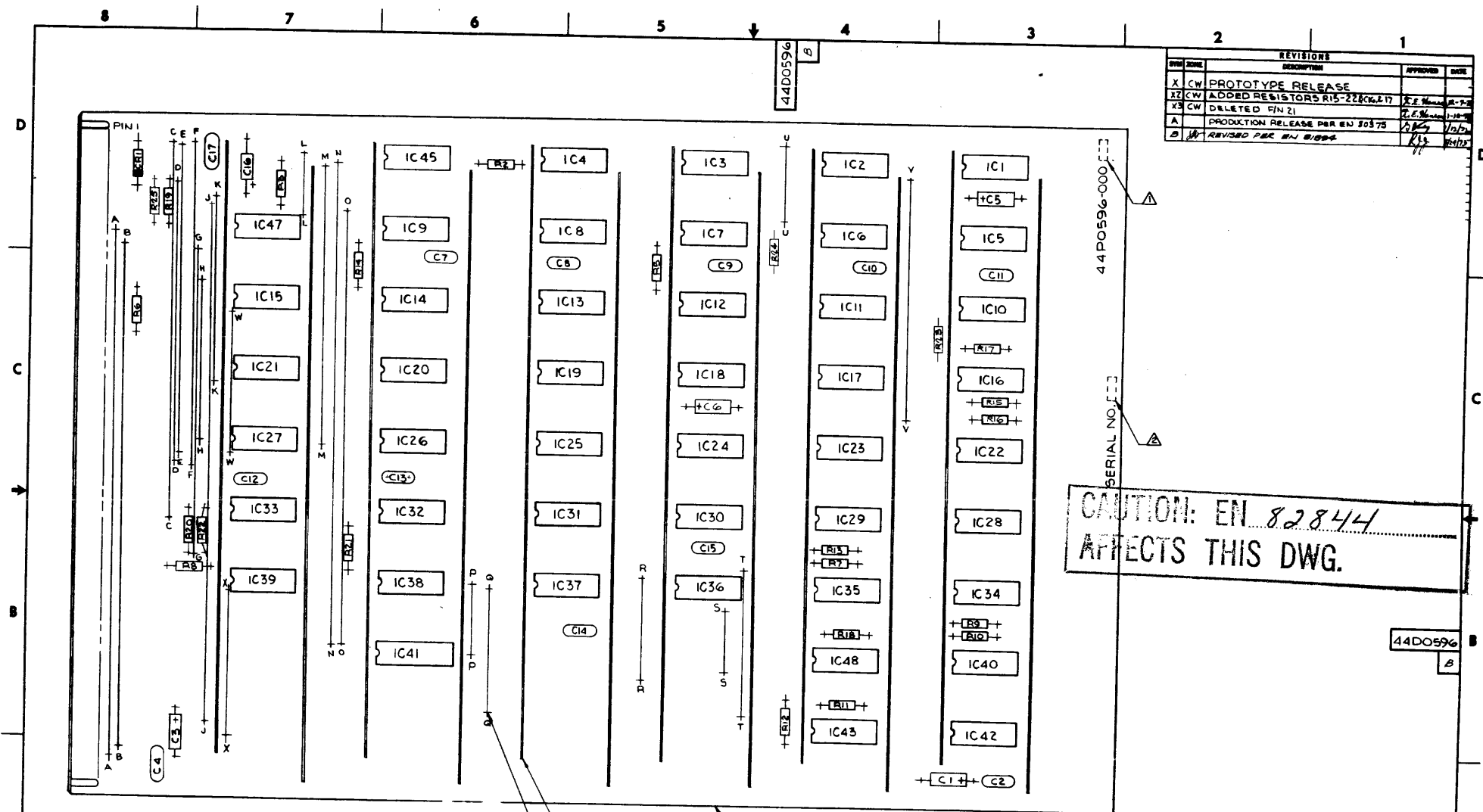
- ⚠ DELAY INSERTION OF R21 UNTIL AFTER CLEANING IN FREON.
 - 3. MOUNT R20, R21, & R22 .06" OFF OF BOARD
 - 2. MARK SERIAL NO. WHERE SHOWN USING .12 HIGH PERMANENT CHARACTERS.
 - ⚠ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED. APPROX. WHERE SHOWN, IDENTIFICATION TO BE .12 HIGH CHARACTERS & PERMANENT.
- NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 LOGIC DIAGRAM --- 91D0359
 PHOTOMASTER --- 97D0703
 BOARD DETAIL --- 40D0508

SOLDERMASK --- 97D0705
 SILKSCREEN --- 97D0704

FOR PARTS LIST SEE 44P0595

MODEL NO. 620/L-100 PART NO. OIA1277 GENERAL	DIMENSIONS ARE TO BE USED AND AFTER PROCESSING TOLERANCES UNLESS OTHERWISE SPECIFIED X ± .1 Y ± .25 Z ± .50 UNLESS OTHERWISE SPECIFIED BREAK ALL SHARP EDGES AND R APPROX. DO NOT SCALE DRAWING	DR. C. WARNER 10-12-71 CHECKED BY: [Signature] 1/17/72 DESIGNED BY: [Signature] ENGR. R. E. [Signature] 1-11-72 APP'D BY: [Signature] 1-11-72 APP'D	TITLE PROCESSOR CONTROL NO. 1 DM 339-ASSEMBLY CASE IDENT. NO. 21101 D DRAWING NO. 44D0595 SCALE 2/1 SHEET 1 OF 1
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REVISIONS				
REV	DATE	DESCRIPTION	APPROVED	DATE
X		CW PROTOTYPE RELEASE		
17		CW ADDED RESISTORS R15-228&L17	R.E. Warner	8-1-78
18		CW DELETED PIN 21	R.E. Warner	1-10-78
A		PRODUCTION RELEASE PER EN 80375	R.E. Warner	1/10/78
B		REVISION PER EN 81002	R.E. Warner	8/15/78

CAUTION: EN 82844 AFFECTS THIS DWG.

⚠ MARK SERIAL NO. WHERE SHOWN USING .12 HIGH PERMANENT CHARACTERS

⚠ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED APPROX WHERE SHOWN IDENTIFICATION TO BE .12 HIGH AND PERMANENT. NOTE, UNLESS OTHERWISE SPECIFIED

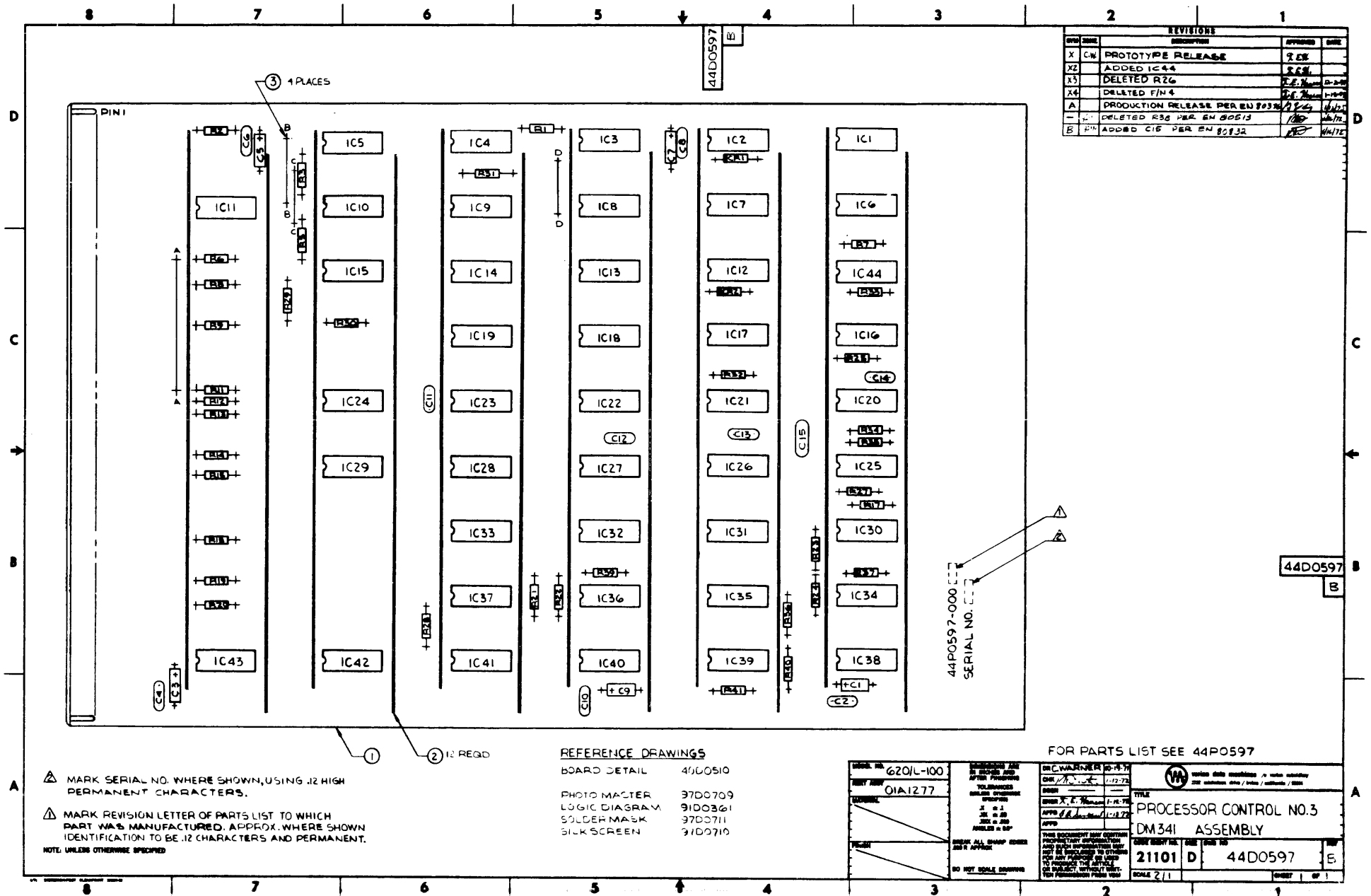
② 12 PLACES
① 7 23 PLACES

REFERENCE DRAWINGS
 LOGIC DIAGRAM 91D0360
 PHOTO MASTER 97D0706
 PW BOARD 40D0509
 SOLDER MASK 97D0708
 SILK SCREEN 97D0707

FOR PARTS LIST SEE 44D0596

MODEL NO. 620/L-100	DESIGNING AND CHECKING AFTER PROOFING	DR. C. WARNER 10-27-77	veritas data machines / a veritas subsidiary 222 midland drive / orlando / florida / 32804
REV. 001	TOLERANCES UNLESS OTHERWISE SPECIFIED	CHK. J. ... 1-12-77	
MATERIAL OIA1277	FINISH	ENGR. R.E. Warner	TITLE PROCESSOR CONTROL NO. 2. ASSEMBLY DM340
	BREAK ALL SHARP EDGES 300 μ APPROX. DO NOT SCALE DRAWING	APPD. R.E. Warner APPD.	COUR. BODY NO. 21101 DATE NO. D 44D0596

44D0596 B



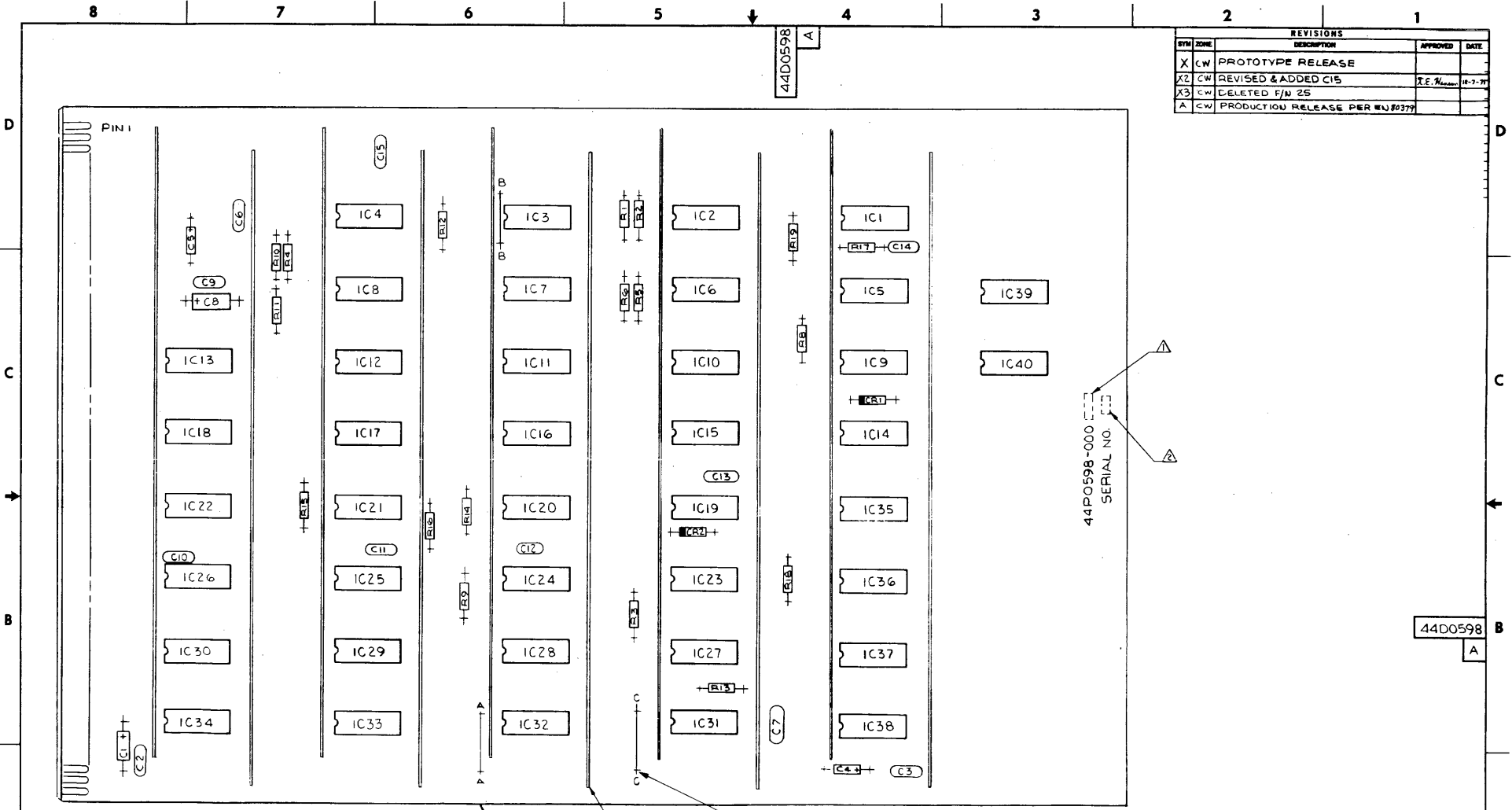
REVISIONS			
REV. NO.	DATE	DESCRIPTION	APPROVED
X	CM	PROTOTYPE RELEASE	9/58
X2		ADDED IC44	9/58
X3		DELETED R26	9/58
X4		DELETED FIN 4	9/58
A		PRODUCTION RELEASE PER EN 80513	9/58
-		DELETED R32 PER EN 80513	9/58
B		ADDED C16 PER EN 80532	10/58

▲ MARK SERIAL NO. WHERE SHOWN, USING .12 HIGH PERMANENT CHARACTERS.
 ▲ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED. APPROX. WHERE SHOWN IDENTIFICATION TO BE .12 CHARACTERS AND PERMANENT.
 NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 BOARD DETAIL 4600510
 PHOTO MASTER 37D0709
 LOGIC DIAGRAM 9100361
 SOLDERMASK 37D0711
 SILK SCREEN 31D0710

FOR PARTS LIST SEE 44P0597

DRAWING NO. 620/L-100 REV. 01A1277	PREPARED BY CHECKED BY DESIGNED BY DATE	DWG. NO. 21101 D SHEET NO. 44D0597 B SCALE 2/1	TITLE PROCESSOR CONTROL NO. 3 DM341 ASSEMBLY
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REVISIONS				
SYM	ZONE	DESCRIPTION	APPROVED	DATE
X	CW	PROTOTYPE RELEASE		
X2	CW	REVISED & ADDED C15	J.E. Wagner	11-7-77
X3	CW	DELETED F/N 25		
A	CW	PRODUCTION RELEASE PER WJ 80377		

44D0598
A

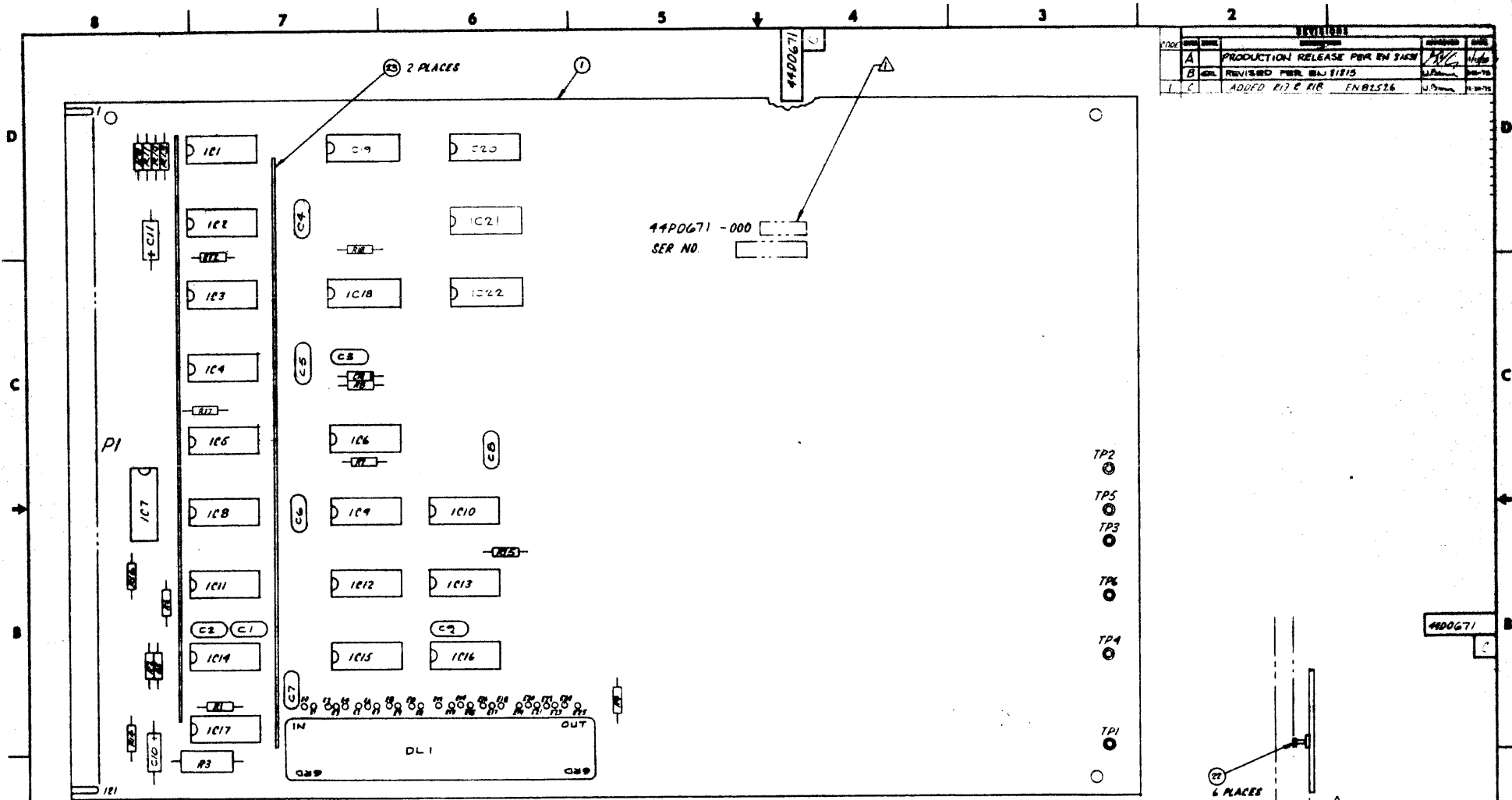
44D0598-000
SERIAL NO.

FOR PARTS LIST SEE 44D0598

⚠ MARK SERIAL NO. WHERE SHOWN USING .12 HIGH PERMANENT CHARACTERS.
 ⚠ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED, APPROX. WHERE SHOWN IDENTIFICATION TO BE .12 HIGH CHARACTERS AND PERMANENT
 NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 LOGIC DIAGRAM 91D0362
 PHOTOMASTER 97D0712
 P/W BOARD 40D0511
 SOLDER MASK 97D0714
 SILKSCREEN 97D0713

MODEL NO. 620/L-100	DRG. WARNER 10-14-77		THIS DOCUMENT MAY CONTAIN PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT WITHOUT WRITTEN PERMISSION FROM IBM
NEXT ASSY. 01A1277	CHK [Signature]		CODE IDENT NO. 21101 SIZE D DWG NO. 44D0598 SHEET 1 OF 1
MATERIAL	DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED TOLERANCES UNLESS OTHERWISE SPECIFIED .01 = .01 .02 = .02 .005 = .005 ANGLES = 45°	TITLE INTERRUPT TRAP ASSEMBLY, DM342	REV A
FINISH	BREAK ALL SHARP EDGES .010 R APPROX. DO NOT SCALE DRAWING		



REV	DATE	DESCRIPTION	BY	CHKD
A		PRODUCTION RELEASE PER EN 5125		
B		REVISED PER EN 5125		
C		ADDED R12 & R10 EN 61526		

44D0671 -000
SER NO.

- TP2
- TP5
- TP3
- TP6
- TP4
- TP1

FOR PARTS LIST SEE 44D0671

A MAXIMUM COMPONENT HEIGHT

MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED AND SERIAL NR APPROXIMATELY WHERE SHOWN IDENTIFICATION TO BE .12 HIGH CHARACTERS AND PERMANENT.

NOTE: UNLESS OTHERWISE SPECIFIED

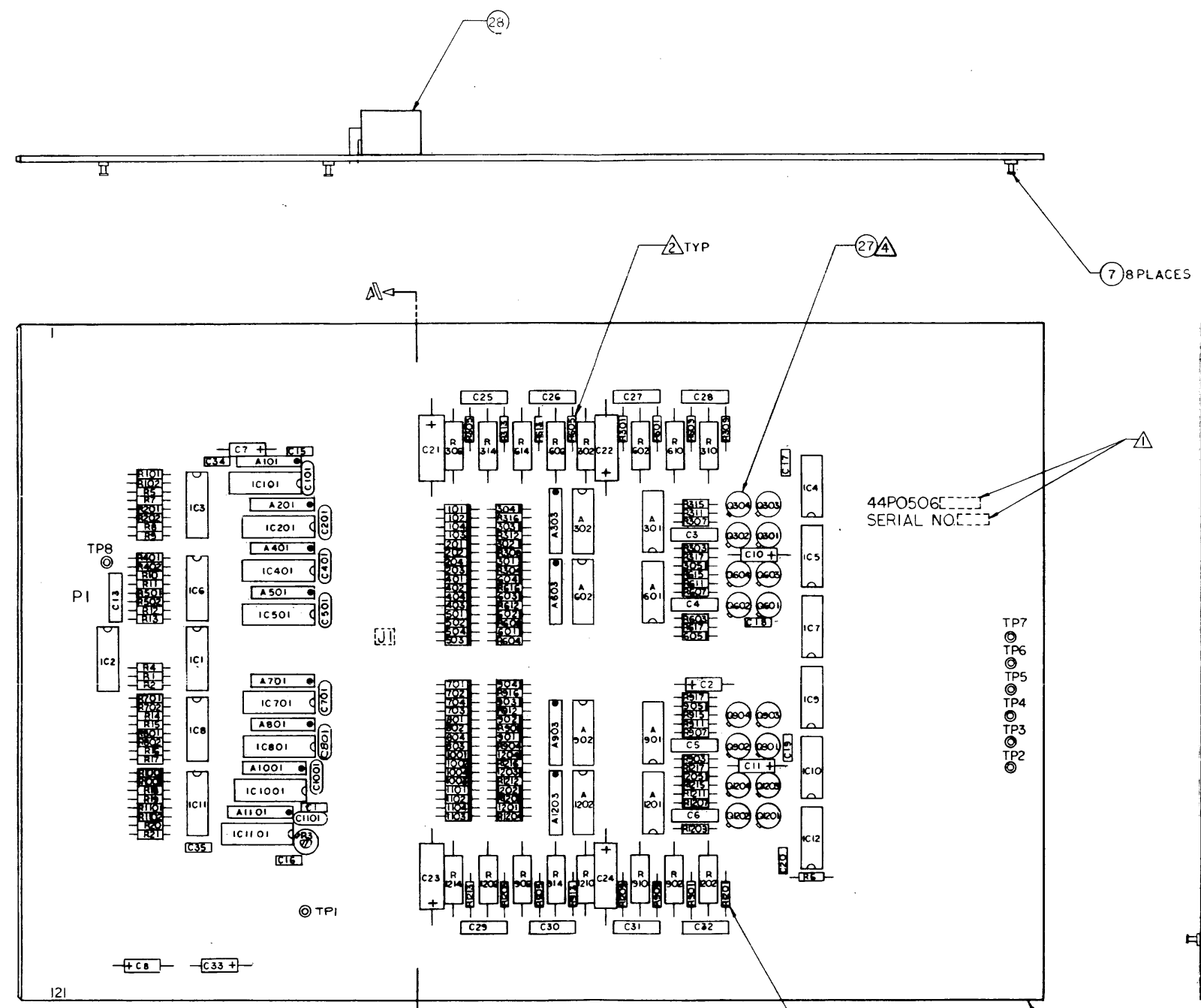
REF DRAWINGS

44D0671	PARTS LIST
40D0601	PW BOARD
91B0436	LOGIC
97D0857	ARTWORK

DRAWING NO. 44D0671 A & B REV. NO. 01/1977 QUANTITY 1000 DATE 01/1977 BY [Signature]	APPROVED FOR RELEASE AND FOR PRODUCTION [Signature] TITLE [Title] DATE 01/1977 BY [Signature]
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PART NO. 44D0671 QUANTITY 1000 DATE 01/1977 BY [Signature]	TITLE MEMORY I & CONT ASSY DM 395
PART NO. 21101 D QUANTITY 1000 DATE 01/1977 BY [Signature]	PART NO. 44D0671 QUANTITY 1000 DATE 01/1977 BY [Signature]

REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
X2		PROTOTYPE RELEASE	
A		PRODUCTION RELEASE EN 5210	
B		RELOCATED F-F & E-E EN 5210	
C	8/11/72	REMOVED CONNECTOR MTS HARDWARE AND STANDOFFS (5 HARDWARE) ADD MTS 3 PER EN 8252	
D	8/11/72	RELOCATED EDGE OF BOARD AND TEST PER EN 555	
E	8/11/72	ADDED DASH NO CHART TO F/D PER EN 5045	
F		REMOVED ALL UPPER WIRES PER EN 8279	
G		ADDED G/N 4 PER EN 8135	
H	1/22/73	REV SEL PLF EN 8172	



- ▲ USE OF TRANSISTOR PADS OPTIONAL.
 - 3 TOP SOLDER FILLET NOT REQUIRED ON INACCESSIBLE COMPONENT OR CONNECTOR.
 - ▲ VALUE FOR THE FOLLOWING COMPONENTS TO BE SELECTED AT FINAL TEST: R301, 305, 309, 313, 601, 605, 609, 613, 901, 905, 909, 913, 1201, 1205, 1209, 1213.
 - ▲ MARK APPLICABLE 44P0506 DASH NUMBER AND REVISION LETTER TO WHICH PART WAS MANUFACTURED AND SERIAL NUMBER USING .12 HIGH CHARACTERS APPROXIMATELY WHERE SHOWN
- NOTE: UNLESS OTHERWISE SPECIFIED

DASH NUMBER CHART	
PART NUMBER	DESCRIPTION
44P0506-000	SENSE & INHIBIT ASSY
44P0506-001	SENSE & INHIBIT ASSY

FOR PARTS LIST SEE 44P0506

- REFERENCE DRAWINGS
- 40D0454 P.W. BOARD
 - 44P0506 PARTS LIST
 - 91D0285 LOGIC DIAGRAM
 - 97D0533 ARTWORK
 - 97D0555 SILKSCREEN
 - 97D0556 SOLDER MASK

620/L	01-1034	DATE	8/11/72	REV	1
SENSE/INHIBIT ASSY		DM288		21101 E 44E0506	
SCALE 2/1		SHEET 1 OF 1			

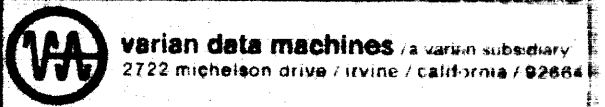
REVISIONS

REV	EN	CHG CODE	DESCRIPTION	DR	APPD
F	B2526	1	UPDATED REV LTR OF REF DWG'S. WAS: B, UPDATED REV LTR OF FIND N° 1, WAS: C, REVISED QTY OF PIN 16, WAS: 12, REVISED PART N° FOR FIND N° 26, WAS: 49A0 508-000	WFX	W.E. 2-21-72

DWG NO. 44P0671

NEXT ASSEMBLY
DIAM97

MODEL NO.
6201L-101-E2861
A&B



BY D. WISE / 1/10/73
CHK M.A.D.

CODE IDENT NO. 21101

TITLE
PARTS LIST - MEMORY
T&C CONT ASSY, DM395

DESN
ENGR
APPD
APPD

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SIZE DWG NO. REV
A 44P0671 F

SHEET 1 OF 3

DWG. NO. 44POG71

CODE IDENT 21101

PARTS LIST

QUANTITY REQ'D PER DASH NO

QTY	REQ'D	PER	DASH	NO	000	FIND NO	PART NUMBER	DESCRIPTION	REMARKS	ZONE
REF	-						44D00671 C	ASSEMBLY		
REF	-						71D00456 C	LOGIC DIAGRAM		
REF	-						97D00837	ARTWORK		
REF	-						99A00935	BOOTSTRAP LOADER PROTECT (E-2047)	ENGR'S DESCRIPTION	
1							40D0007-000 D	R.W. BOARD		
2							49A0039-000	INTEGRATED CIRCUIT	IC6,11	
4							49A0023-000		IC7,13,18,19	
1							49A0041-000		IC16	
2							49A0054-001		IC10,22	
1							49A0003-000		IC9	
1							49A0056-000		IC12	
1							49A0142-001		IC14	
2							49A0079-000		IC8,15	
1							49A0019-000	INTEGRATED CIRCUIT	IC17	
2							69N1500-101	CAP, 100pf 500V	CI,2	
7							71A0004-100	CAP, .1uf 50V	C3-9	
2							71NG100-475	CAP, 4.7uf 10V 10%	CI0,11	
1							77S1017-000	DIODE	CR1	
1							80A0039-001	DELAY LINE	DL1	

TITLE: PARTS LIST

MEMORY T & CONT ASSY DM 395

DWG NO 44POG71

REV F

MODEL NO 6201-701-2801

APPD 014 1-12-73

AFB

NEXT ASSY 0141497

REV	A	B	C	D	E
EN NO	8153181815	8153181815	8153181815	8153181815	8153181815
DATE	1/10/73	1/13/73	1/16/73	1/16/73	1/16/73
DR	W. H. GRL	W. H. GRL	W. H. GRL	W. H. GRL	W. H. GRL
CHK	W. H. GRL	W. H. GRL	W. H. GRL	W. H. GRL	W. H. GRL

SHEET 2 OF 3

DWG NO 44POG71

CODE IDENT: 21101

PARTS LIST

QTY	PART NO	PART NUMBER	DESCRIPTION	REMARKS
17	16	65N2500-102	RES, 1K, 1/4W, ±5%	R1, 2, 5, 7, 9-16, 17, 18
1	17	65N2500-181	RES, 180Ω, 1/4W, ±5%	R4
1	18	65N2500-202	RES, 2K, 1/4W, ±5%	R6
1	20	65N2500-103	RES, 10K, 1/4W, ±5%	R8
1	21	65N1010-680	RES, 68Ω, 1/4W, ±5%	R5
6	22	58A0066-002	TERMINAL	TP1 THRU TP6
2	23	53C0194-000	BUS, POWER	
1	24	49A0060-000	INTEG CIRCUIT	IC20
1	25	49A0007-000		IC2
1	26	49A0042-000		IC4
2	27	49A0510-000		IC1, 5
1	28	49A0516-000	INTEG CIRCUIT	IC3
1	29	49A0128-001	INTEG CIRCUIT	IC21

NOTES:

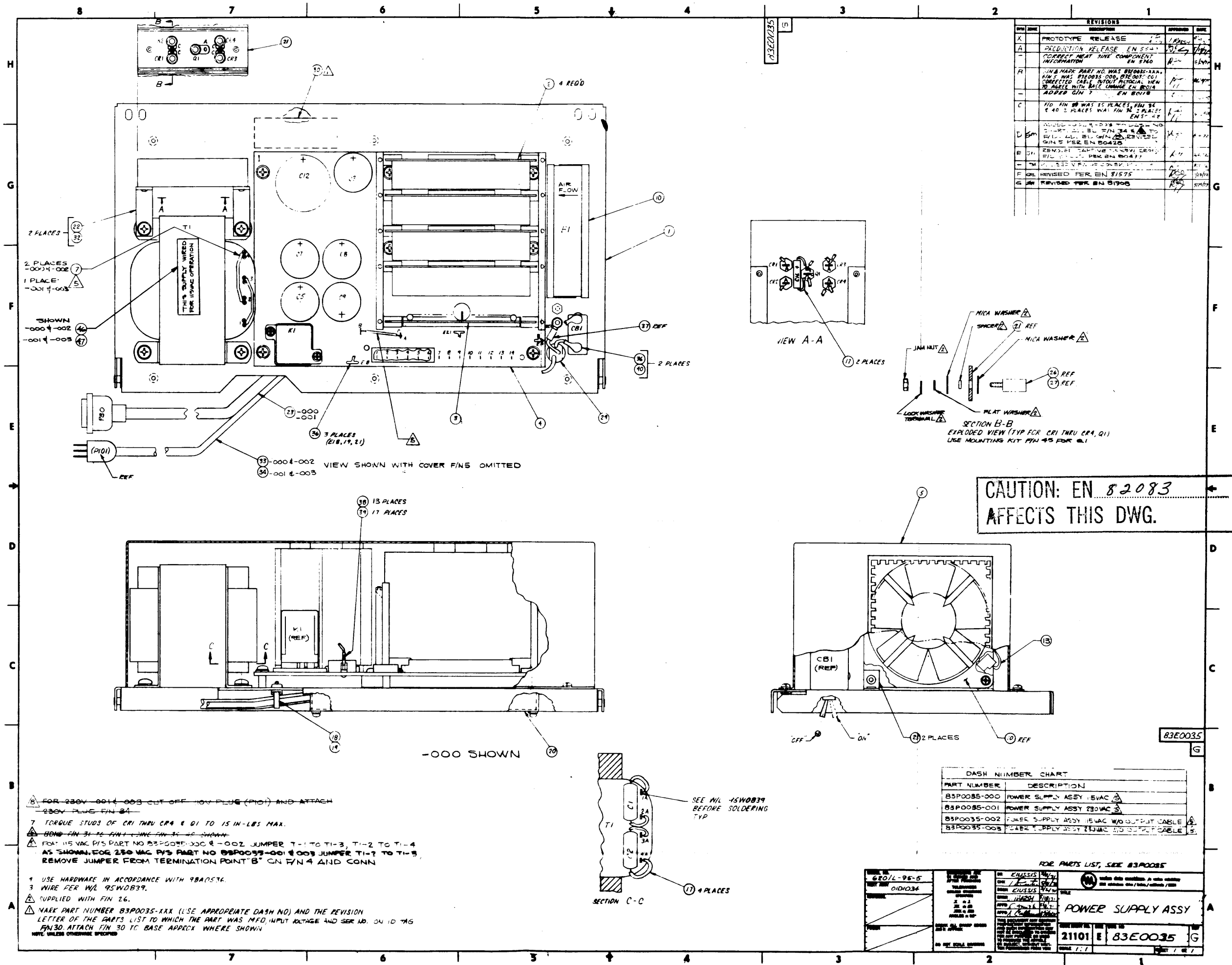
DWG NO

44POG71

REV

F

SHEET 2 OF 3



REVISIONS			
REV	DESCRIPTION	APPROVED	DATE
X	PROTOTYPE RELEASE		
A	PRODUCTION RELEASE EN 82083		
-	CORRECT HEAT SINK COMPONENT INFORMATION EN 82083		
R	FIN MARK PART NO WAS 83E0035-XXX, FIN 5 WAS 83E0035-000, 83E0035-001 CORRECTED CABLE OUTPUT RATIO FROM 10:1 TO 1:1. FIN 2 CHANGE EN 82083. ADDED Q1N 7 EN 80118		
C	FIN 10 WAS 15 PLACES, FIN 24 & 40 2 PLACES WAS FIN 24 2 PLACES EN 81575		
D	ADDED FIN 45 FOR Q1 TO USE MOUNTING KIT FIN 45 FOR Q1. FIN 5 PER EN 80426		
E	REMOVED TAP FIVE FROM CR1, CR2, CR3, CR4 PER EN 80477		
F	REVISED PER EN 81575		
G	REVISED PER EN 81908		

2 PLACES
-0014-002

2 PLACES
-0014-002

1 PLACE
-0014-003

SHOWN
-0004-002

-0014-003

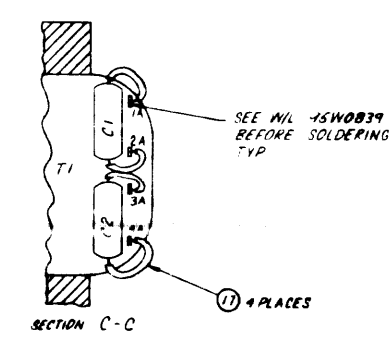
VIEW SHOWN WITH COVER F/NS OMITTED

33-0004-002

34-0014-003

-000 SHOWN

- FOR 230V 0014-003 CUT OFF 10V PLUG (PI01) AND ATTACH 230V PLUG FIN 24
- TORQUE STUDS OF CR1 THRU CR4 & Q1 TO 15 IN-LBS MAX.
- BOUND FIN 31 TO FIN 1. CHANGE FIN 31 TO SHOWN.
- FOR THE VAC P/S PART NO 83P0035-000 & -002 JUMPER T-1 TO T1-3, T1-2 TO T1-4 AS SHOWN. FOR 230 VAC P/S PART NO 83P0035-001 & -003 JUMPER T1-2 TO T1-5. REMOVE JUMPER FROM TERMINATION POINT B* ON F/N 4 AND CONN.
- USE HARDWARE IN ACCORDANCE WITH 98A0536.
- 3 WIRE PER W/L 95W0839.
- SUPPLIED WITH FIN 26.
- MARK PART NUMBER 83P0035-XXX (USE APPROPRIATE DASH NO) AND THE REVISION LETTER OF THE PARTS LIST TO WHICH THE PART WAS MFD, INPUT VOLTAGE AND SER NO. ON ID TAG. FIN 30 ATTACH FIN 30 TO BASE APPROX WHERE SHOWN WIRE UNLESS OTHERWISE SPECIFIED.



PART NUMBER	DESCRIPTION
83P0035-000	POWER SUPPLY ASSY (5VAC)
83P0035-001	POWER SUPPLY ASSY 230VAC
83P0035-002	POWER SUPPLY ASSY (5VAC W/O OUTPUT CABLE)
83P0035-003	POWER SUPPLY ASSY 230VAC W/O OUTPUT CABLE

620/L-96-5	0101034	ENHUSIS	21101 E	83E0035
------------	---------	---------	---------	---------

POWER SUPPLY ASSY

NOTES: (UNLESS OTHERWISE SPECIFIED)

1. ALL RESISTORS ARE 1/4W, ±5%
2. THIS DRAWING CONSISTS OF SHEETS 1.0, 2.0, 3.0, 4.0, 5.0, 6.0, 7.0, 8.0, 9.0, 10.0
3. ON IC'S 1, 3, 4, 6, 7, 8 & 10 PIN 14 IS CONNECTED TO +5 AND PIN 7 IS CONNECTED TO GRD.
4. PREFIX COMPONENT REFERENCE DESIGNATION NO. WITH CIRCUIT NO. EXAMPLE: CKT 1, R₁₀₁; CKT 2, R₂₀₂

D


C

B

A

REFERENCE DESIGNATIONS	
LAST USED	NOT USED
C24	C18, 19
CR15, CR_24	
L2	
Q26	
R51, R_09	
RT 2	
IC 11	
A_04	

REFERENCE DRAWINGS	
44D0578	ASSEMBLY
44P0578	PARTS LIST
40D0497	BOARD DETAIL
97D0673	ARTWORK
97D0672	SILKSCREEN
97D0671	SOLDERMASK

DR: <i>[Signature]</i> 11/7/71 CHK: <i>[Signature]</i> 11/7/71 DSGN: <i>[Signature]</i> 11/7/71 ENGR: <i>[Signature]</i> 11/7/71 APPD: <i>[Signature]</i> 11/7/71 APPR: <i>[Signature]</i> 11/7/71 THIS DRAWING IS THE PROPERTY OF VARIAN CORPORATION AND IS NOT TO BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT PERMISSION FROM VARIAN.	 varian data machines / a varian subsidiary 222 mableton drive / irvine / california / 92614	TITLE LOGIC DIAGRAM DR/SK SW
CODE / CKT NO. SIZE DWS NO. 21101 C 91C0346	REV C	
SCALE ———	6204-100	SHEET 1.0

REVISIONS				
REV	DATE	DESCRIPTION	APPROVED	DATE
X	02	PROTOTYPE RELEASE		
X	02	ADDED DIODES IN CKTS 1-4		
A	02	PRODUCTION RELEASED BY 80250	<i>[Signature]</i>	10/20/74
B	02	C23 & 24 WAS .33UF 50VDC EN 80374	<i>[Signature]</i>	11/20/74
C	02	ADDED CRIS PER EN 81003	<i>[Signature]</i>	5/24/75

TABLE OF CONTENTS

DESCRIPTION	SHEET NO.
COVER	1.0
REVISION, TABLE OF CONTENTS & CONNECTOR PIN ASSIGNMENTS	2.0
DECOUPLING CAPS. & DRIVER SW'S, POSITIVE	3.0
DRIVER SWITCHES, NEGATIVE	4.0
SINK SWITCHES, NEGATIVE	5.0
SINK SWITCHES, POSITIVE	6.0
DRIVER LOGIC	7.0
SINK LOGIC	8.0
PREDRIVER	9.0
CURRENT SOURCES	10.0

CONNECTOR PI

PINS FUNCTION SHEET

1	GRD	
2	GRD	
3	-12V	
4	GRD	
5	TCRX-	9.0
6	YS6B	8.0
7	YS6A	8.0
8	YS7B	8.0
9	YS7A	8.0
10	YS4B	8.0
11	YS4A	8.0
12	YS5B	8.0
13	YS5A	8.0
14	YS2B	8.0
15	YS2A	8.0
16	YS3B	8.0
17	YS3A	8.0
18	YSOB	8.0
19	YSOA	8.0
20	YS1B	8.0
21	YS1A	8.0
22	CAGB	7.0
23	CA1B	7.0
24	CA7A	7.0
25	CAOB	7.0
26	CA4A	7.0
27	CA3B	7.0
28	CABA	7.0
29	CA2B	7.0
30	CA2A	7.0
31	CA7B	7.0
32	CA3A	7.0
33	CAGB	7.0
34	CAOA	7.0
35	CASB	7.0
36	CA1A	7.0
37	CA4B	7.0
38	SPARE	7.0
39	SPARE	7.0
40	GRD	
41	GRD	

PINS FUNCTION SHEET

42	GRD	
43	LI1X+	8.0
44	LI0X+	8.0
45	GRD	
46	LC9X+	8.0
47	GRD	
48	RS1X-	9.0
49	GRD	
50	WS1X-	9.0
51	GRD	
52	SE1X-	9.0
53	GRD	
54	LC2X+	9.0
55	GRD	
56	LO5X+	7.0
57	GRD	
58	SD1S	7.0
59	GRD	
60	RYXX-	7.0
61	NOT AVAILABLE	
62	NOT AVAILABLE	
63	GRD	
64	LO4X+	7.0
65	GRD	
66	LO1X+	7.0
67	GRD	
68	LO0X+	7.0
69	GRD	
70	LC3X+	7.0
71	GRD	
72	RWT2-	9.0
73	GRD	
74	LI2X+	9.0
75	GRD	
76	RWT1-	9.0
77	GRD	
78	LC8X+	8.0
79	LC7X+	8.0
80	LC6X+	8.0
81	GRD	
82	GRD	

PINS FUNCTION SHEET

83	SPARE	7.0
84	CC1B	7.0
85	CC6A	7.0
86	CCOB	7.0
87	CC7A	7.0
88	CC3B	7.0
89	CC4A	7.0
90	CC2B	7.0
91	CC5A	7.0
92	CC5B	7.0
93	CC2A	7.0
94	CC4B	7.0
95	CC3A	7.0
96	CC7B	7.0
97	CCOA	7.0
98	CC6B	7.0
99	CC1A	7.0
100	XS6A	8.0
101	XS6B	8.0
102	XS7A	8.0
103	XS7B	8.0
104	XS4A	8.0
105	XS4B	8.0
106	XS5A	8.0
107	XS5B	8.0
108	XS2A	8.0
109	XS2B	8.0
110	XS3A	8.0
111	XS3B	8.0
112	XSOA	8.0
113	-12V	
114	XS1A	8.0
115	XSOB	8.0
116	+5V	
117	XS1B	8.0
118		
119	+12V	
120	+12V	
121	GRD	
122	GRD	

CONNECTOR FUNCTIONS

CODE	REV	DATE	REV
21101	C	91C0346	C
SCALE			SHEET 2.0

4

3

2

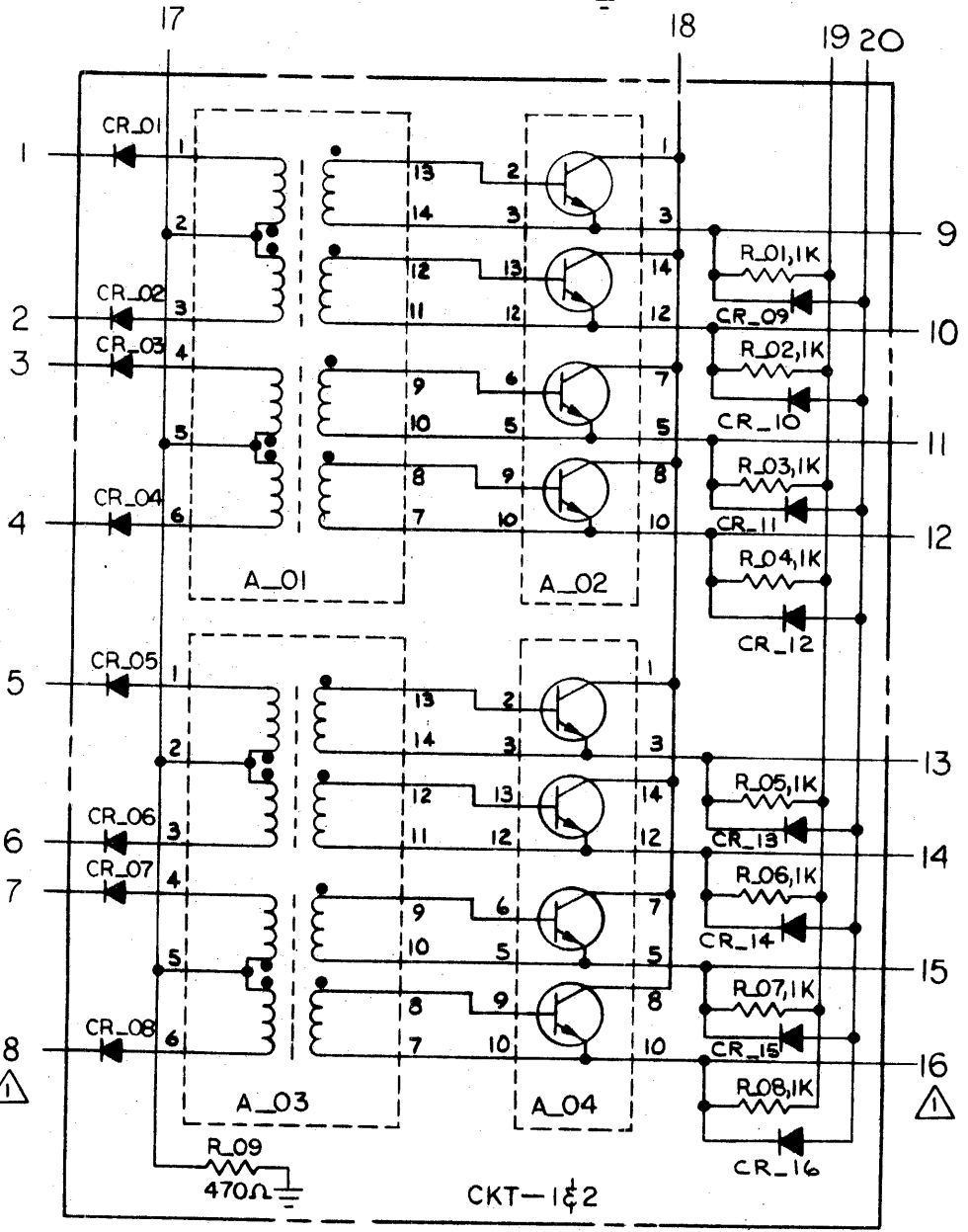
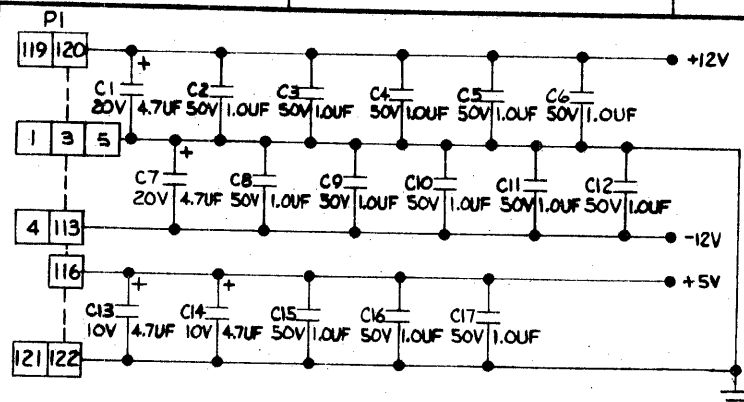
1

U

C

B

A



⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO CIRCUIT BLOCKS 1 & 2 ON SHT 7

DECOUPLING CAPS. & DRIVER SW'S, POSITIVE

DATE CHG'D	BY	DATE TO	REV
21101	C	91C0346	C
SCALE	SHEET 3.0		

4

3

2

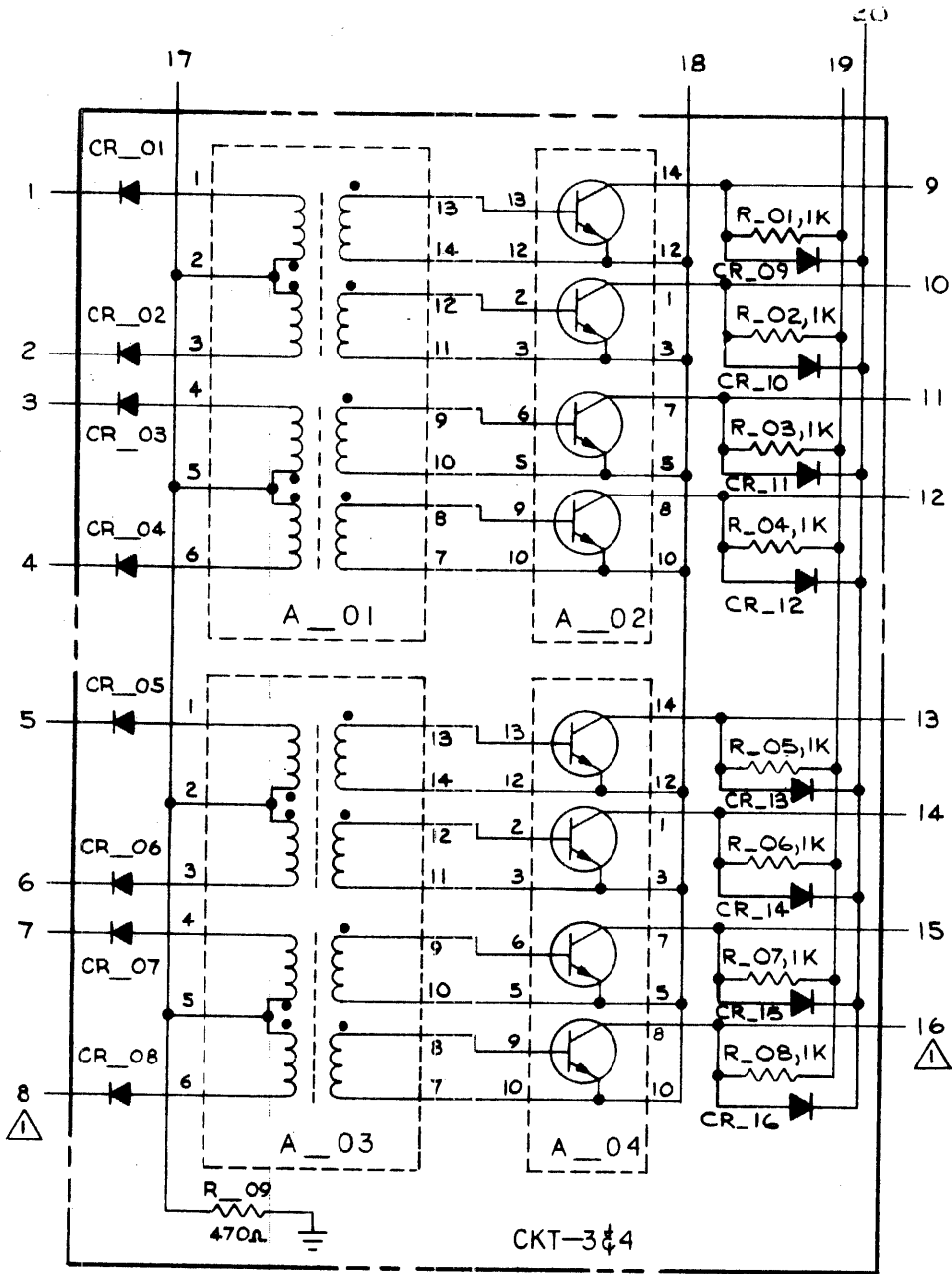
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D

C

B

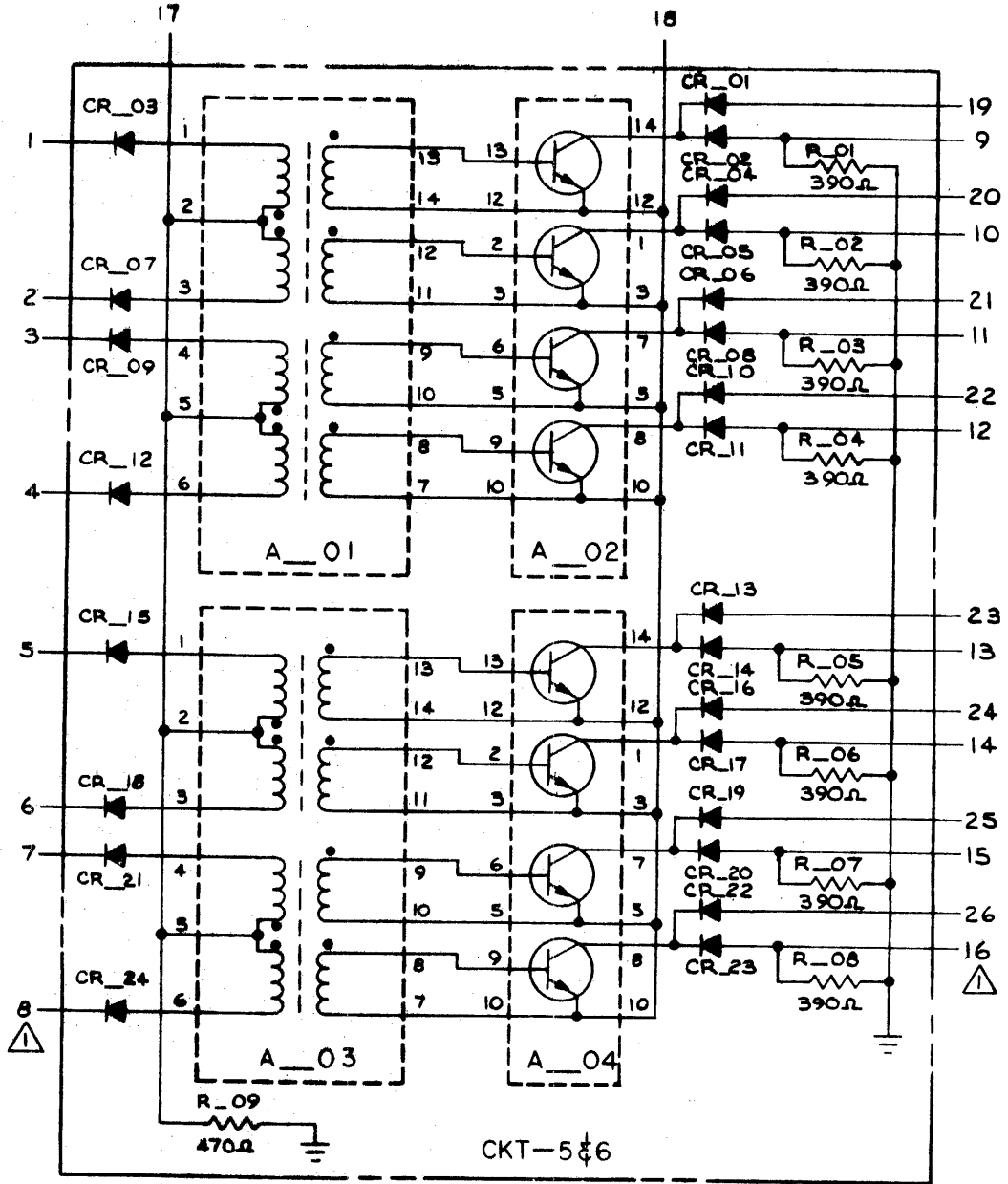
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⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO CIRCUIT BLOCKS 3&4 ON SHT 7

DRIVER SWITCHES, NEGATIVE

CODE	REV	DATE	BY
21101	C	91C0346	C
SCALE	SHEET 4.0		



⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO CIRCUIT BLOCKS 5 & 6 ON SHT 8

SINK SWITCH, NEGATIVE

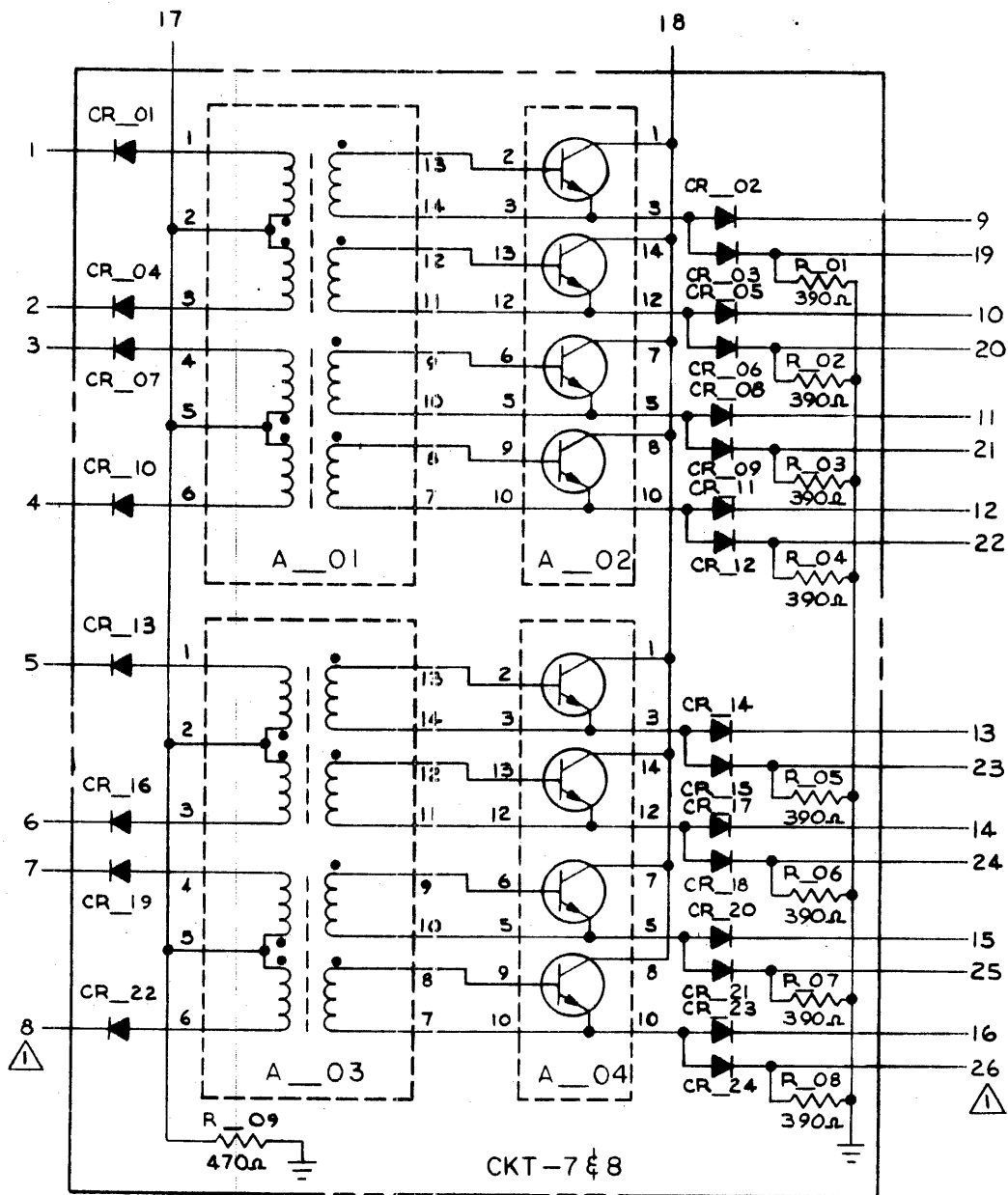
CODE	REV	DATE	BY
21101	C	91C0346	C
SCALE	SHEET 5.0		

D

C

B

A



⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO CIRCUIT BLOCKS 7 & 8 ON SHT 8

SINK SWITCH POSITIVE

ORDER NO.	REV	DATE	REV
21101	C	91C0346	C
SCALE	SHEET 6.0		

4

3

2

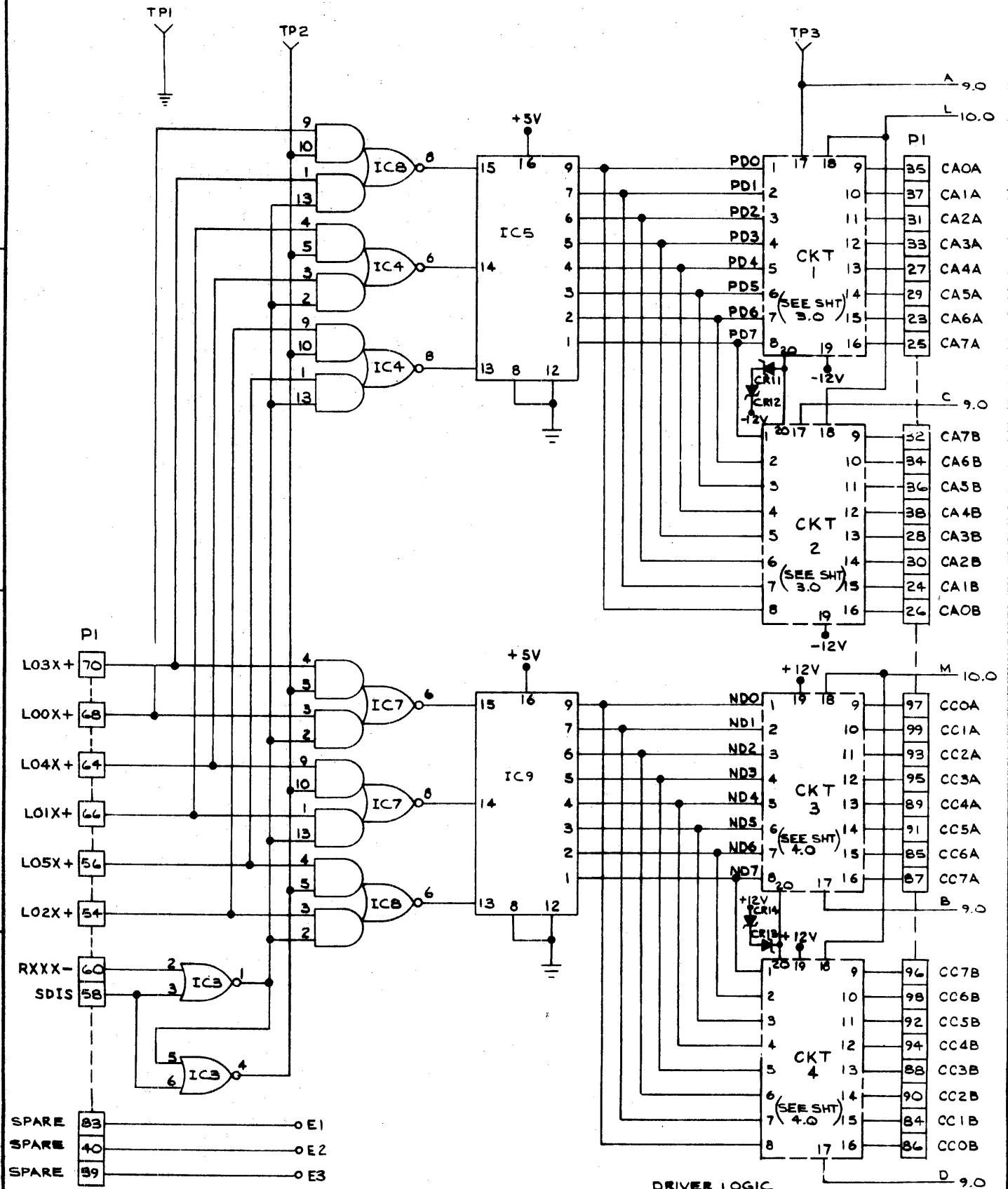
1

D

C

B

A



DRIVER LOGIC

CODE SHEET NO.	REV	DATE	DWG NO.	REV
21101	C		91C0346	C
SCALE				SHEET 7.0

4

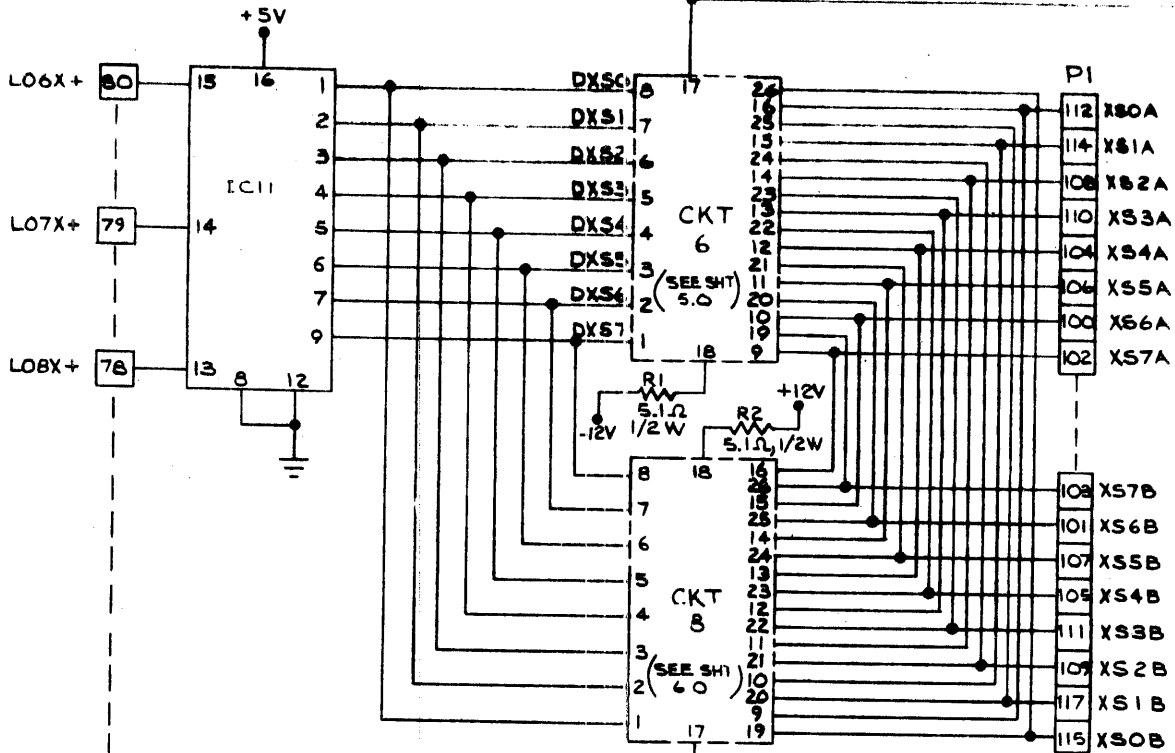
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2

1

D

11 7.0

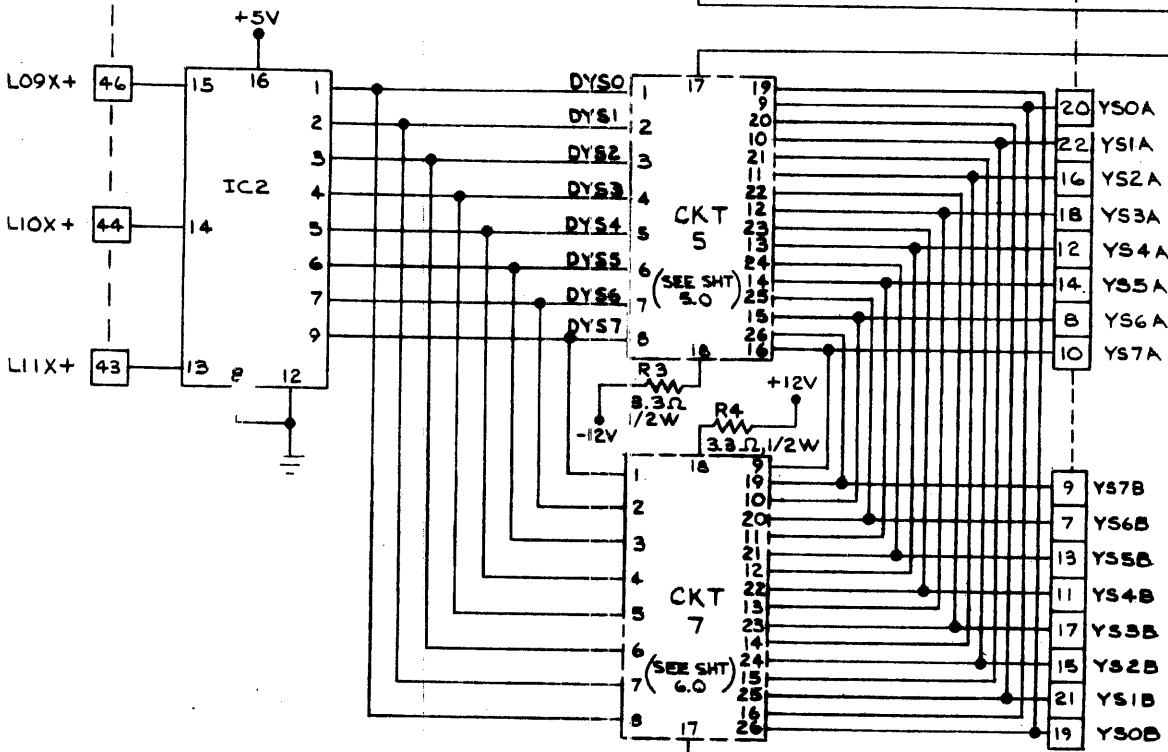


E 9.0

F 9.0

C

B

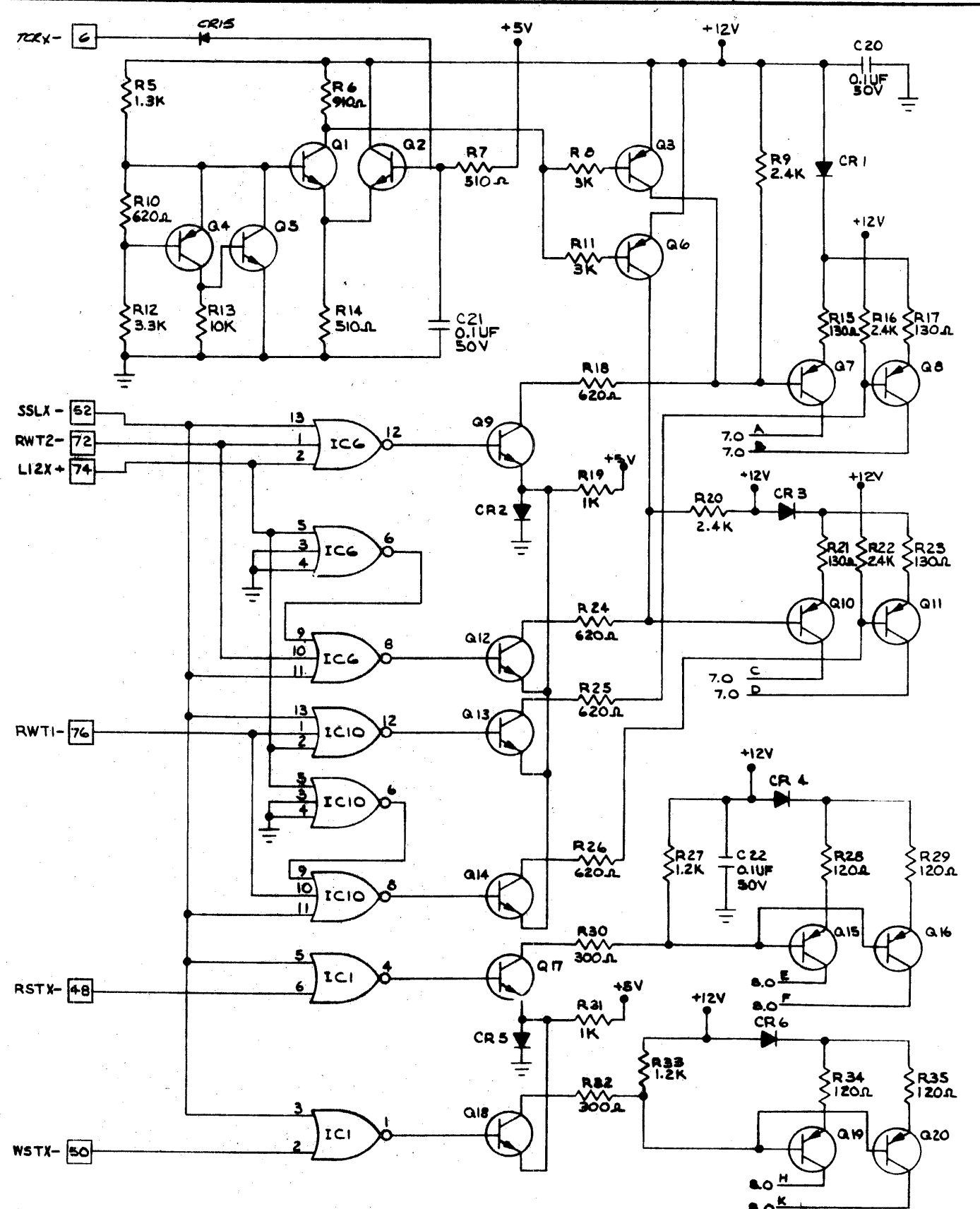


K 9.0

A

SINK LOGIC CIRCUITS

DATE: 10/10/70	REV: 1	BY: JCB
21101	C	91C0346
SCALE: —		SHEET: 3.0



C

B

A

RECEIVER CIRCUITS

DATE REVISED: 1965	DESIGN NO:	REV:
21101	C	91C0346
QTY:	QTY:	QTY: 90

4

3

2

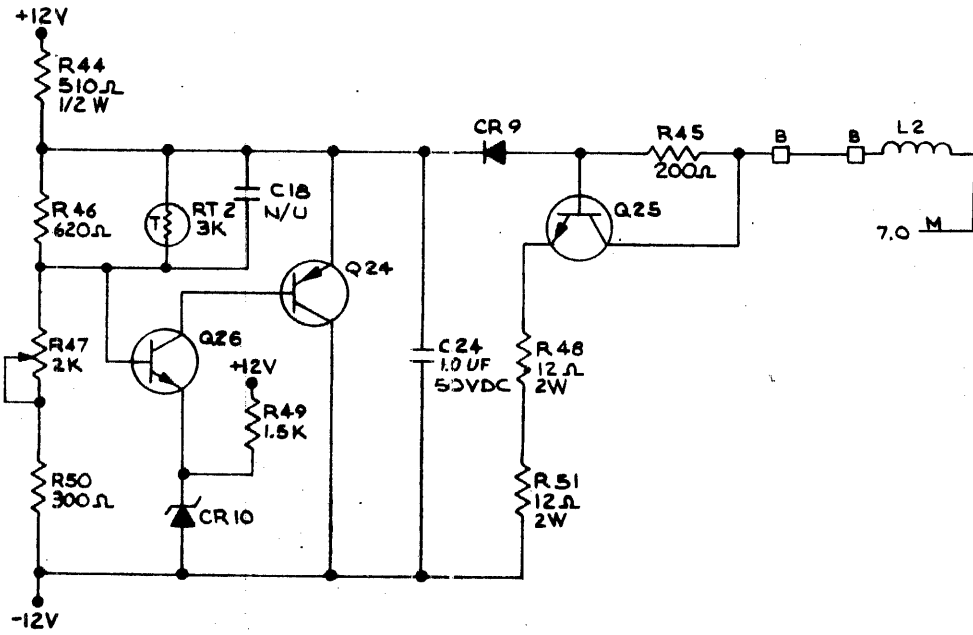
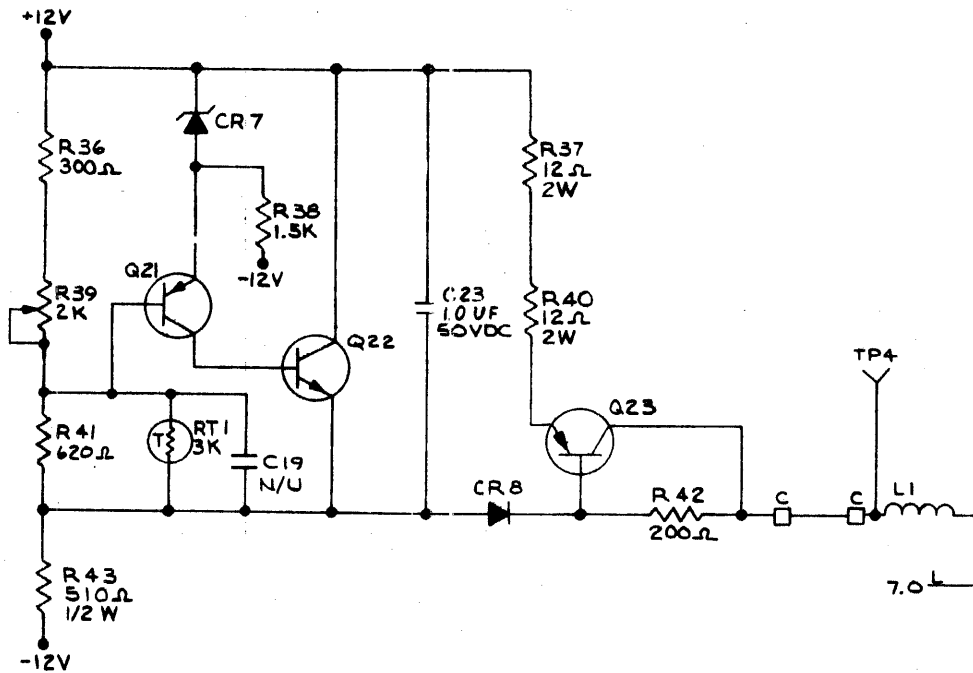
1

D

C

B

A

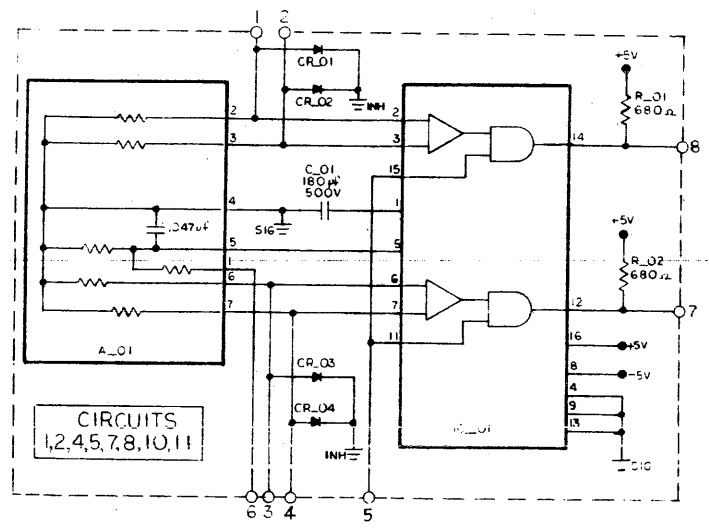
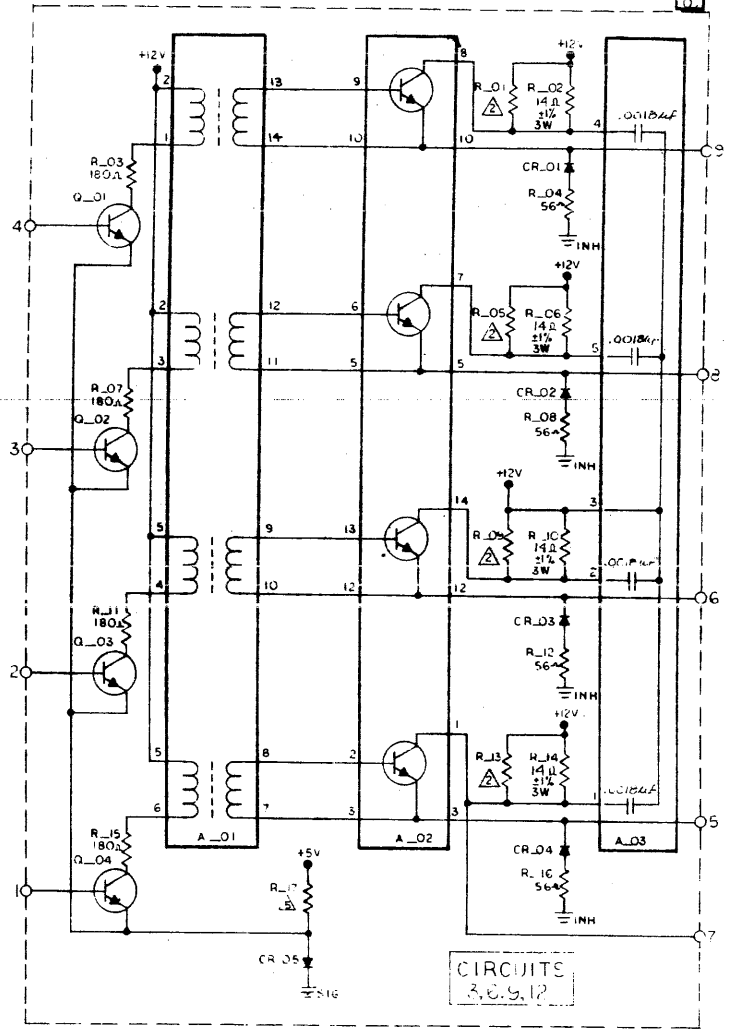


CURRENT SOURCES

CODE	IDENT. NO.	TYPE	DATE	REV.
21101	C	91C0346		C
SCALE				INCHES 10.0

91D0285 E

REVISIONS		APPROVED	DATE
DWG. NO.	DESCRIPTION		
SEE SHEET ONE			

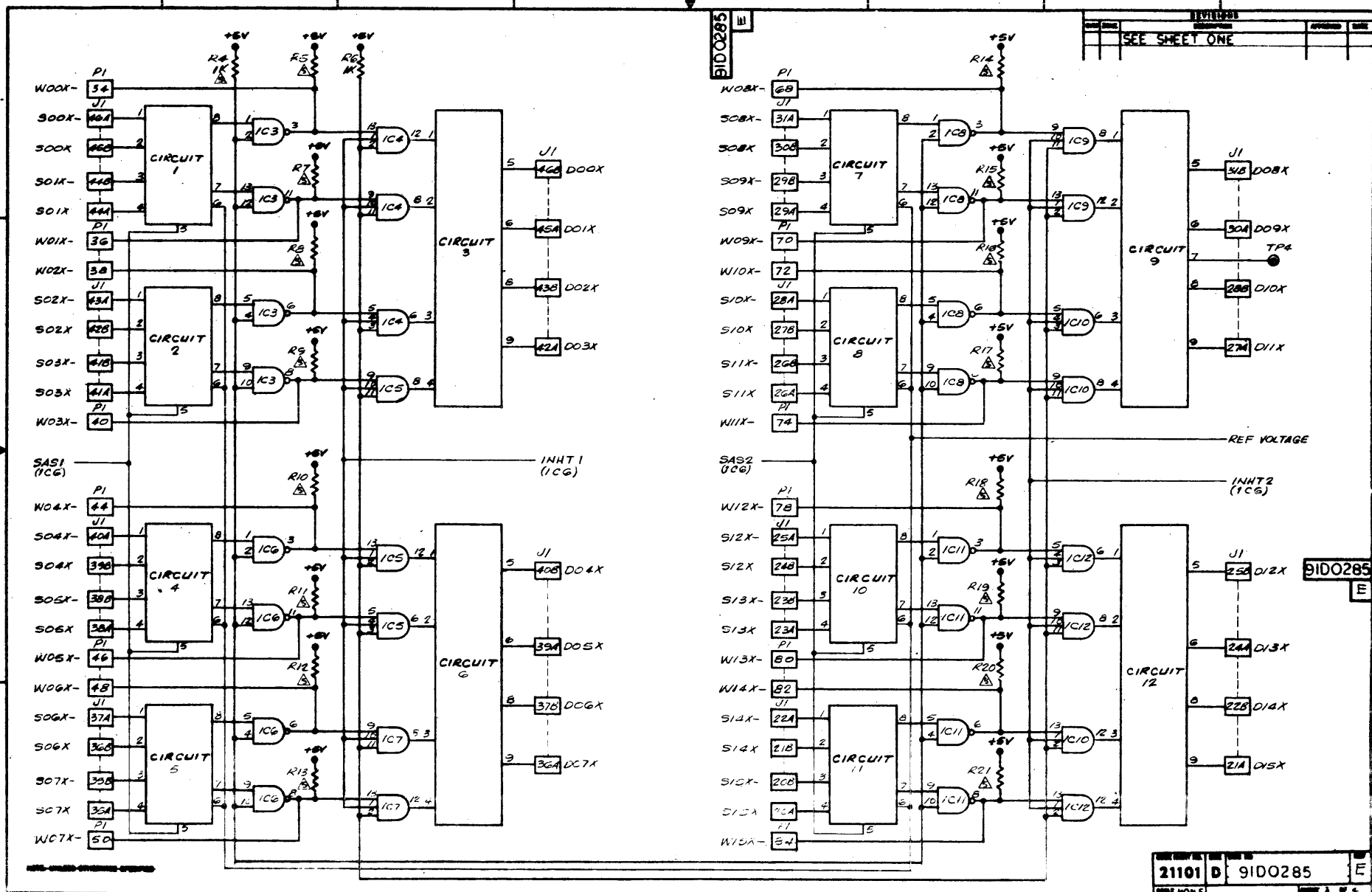


91D0285 E

NOTE: UNLESS OTHERWISE SPECIFIED

CODE IDENT. NO.	REV.	DATE	REV.
21101	D	91D0285	E
SCALE NONE	SHEET 2 OF 4		

TITLE		REV	DATE
SEE SHEET ONE			



91D0285 E

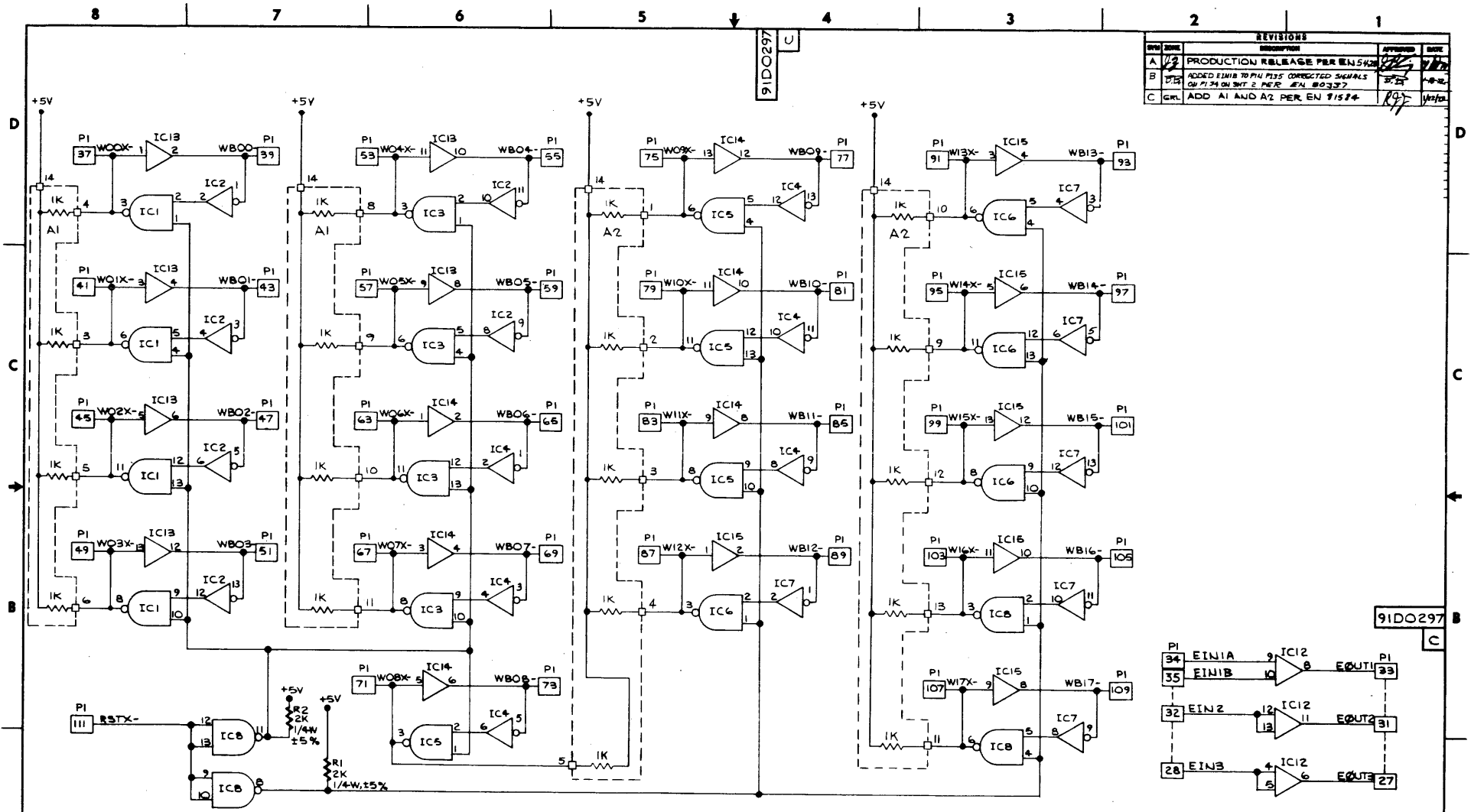
REVISIONS		APPROVED	DATE
SEE SHEET ONE			

CONNECTOR PI			CONNECTOR PI			CONNECTOR PI			CONNECTOR JI			CONNECTOR JI			CONNECTOR JI		
PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET
1	G SIG		42	+5V		85	G SIG		1A	G SIG		39A	005X	3	58A	CCGX-X	
2	G SIG		43	G SIG		86	SASX-		1B	G SIG		39B	SO4X	3	58B	CCGX-X	
3	G SIG		44	W04X-	3	87	G SIG		2A	CA1X-X		40A	SO4X-	3	59A	CA4X-X	
4	G SIG		45	G SIG		88	TSHX		2B	CA1X-X		40B	DO4X	3	59B	CA4X-X	
5	G SIG		46	W05X-	3	89	G SIG		3A	CC1X-X		41A	SO3X	3	60A	CC4X-X	
6	CA0X-X		47	G SIG		90	TSLX		3B	CC1X-X		41B	SO3X-	3	60B	CC4X-X	
7	CC0X-X		48	W06X-	3	91	G SIG		4A	CA3X-X		42A	DO3X	3	61A	CA2X-X	
8	CA2X-X		49	G SIG		92	X50X-X		4B	CA3X-X		42B	SO2X	3	61B	CA2X-X	
9	CC2X-X		50	W07X-	3	93	G SIG		5A	CC3X-X		43A	SO2X-	3	62A	CC2X-X	
10	CA4X-X		51	G SIG		94	X51X-X		5B	CC3X-X		43B	DO2X	3	62B	CC2X-X	
11	CC4X-X		52	W1GX-		95	G SIG		6A	CASX-X		44A	SO1X	3	63A	CA0X-X	
12			53	G SIG		96	X52X-X		6B	CASX-X		44B	SO1X-		63B	CA0X-X	
13	CCGX-X		54	W17X-		97	G SIG		7A	CC5X-X		45A	DO1X	3	64A	CC0X-X	
14			55	G SIG		98	X53X-X		7B	CC5X-X		45B	SO0X		64B	CC0X-X	
15	CAGX-X		56	INH1-	1	99	G SIG		8A	CA7X-X		46A	SO0X-		65A	G SIG	
16			57	G SIG		100	X54X-X		8B	CA7X-X		46B	DO0X	3	65B	G SIG	
17	G SIG		58	9SLX-	1	101	G SIG		9A	CC7X-X		47A	G SIG				
18	YSGX-X		59	G SIG		102	X55X-X		9B	CC7X-X		47B	G SIG				
19	G SIG		60	9ASX-	1	103	G SIG		10A			48A	G SIG				
20	YS7X-X		63	G SIG		104	X56X-X		10B			48B	G SIG				
21	G SIG		64	INH2-X	1	105	CA7X-X		11A			49A	G SIG				
22	YS4X-X		65	G SIG		106	X57X-X		11B			49B	G SIG				
23	G SIG		66	G SIG		107	CASX-X		12A			50A	YS1X-X				
24	YS5X-X		67	G SIG		108			12B			50B	YS0X-X				
25	G SIG		68	W08X-	3	109	CA3X-X		13A	X5GX-X		51A	YS3X-X				
26	YS2X-X		69	G SIG		110	CC7X-X		13B	X57X-X		51B	YS2X-X				
27	G SIG		70	W09X-	3	111	CC1X-X		14A	X54X-X		52A	YS5X-X				
28	YS3X-X		71	G SIG		112	CC5X-X		14B	X55X-X		52B	YS4X-X				
29	G SIG		72	W10X-	3	113	CA1X-X		15A	X52X-X		53A	YS7X-X				
30	YS0X-X		73	G SIG		114	CC3X-X		15B	X53X-X		53B	YS6X-X				
31	G SIG		74	W11X-	3	115	-5V		16A	X50X-X		54A					
32	YS1X-X		75	G SIG		116	+5V		16B	YS1X-X		54B					
33	G SIG		76	G SIG		117	+12V		17A	TSLX		55A					
34	W00X-	5	77	G SIG		118	+12V		17B	TSLX		55B					
35	G SIG		78	W12X-	3	119	+12V		18A	G SIG		56A					
36	W01X-	3	79	G SIG		120	+12V		18B	G SIG		56B					
37	G SIG		80	W13X-	3	121	G SIG		19A	TSHX		57A	CAGX-X				
38	W02X-	3	81	G SIG		122	G SIG		19B	TSHX		57B	CAGX-X				
39	G SIG		82	W14X-	3												
40	W03X-	3	83	G SIG													
41	G SIG		84	W15X-	3												

91D0285 E

NOTE: UNLESS OTHERWISE SPECIFIED

CONF. CONTROL NO.	REV.	DATE	BY
21101	D	91D0285	E
SHEET 4 OF 4			



REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
A	1/2	PRODUCTION RELEASE PER EN 528	[Signature]
B	1/25	ADDED EINB TO PIN 115 CORRECTED SIGNALS ON P124 ON SMT 2 PER EN 80337	[Signature]
C	GR	ADD A1 AND A2 PER EN 91574	[Signature]

A

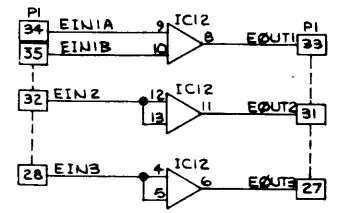
1. IC POWER & GROUND DISTRIBUTION
 +5V - PIN 14 FOR ALL IC'S
 GRD - PIN 7 FOR ALL IC'S

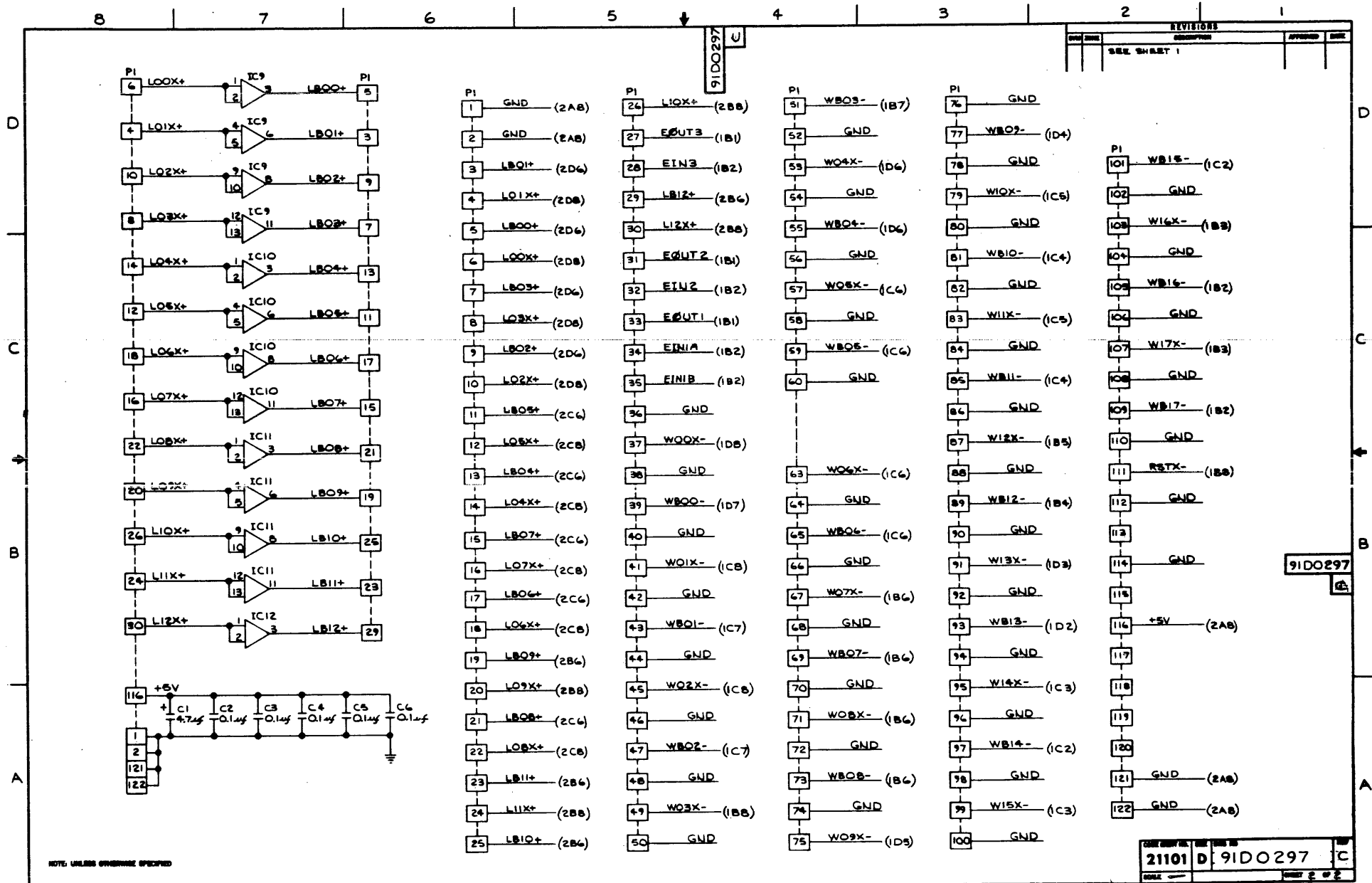
NOTE, UNLESS OTHERWISE SPECIFIED

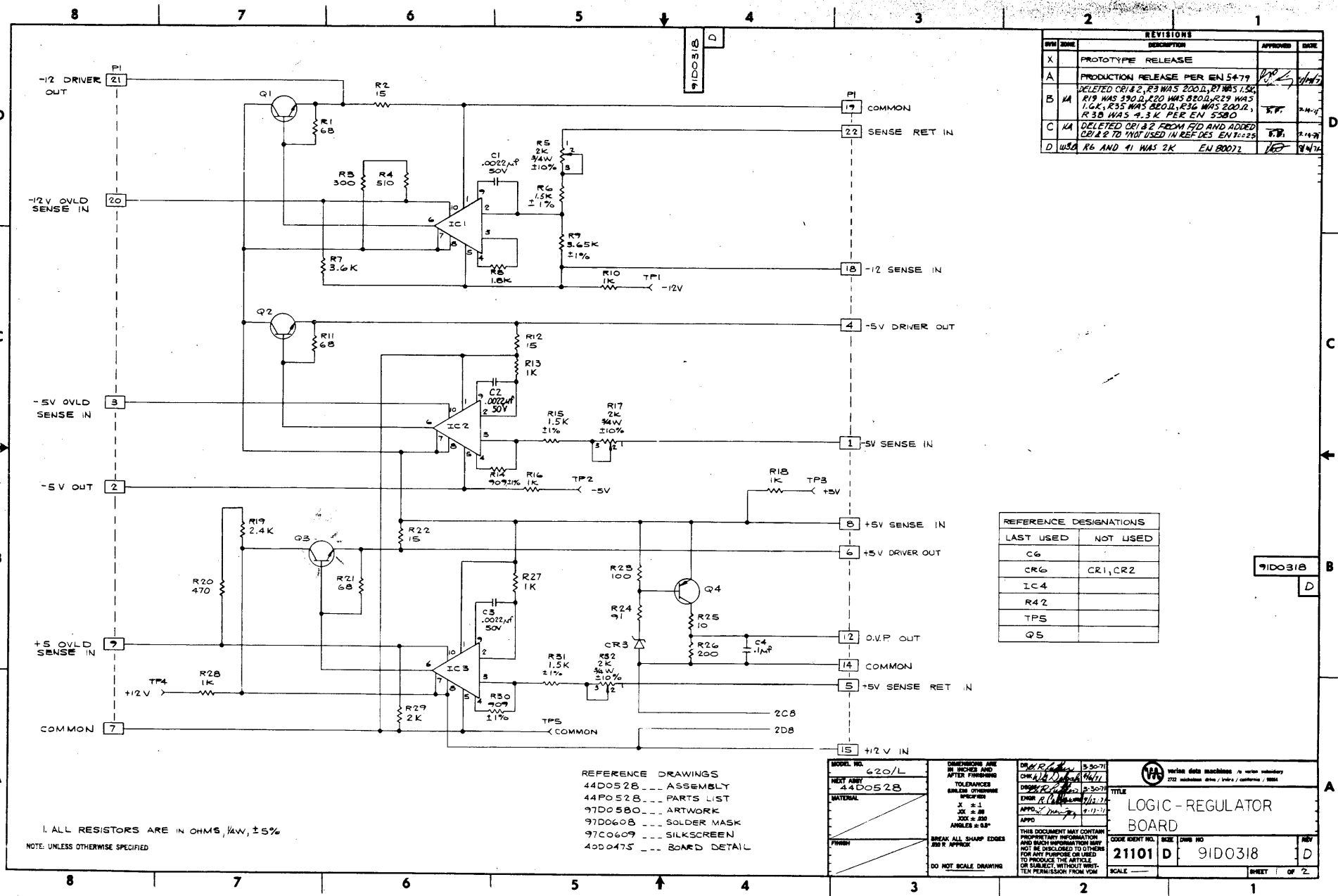
REFERENCE DWGS
 44D0521 ASSEMBLY
 44P0521 PARTS LIST
 40D0469 BD DETAIL

REFERENCE DESIGNATIONS	
USED	NOT USED
R1,2	
C1 THRU C6	
IC1 THRU IC16	
P1	
A2	

MODEL NO G2OL PART NO 44D0521	THIS DOCUMENT IS THE PROPERTY OF THE COMPANY AND IS NOT TO BE REPRODUCED OR DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT WITHOUT WRITTEN PERMISSION FROM YOU	TITLE LOGIC DIAG - MEMORY BUFFER
CODE BODY NO 21101 D	PART NO 91D0297	SHEET 2







REV. NO.		DESCRIPTION	APPROVED	DATE
X		PROTOTYPE RELEASE		
A		PRODUCTION RELEASE PER EN 5479		
B	1A	DELETED CR1, R3 WAS 200Ω, R7 WAS 1.5K, R19 WAS 550Ω, R20 WAS 800Ω, R29 WAS 1.6K, R35 WAS 800Ω, R36 WAS 200Ω, R38 WAS 4.3K PER EN 5580	T.P.	2-14-79
C	1A	DELETED CR1 & 2 FROM FID AND ADDED CR1 & 2 TO *NOT USED IN REF DES EN 10025	T.P.	2-14-79
D	USA	R6 AND 41 WAS 2K EN 80073	T.P.	2-14-79

REFERENCE DESIGNATIONS	
LAST USED	NOT USED
C6	CR1, CR2
CR6	
IC4	
R42	
TP5	
Q5	

- REFERENCE DRAWINGS
 44D052B --- ASSEMBLY
 44P052B --- PARTS LIST
 97D0580 --- ARTWORK
 97D0608 --- SOLDER MASK
 97C0609 --- SILKSCREEN
 4000475 --- BOARD DETAIL

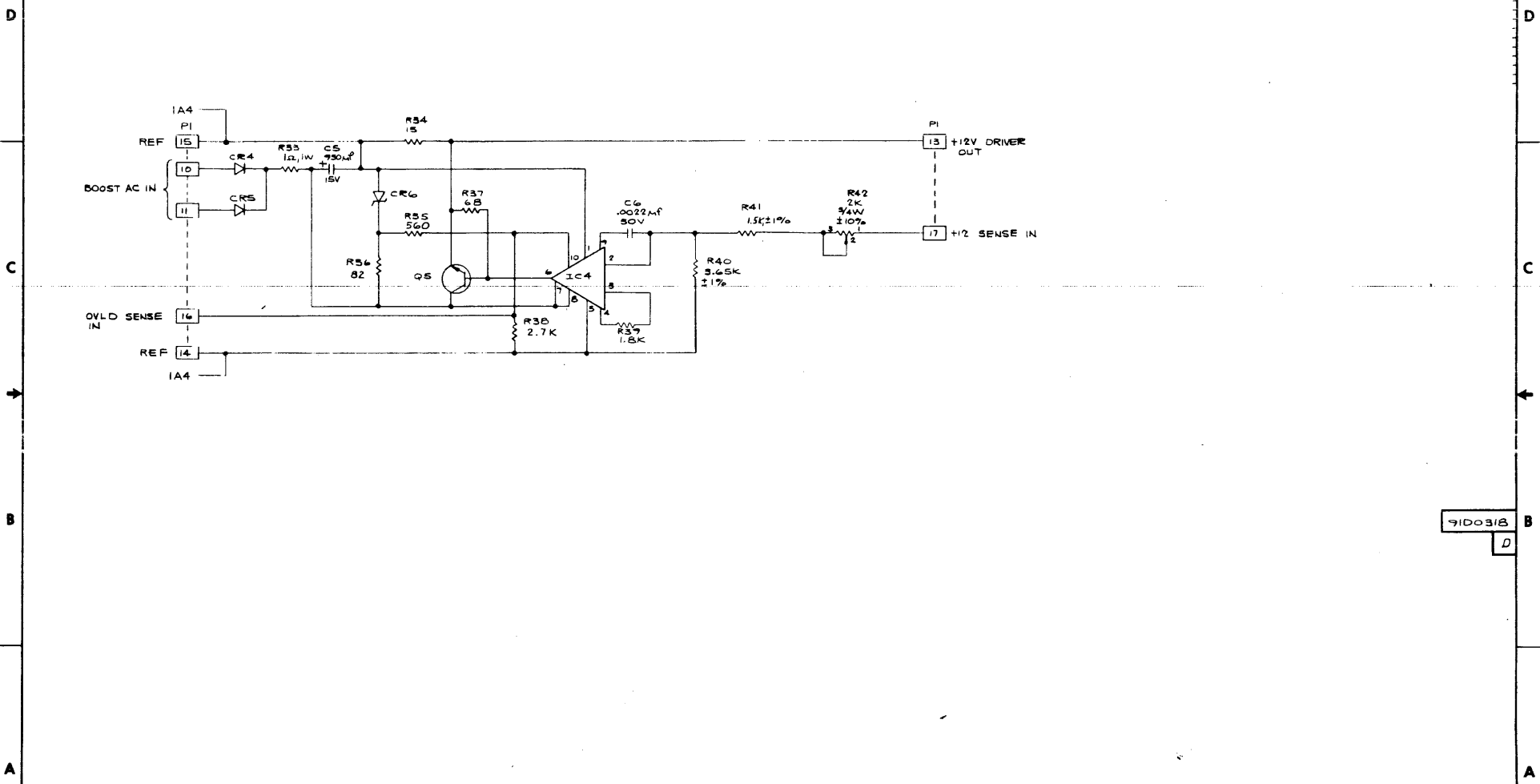
ALL RESISTORS ARE IN OHMS, $\frac{1}{4}$ W, $\pm 5\%$
 NOTE: UNLESS OTHERWISE SPECIFIED

MODEL NO. 44D052B	DESIGNER AND DATE R. J. ... 11/11/77	DRW. NO. 3-30-77	DATE 11/11/77
NEXT ASSY 44D052B	TOLERANCES UNLESS OTHERWISE SPECIFIED	CHK. R. J. ... 11/11/77	DATE 11/11/77
MATERIAL	FINISH	ENGR. R. J. ... 11/11/77	DATE 11/11/77
BREAK ALL SHARP EDGES AND R APPROP.		APPROV. R. J. ... 11/11/77	DATE 11/11/77
DO NOT SCALE DRAWING		APPD.	
THIS DOCUMENT MAY CONTAIN PROPRIETARY INFORMATION WHICH IS UNCLASSIFIED EXCEPT WHERE SHOWN OTHERWISE. IT IS UNCLASSIFIED FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE HEREIN WITHOUT WRIT- TEN PERMISSION FROM VSC.			
SCALE	CODE IDENT NO. 21101 D	SIZE D	REV D
	TITLE LOGIC-REGULATOR BOARD	DATE 11/11/77	SHEET 1 OF 2

91D0318

REVISIONS			
BY	ZONE	DESCRIPTION	DATE
		SEE SHEET 1	

8 7 6 5 4 3 2 1

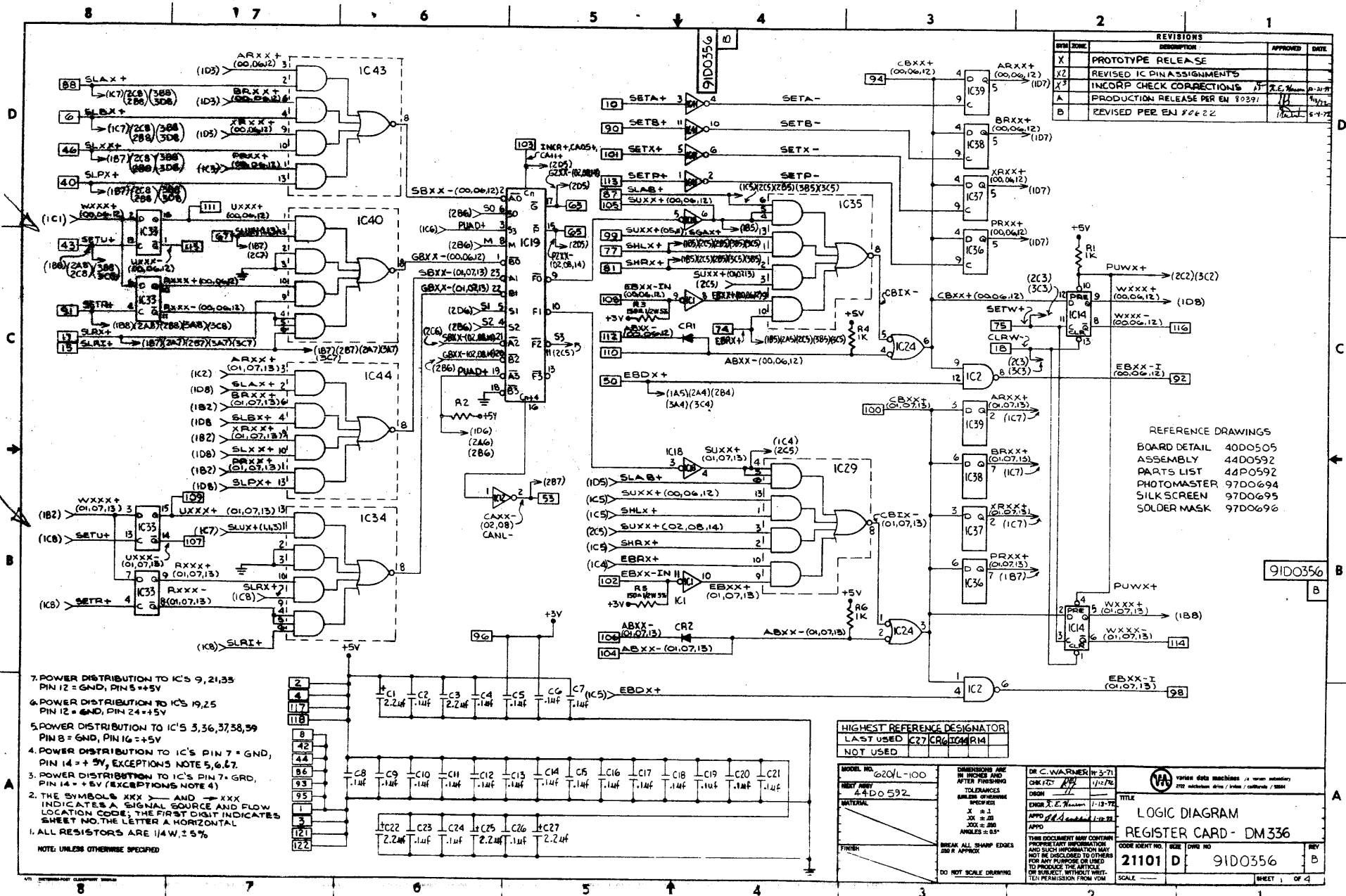


91D0318
D

NOTE: UNLESS OTHERWISE SPECIFIED

CODE	REV	NO	REV	NO
21101	D	91D0318		D
SCALE				SHEET 2 OF 2

8 7 6 5 4 3 2 1



REVISIONS				
SYM	ZONE	DESCRIPTION	APPROVED	DATE
X		PROTOTYPE RELEASE		
X2		REVISED IC PIN ASSIGNMENTS		
X3		INCORP CHECK CORRECTIONS		
A		PRODUCTION RELEASE PER EN 1939		
B		REVISED PER EN 20622		

REFERENCE DRAWINGS
 BOARD DETAIL 400505
 ASSEMBLY 440592
 PARTS LIST 44P0592
 PHOTOMASTER 9700694
 SILK SCREEN 9700695
 SOLDER MASK 9700696

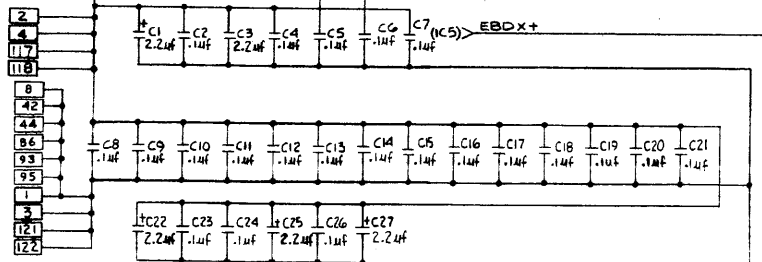
HIGHEST REFERENCE DESIGNATOR	IC27
LAST USED	IC6, IC10, IC14, IC18
NOT USED	

MODEL NO.	620L-100
DATE	44D0592
MATERIAL	
FINISH	

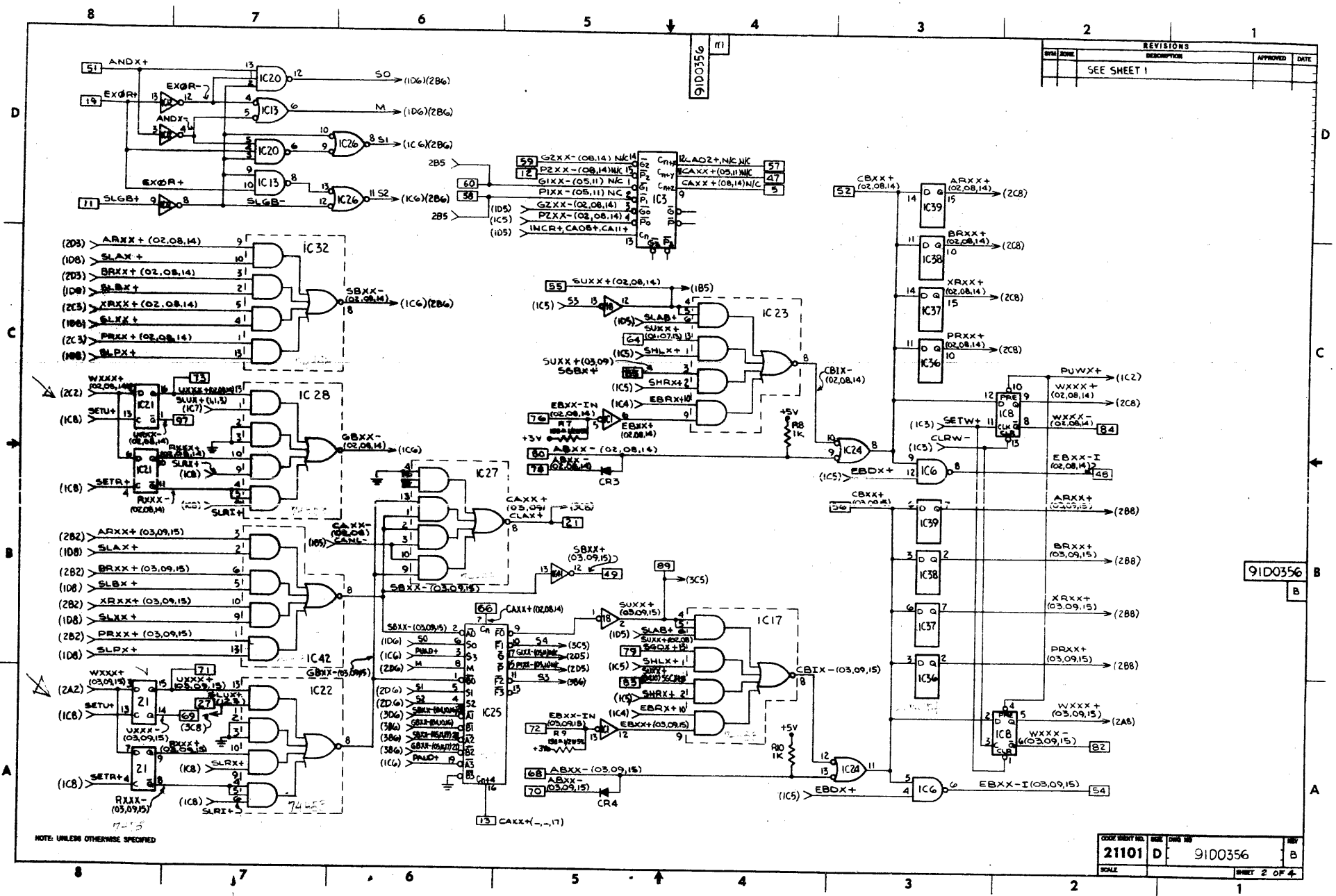
DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED TOLERANCES FRACTIONS DECIMALS ANGLES UNLESS OTHERWISE SPECIFIED	DR. C. W. WALKER CHK. F. J. ... DESIGNED BY ENGR. J. E. ... APPROVED BY DATE
--	---

TITLE LOGIC DIAGRAM REGISTER CARD - DM 336	
CODE IDENT NO. 21101 D	DRAWING NO. 91D0356
SCALE ---	SHEET 1 OF 4

- POWER DISTRIBUTION TO IC'S 9, 21, 35
PIN 12 = GND, PIN 5 = +5V
- POWER DISTRIBUTION TO IC'S 19, 25
PIN 12 = GND, PIN 24 = +5V
- POWER DISTRIBUTION TO IC'S 3, 36, 37, 38, 39
PIN 8 = GND, PIN 16 = +5V
- POWER DISTRIBUTION TO IC'S PIN 7 = GND,
PIN 14 = +5V, EXCEPTIONS NOTE 5, 6, 7.
- POWER DISTRIBUTION TO IC'S PIN 7, GRD,
PIN 14 = +5V (EXCEPTIONS NOTE 4)
- THE SYMBOLS \times AND \rightarrow INDICATE A SIGNAL SOURCE AND FLOW LOCATION CODE; THE FIRST CHIT INDICATES SHEET NO. THE LETTER A HORIZONTAL
- ALL RESISTORS ARE 1/4W, 5%
NOTE: UNLESS OTHERWISE SPECIFIED

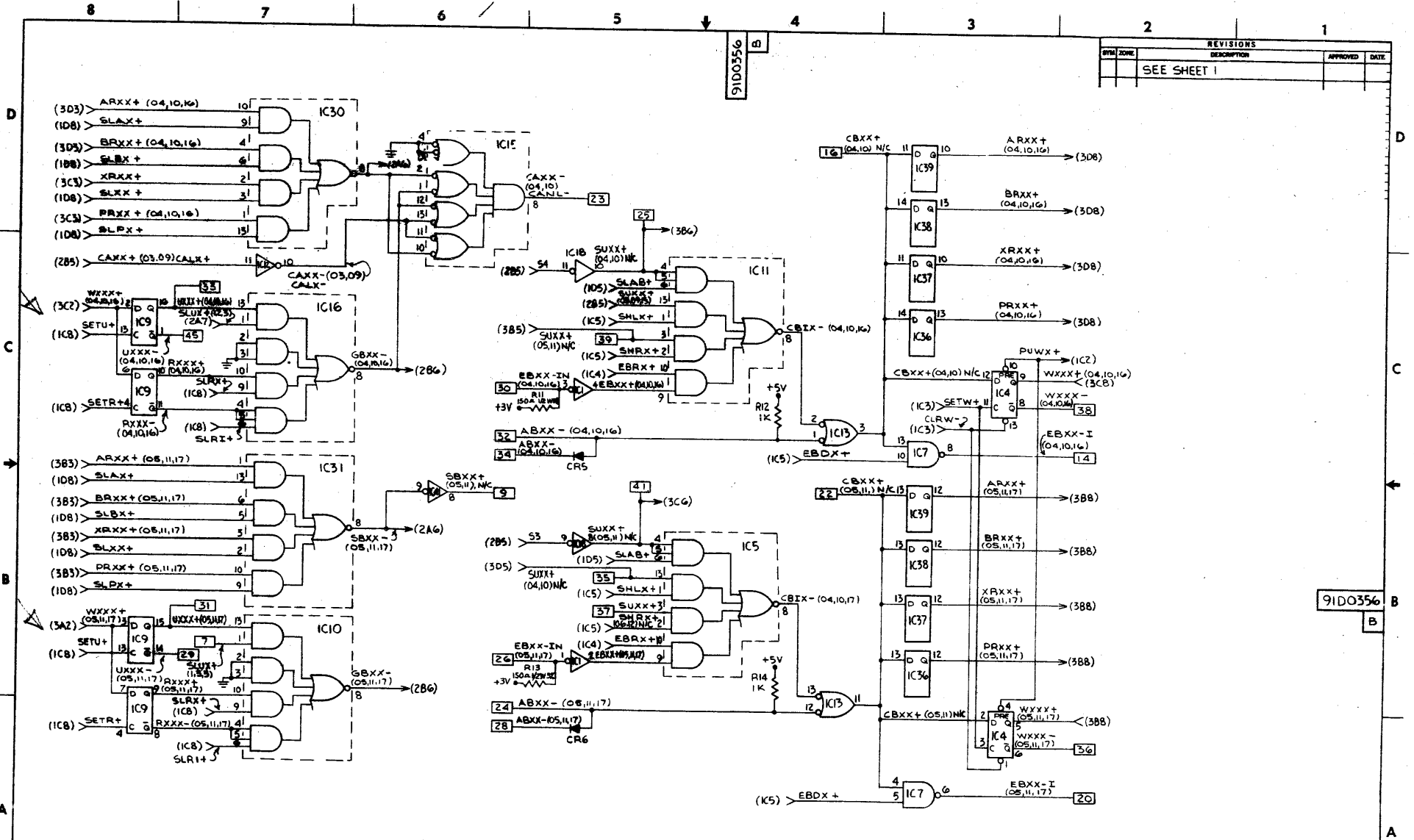


REVISIONS				
REV	ZONE	DESCRIPTION	APPROVED	DATE
		SEE SHEET 1		



CODE ENTRY NO.	REV	DATE	REV
21101	D	91D0356	B
SCALE		SHEET 2 OF 4	

REVISIONS				
SYN	ZONE	DESCRIPTION	APPROVED	DATE
		SEE SHEET 1		

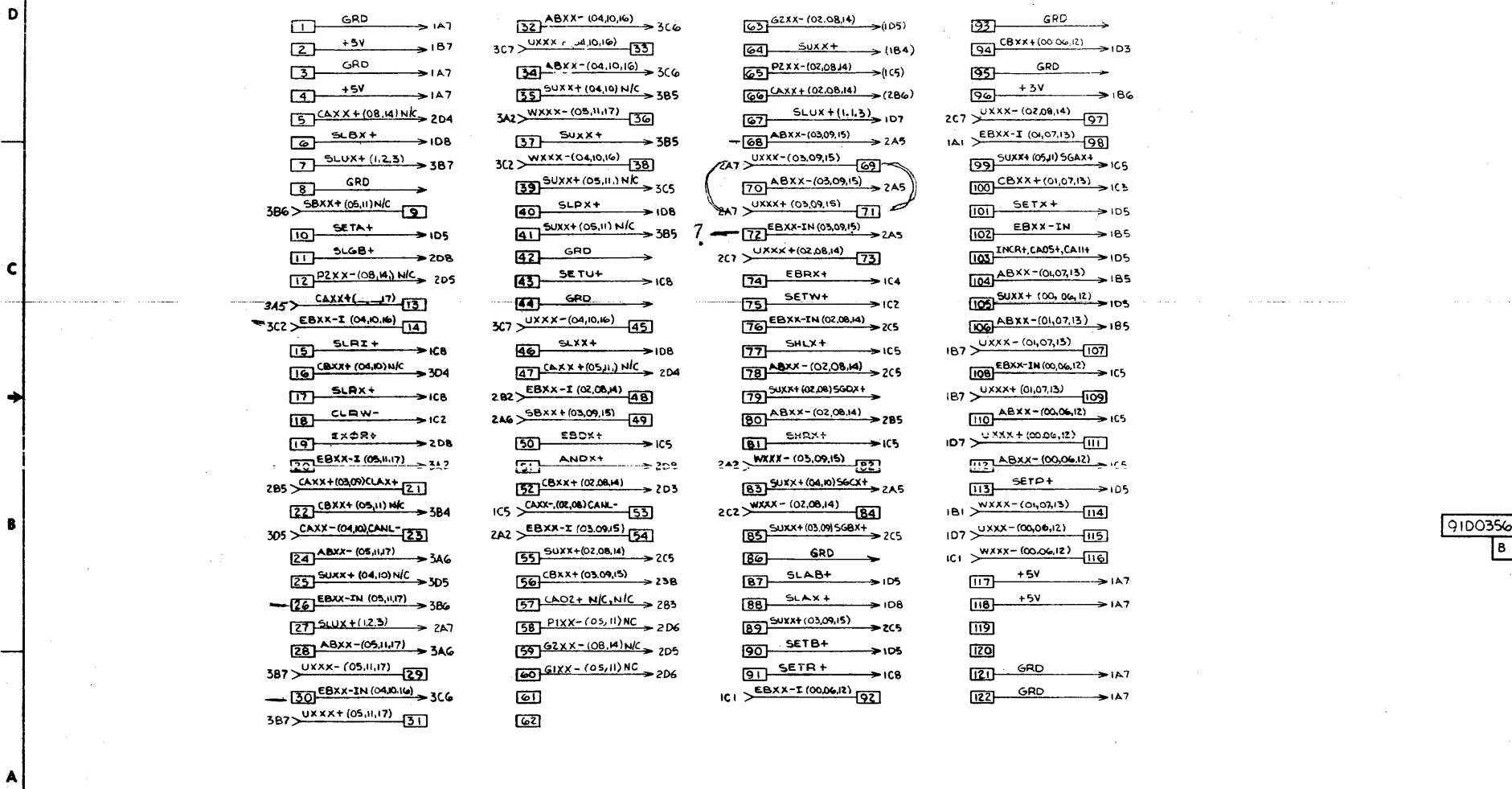


NOTE: UNLESS OTHERWISE SPECIFIED

CODE IDENT. NO.	REV.	DATE	BY
21101	D	91D0356	B
SCALE	SHEET 2 OF 4		

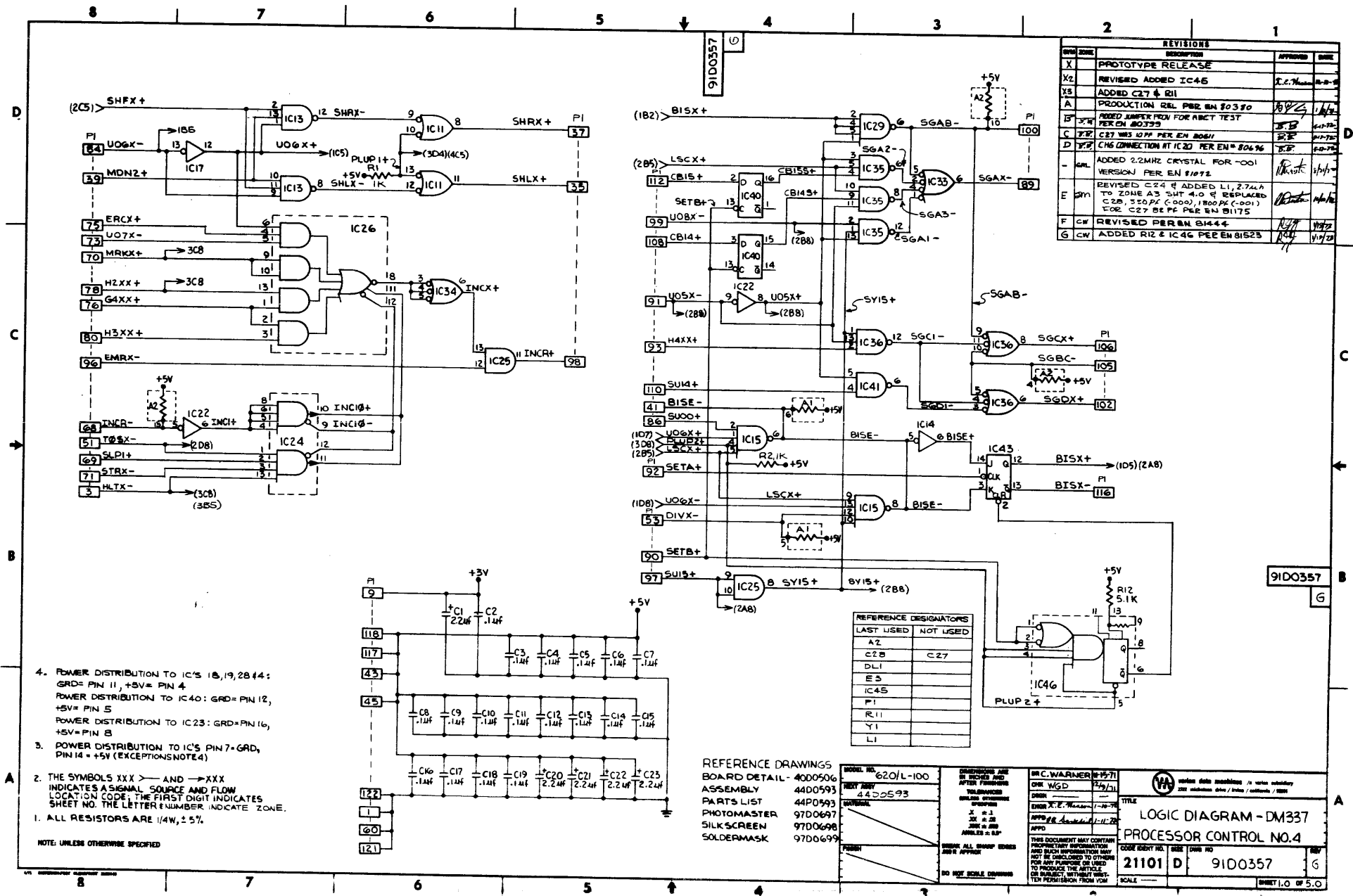
91D0356

REVISIONS			
SYM	ZONE	DESCRIPTION	DATE
		SEE SHEET 1	



NOTE: UNLESS OTHERWISE SPECIFIED

CODE IDENT. NO.	REV.	DATE	NO.
21101	D	91D0356	B
SCALE			SHEET 4 OF 4



REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
X		PROTOTYPE RELEASE	
X2		REVISED ADDED IC46	J.C. Warner
Y5		ADDED C27 & R11	
A		PRODUCTION REL PER EN #0380	
B		FIXED JUMPER FROM FOR ABCT TEST PER EN #0339	
C		C27 WAS 10PF PER EN 20011	
D		CHG CONNECTION AT IC20 PER EN #0496	
-		ADD. ADDED 2.2MHZ CRYSTAL FOR -001 VERSION PER EN #1072	
E		REVISED C24 & ADDED L1, 2.7UH TO ZONE A3 SUP 4.0 & REPLACED C26, 350PF (000), 1800PF (001) FOR C27 BE PF PER EN 01175	
F		REVISED PER EN 01444	
G		ADDED R12 & IC46 PER EN 01523	

REFERENCE DESIGNATORS	
LAST USED	NOT USED
A2	C27
C28	
DL1	
E3	
IC45	
P1	
R11	
Y1	
L1	

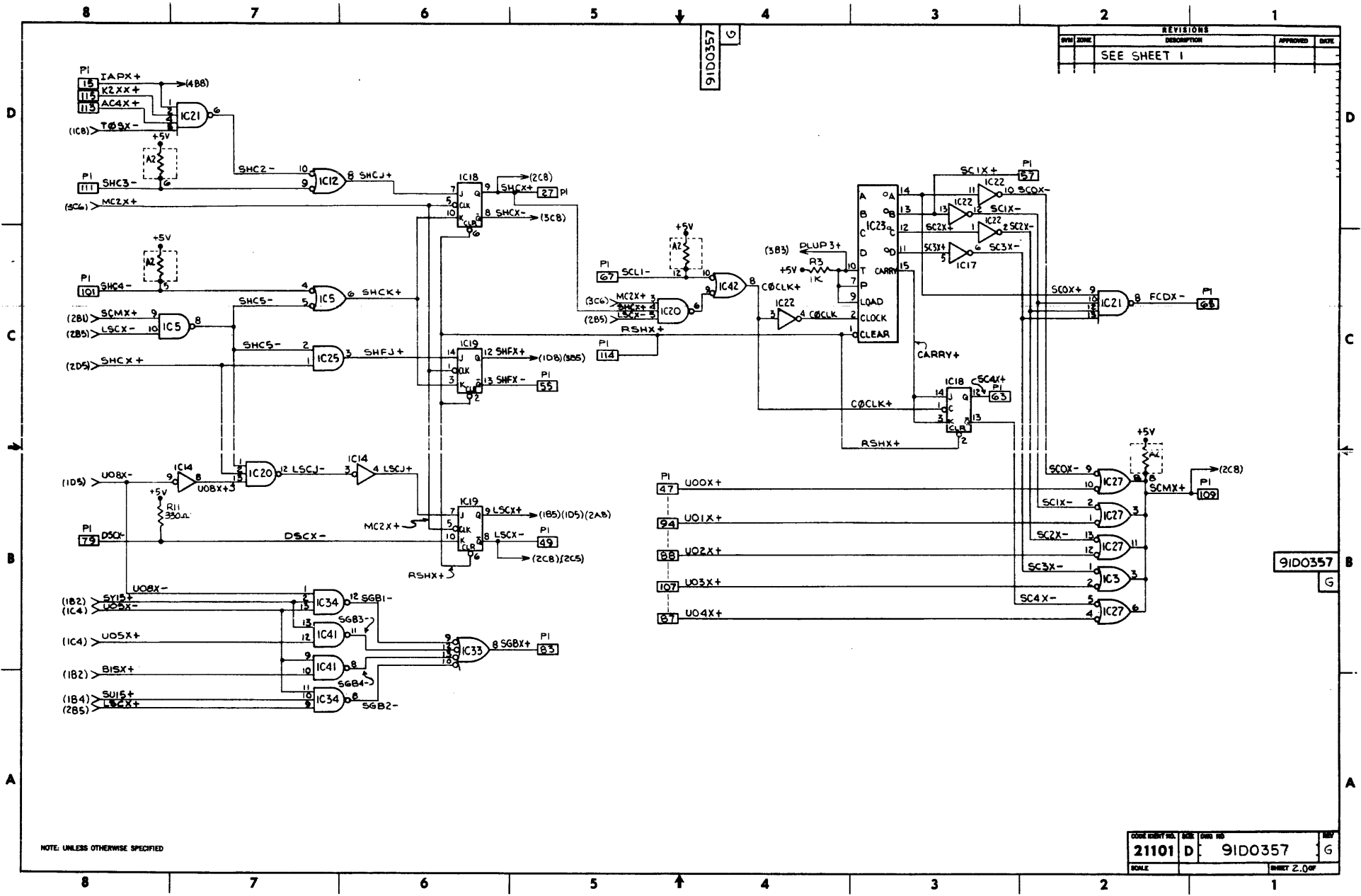
REFERENCE DRAWINGS
 BOARD DETAIL - 4000506
 ASSEMBLY - 4400593
 PARTS LIST - 44P0593
 PHOTOMASTER - 97D0697
 SILKSCREEN - 97D0698
 SOLDERMASK - 97D0699

MODEL NO. 620/L-100 REV. 001 4400593	DIMENSIONS ARE IN INCHES AND AFTER FINISHES TOLERANCES UNLESS OTHERWISE SPECIFIED X = ±.1 XX = ±.05 XXX = ±.02 XXXX = ±.01 FINISH: ALL GROUP ENDS AND R APPROX DO NOT SCALE DRAWING	MR. C. WARNER #1571 ORK WGD #29/71 SHEET: 12/12 ENGR. X. E. Warner 1-24-71 APPROV. [Signature] 1-24-71 APPD. [Signature]	TITLE LOGIC DIAGRAM - DM337 PROCESSOR CONTROL NO.4 CODE IDENT. NO. 21101 DES. DATE NO. D DWG. NO. 91D0357 SCALE: --- SHEET 1 OF 5.0
--	--	---	--

- POWER DISTRIBUTION TO IC'S 18,19,20 & 44:
GRD= PIN 11, +5V= PIN 4
POWER DISTRIBUTION TO IC40: GRD= PIN 12, +5V= PIN 5
POWER DISTRIBUTION TO IC23: GRD=PIN 16, +5V= PIN 8
 - POWER DISTRIBUTION TO IC'S PIN 7: GAD, PIN 14 + 5V (EXCEPTIONS NOTE 4)
 - THE SYMBOLS XXX > AND > XXX INDICATES A SIGNAL SOURCE AND FLOW LOCATION CODE; THE FIRST DIGIT INDICATES SHEET NO. THE LETTER # NUMBER INDICATE ZONE.
 - ALL RESISTORS ARE 1/4W, ± 5%.
- NOTE: UNLESS OTHERWISE SPECIFIED

REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	

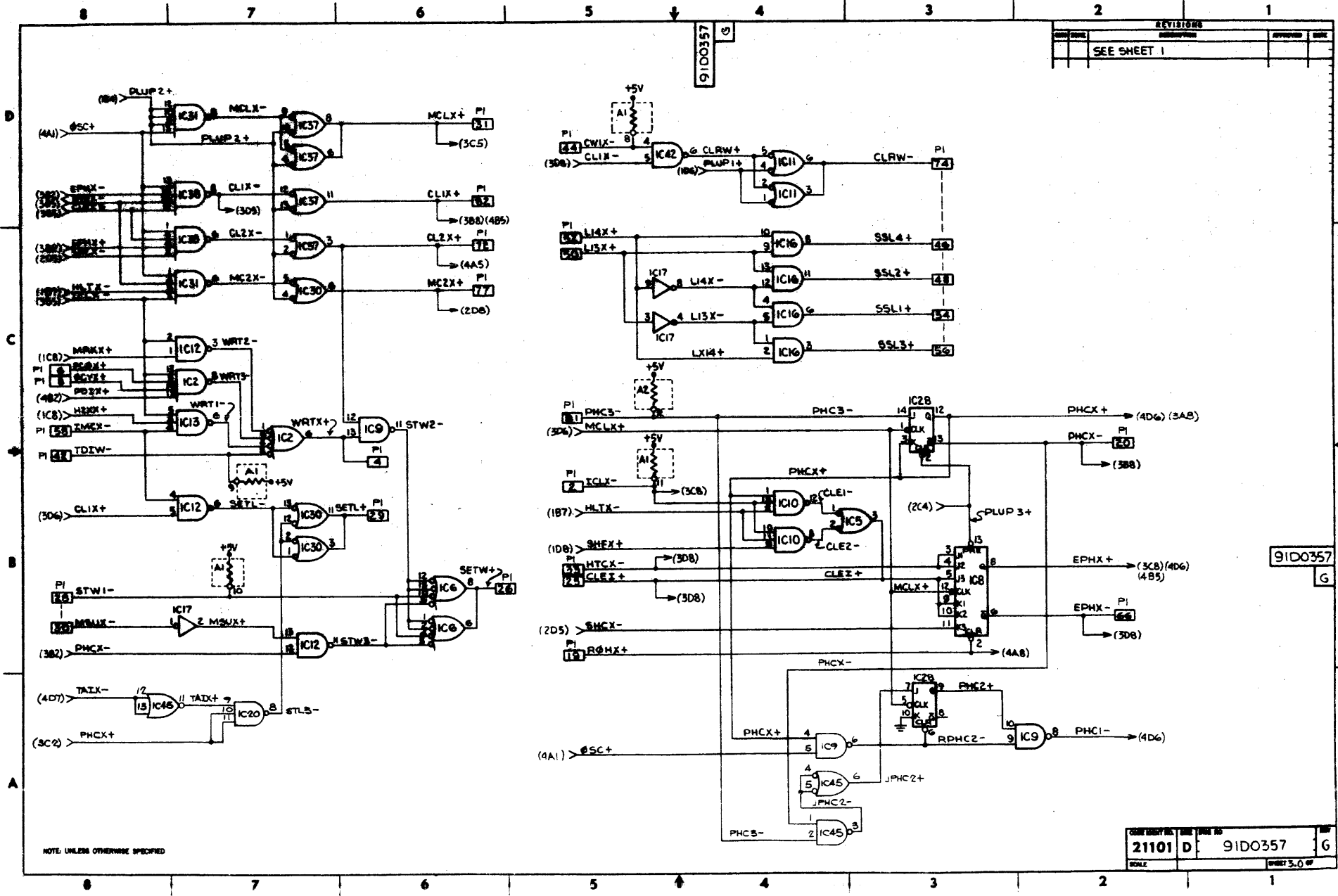
91D0357
G



NOTE: UNLESS OTHERWISE SPECIFIED

DATE	REV	REV NO	REV
21101	D	91D0357	G
SCALE			SHEET 2 OF 2

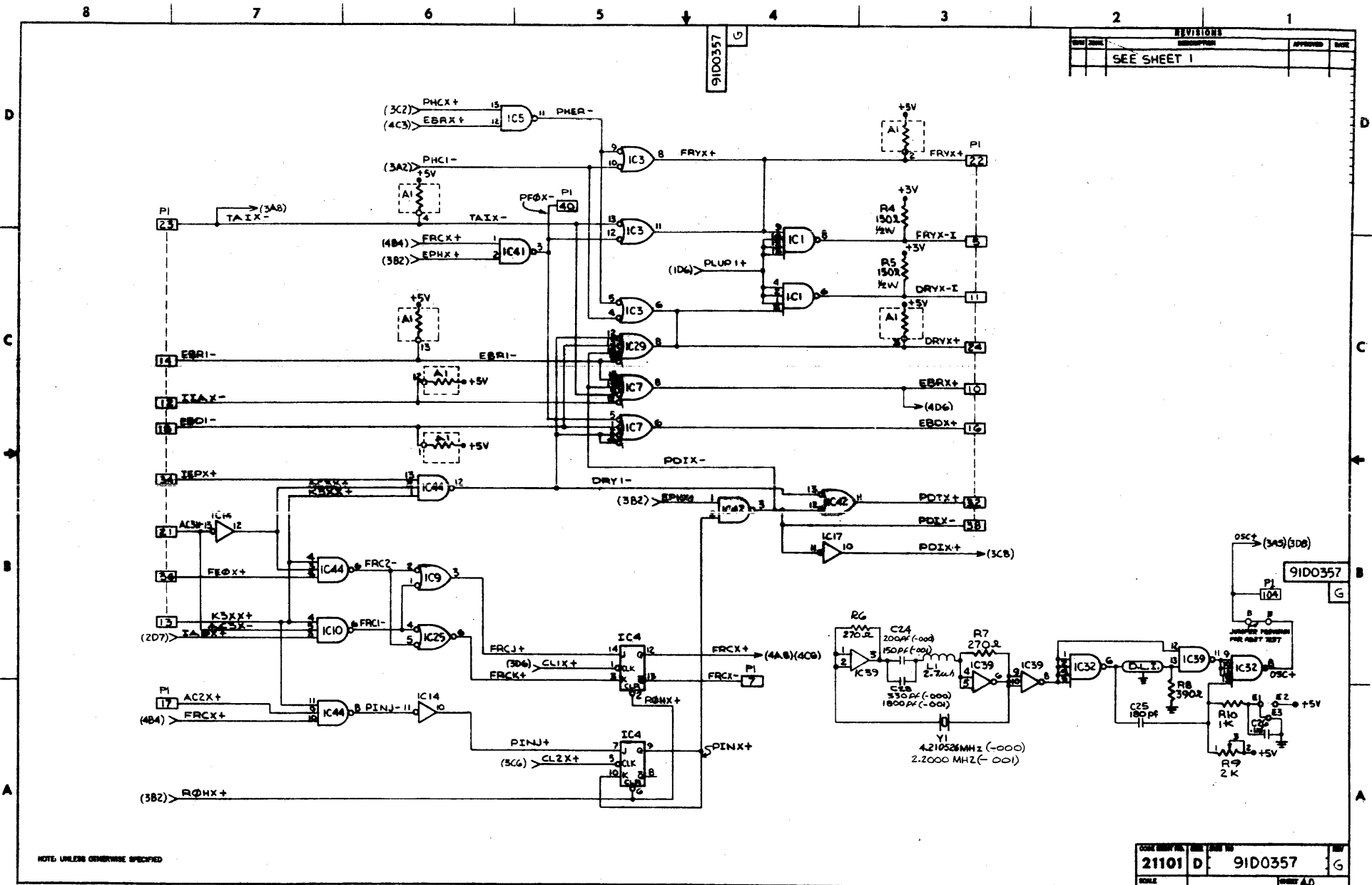
REVISIONS			
NO.	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	



NOTE: UNLESS OTHERWISE SPECIFIED

FORM NO. 100	REV. 1	DATE	NO.
21101	D	91D0357	G
SCALE		SHEET 3.0 OF	

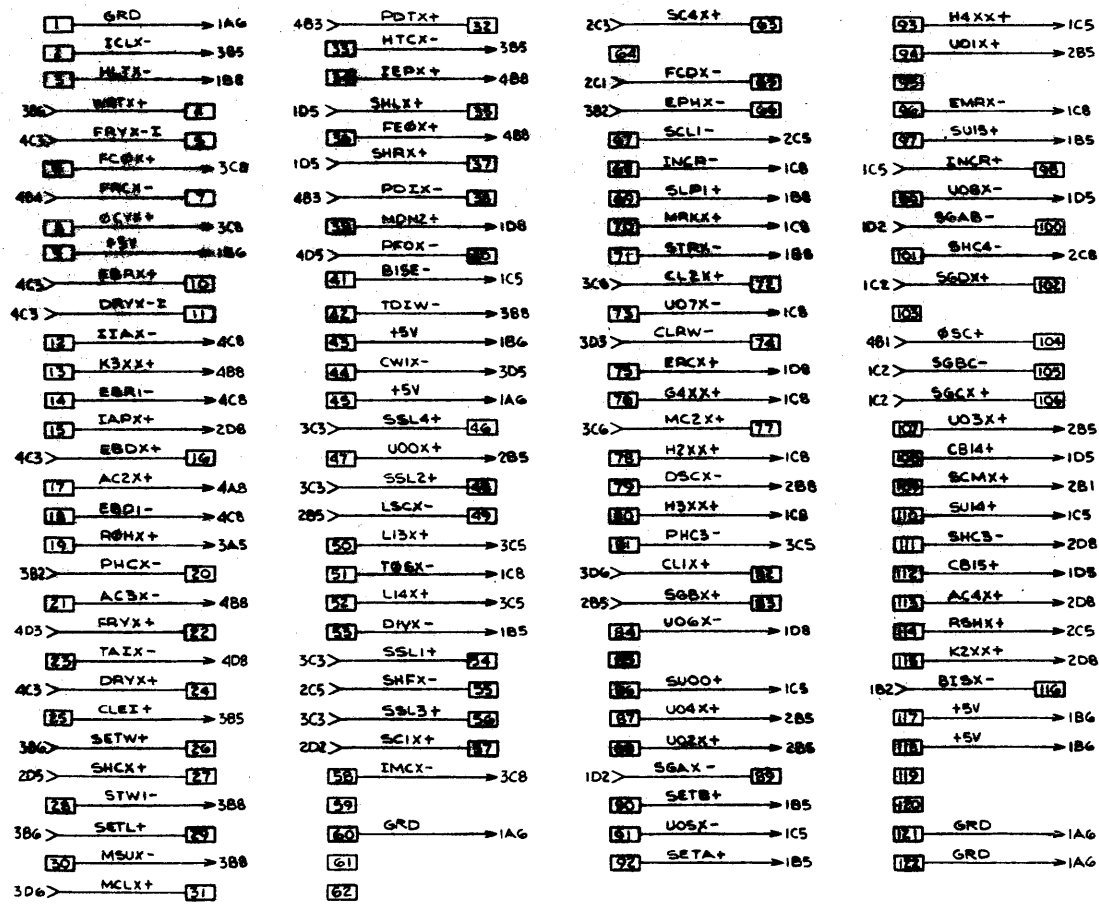
REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	



21101	D	91D0357	G
SCALE		SHEET 4.0	

REVISIONS	
SEE SHEET 1	

91D0357

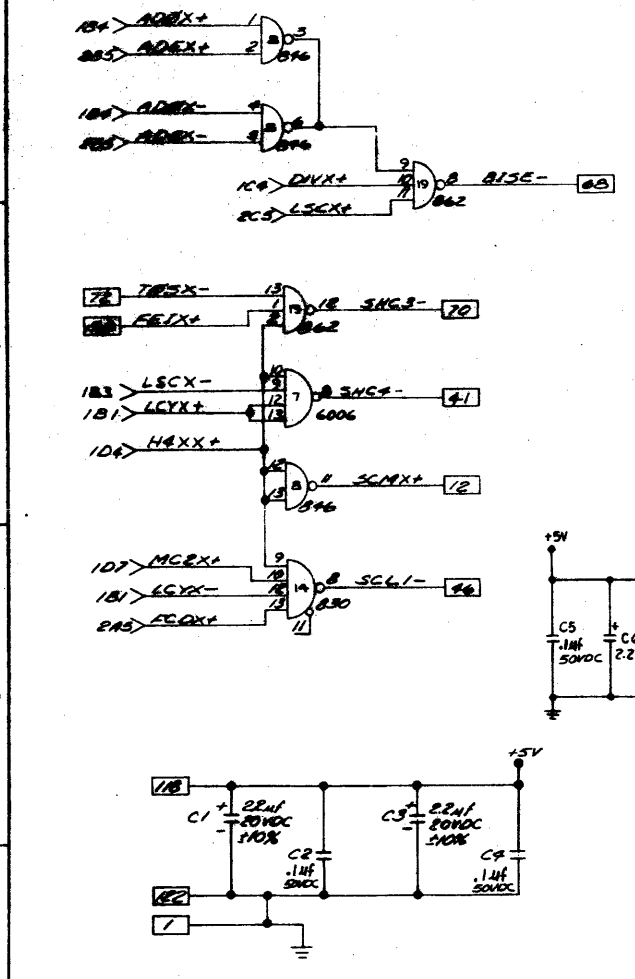


91D0357

NOTE: UNLESS OTHERWISE SPECIFIED

CONNECTOR PIN FUNCTIONS			
21101	D	91D0357	G

REVISION		APPROVED	DATE
SYM	DESC		
1 - MAY BE REWORKED		2 - NEW SHOP PRACTICE	
3 - CANNOT BE REWORKED		4 - RECORD CHANGE	
5 - PARTS MADE OK			
SEE SHIT 10F 3 FOR REVISION			

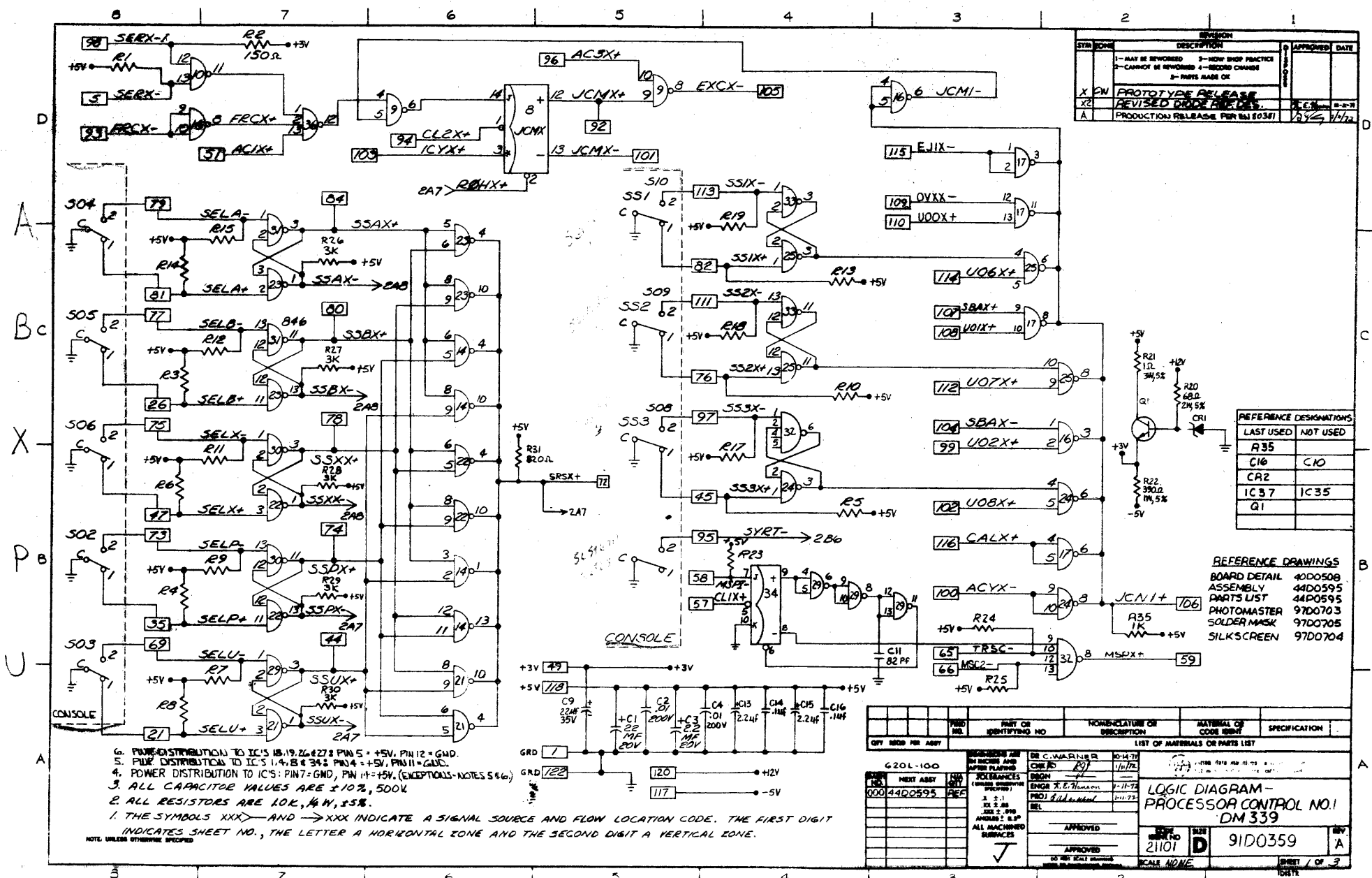


- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10 PHCX-
- 11
- 12 SCMX+
- 13 MDIX+
- 14
- 15 SCIX+
- 16
- 17 SCIX+ (16 BIT)
CB/5+ (16 BIT)
- 18 DSCX-
- 19
- 20
- 21 DIVX+
- 22
- 23 DIVX-
- 24
- 25
- 26 SHX+ (16 BIT)
SUI3+ (16 BIT)
- 27
- 28 SCAB-
- 29
- 30 DIVX-
- 31 LSCX-
- 32 UOX+
- 33
- 34 UOX+
- 35
- 36 UOX+
- 37 UOX+
- 38
- 39 UOX-
- 40 SHC4-
- 41
- 42 UOX-
- 43
- 44 SHEX-
- 45 UOX-
- 46 SCL1-
- 47 HAXX-
- 48 SETB+
- 49 SHCX+
- 50 CBOO+
- 51
- 52 CBZ+ (16 BIT)
CB/5+ (16 BIT)
- 53
- 54 SETR+
- 55
- 56 MCIX+
- 57
- 58 ICLX-
- 59
- 60 PHC3-
- 61
- 62
- 63
- 64 SCL5-

- 65
- 66 FCIX-
- 67
- 68
- 69
- 70 SHC3-
- 71
- 72 TBSX-
- 73
- 74 SETR-
- 75 EMRT-
- 76 SCL1+
- 77
- 78 RBHX+
- 79
- 80 SCBC-
- 81
- 82 SUI+ (16 BIT)
SUI7+ (16 BIT)
- 83
- 84 BLSX-
- 85
- 86 CLEX+
- 87 SYRT+
- 88
- 89 FCIX+
- 90
- 91
- 92 SBAX+
- 93 ACIX+
- 94 MUX- (16 BIT)
MUX- (16 BIT)
- 95
- 96 SBAX+
- 97
- 98
- 99 UOX+
- 100 ACIX+
- 101 UOX-
- 102
- 103 UOX+
- 104
- 105 UOX-
- 106
- 107 UOX+
- 108 ACIX+
- 109
- 110 ACIX+
- 111 UOX-
- 112
- 113
- 114
- 115 SCLX+
- 116
- 117
- 118 +5V
- 119
- 120
- 121
- 122

QTY	REQ	PART OR IDENTIFYING NO	NON-RELATION OR DESCRIPTION	MANUAL OR CODE IDENT	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST					
<p>APPROVED</p> <p>DATE</p> <p>BY</p> <p>SCALE</p> <p>DO NOT SIGN THESE</p> <p>DATE OF APPROVAL</p>					
<p>LOGIC DIAGRAM</p> <p>DM 338</p> <p>MULTIPLY/DIVIDE & E.A.</p>		<p>21101</p> <p>D</p> <p>91D0358</p>		<p>12</p> <p>12</p>	

NOTE: UNLESS OTHERWISE SPECIFIED



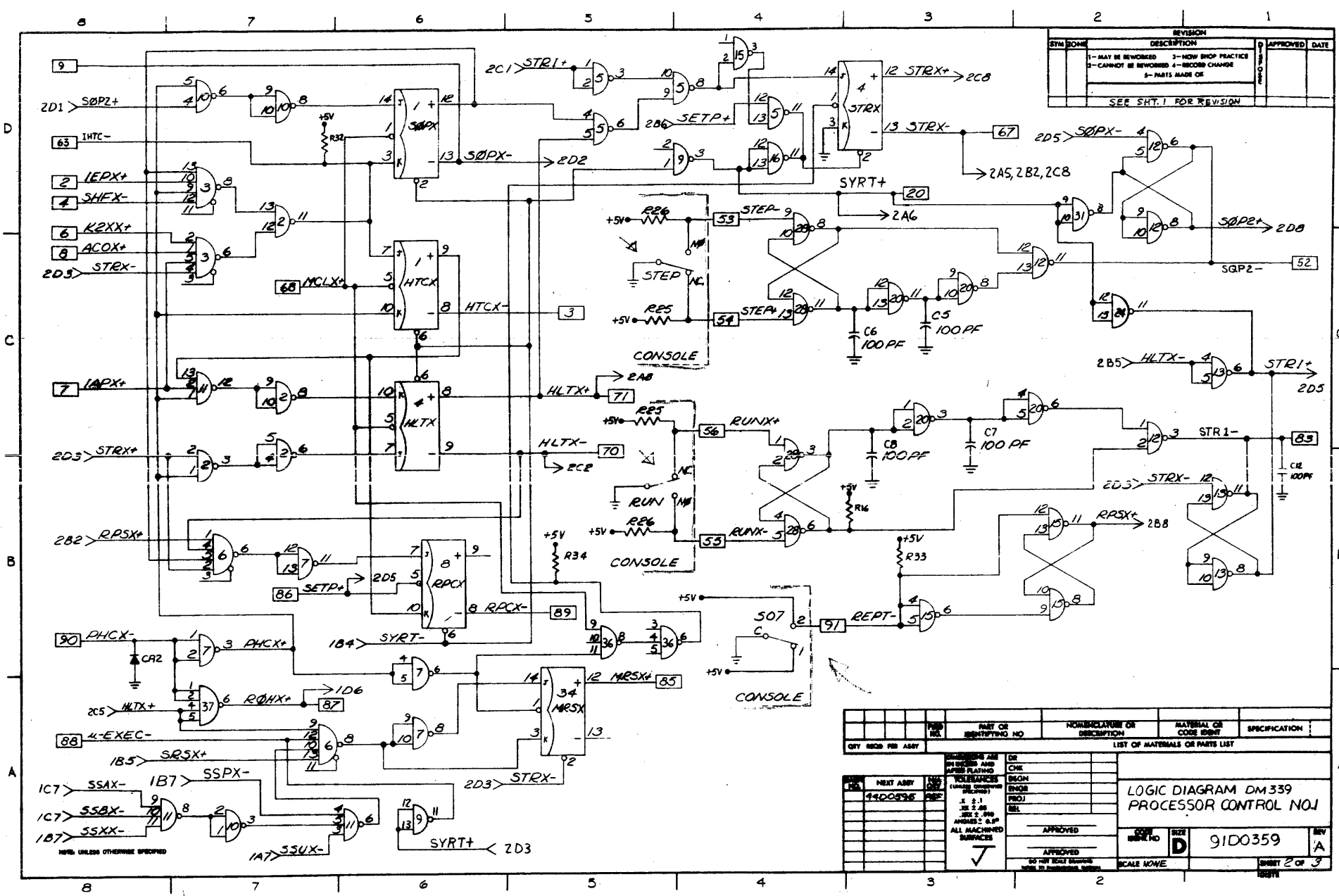
REV	DATE	DESCRIPTION	APPROVED	DATE
X	2/27/52	PROTOTYPE RELEASE		
X2		REVISED DRAWING		
A	2/28/52	PRODUCTION RELEASE (FOR RAJ 60341)		

REFERENCE DESIGNATIONS	
LAST USED	NOT USED
A35	C10
CA2	IC37
IC37	IC35
Q1	

- REFERENCE DRAWINGS
- BOARD DETAIL 40D0508
 - ASSEMBLY 44D0595
 - PARTS LIST 44D0595
 - PHOTOMASK 97D0703
 - SOLDER MASK 97D0705
 - SILKSCREEN 97D0704

QTY	MOD	PAR	ASBY	FIG	NO	PART OR IDENTIFYING NO	NOMENCLATURE OR DESCRIPTION	AMOUNT ON HAND	SPECIFICATION																				
LIST OF MATERIALS OR PARTS LIST																													
620L-100																													
<table border="0"> <tr> <td>DESIGNED BY</td> <td>DR. C. W. BARNER</td> <td>DATE</td> <td>2-24-52</td> </tr> <tr> <td>CHECKED BY</td> <td>W. J. B. BARNER</td> <td>DATE</td> <td>2-24-52</td> </tr> <tr> <td>APPROVED BY</td> <td>ENGR. F. E. BARNER</td> <td>DATE</td> <td>2-11-52</td> </tr> <tr> <td>PROJECT</td> <td>PROJ. 811</td> <td>DATE</td> <td>2-11-52</td> </tr> <tr> <td>SCALE</td> <td>NONE</td> <td></td> <td></td> </tr> </table>										DESIGNED BY	DR. C. W. BARNER	DATE	2-24-52	CHECKED BY	W. J. B. BARNER	DATE	2-24-52	APPROVED BY	ENGR. F. E. BARNER	DATE	2-11-52	PROJECT	PROJ. 811	DATE	2-11-52	SCALE	NONE		
DESIGNED BY	DR. C. W. BARNER	DATE	2-24-52																										
CHECKED BY	W. J. B. BARNER	DATE	2-24-52																										
APPROVED BY	ENGR. F. E. BARNER	DATE	2-11-52																										
PROJECT	PROJ. 811	DATE	2-11-52																										
SCALE	NONE																												
<table border="0"> <tr> <td>APPROVED</td> <td>DATE</td> <td>REV</td> </tr> <tr> <td>APPROVED</td> <td>2/10/52</td> <td>D</td> </tr> <tr> <td colspan="2">PART NO. 91D0359</td> <td>REV. A</td> </tr> </table>										APPROVED	DATE	REV	APPROVED	2/10/52	D	PART NO. 91D0359		REV. A											
APPROVED	DATE	REV																											
APPROVED	2/10/52	D																											
PART NO. 91D0359		REV. A																											

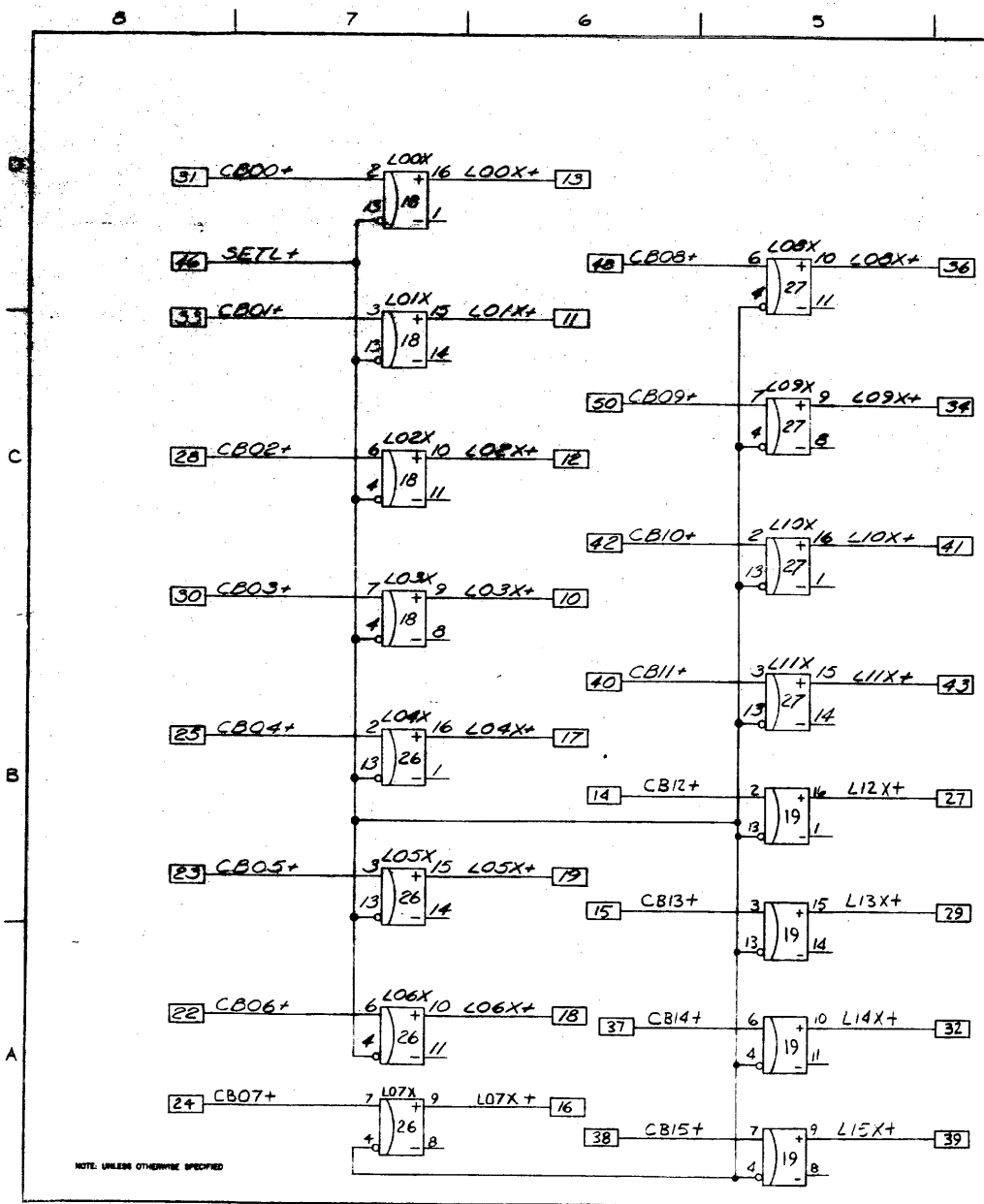
1. THE SYMBOLS XXX AND XXX INDICATE A SIGNAL SOURCE AND FLOW LOCATION CODE. THE FIRST DIGIT INDICATES SHEET NO., THE LETTER A HORIZONTAL ZONE AND THE SECOND DIGIT A VERTICAL ZONE.
2. ALL RESISTORS ARE 10K, 1/4W, 5%. 15%.
3. ALL CAPACITOR VALUES ARE ±10%, 500V.
4. POWER DISTRIBUTION TO IC'S: PIN 7 = GND, PIN 11 = +5V. (EXCEPTIONS - NOTES 5 & 6)
5. POWER DISTRIBUTION TO IC'S: PIN 4 = +5V, PIN 11 = GND.
6. POWER DISTRIBUTION TO IC'S: PIN 5 = +5V, PIN 12 = GND.



REVISION		APPROVED	DATE
SYM	DESCRIPTION		
1-	MAY BE REMOVED		
2-	NEW SHOP PRACTICE		
3-	CANNOT BE REWORKED		
4-	RECORD CHANGE		
5-	PARTS MADE OK		
SEE SH. 1 FOR REVISION			

QTY	REQD PER ASSY	PART OR IDENTIFYING NO.	NUMERICAL OR DESCRIPTION	MATERIAL OR CODE IDENT.	SPECIFICATION																								
LIST OF MATERIALS OR PARTS LIST																													
<table border="1"> <tr> <td>FINISH</td> <td>DR</td> </tr> <tr> <td>PLATING</td> <td>CHK</td> </tr> <tr> <td>TOLERANCES</td> <td>ENGR</td> </tr> <tr> <td>(UNLESS OTHERWISE SPECIFIED)</td> <td>FREQ</td> </tr> <tr> <td>F. 2: 1</td> <td>REL</td> </tr> <tr> <td>M. 2: .015</td> <td></td> </tr> <tr> <td>W. 2: .001</td> <td></td> </tr> <tr> <td>ANGLES: 6.3P</td> <td></td> </tr> <tr> <td>ALL MACHINED SURFACES</td> <td></td> </tr> <tr> <td></td> <td>APPROVED</td> </tr> <tr> <td></td> <td>APPROVED</td> </tr> <tr> <td colspan="2" style="text-align: center;">DO NOT BUILD WITHOUT THIS INSTRUCTIONS, SECTION</td> </tr> </table>						FINISH	DR	PLATING	CHK	TOLERANCES	ENGR	(UNLESS OTHERWISE SPECIFIED)	FREQ	F. 2: 1	REL	M. 2: .015		W. 2: .001		ANGLES: 6.3P		ALL MACHINED SURFACES			APPROVED		APPROVED	DO NOT BUILD WITHOUT THIS INSTRUCTIONS, SECTION	
FINISH	DR																												
PLATING	CHK																												
TOLERANCES	ENGR																												
(UNLESS OTHERWISE SPECIFIED)	FREQ																												
F. 2: 1	REL																												
M. 2: .015																													
W. 2: .001																													
ANGLES: 6.3P																													
ALL MACHINED SURFACES																													
	APPROVED																												
	APPROVED																												
DO NOT BUILD WITHOUT THIS INSTRUCTIONS, SECTION																													
LOGIC DIAGRAM DM339 PROCESSOR CONTROL NO1				CODE 91D0359	REV A																								
				SCALE NONE	SHEET 2 of 3																								

NOTE: UNLESS OTHERWISE SPECIFIED



- | | | | | | | | | | | | |
|----|-------|---|----|-------|---|----|--------|---|-----|--------|---|
| 1 | GND | → | 33 | CBO1+ | → | 63 | IHTC- | → | 93 | FRCX- | → |
| 2 | IEDX+ | → | 34 | LO9X+ | → | 64 | | → | 94 | CL2X+ | → |
| | HTCX- | → | 35 | SEL4+ | → | 65 | TRSC- | → | 95 | SYRT- | → |
| 4 | SHFX- | → | 36 | LO8X+ | → | 66 | MSC2- | → | 96 | AC3X+ | → |
| 5 | SERX- | → | 37 | CB14+ | → | 67 | STEX- | → | 97 | SS3X- | → |
| 6 | K2XX+ | → | 38 | CB15+ | → | 68 | MCLX+ | → | 98 | SERX-I | → |
| 7 | IAPX+ | → | 39 | L15X+ | → | 69 | SELU- | → | 99 | UO2X+ | → |
| 8 | ACOX+ | → | 40 | CB11+ | → | 70 | HLTX- | → | 100 | ACYX- | → |
| | SOPX- | → | 41 | L10X+ | → | 71 | HLTX+ | → | 101 | JCMX- | → |
| | LO3X+ | → | 42 | CB10+ | → | 72 | SRSX+ | → | 102 | UO8X+ | → |
| | LO1X+ | → | 43 | L11X+ | → | 73 | SEL4- | → | 103 | ICYX+ | → |
| | LO2X+ | → | 44 | SSUX+ | → | 74 | SSPX+ | → | 104 | SBAX- | → |
| | LOOX+ | → | 45 | SS3X+ | → | 75 | SELX- | → | 105 | EXCX- | → |
| 14 | CB12+ | → | 46 | SETL+ | → | 76 | SS2X+ | → | 106 | JCN1+ | → |
| 15 | CB13+ | → | 47 | SELX+ | → | 77 | SELB- | → | 107 | SBAX+ | → |
| | LO7X+ | → | 48 | CB08+ | → | 78 | SSXX+ | → | 108 | UO1X+ | → |
| | LO4X+ | → | 49 | +3V | → | 79 | SELA- | → | 109 | OVXX- | → |
| | LO6X+ | → | 50 | CB09+ | → | 80 | SSBX+ | → | 110 | UO0X+ | → |
| | LO5X+ | → | 51 | AC1X+ | → | 81 | SELA+ | → | 111 | SS2X- | → |
| | SYRT+ | → | 52 | SQPE- | → | 82 | SSIX+ | → | 112 | UO7X+ | → |
| 21 | SELU+ | → | 53 | STEP- | → | 83 | STR1- | → | 113 | SS1X- | → |
| 22 | CB06+ | → | 54 | RUNX- | → | 84 | SSAX+ | → | 114 | UO6X+ | → |
| 23 | CB05+ | → | 55 | RUNX+ | → | 85 | MRSX+ | → | 115 | EJ1X- | → |
| 24 | CB07+ | → | 56 | RUNX+ | → | 86 | SETP+ | → | 116 | CALX+ | → |
| 25 | CB04+ | → | 57 | CL1X+ | → | 87 | RDHX+ | → | 117 | -5V | → |
| 26 | SELB+ | → | 58 | MSPI- | → | 88 | μ-EXEC | → | 118 | +5V | → |
| | L12X+ | → | 59 | MSPX+ | → | 89 | EPCX- | → | 119 | | → |
| 28 | CB02+ | → | 60 | | → | 90 | PHCX- | → | 120 | +12V | → |
| 29 | L13X+ | → | 61 | | → | 91 | REPT- | → | 121 | | → |
| 30 | CB03+ | → | 62 | | → | 92 | JCMX+ | → | 122 | GND | → |
| 31 | CB00+ | → | | | | | | | | | |
| | L14X+ | → | | | | | | | | | |

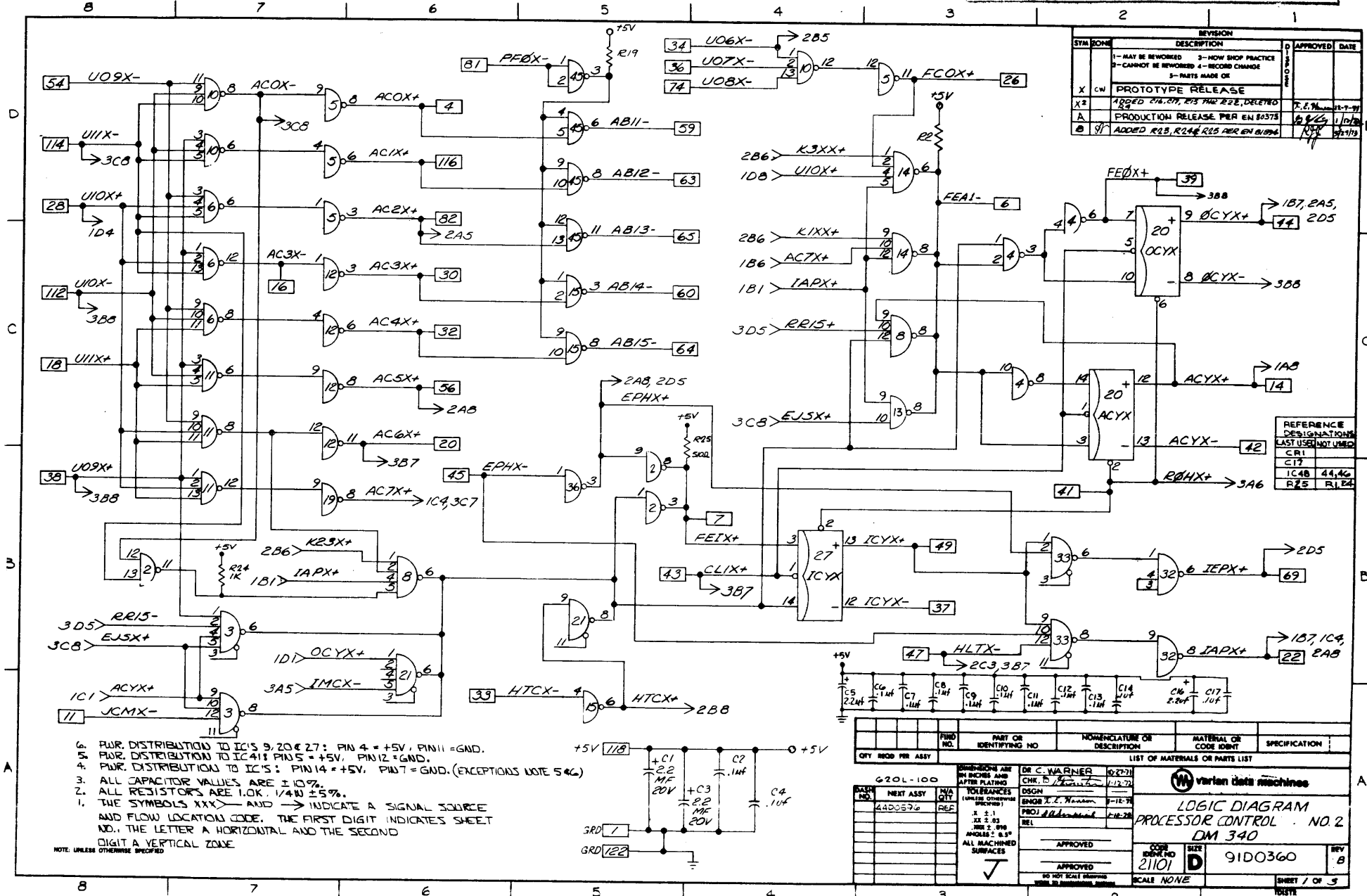
SYMBOL		REVISION		APPROVED		DATE	
1	MAY BE REWORKED	3	NOW SHOP PRACTICE				
2	CANNOT BE REWORKED	4	RECORD CHANGE				
3	DATE MADE OR						
SEE SH1 FOR REVISIONS							

QTY	INCH PER ASSY	PART NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT.	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST						
DIMENSIONS ARE IN INCHES AND AFTER PLATING				DR		
TOLERANCES (UNLESS OTHERWISE SPECIFIED)				CNK		
X ± .1				DSGN		
XX ± .05				ENGR		
XXX ± .010				PROJ		
ANODIZED 2.5-3°				BEL		
ALL MACHINED SURFACES				APPROVED		
				APPROVED		
				IS NOT SEAL TIGHTENING		
				SCALE NONE		

LOGIC DIAGRAM DM 339
PROCESSOR CONTROL
NO. 1

CODE IDENT NO. **D** 9100359 REV **A**

CAUTION: EN 82844
AFFECTS THIS DWG.



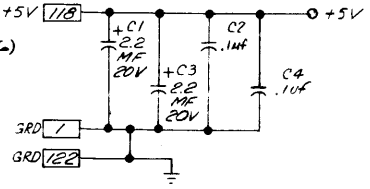
SYMBOL		REVISION		APPROVED	DATE
1	MAY BE REWORKED	2	HOW SHOP PRACTICE		
2	CANNOT BE REWORKED	3	RECORD CHANGE		
3	PARTS MADE OR	4	REWORKED		
4	PROTOTYPE RELEASE	5	PARTS MADE OR		
X2	ADDED R1A, R1B, R13, R14, R22, DELETED				7.2.76
A	PRODUCTION RELEASE PER EN 80573				1.10.76
B	ADDED R23, R24, R25 PER EN 80994				05/1/78

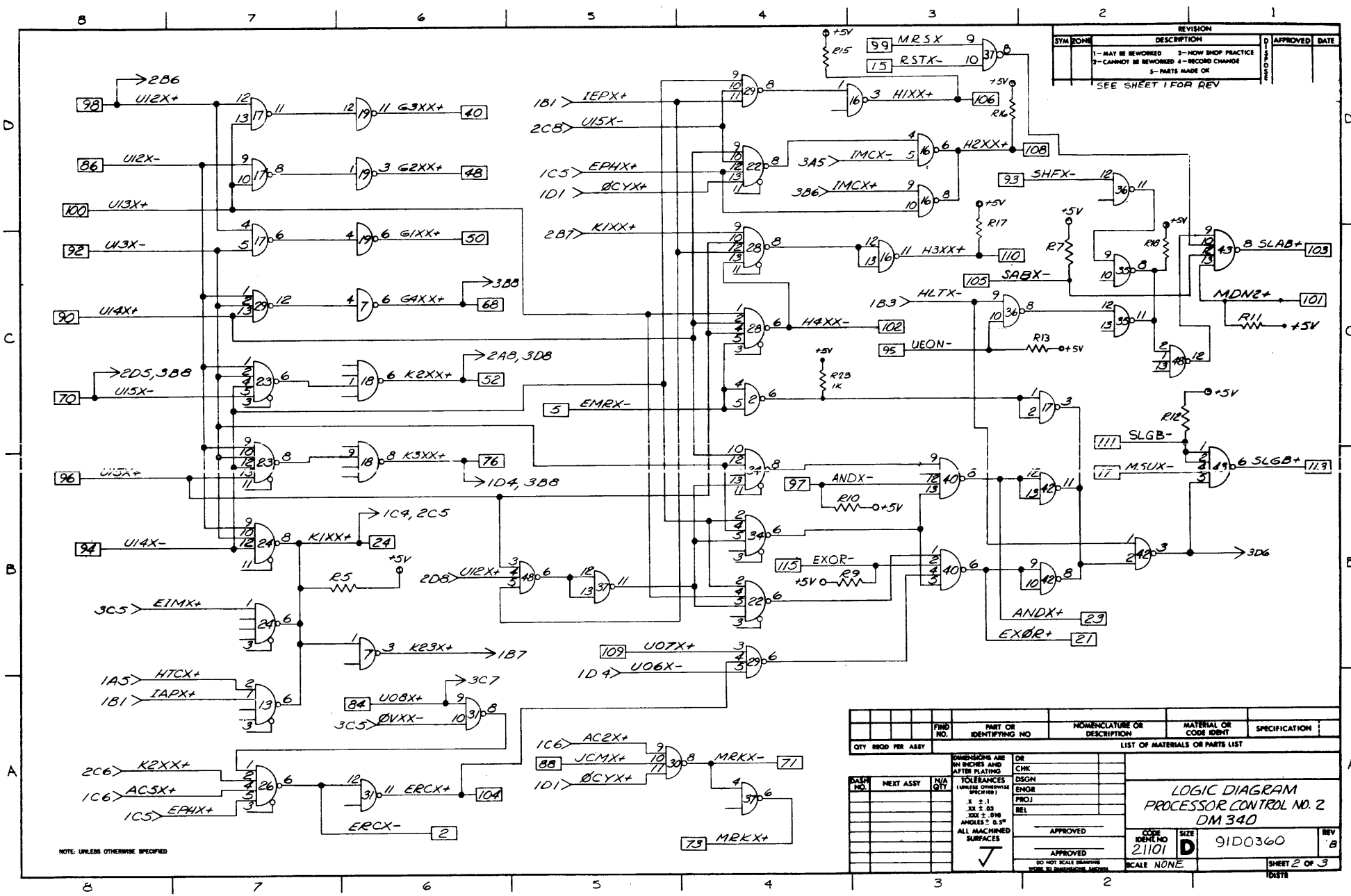
REFERENCE DESIGNATION	LAST USED	NOT USED
C11		
C14	44, 46	
R25	R1, R4	

QTY	REQ	PER	ASBY	REV	NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT.	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST									
620L-100									
INCHES AND AFTER PLATING									
TOLERANCES (UNLESS OTHERWISE SPECIFIED)									
DR. C. WARNER 0-27-71									
CHK. E. J. ... 1/12-71									
DESIGN ENGINEER E. J. ... 7-14-70									
PROJ. J. ... 1-12-70									
REL.									
APPROVED									
APPROVED									
NO NOT BEAT DRAWING									
VARIAN DATA MACHINES									
LOGIC DIAGRAM									
PROCESSOR CONTROL NO. 2									
DM 340									
CORP. IDENT. NO. 21101									
SIZE D									
91D0360									
SCALE NONE									
SHEET 1 OF 3									

6. PWR. DISTRIBUTION TO IC'S 9, 20 & 27: PIN 4 = +5V, PIN 11 = GND.
5. PWR. DISTRIBUTION TO IC 4: PIN 5 = +5V, PIN 12 = GND.
4. PWR. DISTRIBUTION TO IC'S: PIN 14 = +5V, PIN 7 = GND. (EXCEPTIONS NOTE 5 & 6)
3. ALL CAPACITOR VALUES ARE ± 10%.
2. ALL RESISTORS ARE 1.0K, 1/4W ± 5%.
1. THE SYMBOLS XXX > AND → INDICATE A SIGNAL SOURCE AND FLOW LOCATION CODE. THE FIRST DIGIT INDICATES SHEET NO., THE LETTER A HORIZONTAL AND THE SECOND DIGIT A VERTICAL ZONE.

NOTE: UNLESS OTHERWISE SPECIFIED





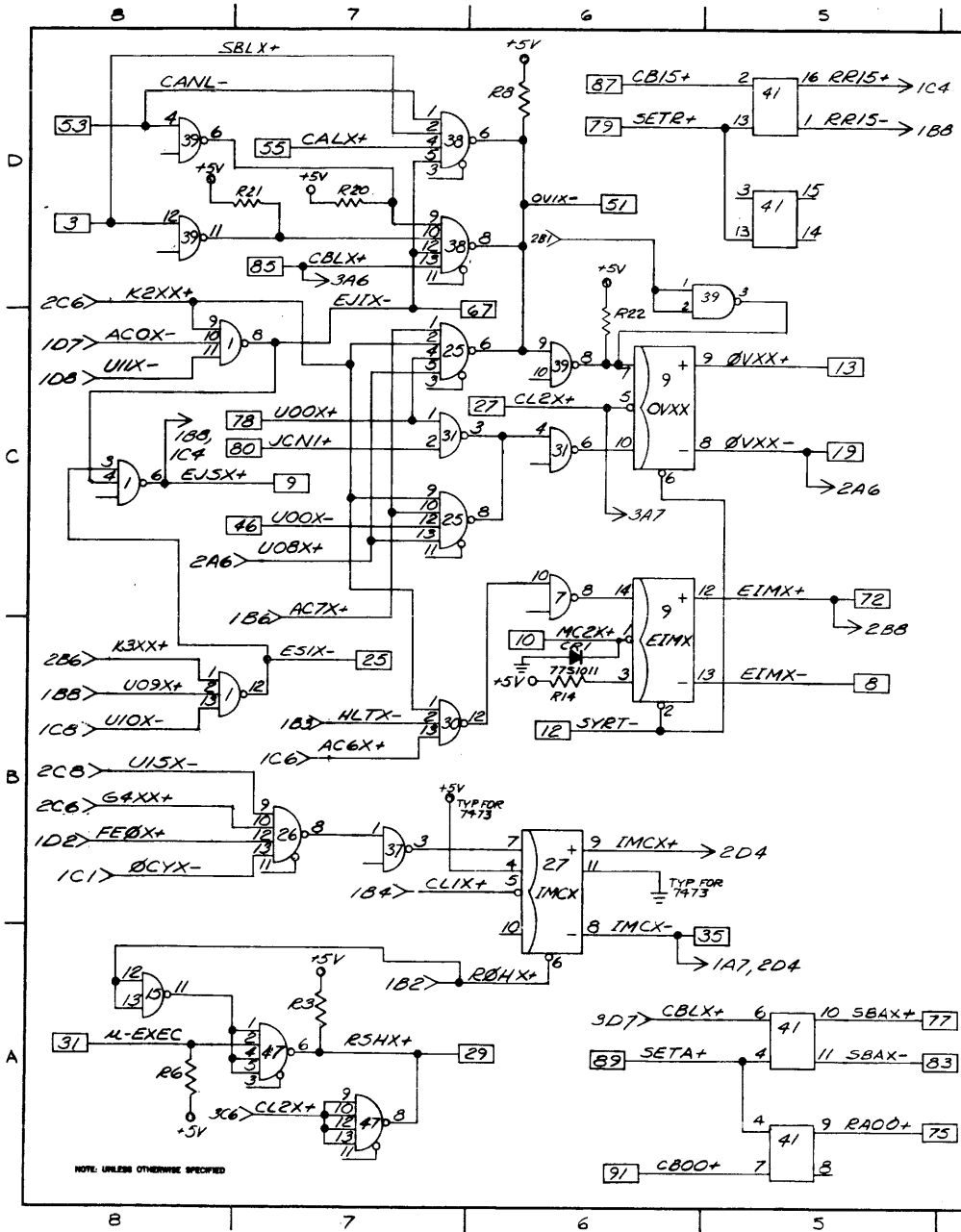
REVISION		APPROVED	DATE
1	MAY BE REWORKED		
2	RECORD CHANGE		
3	NEW SHOP PRACTICE		
4	CANNOT BE REWORKED		
5	PARTS MADE OF		

SEE SHEET 1 FOR REV

QTY	REQ	MOD	PER	ASSY	FIND NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST									
DIMENSIONS ARE IN INCHES AND FRACTIONS UNLESS OTHERWISE SPECIFIED									
OR TOLERANCES UNLESS OTHERWISE SPECIFIED									
X ± .1									
XX ± .03									
XXX ± .015									
ANGLES : 0.5°									
ALL MACHINED SURFACES									
DO NOT SCALE DIMENSIONS									
WORK TO DIMENSIONS, UNLESS									
APPROVED								CODE IDENT NO	SIZE
APPROVED								21101	D
SCALE NONE								91D0360	REV
								B	
								SHEET 2 OF 3	

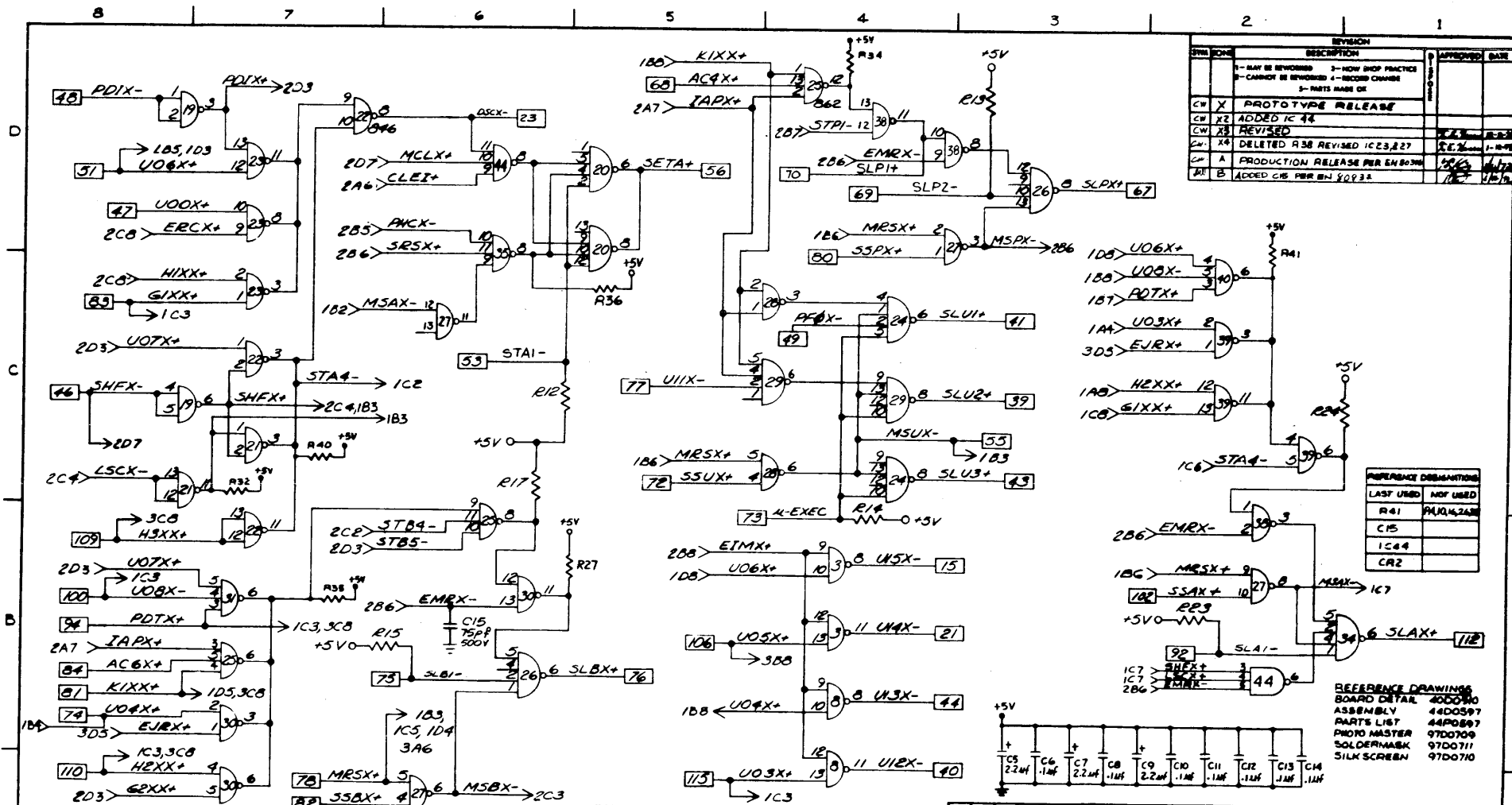
LOGIC DIAGRAM
PROCESSOR CONTROL NO. 2
DM 340

NOTE: UNLESS OTHERWISE SPECIFIED



SYMBOL		REVISION	
DESCRIPTION		APPROVED	DATE
1	GND	1	MAY BE REWORKED
2	ERCX-	2	CANNOT BE REWORKED
3	SBLX+	3	NOW SHOP PRACTICE
4	ACOX+	4	RECORDED CHANGE
5	EMRX-	5	PARTS MADE OK
6	FEAI-	6	
7	FEIX+	7	
8	EIMX-	8	
9	EJSX+	9	
10	MC2X+	10	
11	JCMX-	11	
12	SYRT-	12	
13	OVXX+	13	
14	OVXX-	14	
15	RSTX-	15	
16	AC3X-	16	
17	MSUX-	17	
18	UIIX+	18	
19	OVXX-	19	
20	AC6X+	20	
21	EXOR+	21	
22	IAPX+	22	
23	ANDX+	23	
24	K1XX+	24	
25	ESIX-	25	
26	FCOX+	26	
27	CLEXX+	27	
28	UIOX+	28	
29	RSHX+	29	
30	AC3X+	30	
31	M-EXEC	31	
32	AC4X+	32	
33	HTCX-	33	
34	UO6X-	34	
35	IMCX-	35	
36	UO7X-	36	
37	ICYX-	37	
38	UO9X+	38	
39	FE0X+	39	
40	G3XX+	40	
41	ROHX+	41	
42	ACYX-	42	
43	CLIX+	43	
44	QCYX+	44	
45	EPHX-	45	
46	UO0X-	46	
47	HLTX-	47	
48	G2XX+	48	
49	ICYX+	49	
50	G1XX+	50	
51	OVIX-	51	
52	K2XX+	52	
53	CANL-	53	
54	UO9X-	54	
55	CALX+	55	
56	ACSX+	56	
57		57	
58		58	
59	AB11-	59	
60	AB14-	60	
61		61	
62		62	
63	AB12-	63	
64	AB15-	64	
65	AB13-	65	
66	EJIX-	66	
67	G4XX-	67	
68	IEPX+	68	
69	U15X-	69	
70	MRKX-	70	
71	EIMX+	71	
72	MRKX+	72	
73	UO8X-	73	
74	EA00+	74	
75	K3XX+	75	
76	SBAX+	76	
77	UO0X+	77	
78	JCN1+	78	
79	PE0X-	79	
80	AC2X+	80	
81	SBAX-	81	
82	UO8X+	82	
83	CBLX+	83	
84	UI2X-	84	
85	CB13+	85	
86	JCMX+	86	
87	SETA+	87	
88	U14X+	88	
89	CB00+	89	
90	U13X-	90	
91	SAFX-	91	
92		92	
93		93	
94	U14X-	94	
95	UEDN-	95	
96	U15X+	96	
97	ANDX-	97	
98	UI2X+	98	
99	MESX+	99	
100	U13X+	100	
101	MDN2+	101	
102	H4XX-	102	
103	SLAB+	103	
104	ERCX+	104	
105	SAFX-	105	
106	H1XX+	106	
107		107	
108	H2XX+	108	
109	UO7X+	109	
110	H3XX+	110	
111	SLGB-	111	
112	UIOX-	112	
113	SLGB+	113	
114	UIIX-	114	
115	EXOR-	115	
116	AC1X+	116	
117		117	
118	+5V	118	
119		119	
120		120	
121		121	
122	GND	122	

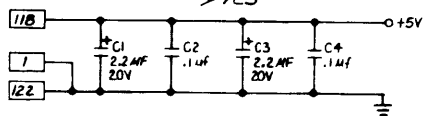
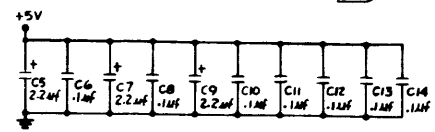
QTY REQD PER ASSY		FIND NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST						
DASH NO.	NEXT ASSY	M/A QTY	DIMENSIONS ARE IN INCHES AND AFTER PLATING (UNLESS OTHERWISE SPECIFIED)		DR	CHK
			TOLERANCES (UNLESS OTHERWISE SPECIFIED)		DSGN	ENGR
			F. 3.1		PROJ	REL
			XX ± .01		APPROVED	
			XXX ± .010		APPROVED	
			ANGLES ± 0.5°		NO HOT SCALE DRAWING	
			ALL MACHINED SURFACES		SCALE	
				CODE IDENT NO	SIZE	REV
				21101	D	9100360
				SHEET 3 OF 3		



REVISION		DESCRIPTION	APPROVED	DATE
1	1	1- MAY BE REWORKED 2- CANNOT BE REWORKED 4- RECORD CHANGE 3- PARTS MADE OK		
CW	X	PROTOTYPE RELEASE		
CW	X2	ADDED IC 44		
CW	X3	REVISED		
CW	X4	DELETED R36 REVISED IC23, 27		
CA	A	PRODUCTION RELEASE PER BR 8039H		
CA	B	ADDED C15 PER BR 8093E		

REFERENCE DESIGNATION	
LAST USED	NOT USED
R41	PA104, 240
C15	
IC44	
CR2	

REFERENCE DRAWINGS
 BOARD DETAIL 4400597
 ASSEMBLY PARTS LIST 4400897
 PHOTO MASTER 9700709
 SOLDERMASK 9700711
 SILK SCREEN 9700710



- POWER DISTRIBUTION TO IC 2 & 12: PIN 4 = +5V, PIN 11 = GND.
- POWER DISTRIBUTION TO IC 5: PIN 7 = GND, PIN 14 = +5V (EXCEPTIONS NOTE 5)
- ALL CAPACITOR VALUES ARE ±10%.
- ALL RESISTORS ARE 1.0K, 1/4W, ±5%.
- THE SYMBOLS XXX AND →XXX INDICATE A SIGNAL SOURCE AND FLOW LOCATION CODE. THE FIRST DIGIT INDICATES SHEET NO., THE LETTER A HORIZONTAL ZONE AND THE SECOND DIGIT A VERTICAL ZONE.

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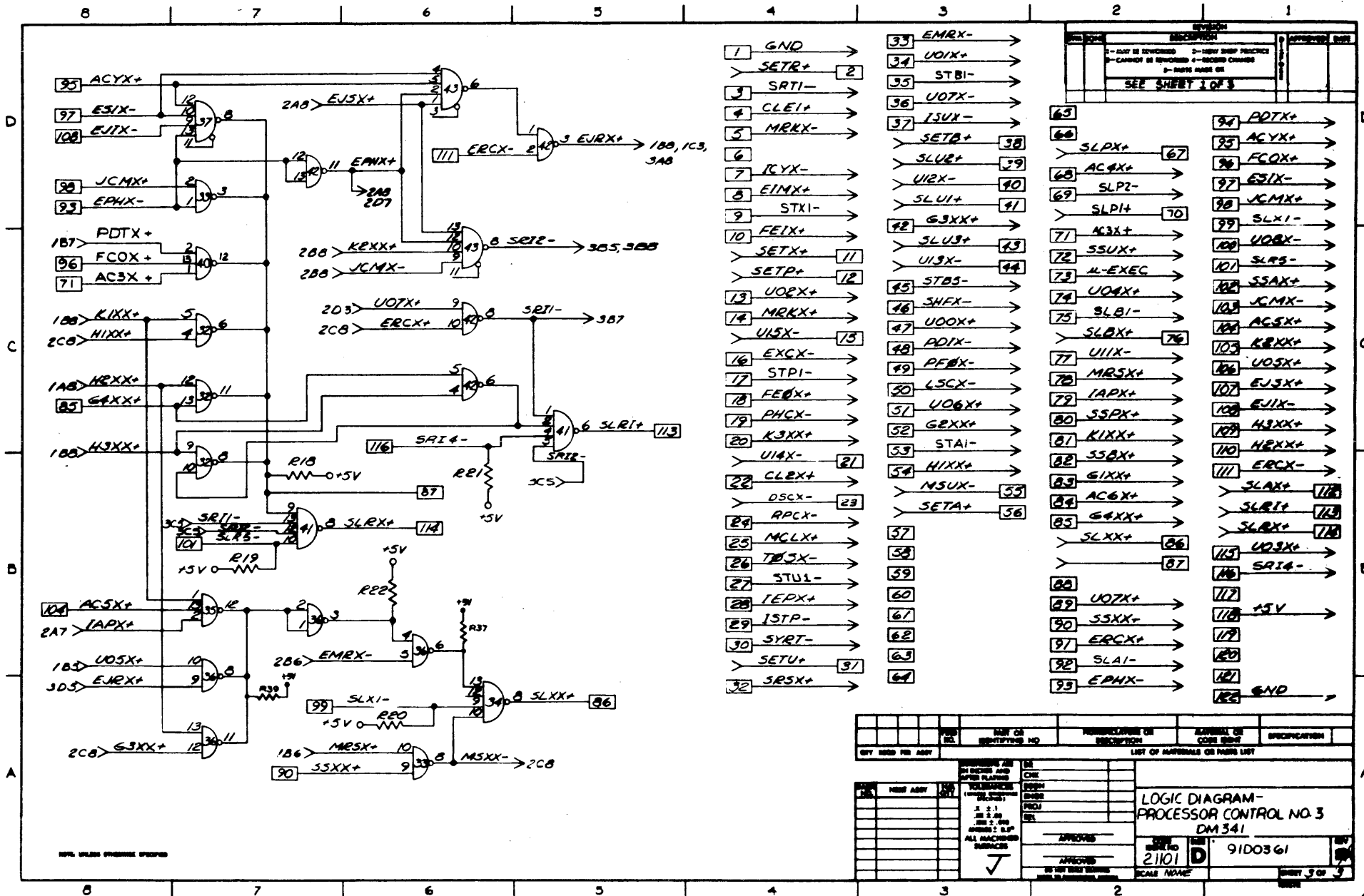
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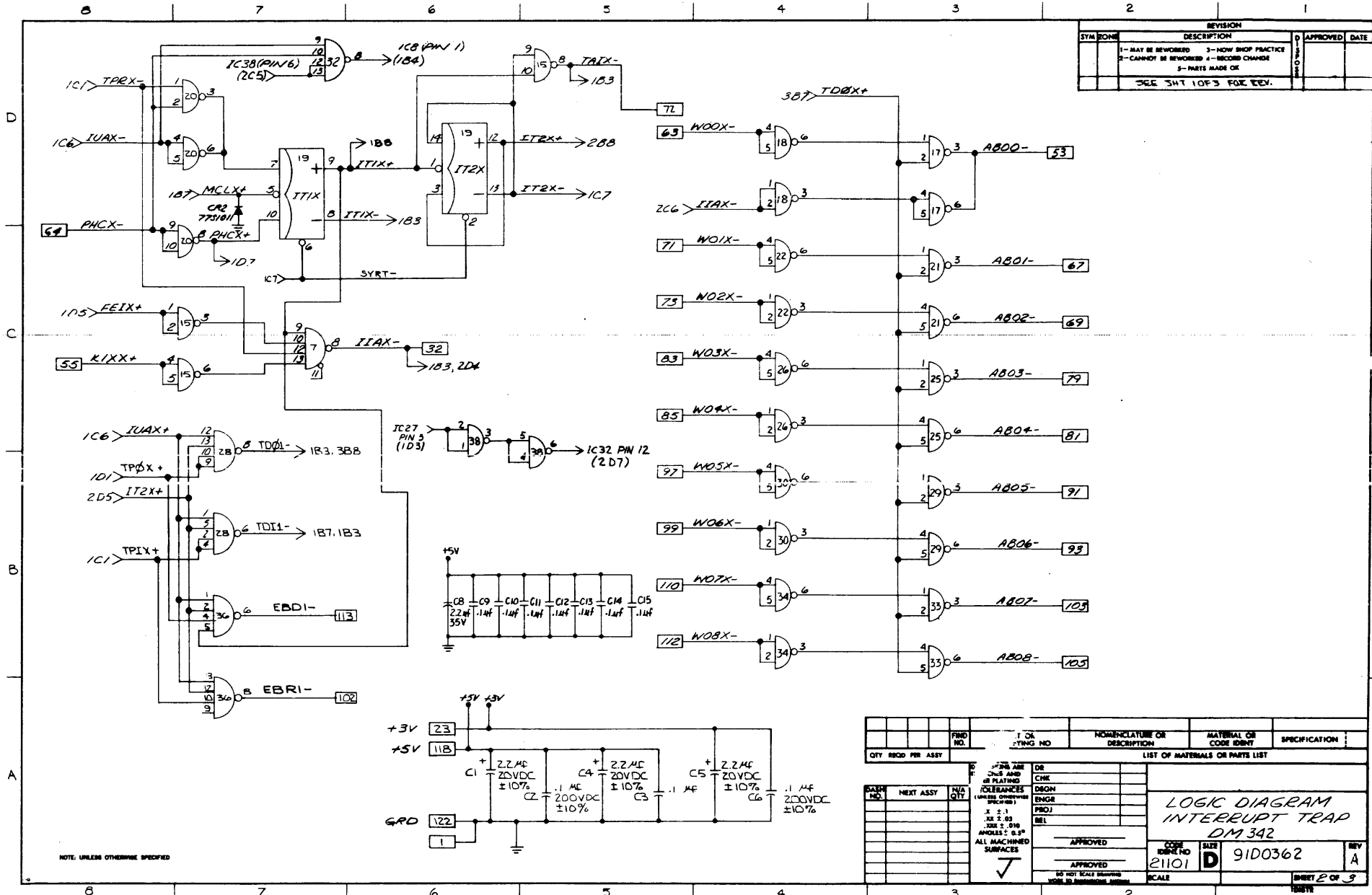
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| 1 | GND | 33 | EMRX- |
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| 3 | SRTI- | 35 | STBI- |
| 4 | CLEI+ | 36 | UOTX- |
| 5 | MKRX- | 37 | ISUX- |
| 6 | RYX- | 38 | SETA+ |
| 7 | EIMX+ | 39 | SLUR+ |
| 8 | STXI- | 40 | UIRX- |
| 9 | FEIX+ | 41 | SLUI+ |
| 10 | SETX+ | 42 | G3XX+ |
| 11 | SETP+ | 43 | SLUS+ |
| 12 | UORX+ | 44 | UIRX- |
| 13 | MKRX+ | 45 | STBS- |
| 14 | UI5X- | 46 | SHFX- |
| 15 | EXCX- | 47 | UOXX+ |
| 16 | STPI- | 48 | ADIX- |
| 17 | FEIX+ | 49 | PEFX- |
| 18 | PHCX- | 50 | L5CX- |
| 19 | K3XX+ | 51 | UO6X+ |
| 20 | UI4X- | 52 | G2XX+ |
| 21 | CLEX+ | 53 | STAI- |
| 22 | OSCX- | 54 | HIXX+ |
| 23 | RPCX- | 55 | NSUX- |
| 24 | MCLX+ | 56 | SETA+ |
| 25 | T0SX- | 57 | |
| 26 | STUL- | 58 | |
| 27 | TEPX+ | 59 | |
| 28 | ISTP- | 60 | |
| 29 | SYRT- | 61 | |
| 30 | SETU+ | 62 | |
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93		ISSUED FOR REVIEW		
94		ISSUED FOR REVIEW		
95		ISSUED FOR REVIEW		
96		ISSUED FOR REVIEW		
97		ISSUED FOR REVIEW		
98		ISSUED FOR REVIEW		
99		ISSUED FOR REVIEW		
100		ISSUED FOR REVIEW		

LOGIC DIAGRAM -
PROCESSOR CONTROL NO. 3
DM 541

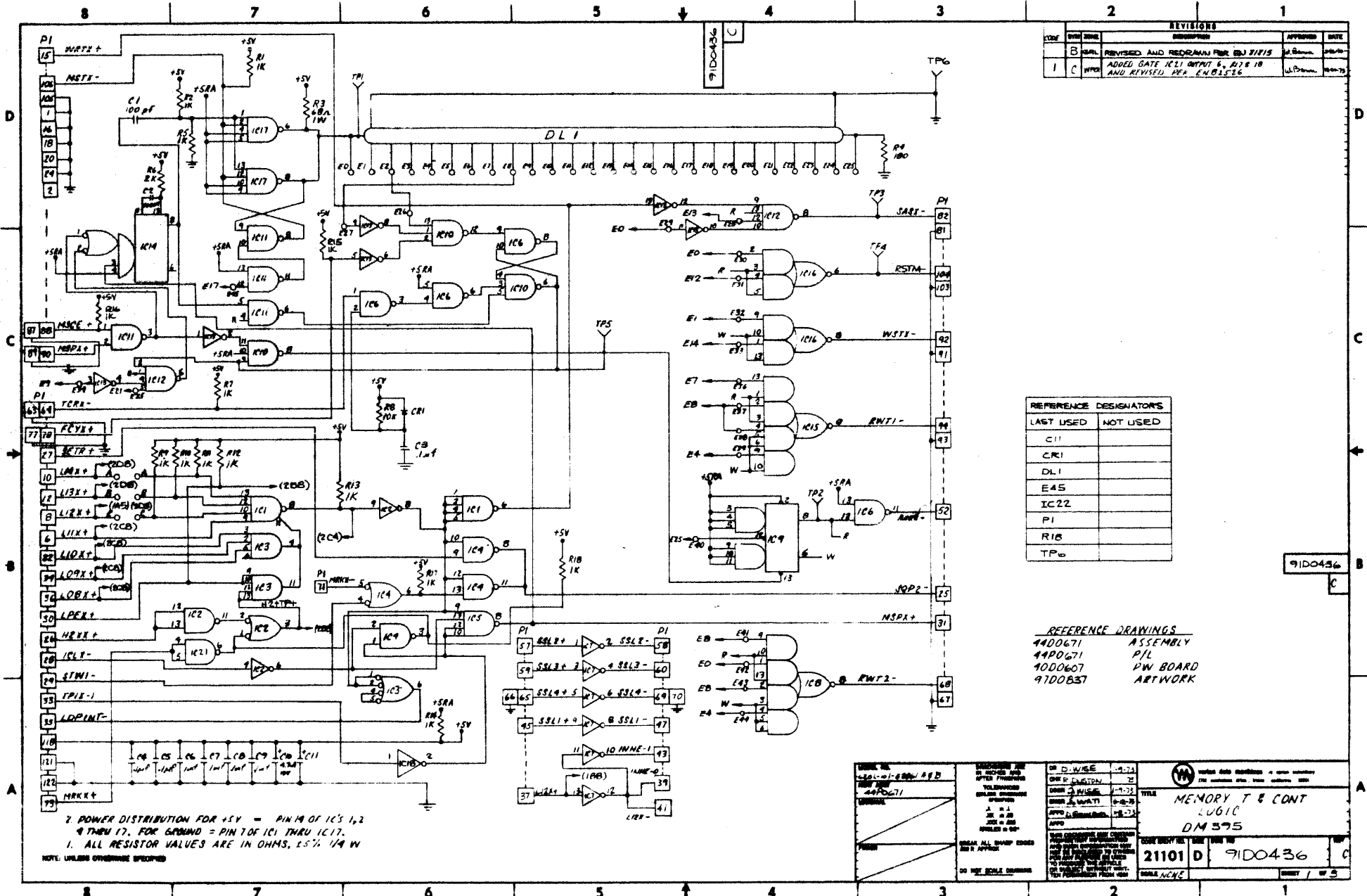
REV NO 2101
SCALE NAME
9100361
SHEET 3 OF 3

SYN ZONE		REVISION		APPROVED	DATE
		1--MAY BE REWORKED	3--NOW SHOP PRACTICE		
		2--CANNOT BE REWORKED	4--RECORD CHANGE		
		5--PARTS MADE OK			
		SEE SHY 10F'S FOR REV.			



NOTE: UNLESS OTHERWISE SPECIFIED

QTY	REQD	PER	ASSY	FIND NO.	OR	DESCRIPTION	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT	SPECIFICATION																								
LIST OF MATERIALS OR PARTS LIST																																	
<table border="1"> <tr> <td>DESIGN</td> <td>CHK</td> <td>DR</td> </tr> <tr> <td>ENGR</td> <td>ENGR</td> <td>ENGR</td> </tr> <tr> <td>PROJ</td> <td>PROJ</td> <td>PROJ</td> </tr> <tr> <td>BEL</td> <td>BEL</td> <td>BEL</td> </tr> <tr> <td colspan="3">APPROVED</td> </tr> <tr> <td colspan="3">APPROVED</td> </tr> <tr> <td colspan="3">DO NOT SCALE DRAWING</td> </tr> <tr> <td colspan="3">FROM 10 DIMENSIONAL SHEET</td> </tr> </table>										DESIGN	CHK	DR	ENGR	ENGR	ENGR	PROJ	PROJ	PROJ	BEL	BEL	BEL	APPROVED			APPROVED			DO NOT SCALE DRAWING			FROM 10 DIMENSIONAL SHEET		
DESIGN	CHK	DR																															
ENGR	ENGR	ENGR																															
PROJ	PROJ	PROJ																															
BEL	BEL	BEL																															
APPROVED																																	
APPROVED																																	
DO NOT SCALE DRAWING																																	
FROM 10 DIMENSIONAL SHEET																																	
LOGIC DIAGRAM INTERRUPT TRAP DM 342								DATE 21101	SCALE D	REV A																							
								91D0362	SHEET 2 OF 5																								



REVISIONS				
CODE	DATE	DESCRIPTION	APPROVED	DATE
B	10/10/71	REVISOR AND REPROGRAM FOR E3 71715	J.P. Brown	10/10/71
I	11/01/71	ADDED GATE IC11 OUTPUT 6, R17 R 18 AND REVISED PER EN 03536	J.P. Brown	11/01/71

REFERENCE DESIGNATORS	
LAST USED	NOT USED
IC11	
CR1	
DL1	
E45	
IC22	
PI	
R18	
TP6	

REFERENCE DRAWINGS
 4400671 ASSEMBLY
 4490671 P/L
 1000607 PW BOARD
 9700837 ARTWORK

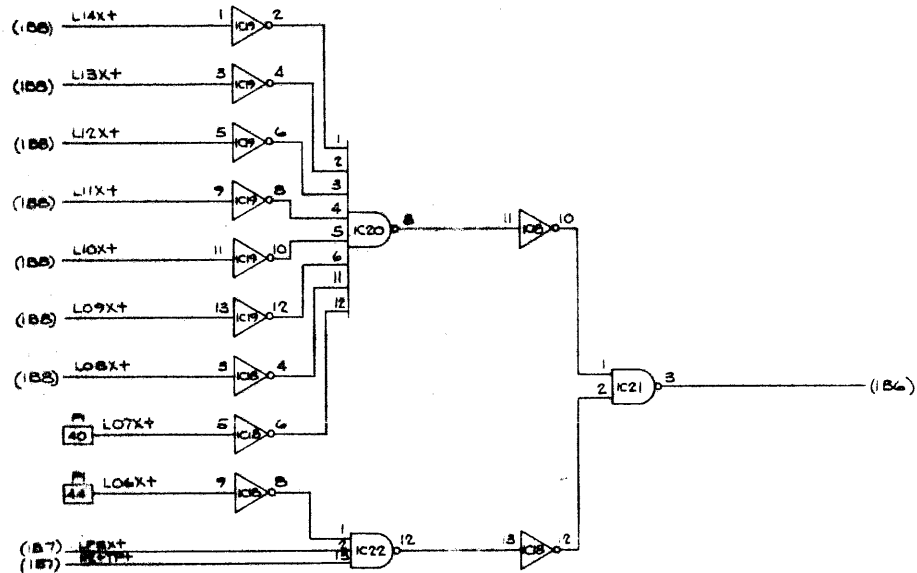
DRAWING NO. 4490671 REV. 1	CHECKED BY J.P. BROWN	DESIGNED BY J.P. BROWN	DRAWN BY J.P. BROWN	DATE 10/10/71	TITLE MEMORY T & CONT LOGIC DM 375
CHECKED BY J.P. BROWN			DATE 10/10/71		CODE BIRTH NO. 21101 D
DRAWN BY J.P. BROWN			DATE 10/10/71		DRAWN BY J.P. BROWN

2. POWER DISTRIBUTION FOR +5V = PIN 14 OF IC'S 1, 2, 9 THRU 17. FOR GROUND = PIN 7 OF IC1 THRU IC17.
 1. ALL RESISTOR VALUES ARE IN OHMS, 1/2% 1/4 W
 NOTE: UNLESS OTHERWISE SPECIFIED

9700436

9100436
C

REVISIONS			
REV	DATE	DESCRIPTION	APPROVED



9100436
C

NOTE: UNLESS OTHERWISE SPECIFIED

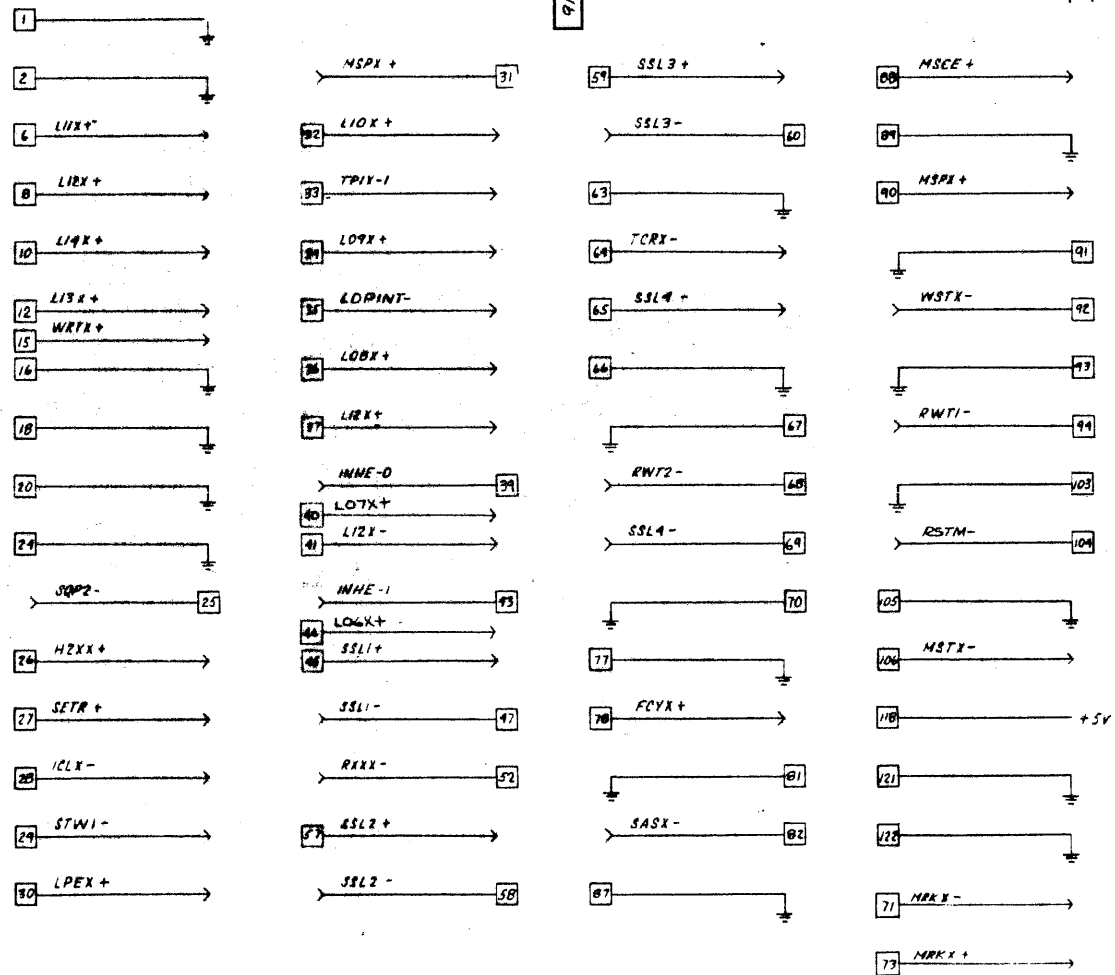
CASE NO.	21101	REV.	D	DATE	9100436	REV.	C
SCALE						SHEET	2 OF 3

9100436

C

REVISIONS

NO.	DATE	DESCRIPTION	APPROVED	BY
		SEE SHEET 1		



9100436

C

NOTE: UNLESS OTHERWISE SPECIFIED

CONTRACT NO.	21101	D	9100436
SCALE			

8

7

6

5

4

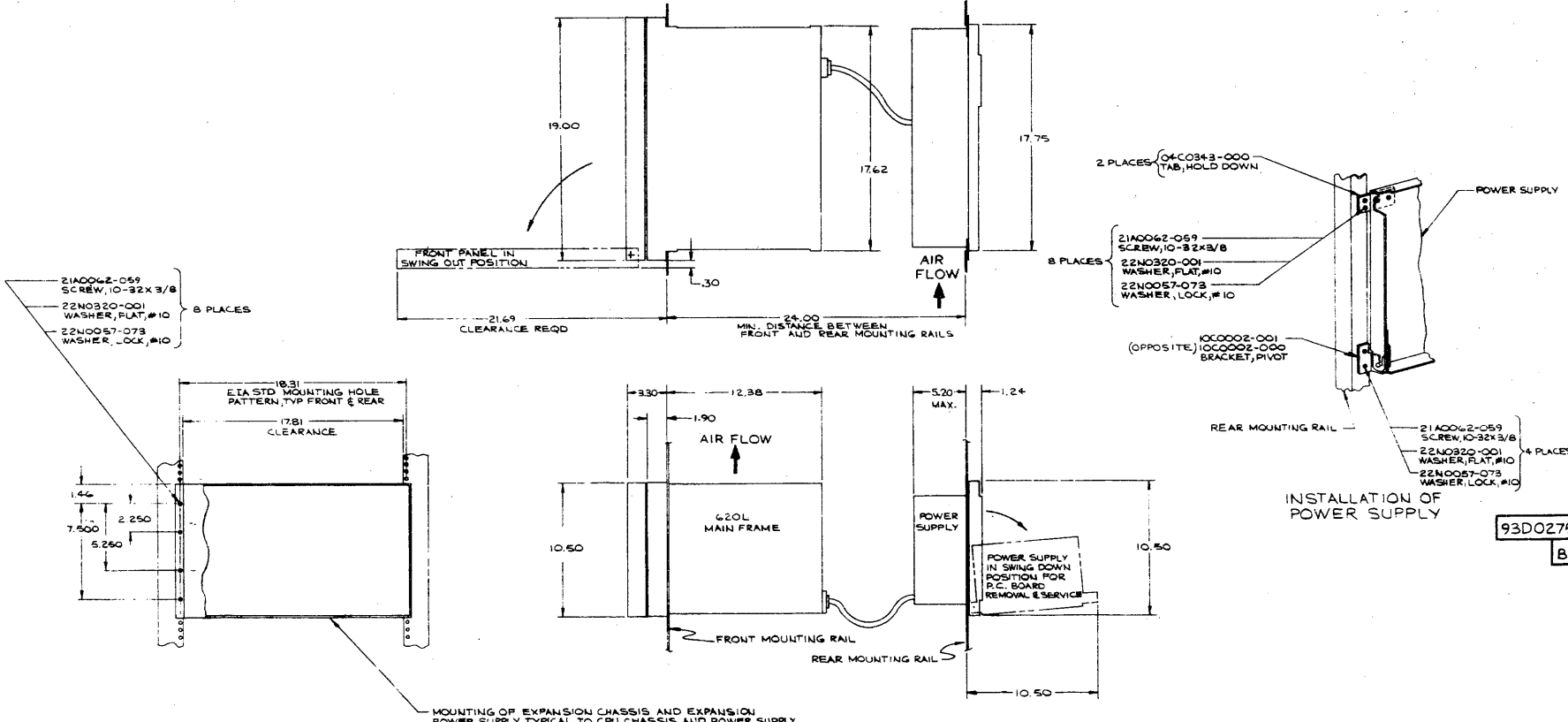
3

2

1

93D0276

REVISIONS				
SYN	ZONE	DESCRIPTION	APPROVED	DATE
A	08	PRODUCTION RELEASE EUN 5412	<i>[Signature]</i>	1/1/74
B	04	REVISE CPU TOP VIEW, ADDED DIM CPU INSIDE VIEW PER EN 5666	<i>[Signature]</i>	1/1/74



NOTE: UNLESS OTHERWISE SPECIFIED

MODEL NO. 620/L	DR <i>[Signature]</i> 07/2/74 CHK <i>[Signature]</i> 05/21/74 DSCR <i>[Signature]</i> EWR <i>[Signature]</i> APPD <i>[Signature]</i> APPD <i>[Signature]</i>	 <small>VFA</small>	<small>FOR THE ARMY</small> <small>772</small>
FRONT VIEW MULTIPLE USAGE	<small>THIS DOCUMENT NOW CONTAINS PROPRIETARY INFORMATION AND SUCH INFORMATION MAY BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT, WITHOUT WRITING PERMISSION FROM VON</small>	TITLE INSTL DWG, PWR SUP, 620L BSC	<small>SCALE</small> <small>SHEET 1 OF 1</small>
DATE/VAL	<small>DO NOT SCALE DRAWING</small>	<small>CODE IDENT NO.</small> 21101 D <small>REV</small> B <small>QTY</small> 93D0275 B	

8

7

6

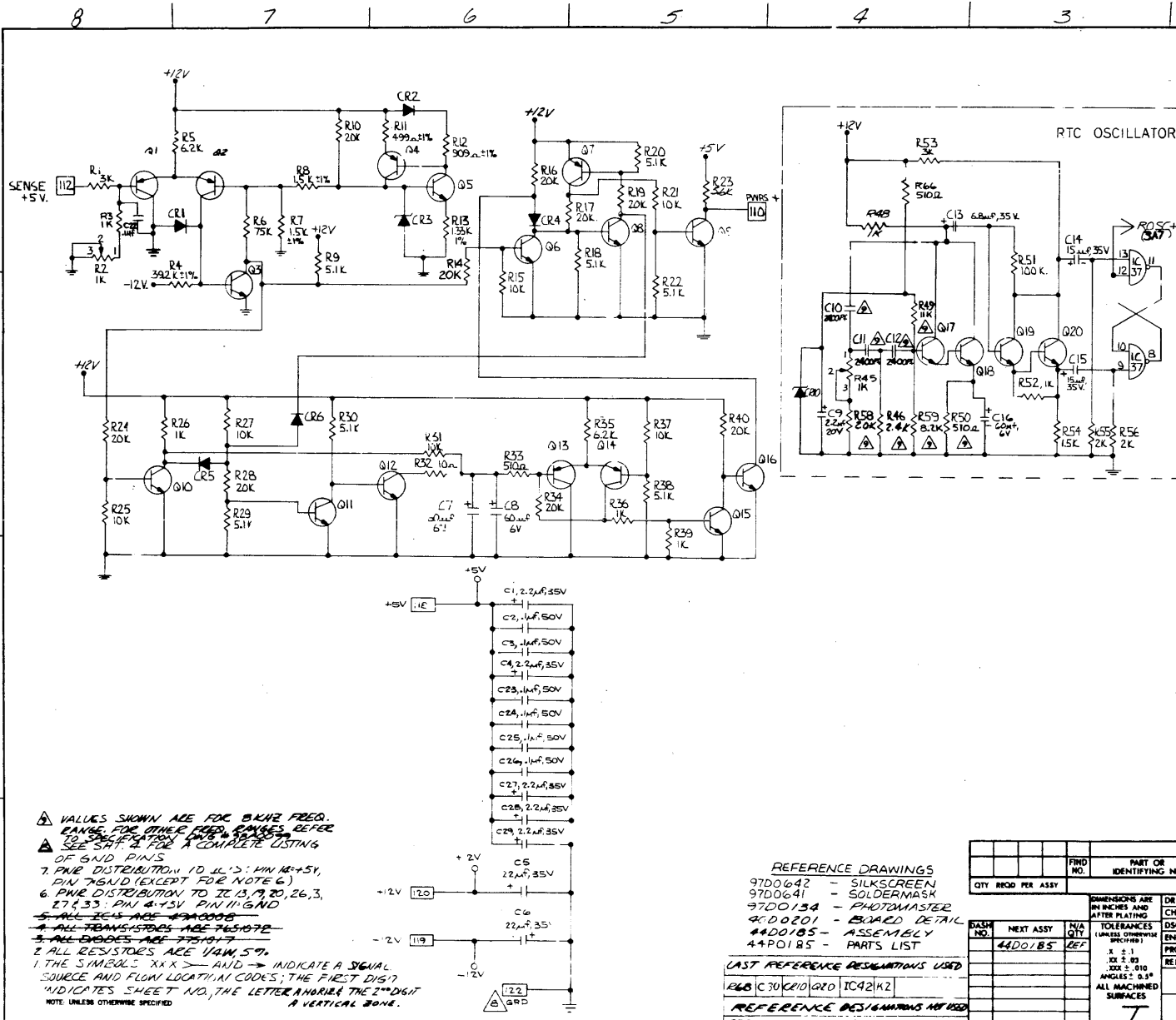
5

4

3

2

1



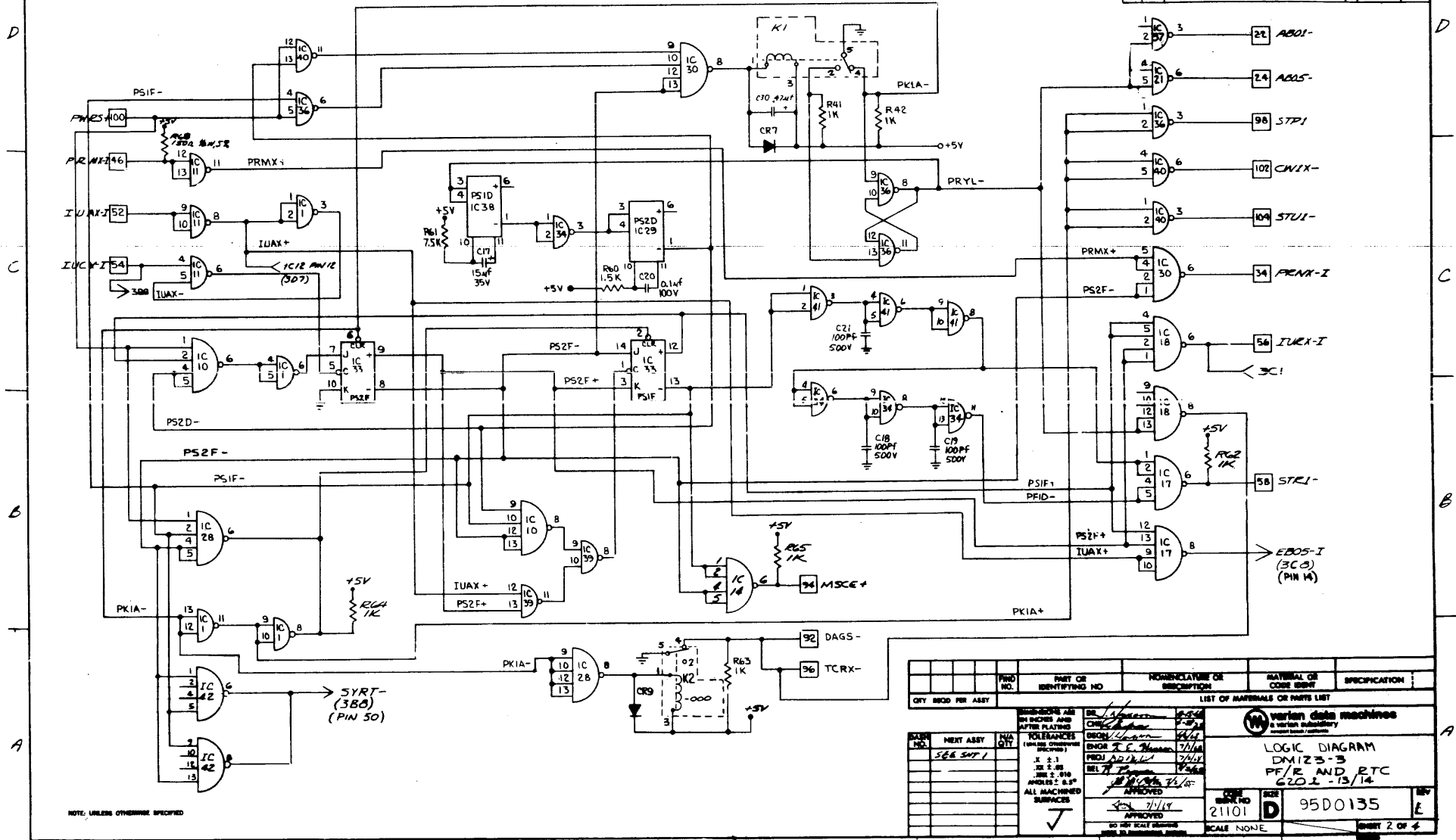
REVISION		DESCRIPTION	APPROVED	DATE
1		1-MAY BE REWORKED 2-CANNOT BE REWORKED 3-NOW SHOP PRACTICE 4-RECORD CHANGE 5-PARTS MADE OK		
X-1		PROTOTYPE RELEASE		
X-2		REVISED & REDRAWN		5-3-68
X-5		REVISED & REDRAWN		5-5-68
A		PRODUCTION RELEASE, EN 2301		5-22-68
B		R45 IS IK WAS 500Ω PER EN 2476		9-20-68
C		ADDED IC41IC21 PER EN 2493		9-20-68
D		ADDED IC42 PER EN 2765		1-23-69
E		SMT 2 KIA2 B2 AD005-000 WAS BRAD004-001 & REVISED PIN NO. 5 PER EN 2906		5-15-69
F		ADDED CR10, DELETED Q21 PER EN 3294		6-10-69
G	JB	DELETED NOTES 3, 4, 5, & ALL VDM PINS FROM P/D PER EN 3471		8/16/69
H	CW	ADDED C22, 1μF CAP PER EN 5581		7-2-69
J	CW	ADDED SILKSCREEN AND SOLDERMASK TO REF DWG PER EN 8002		7-4-69
K	WAL	ADDED C23 THRU C29 PER EN 8102		8/10/72
L	WAL	ADDED C30 SMT 2 EN 8259		12/7/73

Δ VALUES SHOWN ARE FOR 0.1KHZ FREQ. RANGE. FOR OTHER FREQ. RANGES REFER TO SENSITIVITY INFO. SEE SHT 4 FOR A COMPLETE LISTING OF GRID PINS.
 7. PINE DISTRIBUTION TO JL15: MIN IN +5V, PIN 7&8 AND (EXCEPT FOR NOTE 6)
 6. PINE DISTRIBUTION TO JL13, 19, 20, 26, 3, 27, 33: PIN 4 +5V, PIN 11 & 6ND
 5. ALL TRANS ARE BRAD000
 4. ALL TRANSISTORS ARE 74S1072
 3. ALL DIODES ARE 74S1917
 2. ALL RESISTORS ARE 1/4W, 5%
 1. THE SYMBOL: XX X → AND → INDICATE A SIGNAL SOURCE AND FLUX LOCATION CODES; THE FIRST DIGIT INDICATES SHEET NO., THE LETTER AND THE 2ND DIGIT A VERTICAL ZONE.
 NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 97D0642 - SILKSCREEN
 97D0641 - SOLDERMASK
 97D0134 - PHOTOMASTER
 4000201 - BOARD DETAIL
 4400185 - ASSEMBLY
 44P0185 - PARTS LIST
LAST REFERENCE DESIGNATIONS USED
 R68 IC30CR10Q20 IC42K2
REFERENCE DESIGNS HAVING NOT BEEN USED
 CR8, R57, R45, R44, R47

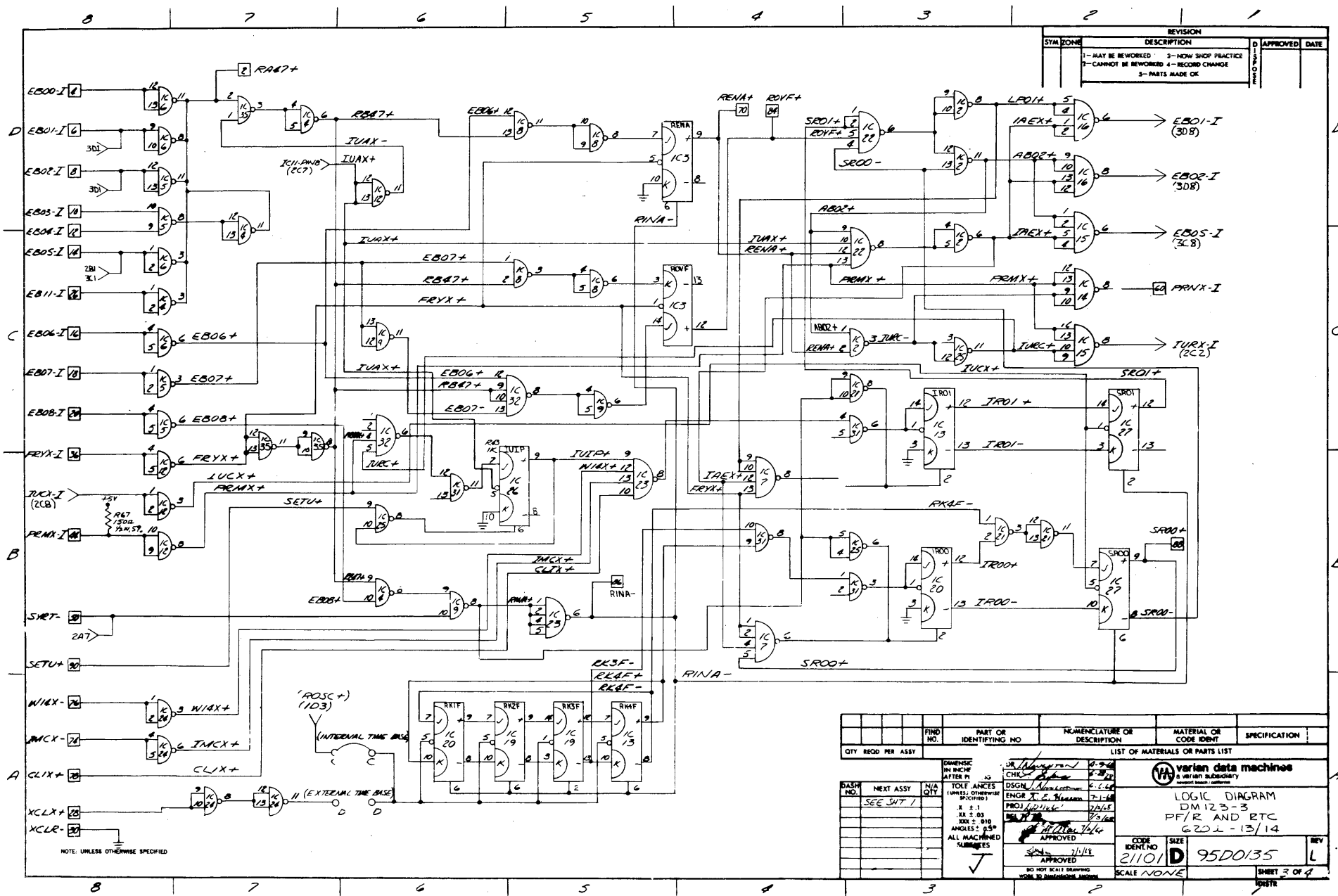
QTY	PROG	PER	ASSY	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST							
DASH NO.				DIMENSIONS ARE IN INCHES AND AFTER PLATING			
NEXT ASSY				TOLERANCES (UNLESS OTHERWISE SPECIFIED)			
4400185				X ±.1 XX ±.05 XXX ±.010 ANGLES ± 0.5° ALL MACHINED SURFACES			
REF				APPROVED			
				APPROVED			
				DO NOT SCALE DRAWING WORK TO DIMENSIONS, MEASURE			
CODE IDENT NO		SIZE		SCALE		REV	
2101		D		9500135		4	
LOGIC DIAGRAM DM 123-3 PFR AND RTC G20 L-13/14							
VARIAN DATA MACHINES							
SHEET 2 OF 4							

REVISION		APPROVED	DATE
SYM	DESCR		
1-	MAY BE REVISIONED	3-	NOW SHOP PRACTICE
2-	CANNOT BE REVISIONED	4-	RECORD CHANGE
3-	PARTS MADE OK		
SEE SHEET 1			



NOTE, UNLESS OTHERWISE SPECIFIED

PART OR IDENTIFYING NO		NOMENCLATURE OR DESCRIPTION		MATERIAL OR CODE IDENT		SPECIFICATION	
QTY REQD PER ASSY							
LIST OF MATERIALS OR PARTS LIST							
<div style="display: flex; justify-content: space-between;"> <div style="width: 20%;"> <p>DATE: 7/14/68</p> <p>BY: [Signature]</p> <p>APPROVED: [Signature]</p> <p>7/14/68</p> </div> <div style="width: 60%;"> <p>ALL DIMENSIONS ARE IN INCHES AND DECIMALS THEREOF UNLESS OTHERWISE SPECIFIED</p> <p>ANGLES: 2 & 8°</p> <p>ALL MACHINED SURFACES</p> </div> <div style="width: 15%;"> <p>variation data machines a variation subsidiary of the Bell Telephone Laboratories</p> </div> </div>							
<p>LOGIC DIAGRAM DM123-3 PFR AND PTC C201-13/14</p>				<p>SCALE: NONE</p>			
<p>DATE: 7/14/68</p> <p>BY: [Signature]</p> <p>APPROVED: [Signature]</p>		<p>QTY: 1</p> <p>REV: E</p>		<p>QTY: 1</p> <p>REV: E</p>		<p>QTY: 1</p> <p>REV: E</p>	
<p>QTY: 1</p> <p>REV: E</p>							

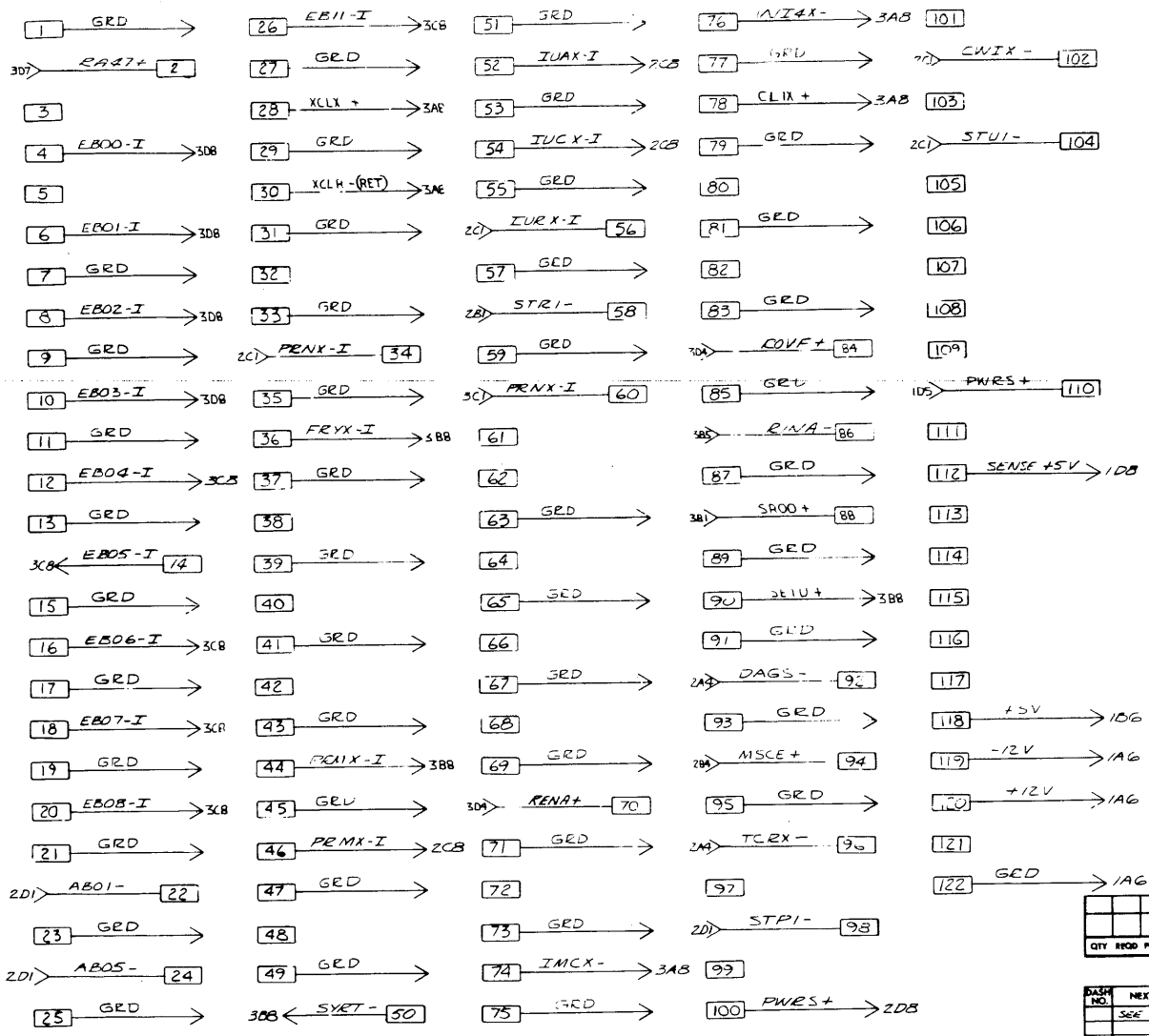


SYM	DONE	REVISION		APPROVED	DATE
		DESCRIPTION			
1-		MAY BE REWORKED	2-	HOW SHOP PRACTICE	
2-		CANNOT BE REWORKED	3-	RECORD CHANGE	
3-		PARTS MADE OK			

QTY	REQD	PER	ASSEMBLY	FIND NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL OR CODE IDENT	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST								
varian data machines								
LOGIC DIAGRAM								
DIM 123-3								
PFR AND ETC								
6201-13/14								
DATE	DESIGN	ENGR	PROJ	CHKD	APPROV	DATE	CODE IDENT NO	SIZE
7/1/58	7/1/58	J. G. Blum	12/15/58	J. G. Blum	J. G. Blum	7/1/58	21101	D
ALL MACHINED SUBASSEMBLIES APPROVED 7/1/58							SCALE	REV
DO NOT SCALE DRAWING FROM DIMENSIONS							NONE	L
SHEET 3 OF 4								REV

NOTE: UNLESS OTHERWISE SPECIFIED

SYN ZONE		REVISION			APPROVED	DATE
		DESCRIPTION				
		1- MAY BE REWORKED	3- NOW SHOP PRACTICE			
		2- CANNOT BE REWORKED	4- RECORD CHANGE			
		5- PARTS MADE OK				
		SEE SHEET ONE				

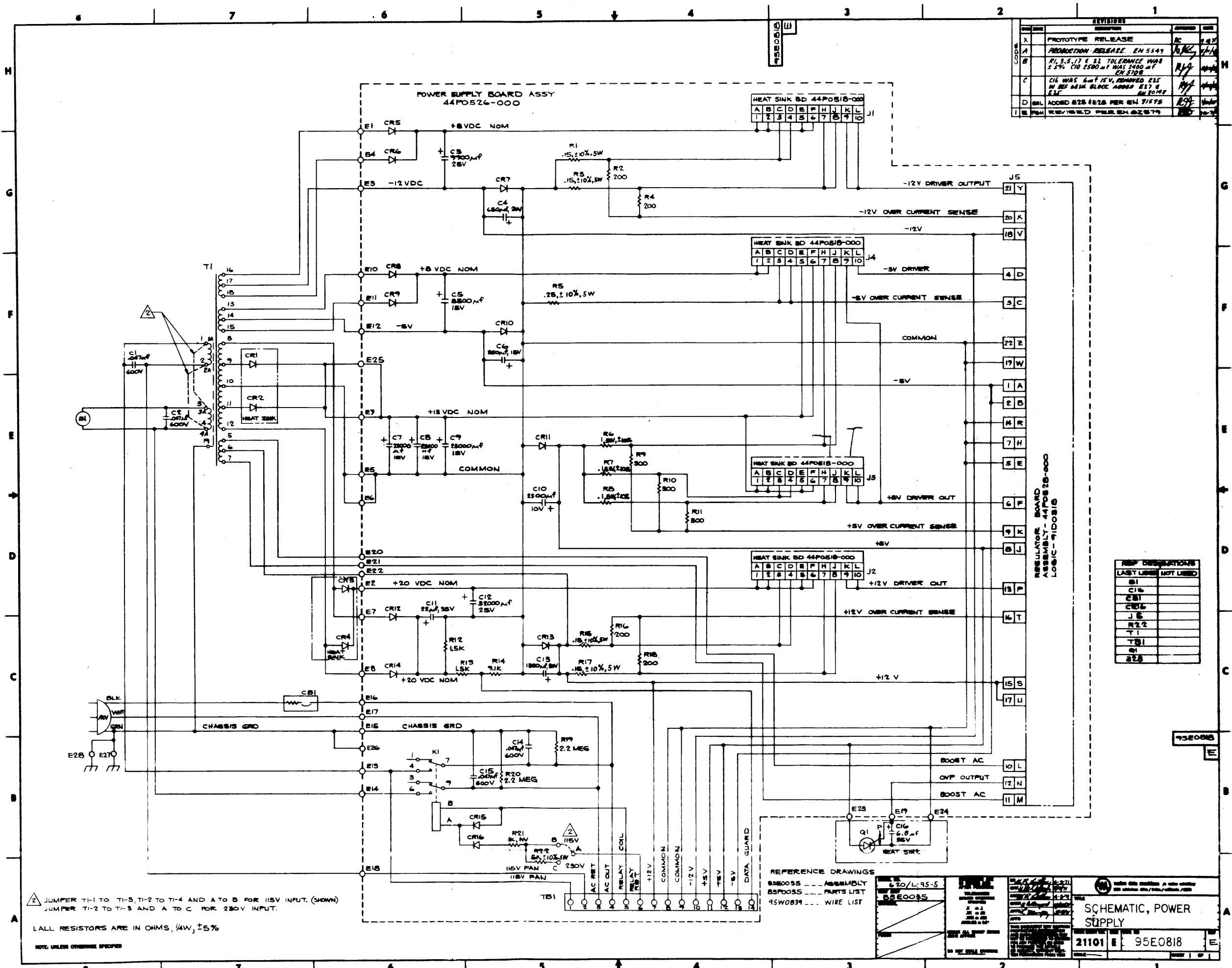


NOTE: UNLESS OTHERWISE SPECIFIED

FIND NO.		PART OR IDENTIFYING NO.		NOMENCLATURE OR DESCRIPTION		MATERIAL OR CODE IDENT		SPECIFICATION	
LIST OF MATERIALS OR PARTS LIST									
DIMENSIONS ARE IN INCHES AND AFTER PLATING									
TOLERANCES (UNLESS OTHERWISE SPECIFIED)									
X ± 0.1									
XX ± 0.05									
XXX ± 0.01									
ANGLES ± 0.5°									
ALL MACHINED SURFACES									
APPROVED									
APPROVED									
DO NOT SCALE DRAWING FROM DIMENSIONS SHOWN									
DASH NO.		NEXT ASSY		M/A QTY		DR. <i>[Signature]</i>		CHK. <i>[Signature]</i>	
SEE SMT 1						ENGR. <i>[Signature]</i>		PROJ. <i>[Signature]</i>	
						REL. <i>[Signature]</i>		DATE <i>[Signature]</i>	
						APPROVED		APPROVED	
						CODE IDENT NO. 21101		SIZE D	
						SCALE		95D0135	
								SHEET 4 OF 4	



LOGIC DIAGRAM
DM 123-3
PFR AND RTC
GZO L-13/14



REVISIONS			
REV	DESCRIPTION	APPROVED	DATE
X	PROTOTYPE RELEASE	RC	1/1/74
A	PRODUCTION RELEASE EN 5549	1/1/74	1/1/74
B	R1, 3.5, 17 & 21 TOLERANCE WAS 2.5%, C10 2500 uF WAS 3400 uF EN 5708	1/1/74	1/1/74
C	C16 WAS 6 uF 15V, REMOVED E15 IN REF 44P0526-000 E17 & E18	1/1/74	1/1/74
D	ADDED E25 & E26 PER EN 71678	1/1/74	1/1/74
E	REVISED PER EN 82679	1/1/74	1/1/74

REF DESIGNATIONS	
LAST LINE	NOT USED
B1	
C16	
E15	
E17	
E18	
J5	
R22	
T1	
TB1	
Q1	
Z28	

▲ JUMPER T1-1 TO T1-3, T1-2 TO T1-4 AND A TO B FOR 115V INPUT. (SHOWN)
 JUMPER T1-2 TO T1-3 AND A TO C FOR 230V INPUT.

ALL RESISTORS ARE IN OHMS, 1/4W, ±5%

NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
 95E0055 ASSEMBLY
 95P0055 PARTS LIST
 95W0834 WIRE LIST

620/L-95-5
 95E0055

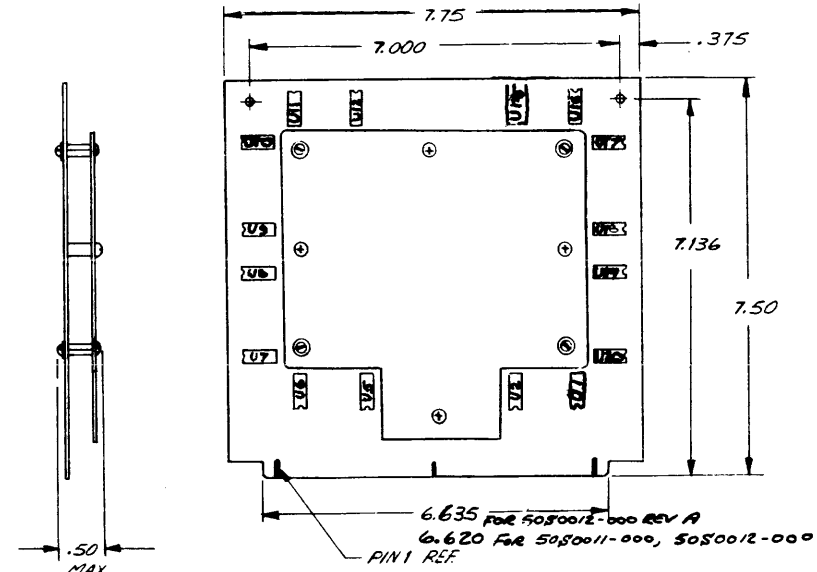
SCHEMATIC, POWER SUPPLY

21101 E 95E0818

REVISIONS			SIGNATURE AND DATE		
LTR	ZONE	DESCRIPTION	DFT	CHK	ENGRG
A		ERN 786-C	3-2-72		

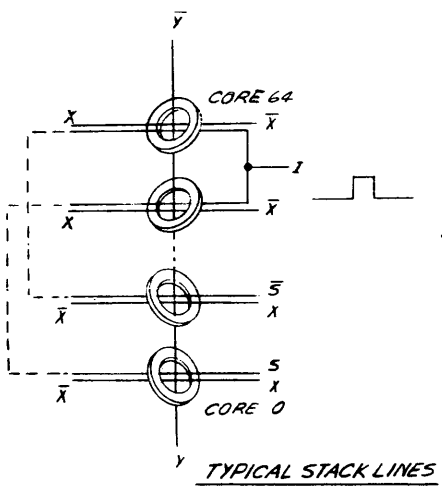
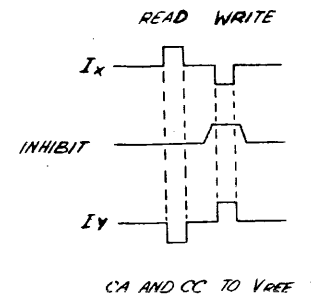
PIN ASSIGNMENT

PIN	SIDE "A"	SIDE "B"
1	GND	GND
2	CA1	CA1
3	CC1	CC1
4	CA3	CA3
5	CC3	CC3
6	CA5	CA5
7	CC5	CC5
8	CA7	CA7
9	CC7	CC7
10	SPARE	SPARE
11	SPARE	SPARE
12	SPARE	SPARE
13	X56	X57
14	X54	X55
15	X52	X53
16	X50	X51
17	RT1-1	RT1-1
18	GND	GND
19	RT1-2	RT1-2
20	S11	S11
21	I11	S6
22	S6	I6
23	S15	S15
24	I15	S9
25	S9	I9
26	S5	S5
27	I5	S7
28	S7	I7
29	S0	S0
30	I0	S4
31	S4	I4
32	SPARE	SPARE
33	SPARE	SPARE
34	SPARE	SPARE
35	S1	S1
36	I1	S12
37	S12	I12
38	S14	S14
39	I14	S10
40	S10	I10
41	S3	S3
42	I3	S13
43	S13	I13
44	S8	S8
45	I8	S2
46	S2	I2
47	SPARE	SPARE
48	GND	GND
49	SPARE	SPARE
50	Y50	Y50
51	Y53	Y52
52	Y55	Y54
53	Y57	Y56
54	SPARE	SPARE
55	SPARE	SPARE
56	SPARE	SPARE
57	CAG	CAG
58	CC6	CC6
59	CA4	CA4
60	CC4	CC4
61	CA2	CA2
62	CC2	CC2
63	CA0	CA0
64	CC0	CC0
65	GND	GND



SIDE "A" SHOWN

TYPICAL TEST PROGRAM TIMING



TYPICAL STACK LINES

DRIVE

U20	U9	U18	U7	U19	U10	U11	U8	
CC0	CC1	CC2	CC3	CC4	CC5	CC6	CC7	
CA0	CA1	CA2	CA3	CA4	CA5	CA6	CA7	
X50	0	2	16	18	45	47	61	63
X51	1	3	17	19	44	46	60	62
X52	4	6	20	22	41	43	57	59
X53	5	7	21	23	40	42	56	58
X54	8	10	24	26	37	39	53	55
X55	9	11	25	27	36	38	52	54
X56	12	14	28	30	33	35	49	51
X57	13	15	29	31	32	34	48	50

"X" DECODE DRIVE

U1	U11	U16	U6	U2	U12	U15	U5	
CC0	CC1	CC2	CC3	CC4	CC5	CC6	CC7	
CA0	CA1	CA2	CA3	CA4	CA5	CA6	CA7	
Y50	0	2	16	18	32	34	48	50
Y51	1	3	17	19	33	35	49	51
Y52	4	6	20	22	36	38	52	54
Y53	5	7	21	23	37	39	53	55
Y54	8	10	24	26	40	42	56	58
Y55	9	11	25	27	41	43	57	59
Y56	12	14	28	30	44	46	60	62
Y57	13	15	29	31	45	47	61	63

"Y" DIODE DECODE

WORST CASE PATTERN

	Y0	Y1	Y2	Y3	Y31	Y32	Y63
X0	1	1	1	1	1	0	0
X1	0	0	0	0	0	1	1
X2	1	1	1	1	1	0	0
X3	0	0	0	0	0	1	1
X63	0	0	0	0	0	1	1

- 6 INTEGRATED CIRCUIT U3, U4, U13 & U14 NOT USED.
 - 5 PCB TYPE: PLANAR PLUGGABLE. COMPONENTS AND CORES ON ONE SIDE.
 - 4 CAPACITY: 4KX18 18 MIL MAX. (COINCIDENT) 8KX9 18 MIL MAX. (COINCIDENT/ANTICOINCIDENT, 4K DECODE).
 - 3 WIRING: 3 WIRE - 3D X & Y - CONTINUOUSLY WIRED SENSE/INHIBIT - BONTIE 3 POINT TERMINATION.
 - 2 DIODE TYPE: INTERGRATED CIRCUIT - DIODE ARRAY, DUAL-IN-LINE 16 DIODES PER PACKAGE. AMPEX PN 586-687.
 - 1 CONNECTION TYPE: TOTAL 130 CONTACTS, 65 CONTACTS EACH SIDE SPACED AT .100. MATING CONNECTOR TYPE IS MASTERITE IND. PN 009GR65-DR-B-X OR EQUIV.
- NOTES: UNLESS OTHERWISE SPECIFIED.

NOTICE		UNLESS OTHERWISE SPECIFIED		SIGNATURE		DATE	
THIS DRAWING SHALL NOT BE DUPLICATED, USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED, IN WHICHEVER MANNER WITHOUT THE WRITTEN CONSENT OF AMPEX CORPORATION, PROVIDED, HOWEVER, THAT IF THIS DRAWING IS SPECIFIED TO BE DELIVERED TO THE GOVERNMENT, OR TO A GOVERNMENT CONTRACTOR, PURSUANT TO A GOVERNMENT PRIME OR SUBCONTRACT, THE GOVERNMENT MAY MAKE SUCH USE OF THIS DRAWING AS IS PERMITTED BY THE APPLICABLE "DATA" CLAUSE SET FORTH IN SUCH CONTRACT OR SUBCONTRACT.		DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES.		DRAWN BY <i>A. H. ...</i>		2/2/72	
TOL.		ANG.		CHK BY			
REMOVE BURRS AND SHARP EDGES		YES <input type="checkbox"/> NO <input type="checkbox"/>		DFT APPD			
DO NOT SCALE THIS PRINT				ENGRG APPD			
MATERIAL:				AUTH BY			
FINISH:				PARTS LIST <input type="checkbox"/>			
APPLICATION		3256973 9403-4411		AMPEX		COMPUTER PRODUCTS DIV Culver City, California 90230	
NEXT ASSY		USED ON		ELECTRICAL/MECHANICAL INTERFACE AND SCHEMATIC		SIZE CODE IDENT NO. D 09150 3238441	
SCALE .50/1.0				SHEET 1 OF 1			

ENGINEERING NOTICE

E.N. NO. 83017

PAGE 1 OF 3

ISSUE DATE 6-4-74



MODEL NUMBER 620/L-100	EFFECTIVE SERIAL ALL	WAS <u>6/4/74</u> PARTS DISPOSITION	
DEVICE DM342		<input type="checkbox"/> USE AS IS	<input type="checkbox"/> SCRAP
INTERRUPT TRAP		<input checked="" type="checkbox"/> REWORK SEE PAGE 3	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION OUTPUT CWIX - (IC34-11) IS OR-TIED ON GROUND PLANE WITH A DTL OUTPUT OF PFR BOARD. DRIVING GATES MUST BE OPEN-COLLECTOR OR DTL. DTL GATE ON PFR UNABLE TO PULL DOWN ACTIVE PULLUP GATE ON INTERRUPT TRAP BOARD RELIABLY.	CHANGE CODE: <u>1</u> OUTSTANDING CODE 2 EN'S: <u>0</u>
--	---

ACTION TO BE TAKEN

44 P0598 P/L-INTERRUPT TRAP ASSY DM342

1. UPDATE REVISION LETTER OF REF DRAWINGS 44 D0598, 91 D0362 AND F/N 1 40 D0511-000 PER THIS EN.
2. F/N 3 (65N2500-102)
 - A. QTY IS: 15 WAS: 12
 - B. REMARKS IS: R8-22 WAS: R8-19
3. F/N 16 (49A0042-000)
 - A. QTY IS: 7 WAS: 6
 - B. REMARKS IS: IC13,17,21,25,29,33,34 WAS: IC17,21,25,29,33,34
4. F/N 18 (49A0039-000)
 - A. QTY IS: 5 WAS: 6
 - B. REMARKS IS: IC18,22,26,30,31 WAS: IC18,22,26,30,31,34

DOCUMENTS AFFECTED

DOCUMENT	REV.		DOCUMENT	REV.		REFERENCE DOCUMENTS
	IS	WAS		IS	WAS	
44D0598	B	A	91C0362	B	A	RECO # 05366
44P0598	E	D	97D0712	C	B	
40D0511	C	B	97D0713	C	B	
			97D0714	C	B	

DRAFTSMAN	CHECKER	PROJECT ENGR.	RES. MGR.	REVIEW BOARD
J. Luther E-2374	Brownfield 6/3/74	T. E. Hanson 6-4-74	<i>[Signature]</i> 6/4/74	<i>[Signature]</i> 6/4/74

ENGINEERING NOTICE

E. N. No. 83017

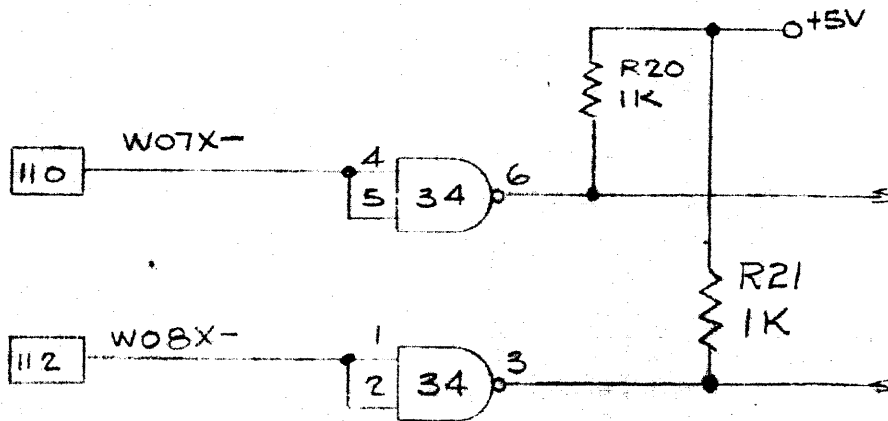
PAGE 2 OF 3



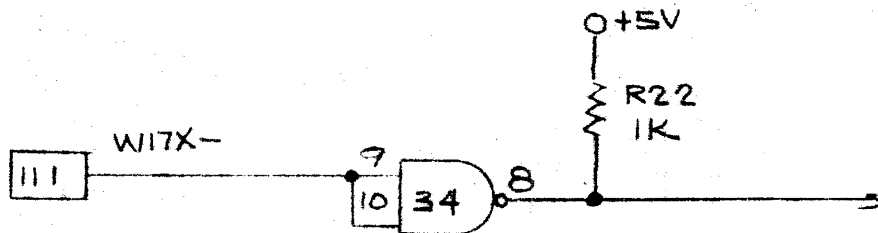
91C0362 LOGIC- INTERRUPT TRAP DM342

1. SHEET 1 REFERENCE DESIGNATION BLOCK
LAST USED IS: R22 WAS: R19

2. SHEET 2 ZONE B4 ADD R20 & R21 AS SHOWN



3. SHEET 3 ZONE B7 ADD R22 AS SHOWN



97D0712 ARTWORK DM342

REVISE TO CONFORM WITH CHANGES TO 91C0362 LOGIC

97D0713 SILKSCREEN DM342

97D0714 SOLDER MASK DM342

44D0598 ASSEMBLY DM342

REVISE TO CONFORM WITH CHANGES TO 97D0712 ARTWORK

40D0511 P.W. BOARD DM342

1. REVISE TO CONFORM WITH CHANGES TO 97D0712 ARTWORK.

2. UPDATE REVISION LETTER OF ARTWORK MASTERS 97D0712,
97D0713 AND 97D0714 PER THIS EN.

ENGINEERING NOTICE

 **varian data machines**
a varian subsidiary

REWORK INSTRUCTIONS

E. N. No. 83017

PAGE 3 OF 3

Reidentify the following revision letters by stamping an "X" over the old revision letter and restamping the new revision letter next to the "X".

Revision Is:

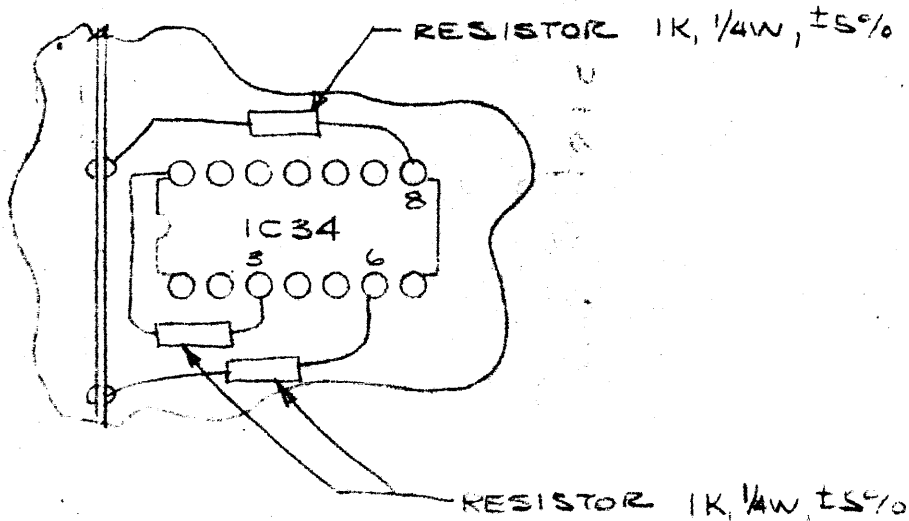
Revision Was:

44P0598-000 E

44P0598-000 D

44P0598-000 INTERRUPT TRAP BOARD DM342

1. REMOVE IC 34 (49A0039-000) AND REPLACE IC 34 USING 49A0042-000 (74HO1)
2. ADD 3 RESISTORS 1K, 1/4W, ±5% (65N2500-102) AS SHOWN:



ENGINEERING NOTICE

E.N. NO. 4553

PAGE 1 OF 2

ISSUE DATE 6-17-73



MODEL NUMBER 620L	EFFECTIVE SERIAL 6/15/73	PARTS DISPOSITION	
DEVICE POWER SUPPLY	EARLIEST OPPORTUNITY	<input checked="" type="checkbox"/> USE AS IS	<input type="checkbox"/> SCRAP
		<input type="checkbox"/> REWORK	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION

1. TO ADD NON-STANDARD HARDWARE REQUIREMENTS
2. TO REVISE HARDWARE CALLOUT
3. TO ELIMINATE POSSIBLE SHORTING OF HDW. TO HEATSHINK
4. TO ADD MTG BRACKETS TO P.S. PARTS LIST

ACTION TO BE TAKEN

83P0035 PARTS LIST POWER SUPPLY

- 1.) UPDATE REVISION LETTER OF REFERENCE DRAWINGS PER THIS EN.
- 2.) DELETE FIND NO. 45 (79A0050-000)
- 3.) ADD THE FOLLOWING, QTY SHOWN SAME FOR ALL DASH NUMBERS.

QTY	F/N	PART NO.	DESC.	REMARKS
2	48	79A0025-034	MICA WASHER	
1	49	79A0050-003	SPACER	
1	50	79A0050-004	WASHER	
1	51	79A0050-002	L.W. TERMINAL	
1	52	79A0050-001	JAM NUT	

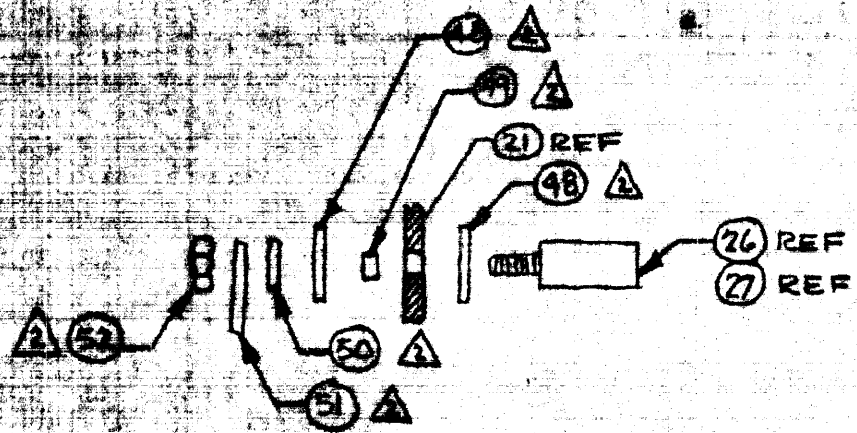
DOCUMENTS AFFECTED

DOCUMENT	REV.	DOCUMENT	REV.	REFERENCE DOCUMENTS
	IS WAS		IS WAS	
83P0035	L K			RECO # 4553
83E0035	H G			4534
				4566

DRAFTSMAN	CHECKER	PROJECT ENGR.	RESP. MGR.	REVIEW BOARD
<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73

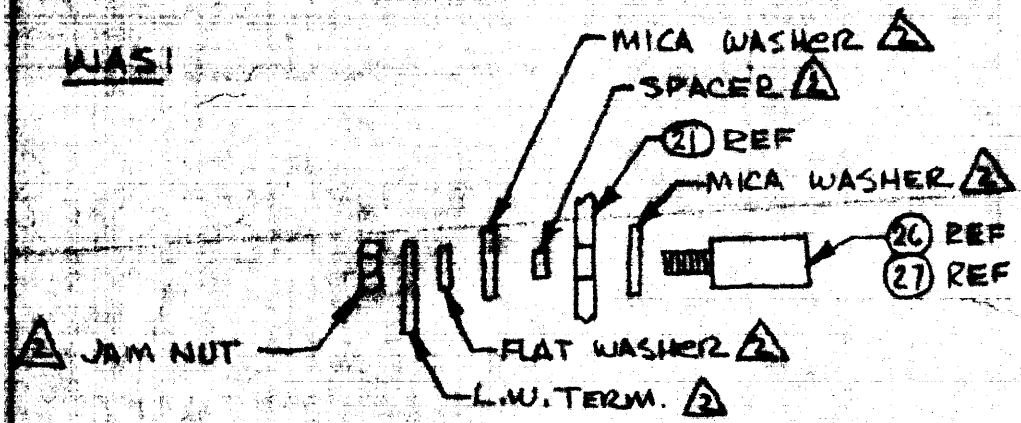
CR-0035 POWER SUPPLY ASSY

1) REVISE SECTION B-B AS SHOWN BELOW



SECTION B-B
EXPLODED VIEW (TYP FOR CR1-CR4, Q1)

WAS!



SECTION B-B
EXPLODED VIEW (TYP FOR CR1-CR4, Q1)
USE MT6 KIT F/N 45 FOR Q1

ENGINEERING NOTICE

E.N. NO. 82260

PAGE 1 OF 2

ISSUE DATE 8-15-73



MODEL NUMBER <u>620/L-100</u>	EFFECTIVE SERIAL <u>EARLIEST OPPORTUNITY</u>	<u>8/15/73</u>	PARTS DISPOSITION			
DEVICE <u>DM 337</u> PROCESSOR CONT			<input checked="" type="checkbox"/>	USE AS IS	<input type="checkbox"/>	SCRAP
			<input type="checkbox"/>	REWORK	<input type="checkbox"/>	NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION

1. TO REMOVE JUMPER A-A

ACTION TO BE TAKEN

97D0697-ARTWORK-PROCESSOR CONTROL-DM337

1. REMOVE ETCH FROM PI-59 TO FEEDTHRU A
2. REMOVE ETCH FROM IC16-7 TO FEED THRU A
3. ADD ETCH FROM PI-59 TO IC16-7.

97D0698-SILKSCREEN-PROCESSOR CONT-DM337

1. REVISE PER CHANGES TO 97D0697

97D0699-SOLDER MASK-PROCESSOR CONT-DM337

1. REVISE PER CHANGES TO 97D0697

40D0506-BD DETAIL-PROCESSOR CONT-DM337

1. UPDATE REV LETTERS OF ARTWORK MASTERS REQ'D PER THIS EN.

DOCUMENTS AFFECTED

DOCUMENT	REV.		DOCUMENT	REV.		REFERENCE DOCUMENTS
	IS	WAS		IS	WAS	
<u>97D0697</u>	H	G	<u>40D0506</u>	L	K	<u>RECO # 04484</u>
<u>97D0698</u>	G	F	<u>44D0593</u>	G	F	
<u>97D0699</u>	G	F	<u>44P0593</u>	N	M	

DRAFTSMAN	CHECKER	PROJECT ENGR.	RESP. ENGR.	REVIEW BOARD
<u>JML</u>	<u>[Signature]</u>	<u>T.E. Hanson</u>	<u>[Signature]</u>	<u>[Signature]</u>
<u>8/14/73</u>	<u>8/13/73</u>	<u>8/14/73</u>	<u>8/14/73</u>	<u>8/14/73</u>

ENGINEERING NOTICE

E. N. No. 82237
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4400506 - CONT'D

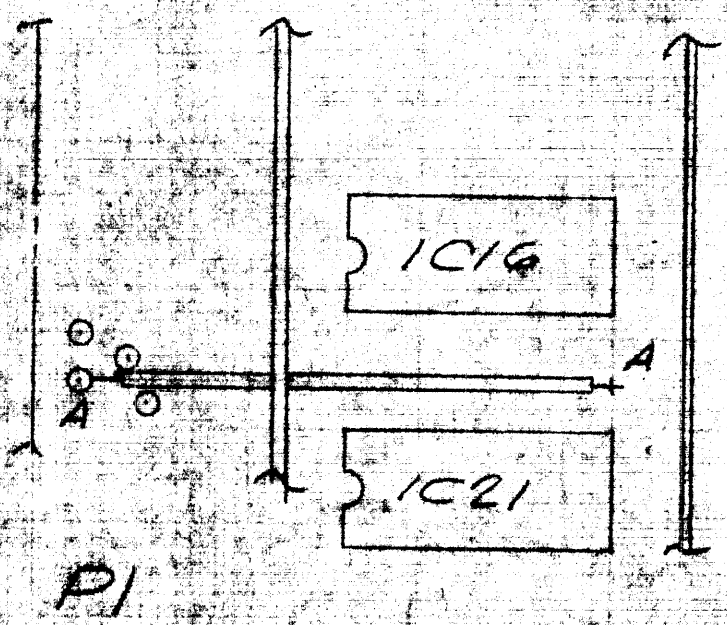
2. HOLE CHART, REVISE QTY FOR SYM A
IS: 1230, WAS: 1232

4400503 - P/L-PROCESSOR CONTROL-DM337

1. UPDATE REV LETTER OF REF DWG'S
PER THIS EN.
2. UPDATE REV LETTER OF FIND N° 1
PER THIS EN.
3. DELETE FIND N° 35, WAS: (QTY, -000, -001)
0, (PART N°) 53A0047-200
(DES) WIRE, TEFLON, (REMARKS) 26 AWG

4400523 - ASSY-PROCESSOR CONTROL-DM337

1. FIELD OF DWG, REMOVE JUMPER A-A
WAS:



ENGINEERING NOTICE

E.N. NO. 82144

PAGE 1 OF 5

ISSUE DATE 3-26-74



MODEL NUMBER <u>620L-100</u>	EFFECTIVE SERIAL <u>10/2/74</u>	PARTS DISPOSITION	
DEVICE PROC. CONT NO 2	SEE BELOW *	<input type="checkbox"/> USE AS IS	<input type="checkbox"/> SCRAP
		<input checked="" type="checkbox"/> REWORK <u>SEE PG. 4</u>	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION <u>TO ENABLE THE 620L-100 TO PASS INSTRUCTION TEST NO. 2 BY ELIMINATING A DECODING SPIKE ON IAPX+</u>	CHANGE CODE <u>1</u> OUTSTANDING CODE <u>ENS</u>
--	---

~~ACTION TO BE TAKEN~~

* EFFECTIVE SERIAL

- 1.) SEE PAGE 5 FOR SER. NO. EFFECTIVITY
- 2.) ALL THOSE USED IN SYSTEMS THAT FAIL TO PASS MARGIN TESTS

ACTION TO BE TAKEN

(SEE PAGE 2)

1/17/74 blue 3/25/74

DOCUMENTS AFFECTED

DOCUMENT	REV.		DOCUMENT	REV.		REFERENCE DOCUMENTS
	IS	WAS		IS	WAS	
<u>9100360</u>	<u>C</u>	<u>B</u>	<u>4000509</u>	<u>E</u>	<u>D</u>	RECO # <u>05220</u> RECO # <u>05190</u>
<u>4400596</u>	<u>F</u>	<u>E</u>	<u>9700706</u>	<u>C</u>	<u>B</u>	
<u>4400596</u>	<u>C</u>	<u>B</u>	<u>9700707</u>	<u>E</u>	<u>D</u>	
			<u>9700708</u>	<u>C</u>	<u>B</u>	

DRAFTSMAN	CHECKER	PROJECT ENGR.	RESP. MGR.	REVIEW BOARD
<i>[Signature]</i> <u>3/21/74</u>	<i>[Signature]</i> <u>3/21/74</u>	<u>R. E. Harrison</u> <u>3/25/74</u>	<i>[Signature]</i> <u>3/25/74</u>	<i>[Signature]</i> <u>3/25/74</u>

ENGINEERING NOTICE



E. N. No. 82844
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44P0596 P/L PROC. CONTROL NO. 2

- 1.) REVISE QTY AND REMARKS OF FIND NO. 4 (49A0008-000) QTY IS 5 WAS 4, REMARKS ADD IC49
- 2.) UPDATE REVISION LETTER OF REFERENCE DRAWINGS AND FIND NO. 1 PER THIS EN.

97D0706 ARTWORK DM340

97D0708 SOLDERMASK DM340

97D0707 SILKSCREEN DM340

- 1.) UPDATE PER CHANGES TO LOGIC DIAGRAM 91D0360

40D0509 P.W. BOARD DM340

- 1.) REVISE F/D TO AGREE WITH CHANGES TO 97D0706
- 2.) UPDATE REVISION LETTERS OF "ARTWORK MASTERS REQUIRED" PER THIS EN.

44D0596 PROC CONTROL NO. 2 ASSY

- 1.) ADD IC49 TO F/D

ENGINEERING NOTICE

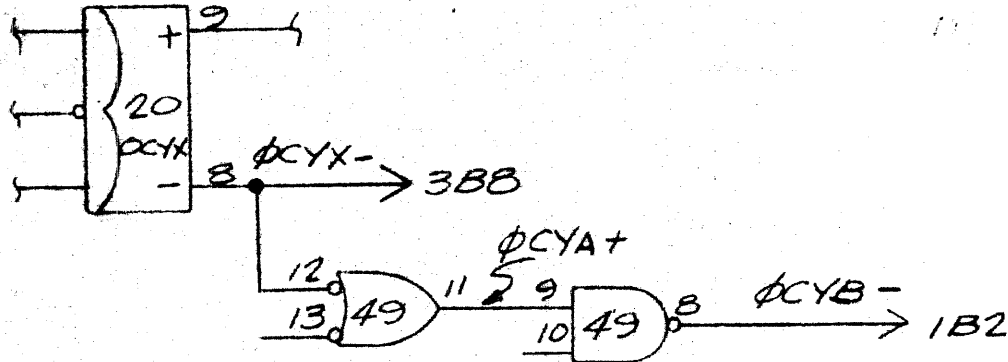


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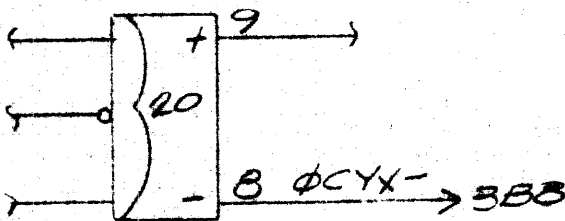
91DO360-LOGIC-PROC. CONTROL N^o2

1. SHEET 1, ZONE C1, REVISE AS FOLLOWS:

IS:

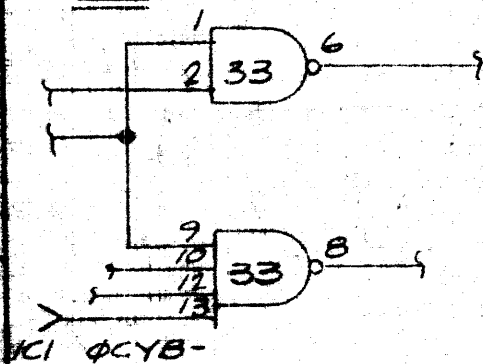


WAS:

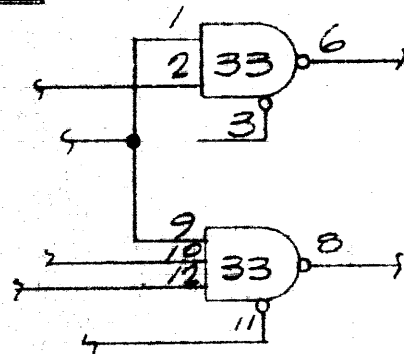


ZONE B2

IS:



WAS:



2. UPDATE REF DES LAST USED BLOCK;

IS: IC 49, WAS IC 48

ENGINEERING NOTICE



REWORK INSTRUCTIONS

E. N. No. 82844

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Reidentify the following revision letters by stamping an "X" over the old revision letter and restamping the new revision letter next to the "X".

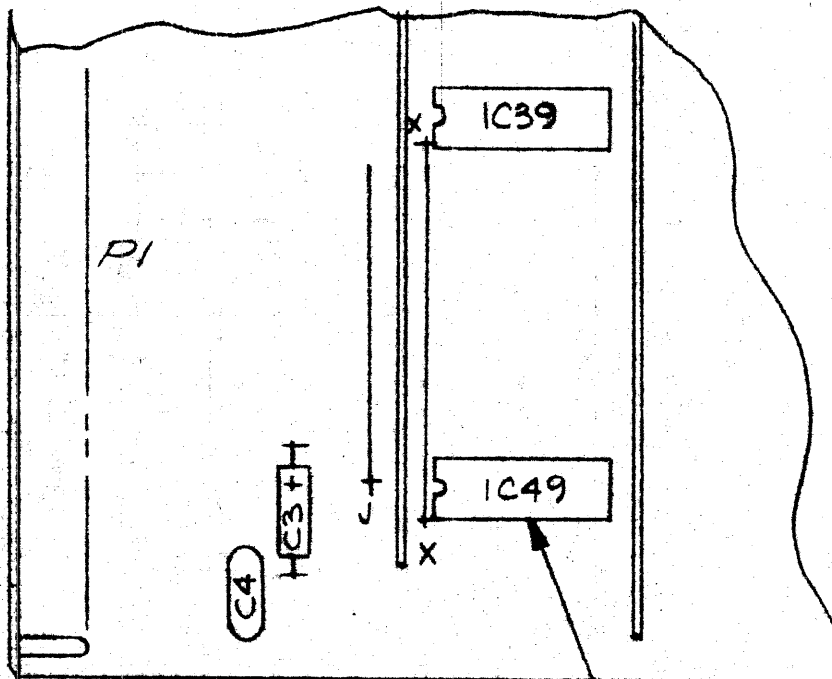
Revision Is:

44P0596-000 F

Revision Was:

44P0596-000 E

- 1.) INSTALL IC49, VDM P/N 49A0008-000 IN SPARE IC LOCATION AS SHOWN BELOW



44P0596-000
COMPONENT
SIDE

ADD IC (49A0008-000)

- 2.) ADD THE FOLLOWING JUMPERS TO COMP. SIDE USING WIRE, 53A0701-000, 30 AWG

FROM	TO
IC49-8	IC33-13
IC49-9	IC49-11
IC49-12	IC20-8

ENGINEERING NOTICE

 varian data machines
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E. N. No. 82844
PAGE 5 OF 5

SERIAL NO. EFFECTIVITY

44P0596-000

SER. NO. 1566 AND ON.

S/N 1166 THRU 1169,

1200

1201

1202

1210

1211

1215

1219

1220

1223

1225

1228

1236

S/N 1243 THRU 1337

S/N 1339 THRU 1359

S/N 1361 THRU 1368

1372

1386

1388

1389

1390

1394

1395

1396

1398

1400

1403

1407

1411

1413

1414

1422

1432

1438

1439

1444

1445

S/N 1451 THRU 1485

1526

1528

1529

1532

1533

1534

1536

1540

1544

S/N 1547 THRU 1550

1554

1557

1559

1561

1565