

**CMOS FAMILY
COMPONENTS LIBRARY**
Packaged Parts

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p-cad[®]
PERSONAL CAD SYSTEMS INC.

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OVERVIEW

This manual and the seven CMOS Components Packaged Parts Diskettes comprise the P-CAD CMOS Components Packaged Parts Library. The library has been developed at the request of our users, and we welcome any suggestions for improvements or additions.

The library diskettes contain the following files for use with the PC-CARDS printed circuit board (PCB) layout program:

- Component files
- Layer structure files, LAYS.PRT and LAYS.PCB
- Standard-size drawing sheet files, ASIZE.PCB through ESIZE.PCB
- CMOS.FIL and CMOS.LIB files

CMOS.FIL is a sample text file used as input into PREPACK to create the binary file CMOS.LIB that contains packaging information for PC-PACK. Both CMOS.FIL and CMOS.LIB contain all the components in the CMOS Components Library. Normal usage is to extract only those components used in a design and put them in a new .FIL file for input to PREPACK.

- Padstack and special symbol files (<filename>.PS and <filename>.SSF)

The padstacks and special symbol files are samples of what can be used in the PC-CARDS environment. Refer to the PC-CARDS User's Manual for more information on how to use padstacks and special symbol files.

FILE MANAGEMENT

The complete CMOS Components Parts Library includes more than 1.9 MB of files. If you are loading the library on the hard disk of your stand-alone computer, you should omit any of the components that you will not need in order to conserve disk space. This is especially important if you are using a 10 MB hard disk.

If your hard disk space is very limited, you may remove individual unneeded parts from the library. Each part is contained in a separate DOS file, and individual parts may be erased using the DOS erase command. Refer to your IBM DOS Manual or the "DOS Reference" chapter included with your PC-CAPS or PC-CARDS User's Manuals for instructions on listing and erasing files.

P-CAD recommends a specific directory structure for efficient system operation. Your library parts are normally placed in a specific subdirectory to make it easy to manage these files. The directory structure is described in your P-CAD Installation Guide.

CREATING A DESIGN

To use the library in a design, run PC-CARDS. Instructions are given in the "Using PC-CARDS" chapter of your PC-CARDS User's Manual. When the menu is displayed, select FILE/LOAD and load the layer structure. You can load LAYS.PCB or one of the standard-size drawing sheet files, ASIZE.PCB through ESIZE.PCB.

Layer Structure

Two layer structure files are included with this library, LAYS.PRT and LAYS.PCB. There is no difference between LAYS.PRT and LAYS.PCB other than the active state of the layers.

The following layer structure, LAYS.PRT, is a standard P-CAD layer structure and is recommended when creating library components.

Table 1. LAYS.PRT Layer Structure

| Layer | Name | Pen | Status | Use |
|-------|--------|-----|--------|-------------------------------------|
| 1 | PADCOM | 4 | ON | Graphic component pads |
| 2 | FLCOMP | 4 | OFF | Flash component pads |
| 3 | PADSLD | 8 | OFF | Graphic solder pads |
| 4 | FLSOLD | 8 | OFF | Flash solder pads |
| 5 | PADINT | 9 | OFF | Graphic internal pads |
| 6 | FLINT | 9 | OFF | Flash internal pads |
| 7 | GNDCON | 10 | OFF | Graphic internal ground connections |
| 8 | FLGCON | 10 | OFF | Flash internal ground connections |
| 9 | CLEAR | 7 | OFF | Graphic universal clearance |
| 10 | FLCLER | 7 | OFF | Flash universal clearance |
| 11 | PWRCON | 13 | OFF | Graphic internal power connections |
| 12 | FLPCON | 13 | OFF | Flash internal power connections |

Table 1 Continued

| Layer | Name | Pen | Status | Use |
|-------|--------|-----|--------|----------------------------|
| 13 | SLDMSK | 14 | OFF | Graphic solder mask relief |
| 14 | FLSMSK | 14 | OFF | Flash solder mask |
| 15 | DRILL | 15 | OFF | Graphic drill template |
| 16 | FLDRLL | 15 | OFF | Flash drill template |
| 17 | PIN | 4 | ABL(A) | Graphic pin connections |
| 18 | BRDOUT | 12 | OFF | Board outline |
| 19 | FLTARG | 11 | OFF | Flash alignment targets |
| 20 | SLKSCR | 6 | ABL | Silkscreen paint |
| 21 | DEVICE | 5 | ABL | Device names |
| 22 | ATTR | 6 | OFF | Attributes |
| 23 | REFDES | 6 | OFF | Reference designators |
| 24 | COMP | 1 | OFF | Component side traces |
| 25 | SOLDER | 2 | OFF | Solder side traces |
| 26 | INT1 | 3 | OFF | Internal layer traces |

The following layer structure, LAYS.PCB, is a standard P-CAD layer structure and is recommended when creating printed circuit board layouts.

Table 2. LAYS.PCB Layer Structure

| Layer | Name | Pen | Status | Use |
|-------|--------|-----|--------|-------------------------------------|
| 1 | PADCOM | 4 | ON | Graphic component pads |
| 2 | FLCOMP | 4 | OFF | Flash component pads |
| 3 | PADSLD | 8 | OFF | Graphic solder pads |
| 4 | FLSOLD | 8 | OFF | Flash solder pads |
| 5 | PADINT | 9 | OFF | Graphic internal pads |
| 6 | FLINT | 9 | OFF | Flash internal pads |
| 7 | GNDCON | 10 | OFF | Graphic internal ground connections |
| 8 | FLGCON | 10 | OFF | Flash internal ground connections |
| 9 | CLEAR | 7 | OFF | Graphic universal clearance |
| 10 | FLCLER | 7 | OFF | Flash universal clearance |
| 11 | PWRCON | 13 | OFF | Graphic internal power connections |

Table 2 Continued

| Layer | Name | Pen | Status | Use |
|-------|--------|-----|---------|--|
| 12 | FLPCON | 13 | OFF | Flash internal power connections |
| 13 | SLDMSK | 14 | OFF | Graphic solder mask relief |
| 14 | FLSMSK | 14 | OFF | Flash solder mask |
| 15 | DRILL | 15 | OFF | Graphic drill template |
| 16 | FLDRLL | 15 | OFF | Flash drill template |
| 17 | PIN | 4 | ON | Graphic pin connections |
| 18 | BRDOUT | 12 | ON | Board outline |
| 19 | FLTARG | 11 | OFF | Flash alignment targets |
| 20 | SLKSCR | 6 | ON | Silkscreen paint |
| 21 | DEVICE | 5 | ON | Device names |
| 22 | ATTR | 6 | OFF | Attributes |
| 23 | REFDES | 6 | ON | Reference designators |
| 24 | COMP | 1 | ABL (A) | Component side traces |
| 25 | SOLDER | 2 | ABL | Solder side traces |
| 26 | INT1 | 3 | OFF | Internal layer traces |

Drawing Sheets

The standard-size drawing sheet files, ASIZE.PCB through ESIZE.PCB, were created using the LAYS.PCB layer structure. When loaded, they provide the correct layer structure for the library plus a standard-size drawing sheet border.

Components

When you have loaded the layer structure or drawing sheet file, you can enter the components, wires, text, instances, and net names. Complete instructions are given in the "Using PC-CARDS" chapter of your PC-CARDS User's Manual.

GENERAL INFORMATION

This library is comprised of parts from four technologies:

1. Standard CMOS 4000 series (CD40XXX)
2. Standard CMOS 4500 series (CD45XXX)
3. High Speed CMOS (74HCXXX)
4. High Speed TTL Compatible CMOS (74HCTXXX)

This library was created using the following sources :

1. Universal Semiconductor Inc. High speed CMOS data book. (1985 version)
2. RCA Solid State QMOS data book. (1985 version)
3. RCA COS/MOS Integrated Circuits book. (1980 version)
4. Motorola Semiconductor Inc. CMOS Integrated Circuits data book. (1978 version)

We have included multiple representations of several symbols to better match your exact needs. The part packages containing more than one single part are denoted by an "S" in their file name.

For example, the HCT175 (quad D flip-flop) is represented as a single flip-flop in the file HT175.PRT and as a single package containing four flip-flops in the file HT175S.PRT.

Due to system limitations regarding filename length, the names of the parts files in this library are truncated versions of the component names:

CDxxxx shortened to Cxxxx.PRT
74HCxxxx shortened to Hxxxx.PRT
74HCTxxxx shortened to HTxxxx.PRT

FOOTPRINT ATTRIBUTES

The components in this library have been assigned footprint attributes on the ATTR layer for PC-PLACE. All DIP parts have the footprint attribute : FP=DIPxx where xx is the number of pins for that part.

COMPONENT LIST BY SEQUENCE

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| CD4000B | 1 | CD1 |
| CD4001B | 1 | CD1 |
| CD4002B | 1 | CD1 |
| CD4006B | 1 | CD1 |
| CD4008B | 1 | CD1 |
| CD4009UB | 1 | CD1 |
| CD4010B | 1 | CD1 |
| CD4011B | 1 | CD1 |
| CD4012B | 1 | CD1 |
| CD4013B | 1 | CD1 |
| CD4013BS | 1 | CD1 |
| CD4014B | 1 | CD1 |
| CD4015B | 1 | CD1 |
| CD4015BS | 1 | CD1 |
| CD4016B | 1 | CD1 |
| CD4016BS | 1 | CD1 |
| CD4017B | 1 | CD1 |
| CD4018B | 1 | CD1 |
| CD4019B | 1 | CD1 |
| CD4019BS | 1 | CD1 |
| CD4020B | 1 | CD1 |
| CD4021B | 1 | CD1 |
| CD4022B | 1 | CD1 |
| CD4023B | 1 | CD1 |
| CD4024B | 1 | CD1 |
| CD4025B | 1 | CD1 |
| CD4026B | 1 | CD1 |
| CD4027B | 1 | CD1 |
| CD4028B | 1 | CD1 |
| CD4029B | 1 | CD1 |
| CD4030B | 1 | CD1 |
| CD4031B | 1 | CD1 |
| CD4032B | 1 | CD1 |
| CD4032BS | 1 | CD1 |
| CD4033B | 1 | CD1 |
| CD4034B | 1 | CD1 |
| CD4035B | 1 | CD1 |
| CD4037A | 1 | CD1 |
| CD4037AS | 1 | CD1 |
| CD4038B | 1 | CD1 |
| CD4038BS | 1 | CD1 |
| CD4040B | 1 | CD1 |
| CD4041UB | 1 | CD1 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| CD4042B | 1 | CD1 |
| CD4042BS | 1 | CD1 |
| CD4043B | 1 | CD1 |
| CD4043BS | 1 | CD1 |
| CD4044B | 1 | CD1 |
| CD4044BS | 1 | CD1 |
| CD4045B | 1 | CD1 |
| CD4046B | 1 | CD1 |
| CD4047B | 1 | CD1 |
| CD4048B | 1 | CD1 |
| CD4049UB | 1 | CD1 |
| CD4050B | 1 | CD1 |
| CD4051B | 1 | CD1 |
| CD4052B | 1 | CD1 |
| CD4053B | 1 | CD1 |
| CD4054B | 1 | CD1 |
| CD4055B | 1 | CD2 |
| CD4056B | 1 | CD2 |
| CD4057A | 1 | CD2 |
| CD4059A | 1 | CD2 |
| CD4060B | 1 | CD2 |
| CD4063B | 1 | CD2 |
| CD4066B | 1 | CD2 |
| CD4067B | 1 | CD2 |
| CD4068B | 1 | CD2 |
| CD4069UB | 1 | CD2 |
| CD4070B | 1 | CD2 |
| CD4071B | 1 | CD2 |
| CD4072B | 1 | CD2 |
| CD4073B | 1 | CD2 |
| CD4075B | 1 | CD2 |
| CD4076B | 1 | CD2 |
| CD4077B | 1 | CD2 |
| CD4078B | 2 | CD2 |
| CD4081B | 2 | CD2 |
| CD4082B | 2 | CD2 |
| CD4085B | 2 | CD2 |
| CD4085BS | 2 | CD2 |
| CD4086B | 2 | CD2 |
| CD4089B | 2 | CD2 |
| CD4093B | 2 | CD2 |
| CD4094B | 2 | CD2 |
| CD4095B | 2 | CD2 |
| CD4096B | 2 | CD2 |
| CD4097B | 2 | CD2 |
| CD4098B | 2 | CD2 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| CD4099B | 2 | CD2 |
| CD4502B | 2 | CD2 |
| CD4502BS | 2 | CD2 |
| CD4503B | 2 | CD2 |
| CD4508B | 2 | CD2 |
| CD4510B | 2 | CD2 |
| CD4511B | 2 | CD2 |
| CD4512B | 2 | CD2 |
| CD4514B | 2 | CD2 |
| CD4515B | 2 | CD2 |
| CD4516B | 2 | CD3 |
| CD4517B | 2 | CD3 |
| CD4517BS | 2 | CD3 |
| CD4518B | 2 | CD3 |
| CD4518BS | 2 | CD3 |
| CD4520B | 2 | CD3 |
| CD4520BS | 2 | CD3 |
| CD4527B | 2 | CD3 |
| CD4532B | 2 | CD3 |
| CD4536B | 2 | CD3 |
| CD4538B | 2 | CD3 |
| CD4538BS | 2 | CD3 |
| CD4541B | 2 | CD3 |
| CD4555B | 2 | CD3 |
| CD4556B | 2 | CD3 |
| CD4585B | 2 | CD3 |
| CD4724B | 2 | CD3 |
| CD22104A | 2 | CD3 |
| CD22105A | 2 | CD3 |
| CD22859 | 2 | CD3 |
| CD40100B | 2 | CD3 |
| CD40101B | 2 | CD3 |
| CD40102B | 2 | CD3 |
| CD40103B | 2 | CD3 |
| CD40104B | 2 | CD3 |
| CD40105B | 2 | CD3 |
| CD40106B | 2 | CD3 |
| CD40107B | 2 | CD3 |
| CD40108B | 2 | CD3 |
| CD40109B | 2 | CD3 |
| CD40109BS | 2 | CD3 |
| CD40110B | 2 | CD3 |
| CD40115 | 2 | CD3 |
| CD40116 | 2 | CD3 |
| CD40117B | 2 | CD3 |
| CD40147B | 2 | CD3 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| CD40160B | 2 | CD3 |
| CD40161B | 2 | CD3 |
| CD40162B | 2 | CD3 |
| CD40163B | 2 | CD3 |
| CD40174B | 2 | CD3 |
| CD40174BS | 2 | CD4 |
| CD40175B | 2 | CD4 |
| CD40175BS | 2 | CD4 |
| CD40181B | 2 | CD4 |
| CD40182B | 2 | CD4 |
| CD40192B | 2 | CD4 |
| CD40193B | 2 | CD4 |
| CD40194B | 2 | CD4 |
| CD40208B | 2 | CD4 |
| CD40257B | 2 | CD4 |
| CD40257BS | 2 | CD4 |
| HC00 | 3 | HC1 |
| HC02 | 3 | HC1 |
| HC03 | 3 | HC1 |
| HC04 | 3 | HC1 |
| HC05 | 3 | HC1 |
| HC08 | 3 | HC1 |
| HC10 | 3 | HC1 |
| HC11 | 3 | HC1 |
| HC14 | 3 | HC1 |
| HC20 | 3 | HC1 |
| HC21 | 3 | HC1 |
| HC27 | 3 | HC1 |
| HC30 | 3 | HC1 |
| HC32 | 3 | HC1 |
| HC42 | 3 | HC1 |
| HC44 | 3 | HC1 |
| HC51 | 3 | HC1 |
| HC73 | 3 | HC1 |
| HC74 | 3 | HC1 |
| HC75 | 3 | HC1 |
| HC76 | 3 | HC1 |
| HC85 | 3 | HC1 |
| HC86 | 3 | HC1 |
| HC93 | 3 | HC1 |
| HC107 | 3 | HC1 |
| HC109 | 3 | HC1 |
| HC112 | 3 | HC1 |
| HC123 | 3 | HC1 |
| HC125 | 3 | HC1 |
| HC126 | 3 | HC1 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HC132 | 3 | HC1 |
| HC133 | 3 | HC1 |
| HC137 | 3 | HC1 |
| HC138 | 3 | HC1 |
| HC139 | 3 | HC1 |
| HC145 | 3 | HC1 |
| HC147 | 3 | HC1 |
| HC151 | 3 | HC1 |
| HC153 | 3 | HC1 |
| HC153S | 3 | HC1 |
| HC154 | 3 | HC1 |
| HC157 | 3 | HC1 |
| HC157S | 3 | HC1 |
| HC158 | 3 | HC1 |
| HC158S | 3 | HC1 |
| HC160 | 3 | HC1 |
| HC161 | 3 | HC1 |
| HC162 | 3 | HC1 |
| HC163 | 3 | HC1 |
| HC164 | 3 | HC1 |
| HC165 | 3 | HC1 |
| HC166 | 3 | HC1 |
| HC173 | 3 | HC1 |
| HC174 | 3 | HC1 |
| HC174S | 3 | HC1 |
| HC175 | 3 | HC1 |
| HC175S | 3 | HC1 |
| HC181 | 3 | HC1 |
| HC182 | 3 | HC2 |
| HC190 | 3 | HC2 |
| HC191 | 3 | HC2 |
| HC192 | 3 | HC2 |
| HC193 | 3 | HC2 |
| HC194 | 3 | HC2 |
| HC195 | 3 | HC2 |
| HC221 | 3 | HC2 |
| HC237 | 3 | HC2 |
| HC238 | 3 | HC2 |
| HC240 | 3 | HC2 |
| HC240S | 3 | HC2 |
| HC241 | 3 | HC2 |
| HC242 | 3 | HC2 |
| HC243 | 3 | HC2 |
| HC244 | 3 | HC2 |
| HC244S | 3 | HC2 |
| HC245 | 3 | HC2 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HC251 | 3 | HC2 |
| HC253 | 3 | HC2 |
| HC253S | 4 | HC2 |
| HC257 | 4 | HC2 |
| HC258 | 4 | HC2 |
| HC259 | 4 | HC2 |
| HC266 | 4 | HC2 |
| HC273 | 4 | HC2 |
| HC280 | 4 | HC2 |
| HC283 | 4 | HC2 |
| HC297 | 4 | HC2 |
| HC299 | 4 | HC2 |
| HC354 | 4 | HC2 |
| HC356 | 4 | HC2 |
| HC365 | 4 | HC2 |
| HC365S | 4 | HC2 |
| HC366 | 4 | HC2 |
| HC366S | 4 | HC2 |
| HC367 | 4 | HC2 |
| HC368 | 4 | HC2 |
| HC373 | 4 | HC2 |
| HC374 | 4 | HC2 |
| HC375 | 4 | HC2 |
| HC375S | 4 | HC2 |
| HC377 | 4 | HC2 |
| HC390 | 4 | HC2 |
| HC393 | 4 | HC2 |
| HC423 | 4 | HC2 |
| HC533 | 4 | HC2 |
| HC534 | 4 | HC2 |
| HC540 | 4 | HC2 |
| HC541 | 4 | HC2 |
| HC563 | 4 | HC2 |
| HC564 | 4 | HC2 |
| HC573 | 4 | HC2 |
| HC574 | 4 | HC2 |
| HC583 | 4 | HC2 |
| HC597 | 4 | HC2 |
| HC640 | 4 | HC2 |
| HC643 | 4 | HC2 |
| HC646 | 4 | HC2 |
| HC648 | 4 | HC3 |
| HC670 | 4 | HC3 |
| HC688 | 4 | HC3 |
| HC4002 | 4 | HC3 |
| HC4015 | 4 | HC3 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HC4016 | 4 | HC3 |
| HC4017 | 4 | HC3 |
| HC4020 | 4 | HC3 |
| HC4024 | 4 | HC3 |
| HC4040 | 4 | HC3 |
| HC4046 | 4 | HC3 |
| HC4049 | 4 | HC3 |
| HC4050 | 4 | HC3 |
| HC4051 | 4 | HC3 |
| HC4052 | 4 | HC3 |
| HC4053 | 4 | HC3 |
| HC4059 | 4 | HC3 |
| HC4060 | 4 | HC3 |
| HC4066 | 4 | HC3 |
| HC4067 | 4 | HC3 |
| HC4075 | 4 | HC3 |
| HC4078 | 4 | HC3 |
| HC4094 | 4 | HC3 |
| HC4316 | 4 | HC3 |
| HC4316S | 4 | HC3 |
| HC4351 | 4 | HC3 |
| HC4352 | 4 | HC3 |
| HC4353 | 4 | HC3 |
| HC4510 | 4 | HC3 |
| HC4511 | 4 | HC3 |
| HC4514 | 4 | HC3 |
| HC4515 | 4 | HC3 |
| HC4516 | 4 | HC3 |
| HC4518 | 4 | HC3 |
| HC4520 | 4 | HC3 |
| HC4538 | 4 | HC3 |
| HC7046 | 4 | HC3 |
| HC7266 | 4 | HC3 |
| HC40102 | 4 | HC3 |
| HC40103 | 4 | HC3 |
| HC40104 | 4 | HC3 |
| HC40105 | 4 | HC3 |
| HCT00 | 5 | HCT1 |
| HCT02 | 5 | HCT1 |
| HCT03 | 5 | HCT1 |
| HCT04 | 5 | HCT1 |
| HCT05 | 5 | HCT1 |
| HCT08 | 5 | HCT1 |
| HCT10 | 5 | HCT1 |
| HCT11 | 5 | HCT1 |
| HCT14 | 5 | HCT1 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HCT20 | 5 | HCT1 |
| HCT21 | 5 | HCT1 |
| HCT27 | 5 | HCT1 |
| HCT30 | 5 | HCT1 |
| HCT32 | 5 | HCT1 |
| HCT42 | 5 | HCT1 |
| HCT44 | 5 | HCT1 |
| HCT51 | 5 | HCT1 |
| HCT73 | 5 | HCT1 |
| HCT74 | 5 | HCT1 |
| HCT75 | 5 | HCT1 |
| HCT76 | 5 | HCT1 |
| HCT85 | 5 | HCT1 |
| HCT86 | 5 | HCT1 |
| HCT93 | 5 | HCT1 |
| HCT107 | 5 | HCT1 |
| HCT109 | 5 | HCT1 |
| HCT112 | 5 | HCT1 |
| HCT123 | 5 | HCT1 |
| HCT125 | 5 | HCT1 |
| HCT126 | 5 | HCT1 |
| HCT132 | 5 | HCT1 |
| HCT133 | 5 | HCT1 |
| HCT137 | 5 | HCT1 |
| HCT138 | 5 | HCT1 |
| HCT139 | 5 | HCT1 |
| HCT145 | 5 | HCT1 |
| HCT147 | 5 | HCT1 |
| HCT151 | 5 | HCT1 |
| HCT153 | 5 | HCT1 |
| HCT153S | 5 | HCT1 |
| HCT154 | 5 | HCT1 |
| HCT157 | 5 | HCT1 |
| HCT157S | 5 | HCT1 |
| HCT158 | 5 | HCT1 |
| HCT158S | 5 | HCT1 |
| HCT160 | 5 | HCT1 |
| HCT161 | 5 | HCT1 |
| HCT162 | 5 | HCT1 |
| HCT163 | 5 | HCT1 |
| HCT164 | 5 | HCT1 |
| HCT165 | 5 | HCT1 |
| HCT166 | 5 | HCT1 |
| HCT173 | 5 | HCT1 |
| HCT174 | 5 | HCT1 |
| HCT174S | 5 | HCT1 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HCT175 | 5 | HCT1 |
| HCT175S | 5 | HCT1 |
| HCT181 | 5 | HCT1 |
| HCT182 | 5 | HCT2 |
| HCT190 | 5 | HCT2 |
| HCT191 | 5 | HCT2 |
| HCT192 | 5 | HCT2 |
| HCT193 | 5 | HCT2 |
| HCT194 | 5 | HCT2 |
| HCT195 | 5 | HCT2 |
| HCT221 | 5 | HCT2 |
| HCT237 | 5 | HCT2 |
| HCT238 | 5 | HCT2 |
| HCT240 | 5 | HCT2 |
| HCT240S | 5 | HCT2 |
| HCT241 | 5 | HCT2 |
| HCT242 | 5 | HCT2 |
| HCT243 | 5 | HCT2 |
| HCT244 | 5 | HCT2 |
| HCT244S | 5 | HCT2 |
| HCT245 | 5 | HCT2 |
| HCT251 | 5 | HCT2 |
| HCT253 | 5 | HCT2 |
| HCT253S | 5 | HCT2 |
| HCT257 | 5 | HCT2 |
| HCT258 | 6 | HCT2 |
| HCT259 | 6 | HCT2 |
| HCT266 | 6 | HCT2 |
| HCT273 | 6 | HCT2 |
| HCT280 | 6 | HCT2 |
| HCT283 | 6 | HCT2 |
| HCT297 | 6 | HCT2 |
| HCT299 | 6 | HCT2 |
| HCT354 | 6 | HCT2 |
| HCT356 | 6 | HCT2 |
| HCT365 | 6 | HCT2 |
| HCT365S | 6 | HCT2 |
| HCT366 | 6 | HCT2 |
| HCT366S | 6 | HCT2 |
| HCT367 | 6 | HCT2 |
| HCT368 | 6 | HCT2 |
| HCT373 | 6 | HCT2 |
| HCT374 | 6 | HCT2 |
| HCT375 | 6 | HCT2 |
| HCT375S | 6 | HCT2 |
| HCT377 | 6 | HCT2 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HCT390 | 6 | HCT2 |
| HCT393 | 6 | HCT2 |
| HCT423 | 6 | HCT2 |
| HCT533 | 6 | HCT2 |
| HCT534 | 6 | HCT2 |
| HCT540 | 6 | HCT2 |
| HCT541 | 6 | HCT2 |
| HCT563 | 6 | HCT2 |
| HCT564 | 6 | HCT2 |
| HCT573 | 6 | HCT2 |
| HCT574 | 6 | HCT2 |
| HCT583 | 6 | HCT2 |
| HCT597 | 6 | HCT2 |
| HCT640 | 6 | HCT2 |
| HCT643 | 6 | HCT2 |
| HCT646 | 6 | HCT2 |
| HCT648 | 6 | HCT3 |
| HCT670 | 6 | HCT3 |
| HCT688 | 6 | HCT3 |
| HCT4002 | 6 | HCT3 |
| HCT4015 | 6 | HCT3 |
| HCT4016 | 6 | HCT3 |
| HCT4017 | 6 | HCT3 |
| HCT4020 | 6 | HCT3 |
| HCT4024 | 6 | HCT3 |
| HCT4040 | 6 | HCT3 |
| HCT4046 | 6 | HCT3 |
| HCT4049 | 6 | HCT3 |
| HCT4050 | 6 | HCT3 |
| HCT4051 | 6 | HCT3 |
| HCT4052 | 6 | HCT3 |
| HCT4053 | 6 | HCT3 |
| HCT4059 | 6 | HCT3 |
| HCT4060 | 6 | HCT3 |
| HCT4066 | 6 | HCT3 |
| HCT4067 | 6 | HCT3 |
| HCT4075 | 6 | HCT3 |
| HCT4078 | 6 | HCT3 |
| HCT4094 | 6 | HCT3 |
| HCT4316 | 6 | HCT3 |
| HCT4316S | 6 | HCT3 |
| HCT4351 | 6 | HCT3 |
| HCT4352 | 6 | HCT3 |
| HCT4353 | 6 | HCT3 |
| HCT4510 | 6 | HCT3 |
| HCT4511 | 6 | HCT3 |

| COMPONENT | DISK NUMBER | PLOT NUMBER |
|-----------|-------------|-------------|
| HCT4514 | 6 | HCT3 |
| HCT4515 | 6 | HCT3 |
| HCT4516 | 6 | HCT3 |
| HCT4518 | 6 | HCT3 |
| HCT4520 | 6 | HCT3 |
| HCT4538 | 6 | HCT3 |
| HCT7046 | 6 | HCT3 |
| HCT7266 | 6 | HCT3 |
| HCT40102 | 6 | HCT3 |
| HCT40103 | 6 | HCT3 |
| HCT40104 | 6 | HCT3 |
| HCT40105 | 6 | HCT3 |



COMPONENT LIST BY FUNCTION

This list includes the following functional categories:

AND/NAND GATES
 ARITHMETIC CIRCUITS
 BUFFERS AND INVERTERS
 COUNTERS
 DECODERS/ENCODERS
 DISPLAY DRIVERS
 FLIP-FLOPS
 INTERFACE CIRCUITS
 LATCHES
 MULTIFUNCTION AND-OR-INVERT GATES
 MULTIPLEXERS/DEMULTIPLEXERS
 MULTIVIBRATORS
 OR/NOR GATES
 PHASE-LOCKED LOOPS
 REGISTERS
 SCHMITT TRIGGERS
 SWITCHES
 TIMING CIRCUITS
 TRANSCEIVERS

AND/NAND GATES

| | |
|----------|-----------------------------------|
| CD4011B | Quad 2-input NAND gate |
| CD4012B | Dual 4-input NAND gate |
| CD4023B | Triple 3-input NAND gate |
| CD4068B | 8-input NAND/AND gate |
| CD4073B | Triple 3-input AND gate |
| CD4081B | Quad 2-input AND gate |
| CD4082B | Dual 4-input AND gate |
| CD40107B | Dual 2-input NAND buffer/driver |
| HC00 | Quad 2-input NAND gate |
| HC03 | Quad 2-input open drain NAND gate |
| HC08 | Quad 2-input AND gate |
| HC10 | Triple 3-input NAND gate |
| HC11 | Triple 3-input AND gate |
| HC20 | Dual 4-input NAND gate |
| HC21 | Dual 4-input AND gate |
| HC30 | 8-input NAND gate |

| | |
|--------|-----------------------------------|
| HC133 | 13-input NAND gate |
| HCT00 | Quad 2-input NAND gate |
| HCT03 | Quad 2-input open drain NAND gate |
| HCT08 | Quad 2-input AND gate |
| HCT10 | Triple 3-input NAND gate |
| HCT11 | Triple 3-input AND gate |
| HCT20 | Dual 4-input NAND gate |
| HCT21 | Dual 4-input AND gate |
| HCT30 | 8-input NAND gate |
| HCT133 | 13-input NAND gate |

ARITHMETIC CIRCUITS

| | |
|----------|---|
| CD4008B | 4-bit full adder |
| CD4032B | Triple serial adder, positive logic |
| CD4038B | Triple serial adder, negative logic |
| CD4057A | 4-bit arithmetic logic unit |
| CD4063B | 4-bit magnitude comparator |
| CD4089B | Binary rate multiplier |
| CD40101B | 9-bit parity generator/checker |
| CD40181B | Arithmetic logic unit |
| CD40182B | Look-ahead carry generator |
| CD4527B | BCD rate multiplier |
| CD4585B | 4-bit magnitude comparator |
| HC85 | 4-bit magnitude comparator |
| HC181 | Arithmetic Logic Unit |
| HC182 | Carry generator |
| HC280 | 8-bit odd/even parity generator/checker |
| HC283 | 4-bit full adder with fast carry |
| HC583 | 4-bit full adder with fast carry |
| HC688 | 8-bit magnitude comparator |
| HCT85 | 4-bit magnitude comparator |
| HCT181 | Arithmetic Logic Unit |
| HCT182 | Carry generator |
| HCT280 | 8-bit odd/even parity generator/checker |
| HCT283 | 4-bit full adder with fast carry |
| HCT583 | 4-bit full adder with fast carry |
| HCT688 | 8-bit magnitude comparator |

BUFFERS AND INVERTERS

| | |
|----------|---|
| CD4009UB | Hex buffer/convertor (inverting) |
| CD4010B | Hex buffer/convertor (non-inverting) |
| CD4041UB | Quad true/complement buffer |
| CD4049UB | Hex buffer/convertor (inverting) |
| CD4050B | Hex buffer/convertor (non-inverting) |
| CD4069UB | Hex inverter |
| CD4502B | Hex inverter/buffer (3-state) |
| CD4503B | Hex buffer(3-state non-inverting) |
| HC04 | Hex inverter (triple buffered) |
| HC05 | Hex inverter with open-drain output |
| HC125 | Quad tri-state buffer |
| HC126 | Quad tri-state buffer |
| HC240 | Octal tri-state buffer (inverting) |
| HC241 | Octal tri-state buffer |
| HC244 | Octal tri-state buffer |
| HC365 | Hex buffer/line driver (3-state) |
| HC366 | Hex buffer/line driver (3-state inverting) |
| HC367 | Hex buffer/line driver (3-state) |
| HC368 | Hex buffer/line driver (3-state inverting) |
| HC540 | Octal buffer/line driver (3-state inverting) |
| HC541 | Octal buffer/line driver(3-state) |
| HCT04 | Hex inverter (triple buffered) |
| HCT05 | Hex inverter with open-drain output |
| HCT125 | Quad tri-state buffer |
| HCT126 | Quad tri-state buffer |
| HCT240 | Octal tri-state buffer (inverting) |
| HCT241 | Octal tri-state buffer |
| HCT244 | Octal tri-state buffer |
| HCT365 | Hex buffer/line driver (3-state) |
| HCT366 | Hex buffer/line driver (3-state inverting) |
| HCT367 | Hex buffer/line driver (3-state) |
| HCT368 | Hex buffer/line driver (3-state inverting) |
| HCT540 | Octal buffer/line driver (3-state inverting) |
| HCT541 | Octal buffer/line driver(3-state) |

COUNTERS

| | |
|----------|--|
| CD4017B | Decade counter/divider plus 10 decoded decimal outputs |
| CD4018B | Programmable divide-by-N counter |
| CD4020B | 14-stage ripple-carry binary counter |
| CD4022B | Divide-by-8 counter/divider with 8 decimal outputs |
| CD4024B | 7-stage counter |
| CD4029B | Presettable up/down counter |
| CD4040B | 12-stage counter |
| CD4059A | Programmable divide-by-N counter |
| CD4060B | 14-stage counter/divider and oscillator |
| CD40102B | Presettable 2-decade BCD down counter |
| CD40103B | Presettable 8-bit binary down counter |
| CD40110B | Decade up/down counter/latch/display driver |
| CD40160B | Decade counter/asynchronous clear |
| CD40161B | Binary counter/asynchronous clear |
| CD40162B | Decade counter/synchronous clear |
| CD40163B | Binary counter/synchronous clear |
| CD40192B | Presettable 4-bit BCD up/down counter |
| CD40193B | Presettable 4-bit binary up/down counter |
| CD4510B | Presettable 4-bit BCD up/down counter |
| CD4516B | Presettable 4-bit binary up/down counter |
| CD4518B | Dual BCD up counter |
| CD4520B | Dual binary up counter |
| HC93 | 4-bit binary ripple counter |
| HC160 | Synchronous BCD decade counter with asynchronous reset |
| HC161 | Synchronous 4-bit binary counter with asynchronous reset |
| HC162 | Synchronous BCD decade counter with synchronous reset |
| HC163 | Synchronous 4-bit binary counter with synchronous reset |
| HC190 | Presettable synchronous BCD decade up/down counter |

| | |
|---------|--|
| HC191 | Synchronous binary up/down counter with mode control |
| HC192 | Synchronous BCD decade up/down counter |
| HC193 | Synchronous 4-bit binary up/down counter |
| HC390 | Dual 4-bit decade counter |
| HC393 | Dual 4-bit binary ripple counter |
| HC4017 | Johnson decade counter with 10 decoded outputs |
| HC4020 | 14-stage binary ripple counter |
| HC4024 | 7-stage binary ripple counter |
| HC4040 | 12-bit binary counter |
| HC4059 | Programmable divide by N counter |
| HC4060 | 14-stage binary counter with oscillator |
| HC40102 | 8-bit synchronous BCD down counter |
| HC40103 | 8-bit binary down counter |
| HC4510 | Up/down BCD counter |
| HC4516 | Up/down binary counter |
| HC4518 | Dual synchronous BCD counter |
| HC4520 | Dual 4-bit synchronous binary counter |
| HCT93 | 4-bit binary ripple counter |
| HCT160 | Synchronous BCD decade counter with asynchronous reset |
| HCT161 | Synchronous 4-bit binary counter with asynchronous reset |
| HCT162 | Synchronous BCD decade counter with synchronous reset |
| HCT163 | Synchronous 4-bit binary counter with synchronous reset |
| HCT190 | Presettable synchronous BCD decade up/down counter |
| HCT191 | Synchronous binary up/down counter with mode control |
| HCT192 | Synchronous BCD decade up/down counter |
| HCT193 | Synchronous 4-bit binary up/down counter |
| HCT390 | Dual 4-bit decade counter |
| HCT393 | Dual 4-bit binary ripple counter |
| HCT4017 | Johnson decade counter with 10 decoded outputs |
| HCT4020 | 14-stage binary ripple counter |
| HCT4024 | 7-stage binary ripple counter |
| HCT4040 | 12-bit binary counter |
| HCT4059 | Programmable divide by N counter |

HCT4060 14-stage binary counter with oscillator
 HCT40102 8-bit synchronous BCD down counter
 HCT40103 8-bit binary down counter
 HCT4510 Up/down BCD counter
 HCT4516 Up/down binary counter
 HCT4518 Dual synchronous BCD counter
 HCT4520 Dual 4-bit synchronous binary counter

DECODERS / ENCODERS

CD4028B BCD-to-decimal decoder
 CD40147B 10-line to 4-line BCD priority encoder
 CD4514B 4-bit latch/4-to-16 line decoder (outputs high)
 CD4515B 4-bit latch/4-to-16 line decoder (outputs low)
 CD4532B 8-bit priority encoder
 CD4555B Dual 1-of-4 decoder/demultiplexer (outputs high)
 CD4556B Dual 1-of-4 decoder/demultiplexer (outputs low)
 HC42 BCD-to-decimal decoder (1-to-10)
 HC44 1-of-10 decoder
 HC137 3-to-8 line decoder (inverting) with latch
 HC138 Dual 3-to-8 line decoder
 HC139 Dual 2-to-4 line decoder
 HC145 1-of-10 decoder/driver with open drain outputs
 HC147 10-to-4 line priority encoder
 HC154 4-to-16 line decoder/demultiplexer
 HC237 3-to-8 line decoder with address latches
 HC238 3-to-8 line decoder/demultiplexer
 HC4511 BCD-to-7 segment decoder/latch/driver
 HC4514 4-to-16 decoder/demultiplexer with input latch
 HC4515 4-to-16 line decoder with input latch
 HCT42 BCD-to-decimal decoder (1-of-10)

| | |
|---------|---|
| HCT44 | 1-of-10 decoder |
| HCT137 | 3-to-8 line decoder (inverting) with latch |
| HCT138 | Dual 3-to-8 line decoder |
| HCT139 | Dual 2-to-4 line decoder |
| HCT145 | 1-of-10 decoder/driver with open drain outputs |
| HCT147 | 10-to-4 line priority encoder |
| HCT154 | 4-to-16 line decoder/demultiplexer |
| HCT237 | 3-to-8 line decoder |
| HCT238 | 3-to-8 line decoder/demultiplexer |
| HCT4511 | BCD-to-7 segment decoder/latch/ driver |
| HCT4514 | 4-to-16 decoder/demultiplexer with input latch |
| HCT4515 | 4-to-16 line decoder with input latch |

DISPLAY DRIVERS

| | |
|----------|---|
| CD4026B | Decade counter/divider with 7-segment display outputs and display enable |
| CD4033B | Decade counter/divider with 7-segment display outputs and ripple blanking |
| CD4054B | 4-segment display driver |
| CD4055B | BCD-to-7-segment decoder/driver with "display-frequency" output |
| CD4056B | BCD-to-7-segment decoder/driver with strobe-latch function |
| CD4511B | BCD-to-7-segment latch decoder/driver |
| CD22104A | 4-digit decoder/driver with decimal display |
| CD22105A | 4-digit decoder/driver with decimal display |

FLIP-FLOPS

| | |
|---------|--|
| CD4013B | Dual D-type flip-flop with set/reset capability |
|---------|--|

| | |
|----------|--|
| CD4027B | Dual J-K flip-flop with set/reset capability |
| CD4095B | Gated J-K master-slave flip-flops (non-inverting inputs) |
| CD4096B | Gated J-K master-slave flip-flops (inverting/non-inverting inputs) |
| CD40174B | Hex D-type flip-flop with clear |
| CD40175B | Quad D-type flip-flop with clear |
| HC73 | Dual J-K flip-flops with clear |
| HC74 | Dual D flip-flops with preset and clear |
| HC76 | Dual J-K flip-flops with preset and clear |
| HC107 | Dual J-K flip-flops with clear |
| HC109 | Dual J-K flip-flop with preset and clear |
| HC112 | Dual J-K flip-flops with preset and clear |
| HC173 | Quad D type flip-flops (3-state) |
| HC174 | Hex D flip-flops with clear |
| HC175 | Quad D flip-flops with clear |
| HC273 | Octal D flip-flops with clear |
| HC373 | Octal D flip-flops with 3-state outputs |
| HC374 | Octal D flip-flops with 3-state outputs |
| HC377 | Octal D flip-flop |
| HC534 | Octal D flip-flop with 3-state inverted outputs |
| HC564 | Octal D flip-flop (3-state inverting) |
| HC574 | Octal D flip-flop (3-state) |
| HCT73 | Dual J-K flip-flops with clear |
| HCT74 | Dual D flip-flops with preset and clear |
| HCT76 | Dual J-K flip-flops with preset and clear |
| HCT107 | Dual J-K flip-flops with clear |
| HCT109 | Dual J-K flip-flop with preset and clear |
| HCT112 | Dual J-K flip-flops with preset and clear |
| HCT173 | Quad D type flip-flops (3-state) |
| HCT174 | Hex D flip-flops with clear |
| HCT175 | Quad D flip-flops with clear |
| HCT273 | Octal D flip-flops with clear |

| | |
|--------|---|
| HCT373 | Octal D flip-flops with 3-state outputs |
| HCT374 | Octal D flip-flops with 3-state outputs |
| HCT377 | Octal D flip-flop |
| HCT534 | Octal D flip-flop with 3-state inverted outputs |
| HCT564 | Octal D flip-flop (3-state inverting) |
| HCT574 | Octal D flip-flop (3-state) |

INTERFACE CIRCUITS

| | |
|----------|---|
| CD40109B | Quad low-to-high voltage level shifter |
| CD40115 | 8-bit bidirectional CMOS-to-TTL level converter |
| CD40116 | 8-bit bidirectional CMOS-to-TTL level converter |
| CD40117B | Programmable dual 4-bit terminator |
| HC4049 | Hex inverting HIGH-TO-LOW level shifter |
| HC4050 | Hex HIGH-TO-LOW level shifter |
| HCT4049 | Hex inverting HIGH-TO-LOW level shifter |
| HCT4050 | Hex HIGH-TO-LOW level shifter |

LATCHES

| | |
|---------|---|
| CD4042B | Quad clocked D-type latch |
| CD4043B | Quad NOR R-S latch (3-state outputs) |
| CD4044B | Quad NAND R-S latch (3-state outputs) |
| CD4099B | 8-bit addressable latch |
| CD4508B | Dual 4-bit latch |
| CD4724B | 8-bit addressable latch |
| HC75 | 4-bit bistable latch with complimentary outputs |
| HC259 | 8-bit addressable latch |
| HC375 | 4-bit latch |

| | |
|--------|---|
| HC533 | Octal D type latch with 3-state inverted outputs |
| HC563 | Octal transparent latch (3-state inverting) |
| HC573 | Octal transparent latch (3-state) |
| HCT75 | 4-bit bistable latch with complimentary outputs |
| HCT259 | 8-bit addressable latch |
| HCT375 | 4-bit latch |
| HCT533 | Octal D type latch with 3-state inverted outputs |
| HCT563 | Octal transparent latch (3-state inverting) |
| HCT573 | Octal transparent latch (3-state) |

MULTIFUNCTION AND-OR-INVERT GATES

| | |
|---------|--|
| CD4019B | Quad AND/OR select gate |
| CD4037A | Triple AND-OR bi-phase pairs |
| CD4048B | Multifunctional expandable 8-input gate |
| CD4085B | Dual 2-wide, 2-input AND-OR- invert gate |
| CD4086B | Expandable 4-wide, 2-input AND-OR-invert gate |
| HC51 | Dual AND-OR-INVERT gate |
| HCT51 | Dual AND-OR-INVERT gate |

MULTIPLEXERS/DEMULTIPLEXERS

| | |
|----------|---|
| CD4051B | Single 8-channel multiplexer/ demultiplexer |
| CD4052B | Differential 4-channel multiplexer/demultiplexer |
| CD4053B | Triple 2-channel multiplexer/ demultiplexer |
| CD4067B | Single 16-channel multiplexer/ demultiplexer |
| CD4097B | Differential 8-channel multiplexer/demultiplexer |
| CD40257B | Quad 2-line-to-1-line data select/multiplexer |
| CD4512B | 8-channel data selector |

| | |
|---------|--|
| HC151 | 8-channel digital multiplexer |
| HC153 | Dual 4-input multiplexer |
| HC157 | Quad 2-input multiplexer |
| HC158 | Quad 2-input multiplexer |
| HC251 | 8-channel 3-state multiplexer |
| HC253 | Dual 4-input multiplexer(3-state) |
| HC257 | Quad 2-channel 3-state multiplexer |
| HC258 | Quad 2-channel 3-state multiplexer |
| HC354 | 8-input multiplexer/register (3-state) |
| HC356 | 8-input multiplexer/register (3-state) |
| HC4051 | 8-channel analog multiplexer/ demultiplexer |
| HC4052 | Dual 4-channel analog multiplexer/demultiplexer |
| HC4053 | Triple 2-channel analog multiplexer/demultiplexer |
| HC4067 | 16-channel analog multiplexer/ demultiplexer |
| HC4351 | Analog multiplexer with latch |
| HC4352 | Analog multiplexer with latch |
| HC4353 | Analog multiplexer with latch |
| HCT151 | 8-channel digital multiplexer |
| HCT153 | Dual 4-input multiplexer |
| HCT157 | Quad 2-input multiplexer |
| HCT158 | Quad 2-input multiplexer |
| HCT251 | 8-channel 3-state multiplexer |
| HCT253 | Dual 4-input multiplexer(3-state) |
| HCT257 | Quad 2-channel 3-state multiplexer |
| HCT258 | Quad 2-channel 3-state multiplexer |
| HCT354 | 8-input multiplexer/register (3-state) |
| HCT356 | 8-input multiplexer/register (3-state) |
| HCT4051 | 8-channel analog multiplexer/ demultiplexer |
| HCT4052 | Dual 4-channel analog multiplexer/demultiplexer |
| HCT4053 | Triple 2-channel analog multiplexer/demultiplexer |
| HCT4067 | 16-channel analog multiplexer/ demultiplexer |
| HCT4351 | Analog multiplexer with latch |
| HCT4352 | Analog multiplexer with latch |
| HCT4353 | Analog multiplexer with latch |

MULTIVIBRATORS

| | |
|---------|--|
| CD4047B | Monostable/astable multivibrator |
| CD4098B | Dual monostable multivibrator |
| CD4538B | Dual precision monostable multivibrator |
| HC123 | Dual retriggerable monostable multivibrator |
| HC221 | Dual non-retriggerable monostable multivibrator |
| HC423 | Dual retriggerable monostable multivibrator with reset |
| HC4538 | Dual precision monostable multivibrator |
| HCT123 | Dual retriggerable monostable multivibrator |
| HCT221 | Dual non-retriggerable monostable multivibrator |
| HCT423 | Dual retriggerable monostable multivibrator with reset |
| HCT4538 | Dual precision monostable multivibrator |

OR/NOR GATES

| | |
|---------|-------------------------------------|
| CD4000B | Dual 3-input NOR gate plus inverter |
| CD4001B | Quad 2-input NOR gate |
| CD4002B | Dual 4-input NOR gate |
| CD4025B | Triple 3-input NOR gate |
| CD4030B | Quad exclusive-OR gate |
| CD4070B | Quad exclusive-OR gate |
| CD4071B | Quad 2-input OR gate |
| CD4072B | Dual 4-input OR gate |
| CD4075B | Triple 3-input OR gate |
| CD4077B | Quad exclusive-NOR gate |
| CD4078B | 8-input NOR/OR gate |
| HC02 | Quad 2-input NOR gate |
| HC27 | Triple 3-input NOR gate |
| HC32 | Quad 2-input OR gate |
| HC86 | Quad 2-input exclusive OR gate |
| HC266 | Quad 2-input exclusive NOR gate |
| HC4002 | Dual 4-input NOR gate |
| HC4075 | Triple 3-input OR gate |
| HC4078 | 8-input NOR/OR gate |

| | |
|---------|---------------------------------|
| HC7266 | Quad exclusive NOR gates |
| HCT02 | Quad 2-input NOR gate |
| HCT27 | Triple 3-input NOR gate |
| HCT32 | Quad 2-input OR gate |
| HCT86 | Quad 2-input exclusive OR gate |
| HCT266 | Quad 2-input exclusive NOR gate |
| HCT4002 | Dual 4-input NOR gate |
| HCT4075 | Triple 3-input OR gate |
| HCT4078 | 8-input NOR/OR gate |
| HCT7266 | Quad exclusive NOR gates |

PHASE-LOCKED LOOPS

| | |
|---------|---|
| CD4046B | Micropower phase-locked loop |
| HC297 | Digital phase-locked loop filter |
| HC4046 | Phase-locked loop |
| HC7046 | Phase-locked loop with IN-LOCK detection |
| HCT297 | Digital phase-locked loop filter |
| HCT4046 | Phase-locked loop |
| HCT7046 | Phase-locked loop with IN-LOCK detection |

REGISTERS

| | |
|---------|---|
| CD4006B | 18-stage static shift register |
| CD4014B | 8-stage with synchronous parallel or serial input/serial output static shift register |
| CD4015B | Dual 4-stage with serial input/ parallel output static shift register |
| CD4021B | 8-stage with asynchronous parallel input or synchronous serial input/serial output static shift register |
| CD4031B | 64-stage static shift register |
| CD4034B | 8-stage bidirectional parallel or serial input/parallel output static shift register |

| | |
|----------|--|
| CD4035B | 4-stage parallel-in/parallel-out with J-K input and true/complement output static shift register |
| CD4076B | 4-bit register with D-type flip-flops (3-state outputs) |
| CD4094B | 8-stage shift-and-store bus register |
| CD40100B | 32-bit left/right static shift register |
| CD40104B | 4-bit universal bidirectional static shift register with 3-state outputs |
| CD40105B | 4-bit x 16 word FIFO buffer register |
| CD40108B | 4 x 4 multiport register |
| CD40194B | 4-bit universal bidirectional shift register |
| CD40208B | 4 x 4 multiport register |
| CD4517B | Dual 64-stage static shift register |
| HC164 | 8-bit serial-in/parallel-out shift register |
| HC165 | 8-bit parallel-in serial-out shift register |
| HC166 | 8-bit parallel-in serial-out shift register |
| HC194 | 4-bit bidirectional universal shift register |
| HC195 | 4-bit parallel access shift register |
| HC299 | 8-bit universal shift register (3-state) |
| HC597 | 8-bit shift register with I/P latch |
| HC670 | 4 x 4 register file (3-state) |
| HC4015 | Dual 4-bit serial-in/parallel-out shift register |
| HC4094 | 8-stage shift-and-store bus register |
| HC40104 | 4-bit bidirectional universal shift register (3-state) |
| HC40105 | 4-bit x 16-words FIFO register |
| HCT164 | 8-bit serial-in/parallel-out shift register |
| HCT165 | 8-bit parallel-in serial-out shift register |
| HCT166 | 8-bit parallel-in serial-out shift register |

| | |
|----------|--|
| HCT194 | 4-bit bidirectional universal shift register |
| HCT195 | 4-bit parallel access shift register |
| HCT299 | 8-bit universal shift register (3-state) |
| HCT597 | 8-bit shift register with I/P latch |
| HCT670 | 4 x 4 register file (3-state) |
| HCT4015 | Dual 4-bit serial-in/parallel-out shift register |
| HCT4094 | 8-stage shift-and-store bus register |
| HCT40104 | 4-bit bidirectional universal shift register (3-state) |
| HCT40105 | 4-bit x 16-words FIFO register |

SCHMITT TRIGGERS

| | |
|----------|-----------------------------------|
| CD4093B | Quad 2-input NAND Schmitt trigger |
| CD40106B | Hex Schmitt trigger |
| HC14 | Hex inverting Schmitt trigger |
| HC132 | Quad 2-input NAND Schmitt trigger |
| HCT14 | Hex inverting Schmitt trigger |
| HCT132 | Quad 2-input NAND Schmitt trigger |

SWITCHES

| | |
|---------|-----------------------|
| CD4016B | Quad bilateral switch |
| CD4066B | Quad bilateral switch |
| HC4016 | Quad bilateral switch |
| HC4066 | Quad bilateral switch |
| HC4316 | Quad analog switch |
| HCT4016 | Quad bilateral switch |
| HCT4066 | Quad bilateral switch |
| HCT4316 | Quad analog switch |

TIMING CIRCUITS

- CD22859 Dual-tone multi frequency
 tone generator
- CD4045B 21-stage counter timing circuit
- CD4536B Programmable timing circuit with
 24 ripple-binary counter stages
- CD4541B Programmable timing circuit with
 16-stage binary counter

TRANSCEIVERS

- HC242 Quad bus transceiver
 (3-state inverting)
- HC243 Quad bus transceiver (3-state)
- HC245 Octal 3-state transceiver
- HC640 Octal bus transceiver
 (3-state inverting)
- HC643 Octal bus transceiver
 (3-state; true inverting)
- HC646 Octal bus transceiver/register
 (3-state)
- HC648 Octal bus transceiver/register
 (3-state inverting)
- HCT242 Quad bus transceiver
 (3-state inverting)
- HCT243 Quad bus transceiver (3-state)
- HCT245 Octal 3-state transceiver
- HCT640 Octal bus transceiver
 (3-state inverting)
- HCT643 Octal bus transceiver
 (3-state; true inverting)
- HCT646 Octal bus transceiver/register
 (3-state)
- HCT648 Octal bus transceiver/register
 (3-state inverting)

COMPONENT PIN SEQUENCES

C4000B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | {n/c} | 6 | H | 11 | D |
| 2 | {n/c} | 7 | [GND] | 12 | E |
| 3 | A | 8 | G | 13 | F |
| 4 | B | 9 | L | 14 | [VCC] |
| 5 | C | 10 | K | | |

C4001B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | INA (A) | 6 | INB (B) | 11 | OUTY (D) |
| 2 | INB (A) | 7 | [GND] | 12 | INA (D) |
| 3 | OUTY (A) | 8 | INA (C) | 13 | INB (D) |
| 4 | OUTY (B) | 9 | INB (C) | 14 | [VCC] |
| 5 | INA (B) | 10 | OUTY (C) | | |

C4002B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | OUTY (A) | 6 | {n/c} | 11 | INC (B) |
| 2 | INA (A) | 7 | [GND] | 12 | IND (B) |
| 3 | INB (A) | 8 | {n/c} | 13 | OUTY (B) |
| 4 | INC (A) | 9 | INA (B) | 14 | [VCC] |
| 5 | IND (A) | 10 | INB (B) | | |

C4006B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | D1 | 6 | D4 | 11 | D2+4 |
| 2 | D1+4' | 7 | [GND] | 12 | D2+5 |
| 3 | CLK | 8 | D4+4 | 13 | D1+4 |
| 4 | D2 | 9 | D4+5 | 14 | [VCC] |
| 5 | D3 | 10 | D3+4 | | |

C4008B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | A4 | 7 = | A1 | 12 = | S3 |
| 2 = | B3 | 8 = | [GND] | 13 = | S4 |
| 3 = | A3 | 9 = | CIN | 14 = | COUT |
| 4 = | B2 | 10 = | S1 | 15 = | B4 |
| 5 = | A2 | 11 = | S2 | 16 = | [VCC] |
| 6 = | B1 | | | | |

C4009UB: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | VCC | 7 = | INC | 12 = | OUTE' |
| 2 = | OUTA' | 8 = | [GND] | 13 = | {n/c} |
| 3 = | INA | 9 = | IND | 14 = | OUTF' |
| 4 = | OUTB' | 10 = | OUTD' | 15 = | OUTF' |
| 5 = | INB | 11 = | INE | 16 = | [VCC] |
| 6 = | OUTC' | | | | |

C4010B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | VCC | 7 = | INC | 12 = | OUTE |
| 2 = | OUTA | 8 = | [GND] | 13 = | {n/c} |
| 3 = | INA | 9 = | IND | 14 = | INF |
| 4 = | OUTB | 10 = | OUTD | 15 = | OUTF |
| 5 = | INB | 11 = | INE | 16 = | [VCC] |
| 6 = | OUTC | | | | |

C4011B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | INA (A) | 6 = | INB (B) | 11 = | OUTY (D) |
| 2 = | INB (A) | 7 = | [GND] | 12 = | INA (D) |
| 3 = | OUTY (A) | 8 = | INA (C) | 13 = | INB (D) |
| 4 = | OUTY (B) | 9 = | INB (C) | 14 = | [VCC] |
| 5 = | INA (B) | 10 = | OUTY (C) | | |

C4012B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = {n/c} | 11 | = INC (B) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = IND (B) |
| 3 | = INB (A) | 8 | = {n/c} | 13 | = OUTY (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

C4013B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q (A) | 6 | = SET (A) | 11 | = CLK (B) |
| 2 | = Q' (A) | 7 | = [GND] | 12 | = Q' (B) |
| 3 | = CLK (A) | 8 | = SET (B) | 13 | = Q (B) |
| 4 | = RESET (A) | 9 | = D (B) | 14 | = [VCC] |
| 5 | = D (A) | 10 | = RESET (B) | | |

C4013BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q1 | 6 | = SET1 | 11 | = CLK2 |
| 2 | = Q1' | 7 | = [GND] | 12 | = Q2' |
| 3 | = CLK1 | 8 | = SET2 | 13 | = Q2 |
| 4 | = RESET1 | 9 | = D2 | 14 | = [VCC] |
| 5 | = D1 | 10 | = RESET2 | | |

C4014B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PIN8 | 7 | = PIN1 | 12 | = POUT7 |
| 2 | = POUT6 | 8 | = [GND] | 13 | = PIN5 |
| 3 | = POUT8 | 9 | = PSC | 14 | = PIN6 |
| 4 | = PIN4 | 10 | = CLK | 15 | = PIN7 |
| 5 | = PIN3 | 11 | = SIN | 16 | = [VCC] |
| 6 | = PIN2 | | | | |

C4015B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (B) | 7 | = DATA (A) | 12 | = Q2 (B) |
| 2 | = Q4 (B) | 8 | = [GND] | 13 | = Q1 (B) |
| 3 | = Q3 (A) | 9 | = CLK (A) | 14 | = RESET (B) |
| 4 | = Q2 (A) | 10 | = Q4 (A) | 15 | = DATA (B) |
| 5 | = Q1 (A) | 11 | = Q3 (B) | 16 | = [VCC] |
| 6 | = RESET (A) | | | | |

C4015BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLKB | 7 | = DATAA | 12 | = Q2B |
| 2 | = Q4B | 8 | = [GND] | 13 | = Q1B |
| 3 | = Q3A | 9 | = CLKA | 14 | = RESETB |
| 4 | = Q2A | 10 | = Q4A | 15 | = DATAB |
| 5 | = Q1A | 11 | = Q3B | 16 | = [VCC] |
| 6 | = RESETA | | | | |

C4016B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IO (A) | 6 | = CNTRL (C) | 11 | = IO (D) |
| 2 | = OI (A) | 7 | = [GND] | 12 | = CNTRL (D) |
| 3 | = OI (B) | 8 | = IO (C) | 13 | = CNTRL (A) |
| 4 | = IO (B) | 9 | = OI (C) | 14 | = [VCC] |
| 5 | = CNTRL (B) | 10 | = OI (D) | | |

C4016BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A-IO | 6 | = CNTRL-C | 11 | = D-IO |
| 2 | = A-OI | 7 | = [GND] | 12 | = CNTRL-D |
| 3 | = B-OI | 8 | = C-IO | 13 | = CNTRL-A |
| 4 | = B-IO | 9 | = C-OI | 14 | = [VCC] |
| 5 | = CNTRL-B | 10 | = D-OI | | |

C4017B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q5 | 7 | = Q3 | 12 | = CO |
| 2 | = Q1 | 8 | = [GND] | 13 | = CLKE' |
| 3 | = Q0 | 9 | = Q8 | 14 | = CLK |
| 4 | = Q2 | 10 | = Q4 | 15 | = RST |
| 5 | = Q6 | 11 | = Q9 | 16 | = [VCC] |
| 6 | = Q7 | | | | |

C4018B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DATA | 7 | = JAM3 | 12 | = JAM5 |
| 2 | = JAM1 | 8 | = [GND] | 13 | = Q5' |
| 3 | = JAM2 | 9 | = JAM4 | 14 | = CLK |
| 4 | = Q2' | 10 | = PREN | 15 | = RESET |
| 5 | = Q1' | 11 | = Q4' | 16 | = [VCC] |
| 6 | = Q3' | | | | |

C4019B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B (D) | 7 | = B (A) | 12 | = D (C) |
| 2 | = A (C) | 8 | = [GND] | 13 | = D (D) |
| 3 | = B (C) | 9 | = KA (D) | 14 | = KB (D) |
| 4 | = A (B) | 10 | = D (A) | 15 | = A (D) |
| 5 | = B (B) | 11 | = D (B) | 16 | = [VCC] |
| 6 | = A (A) | | | | |

C4019BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B4 | 7 | = B1 | 12 | = D3 |
| 2 | = A3 | 8 | = [GND] | 13 | = D4 |
| 3 | = B3 | 9 | = KA | 14 | = KB |
| 4 | = A2 | 10 | = D1 | 15 | = A4 |
| 5 | = B2 | 11 | = D2 | 16 | = [VCC] |
| 6 | = A1 | | | | |

C4020B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q4 | 12 | = Q9 |
| 2 | = Q13 | 8 | = [GND] | 13 | = Q8 |
| 3 | = Q14 | 9 | = Q1 | 14 | = Q10 |
| 4 | = Q6 | 10 | = CLK | 15 | = Q11 |
| 5 | = Q5 | 11 | = RESET | 16 | = [VCC] |
| 6 | = Q7 | | | | |

C4021B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PIN8 | 7 | = PIN1 | 12 | = POUT7 |
| 2 | = POUT6 | 8 | = [GND] | 13 | = PIN5 |
| 3 | = POUT8 | 9 | = PSC | 14 | = PIN6 |
| 4 | = PIN4 | 10 | = CLK | 15 | = PIN7 |
| 5 | = PIN3 | 11 | = SIN | 16 | = [VCC] |
| 6 | = PIN2 | | | | |

C4022B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q1 | 7 | = Q3 | 12 | = CO |
| 2 | = Q0 | 8 | = [GND] | 13 | = CLKE' |
| 3 | = Q2 | 9 | = {n/c} | 14 | = CLK |
| 4 | = Q5 | 10 | = Q7 | 15 | = RST |
| 5 | = Q6 | 11 | = Q4 | 16 | = [VCC] |
| 6 | = {n/c} | | | | |

C4023B: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INB (C) |
| 3 | = INA (B) | 8 | = INC (A) | 13 | = INA (C) |
| 4 | = INB (B) | 9 | = OUTY (A) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = OUTY (C) | | |

C4024B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 6 | = Q4 | 11 | = Q2 |
| 2 | = RESET | 7 | = [GND] | 12 | = Q1 |
| 3 | = Q7 | 8 | = {n/c} | 13 | = {n/c} |
| 4 | = Q6 | 9 | = Q3 | 14 | = [VCC] |
| 5 | = Q5 | 10 | = {n/c} | | |

C4025B: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INB (C) |
| 3 | = INA (B) | 8 | = INC (A) | 13 | = INA (C) |
| 4 | = INB (B) | 9 | = OUTY (A) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = OUTY (C) | | |

C4026B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 7 | = G | 12 | = B |
| 2 | = CLK-INH | 8 | = [GND] | 13 | = C |
| 3 | = DEIN | 9 | = D | 14 | = UG-C |
| 4 | = DEOUT | 10 | = A | 15 | = RESET |
| 5 | = CO | 11 | = E | 16 | = [VCC] |
| 6 | = F | | | | |

C4027B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q (B) | 7 | = SET (B) | 12 | = RESET (A) |
| 2 | = Q' (B) | 8 | = [GND] | 13 | = CLK (A) |
| 3 | = CLK (B) | 9 | = SET (A) | 14 | = Q' (A) |
| 4 | = RESET (B) | 10 | = J (A) | 15 | = Q (A) |
| 5 | = K (B) | 11 | = K (A) | 16 | = [VCC] |
| 6 | = J (B) | | | | |

C4028B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 4 | 7 | = 6 | 12 | = C |
| 2 | = 2 | 8 | = [GND] | 13 | = B |
| 3 | = 0 | 9 | = 8 | 14 | = 1 |
| 4 | = 7 | 10 | = A | 15 | = 3 |
| 5 | = 9 | 11 | = D | 16 | = [VCC] |
| 6 | = 5 | | | | |

C4029B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PE | 7 | = CO' | 12 | = JAM2 |
| 2 | = Q4 | 8 | = [GND] | 13 | = JAM3 |
| 3 | = JAM4 | 9 | = B/D | 14 | = Q3 |
| 4 | = JAM1 | 10 | = U/D | 15 | = CLK |
| 5 | = CIN' | 11 | = Q2 | 16 | = [VCC] |
| 6 | = Q1 | | | | |

C4030B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

C4031B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DIN2 | 7 | = Q' | 12 | = {n/c} |
| 2 | = CLK | 8 | = [GND] | 13 | = {n/c} |
| 3 | = {n/c} | 9 | = CLKD | 14 | = {n/c} |
| 4 | = {n/c} | 10 | = SEL | 15 | = DIN1 |
| 5 | = QBAR | 11 | = {n/c} | 16 | = [VCC] |
| 6 | = Q | | | | |

C4032B: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SUM (C) | 7 | = INV (A) | 12 | = B (B) |
| 2 | = INV (C) | 8 | = [GND] | 13 | = A (B) |
| 3 | = CLK (C) | 9 | = SUM (A) | 14 | = B (C) |
| 4 | = SUM (B) | 10 | = A (A) | 15 | = A (C) |
| 5 | = INV (B) | 11 | = B (A) | 16 | = [VCC] |
| 6 | = CR (C) | | | | |

C4032BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SUM3 | 7 | = INV1 | 12 | = B2 |
| 2 | = INV3 | 8 | = [GND] | 13 | = A2 |
| 3 | = CLK | 9 | = SUM1 | 14 | = B3 |
| 4 | = SUM2 | 10 | = A1 | 15 | = A3 |
| 5 | = INV2 | 11 | = B1 | 16 | = [VCC] |
| 6 | = CR | | | | |

C4033B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 7 | = G | 12 | = B |
| 2 | = CLK-INH | 8 | = [GND] | 13 | = C |
| 3 | = RBI | 9 | = D | 14 | = LT |
| 4 | = RBO | 10 | = A | 15 | = RESET |
| 5 | = CO | 11 | = E | 16 | = [VCC] |
| 6 | = F | | | | |

C4034B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B8 | 9 | = EN-A | 17 | = A2 |
| 2 | = B7 | 10 | = SERIN | 18 | = A3 |
| 3 | = B6 | 11 | = A-B | 19 | = A4 |
| 4 | = B5 | 12 | = [GND] | 20 | = A5 |
| 5 | = B4 | 13 | = P-S | 21 | = A6 |
| 6 | = B3 | 14 | = A-S | 22 | = A7 |
| 7 | = B2 | 15 | = CLK | 23 | = A8 |
| 8 | = B1 | 16 | = A1 | 24 | = [VCC] |

C4035B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q1 | 7 | = P/S | 12 | = PI-4 |
| 2 | = T/C | 8 | = [GND] | 13 | = Q4 |
| 3 | = K' | 9 | = PI-1 | 14 | = Q3 |
| 4 | = J | 10 | = PI-2 | 15 | = Q2 |
| 5 | = RESET | 11 | = PI-3 | 16 | = [VCC] |
| 6 | = CLK | | | | |

C4037A: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = VCC (C) | 6 | = C (C) | 11 | = E (B) |
| 2 | = B (C) | 7 | = [GND] | 12 | = E (A) |
| 3 | = C (A) | 8 | = D (C) | 13 | = D (A) |
| 4 | = A (C) | 9 | = E (C) | 14 | = [VCC] |
| 5 | = C (B) | 10 | = D (B) | | |

C4037AS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = VCC | 6 | = C3 | 11 | = E2 |
| 2 | = B | 7 | = [GND] | 12 | = E1 |
| 3 | = C1 | 8 | = D3 | 13 | = D1 |
| 4 | = A | 9 | = E3 | 14 | = [VCC] |
| 5 | = C2 | 10 | = D2 | | |

C4038B: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SUM (C) | 7 | = INV (A) | 12 | = B (B) |
| 2 | = INV (C) | 8 | = [GND] | 13 | = A (B) |
| 3 | = CLK (C) | 9 | = SUM (A) | 14 | = B (C) |
| 4 | = SUM (B) | 10 | = A (A) | 15 | = A (C) |
| 5 | = INV (B) | 11 | = B (A) | 16 | = [VCC] |
| 6 | = CR (C) | | | | |

C4038BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SUM3 | 7 | = INV1 | 12 | = B2 |
| 2 | = INV3 | 8 | = [GND] | 13 | = A2 |
| 3 | = CLK | 9 | = SUM1 | 14 | = B3 |
| 4 | = SUM2 | 10 | = A1 | 15 | = A3 |
| 5 | = INV2 | 11 | = B1 | 16 | = [VCC] |
| 6 | = CR | | | | |

C4040B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q2 | 12 | = Q9 |
| 2 | = Q6 | 8 | = [GND] | 13 | = Q8 |
| 3 | = Q5 | 9 | = Q1 | 14 | = Q10 |
| 4 | = Q7 | 10 | = CLK | 15 | = Q11 |
| 5 | = Q4 | 11 | = RESET | 16 | = [VCC] |
| 6 | = Q3 | | | | |

C4041UB: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUT (A) | 6 | = IN (B) | 11 | = OUT (D) |
| 2 | = OUT' (A) | 7 | = [GND] | 12 | = OUT' (D) |
| 3 | = IN (A) | 8 | = OUT (C) | 13 | = IN (D) |
| 4 | = OUT (B) | 9 | = OUT' (C) | 14 | = [VCC] |
| 5 | = OUT' (B) | 10 | = IN (C) | | |

C4042B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q (D) | 7 | = D (B) | 12 | = Q' (C) |
| 2 | = Q (A) | 8 | = [GND] | 13 | = D (C) |
| 3 | = Q' (A) | 9 | = Q' (B) | 14 | = D (D) |
| 4 | = D (A) | 10 | = Q (B) | 15 | = Q' (D) |
| 5 | = CLK (D) | 11 | = Q (C) | 16 | = [VCC] |
| 6 | = POL (D) | | | | |

C4042BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q4 | 7 | = D2 | 12 | = Q3' |
| 2 | = Q1 | 8 | = [GND] | 13 | = D3 |
| 3 | = Q1' | 9 | = Q2' | 14 | = D4 |
| 4 | = D1 | 10 | = Q2 | 15 | = Q4' |
| 5 | = CLK | 11 | = Q3 | 16 | = [VCC] |
| 6 | = POL | | | | |

C4043B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q (D) | 7 | = R (B) | 12 | = S (C) |
| 2 | = Q (A) | 8 | = [GND] | 13 | = {n/c} |
| 3 | = R (A) | 9 | = Q (B) | 14 | = S (D) |
| 4 | = S (A) | 10 | = Q (C) | 15 | = R (D) |
| 5 | = EN (D) | 11 | = R (C) | 16 | = [VCC] |
| 6 | = S (B) | | | | |

C4043BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q4 | 7 | = R2 | 12 | = S3 |
| 2 | = Q1 | 8 | = [GND] | 13 | = {n/c} |
| 3 | = R1 | 9 | = Q2 | 14 | = S4 |
| 4 | = S1 | 10 | = Q3 | 15 | = R4 |
| 5 | = EN | 11 | = R3 | 16 | = [VCC] |
| 6 | = S2 | | | | |

C4044B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q (D) | 7 | = S (B) | 12 | = R (C) |
| 2 | = {n/c} | 8 | = [GND] | 13 | = Q (A) |
| 3 | = S (A) | 9 | = Q (B) | 14 | = R (D) |
| 4 | = R (A) | 10 | = Q (C) | 15 | = S (D) |
| 5 | = EN (D) | 11 | = S (C) | 16 | = [VCC] |
| 6 | = R (B) | | | | |

C4044BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q4 | 7 | = S2 | 12 | = R3 |
| 2 | = {n/c} | 8 | = [GND] | 13 | = Q1 |
| 3 | = S1 | 9 | = Q2 | 14 | = R4 |
| 4 | = R1 | 10 | = Q3 | 15 | = S4 |
| 5 | = EN | 11 | = S3 | 16 | = [VCC] |
| 6 | = R2 | | | | |

C4045B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SP | 7 | = Y | 12 | = {n/c} |
| 2 | = SN | 8 | = Y+D | 13 | = {n/c} |
| 3 | = [VCC] | 9 | = {n/c} | 14 | = [GND] |
| 4 | = {n/c} | 10 | = {n/c} | 15 | = X0 |
| 5 | = {n/c} | 11 | = {n/c} | 16 | = X1 |
| 6 | = {n/c} | | | | |

C4046B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PPULSE | 7 | = C1-2 | 12 | = RX2 |
| 2 | = PCOMP1 | 8 | = [GND] | 13 | = PCOMP2 |
| 3 | = COMP | 9 | = VCOIN | 14 | = SIGIN |
| 4 | = VCOOUT | 10 | = DMOD | 15 | = ZENER |
| 5 | = INH | 11 | = RX1 | 16 | = [VCC] |
| 6 | = C1-1 | | | | |

C4047B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = C | 6 | = N-TRIG | 11 | = Q' |
| 2 | = R | 7 | = [GND] | 12 | = RE-TRIG |
| 3 | = RC-COM | 8 | = P-TRIG | 13 | = OSC-OUT |
| 4 | = ASTABL' | 9 | = EX-RSET | 14 | = [VCC] |
| 5 | = ASTABL | 10 | = Q | | |

C4048B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|-------------|---------------|
| 1 = J | | 7 = KB | | 12 = C | |
| 2 = KD | | 8 = [GND] | | 13 = B | |
| 3 = H | | 9 = KC | | 14 = A | |
| 4 = G | | 10 = KA | | 15 = EXPAND | |
| 5 = F | | 11 = D | | 16 = [VCC] | |
| 6 = E | | | | | |

C4049UB: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|-------------|---------------|--------------|---------------|--------------|---------------|
| 1 = [VCC] | | 7 = IN (C) | | 12 = OUT (E) | |
| 2 = OUT (A) | | 8 = [GND] | | 13 = {n/c} | |
| 3 = IN (A) | | 9 = IN (D) | | 14 = IN (F) | |
| 4 = OUT (B) | | 10 = OUT (D) | | 15 = OUT (F) | |
| 5 = IN (B) | | 11 = IN (E) | | 16 = {n/c} | |
| 6 = OUT (C) | | | | | |

C4050B: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|-------------|---------------|--------------|---------------|--------------|---------------|
| 1 = [VCC] | | 7 = IN (C) | | 12 = OUT (E) | |
| 2 = OUT (A) | | 8 = [GND] | | 13 = {n/c} | |
| 3 = IN (A) | | 9 = IN (D) | | 14 = IN (F) | |
| 4 = OUT (B) | | 10 = OUT (D) | | 15 = OUT (F) | |
| 5 = IN (B) | | 11 = IN (E) | | 16 = {n/c} | |
| 6 = OUT (C) | | | | | |

C4051B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = 4 | | 7 = VEE | | 12 = 3 | |
| 2 = 6 | | 8 = [GND] | | 13 = 0 | |
| 3 = O/I | | 9 = C | | 14 = 1 | |
| 4 = 7 | | 10 = B | | 15 = 2 | |
| 5 = 5 | | 11 = A | | 16 = [VCC] | |
| 6 = INH | | | | | |

C4052B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0 | 7 | = VEE | 12 | = X0 |
| 2 | = Y2 | 8 | = [GND] | 13 | = XO/I |
| 3 | = YO/I | 9 | = B | 14 | = X1 |
| 4 | = Y3 | 10 | = A | 15 | = X2 |
| 5 | = Y1 | 11 | = X3 | 16 | = [VCC] |
| 6 | = INH | | | | |

C4053B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = BY | 7 | = VEE | 12 | = AX |
| 2 | = BX | 8 | = [GND] | 13 | = AY |
| 3 | = CY | 9 | = C | 14 | = AO/I |
| 4 | = CO/I | 10 | = B | 15 | = BO/I |
| 5 | = CX | 11 | = A | 16 | = [VCC] |
| 6 | = INH | | | | |

C4054B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STRB4 | 7 | = VEE | 12 | = STRB2 |
| 2 | = DFIN | 8 | = [GND] | 13 | = IN3 |
| 3 | = OUT4 | 9 | = IN1 | 14 | = STRB3 |
| 4 | = OUT3 | 10 | = STRB1 | 15 | = IN4 |
| 5 | = OUT2 | 11 | = IN2 | 16 | = [VCC] |
| 6 | = OUT1 | | | | |

C4055B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DFOUT | 7 | = VEE | 12 | = D |
| 2 | = IN2 | 8 | = [GND] | 13 | = E |
| 3 | = IN1 | 9 | = A | 14 | = G |
| 4 | = IN3 | 10 | = B | 15 | = F |
| 5 | = IN0 | 11 | = C | 16 | = [VCC] |
| 6 | = DFIN | | | | |

C4056B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STRB | 7 | = VEE | 12 | = D |
| 2 | = IN2 | 8 | = [GND] | 13 | = E |
| 3 | = IN1 | 9 | = A | 14 | = G |
| 4 | = IN3 | 10 | = B | 15 | = F |
| 5 | = IN0 | 11 | = C | 16 | = [VCC] |
| 6 | = DFIN | | | | |

C4057A: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D1 | 11 | = BYPASS | 20 | = CLK |
| 2 | = D4 | 12 | = (n/c) | 21 | = SELB |
| 3 | = D2 | 13 | = M1 | 22 | = SELA |
| 4 | = NEG | 14 | = ROT1 | 23 | = O/CNTRL |
| 5 | = ZI/IN | 15 | = M2 | 24 | = ZI/OUT |
| 6 | = SELC | 16 | = O/FLOW | 25 | = [GND] |
| 7 | = SELD | 17 | = O/IND | 26 | = [VCC] |
| 8 | = COND/A | 18 | = LEFT | 27 | = D3 |
| 9 | = COND/C | 19 | = COND/B | 28 | = ROT2 |
| 10 | = RIGHT | | | | |

C4059A: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 9 | = J14 | 17 | = J10 |
| 2 | = L | 10 | = J13 | 18 | = J9 |
| 3 | = J1 | 11 | = KC | 19 | = J8 |
| 4 | = J2 | 12 | = [GND] | 20 | = J7 |
| 5 | = J3 | 13 | = KB | 21 | = J6 |
| 6 | = J4 | 14 | = KA | 22 | = J5 |
| 7 | = J16 | 15 | = J12 | 23 | = OUT |
| 8 | = J15 | 16 | = J11 | 24 | = [VCC] |

C4060B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q4 | 12 | = RESET |
| 2 | = Q13 | 8 | = [GND] | 13 | = Q9 |
| 3 | = Q14 | 9 | = FYO | 14 | = Q8 |
| 4 | = Q6 | 10 | = FYO' | 15 | = Q10 |
| 5 | = Q5 | 11 | = FYI | 16 | = [VCC] |
| 6 | = Q7 | | | | |

C4063B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B3 | 7 | = A<B | 12 | = A1 |
| 2 | = IA<B | 8 | = [GND] | 13 | = A2 |
| 3 | = IA=B | 9 | = B0 | 14 | = B2 |
| 4 | = IA>B | 10 | = A0 | 15 | = A3 |
| 5 | = A>B | 11 | = B1 | 16 | = [VCC] |
| 6 | = A=B | | | | |

C4066B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IO (A) | 6 | = CNTRL (C) | 11 | = IO (D) |
| 2 | = OI (A) | 7 | = [GND] | 12 | = CNTRL (D) |
| 3 | = OI (B) | 8 | = IO (C) | 13 | = CNTRL (A) |
| 4 | = IO (B) | 9 | = OI (C) | 14 | = [VCC] |
| 5 | = CNTRL (B) | 10 | = OI (D) | | |

C4067B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUT/IN | 9 | = 0 | 17 | = 14 |
| 2 | = 7 | 10 | = A | 18 | = 13 |
| 3 | = 6 | 11 | = B | 19 | = 12 |
| 4 | = 5 | 12 | = [GND] | 20 | = 11 |
| 5 | = 4 | 13 | = D | 21 | = 10 |
| 6 | = 3 | 14 | = C | 22 | = 9 |
| 7 | = 2 | 15 | = INH | 23 | = 8 |
| 8 | = 1 | 16 | = 15 | 24 | = [VCC] |

C4068B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY | 6 | = {n/c} | 11 | = ING |
| 2 | = INA | 7 | = [GND] | 12 | = INH |
| 3 | = INB | 8 | = {n/c} | 13 | = OUTY' |
| 4 | = INC | 9 | = INE | 14 | = [VCC] |
| 5 | = IND | 10 | = INF | | |

C4069UB: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IN (A) | 6 | = OUT (C) | 11 | = IN (E) |
| 2 | = OUT (A) | 7 | = [GND] | 12 | = OUT (F) |
| 3 | = IN (B) | 8 | = OUT (D) | 13 | = IN (F) |
| 4 | = OUT (B) | 9 | = IN (D) | 14 | = [VCC] |
| 5 | = IN (C) | 10 | = OUT (E) | | |

C4070B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

C4071B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INB (A) | 6 | = INA (B) | 11 | = OUTY (D) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = INB (D) |
| 3 | = OUTY (A) | 8 | = INB (C) | 13 | = INA (D) |
| 4 | = OUTY (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = OUTY (C) | | |

C4072B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = {n/c} | 11 | = INC (B) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = IND (B) |
| 3 | = INB (A) | 8 | = {n/c} | 13 | = OUTY (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

C4073B: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INA (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INB (C) |
| 3 | = INA (B) | 8 | = INC (A) | 13 | = INC (C) |
| 4 | = INB (B) | 9 | = OUTY (A) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = OUTY (C) | | |

C4075B: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INC (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INB (C) |
| 3 | = INC (B) | 8 | = INA (A) | 13 | = INA (C) |
| 4 | = INB (B) | 9 | = OUTY (A) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

C4076B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = M | 7 | = CLK | 12 | = D3 |
| 2 | = N | 8 | = [GND] | 13 | = D2 |
| 3 | = Q1 | 9 | = G1 | 14 | = D1 |
| 4 | = Q2 | 10 | = G2 | 15 | = RESET |
| 5 | = Q3 | 11 | = D4 | 16 | = [VCC] |
| 6 | = Q4 | | | | |

C4077B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

C4078B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = K | 6 | = {n/c} | 11 | = G |
| 2 | = A | 7 | = [GND] | 12 | = H |
| 3 | = B | 8 | = {n/c} | 13 | = J |
| 4 | = C | 9 | = E | 14 | = [VCC] |
| 5 | = D | 10 | = F | | |

C4081B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

C4082B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = {n/c} | 11 | = INC (B) |
| 2 | = IND (A) | 7 | = [GND] | 12 | = IND (B) |
| 3 | = INC (A) | 8 | = {n/c} | 13 | = OUTY (B) |
| 4 | = INB (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = INA (A) | 10 | = INB (B) | | |

C4085B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 6 | = B (B) | 11 | = INH (B) |
| 2 | = B (A) | 7 | = [GND] | 12 | = C (A) |
| 3 | = E (A) | 8 | = C (B) | 13 | = D (A) |
| 4 | = E (B) | 9 | = D (B) | 14 | = [VCC] |
| 5 | = A (B) | 10 | = INH (A) | | |

C4085BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A1 | 6 | = B2 | 11 | = INH2 |
| 2 | = B1 | 7 | = [GND] | 12 | = C1 |
| 3 | = E1 | 8 | = C2 | 13 | = D1 |
| 4 | = E2 | 9 | = D2 | 14 | = [VCC] |
| 5 | = A2 | 10 | = INH1 | | |

C4086B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A | 6 | = F | 11 | = EN/EXP' |
| 2 | = B | 7 | = [GND] | 12 | = C |
| 3 | = J' | 8 | = G | 13 | = D |
| 4 | = {n/c} | 9 | = H | 14 | = [VCC] |
| 5 | = E | 10 | = INH/EXP | | |

C4089B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 15-OUT | 7 | = INH-CO | 12 | = CAS |
| 2 | = C | 8 | = [GND] | 13 | = CLR |
| 3 | = D | 9 | = CLK | 14 | = A |
| 4 | = SET-15 | 10 | = STRB | 15 | = B |
| 5 | = OUT' | 11 | = INH-CIN | 16 | = [VCC] |
| 6 | = OUT | | | | |

C4093B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

C4094B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STRB | 7 | = Q4 | 12 | = Q7 |
| 2 | = SERIN | 8 | = [GND] | 13 | = Q6 |
| 3 | = CLK | 9 | = QS | 14 | = Q5 |
| 4 | = Q1 | 10 | = QS' | 15 | = OE |
| 5 | = Q2 | 11 | = Q8 | 16 | = [VCC] |
| 6 | = Q3 | | | | |

C4095B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = {n/c} | 6 | = Q' | 11 | = K1 |
| 2 | = RESET | 7 | = [GND] | 12 | = CLK |
| 3 | = J1 | 8 | = Q | 13 | = SET |
| 4 | = J2 | 9 | = K3 | 14 | = [VCC] |
| 5 | = J3 | 10 | = K2 | | |

C4096B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = {n/c} | 6 | = Q' | 11 | = K1 |
| 2 | = RESET | 7 | = [GND] | 12 | = CLK |
| 3 | = J1 | 8 | = Q | 13 | = SET |
| 4 | = J2 | 9 | = K3' | 14 | = [VCC] |
| 5 | = J3' | 10 | = K2 | | |

C4097B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = XOUT/IN | 9 | = X0 | 17 | = YOUT/IN |
| 2 | = X7 | 10 | = A | 18 | = Y5 |
| 3 | = X6 | 11 | = B | 19 | = Y4 |
| 4 | = X5 | 12 | = [GND] | 20 | = Y3 |
| 5 | = X4 | 13 | = INH | 21 | = Y2 |
| 6 | = X3 | 14 | = C | 22 | = Y1 |
| 7 | = X2 | 15 | = Y7 | 23 | = Y0 |
| 8 | = X1 | 16 | = Y6 | 24 | = [VCC] |

C4098B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CX (A) | 7 | = Q' (A) | 12 | = TR+ (B) |
| 2 | = RXCX (A) | 8 | = [GND] | 13 | = RESET' (B) |
| 3 | = RESET' (A) | 9 | = Q' (B) | 14 | = RXCX (B) |
| 4 | = TR+ (A) | 10 | = Q (B) | 15 | = CX (B) |
| 5 | = TR- (A) | 11 | = TR- (B) | 16 | = [VCC] |
| 6 | = Q (A) | | | | |

C4099B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q7 | 7 | = A2 | 12 | = Q3 |
| 2 | = RESET | 8 | = [GND] | 13 | = Q4 |
| 3 | = DATA | 9 | = Q0 | 14 | = Q5 |
| 4 | = WR-DIS | 10 | = Q1 | 15 | = Q6 |
| 5 | = A0 | 11 | = Q2 | 16 | = [VCC] |
| 6 | = A1 | | | | |

C4502B: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D (C) | 7 | = Q (B) | 12 | = INH (F) |
| 2 | = Q (C) | 8 | = [GND] | 13 | = D (E) |
| 3 | = D (A) | 9 | = Q (D) | 14 | = Q (F) |
| 4 | = OE' (F) | 10 | = D (D) | 15 | = D (F) |
| 5 | = Q (A) | 11 | = Q (E) | 16 | = [VCC] |
| 6 | = D (B) | | | | |

C4502BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D3 | 7 | = Q2 | 12 | = INH |
| 2 | = Q3 | 8 | = [GND] | 13 | = D5 |
| 3 | = D1 | 9 | = Q4 | 14 | = Q6 |
| 4 | = OE' | 10 | = D4 | 15 | = D6 |
| 5 | = Q1 | 11 | = Q5 | 16 | = [VCC] |
| 6 | = D2 | | | | |

C4503B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DIS-A | 7 | = Q3 | 12 | = D5 |
| 2 | = D1 | 8 | = [GND] | 13 | = Q6 |
| 3 | = Q1 | 9 | = Q4 | 14 | = D6 |
| 4 | = D2 | 10 | = D4 | 15 | = DIS-B |
| 5 | = Q2 | 11 | = Q5 | 16 | = [VCC] |
| 6 | = D3 | | | | |

C4508B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = RESET (A) | 9 | = Q2 (A) | 17 | = Q0 (B) |
| 2 | = STRB (A) | 10 | = D3 (A) | 18 | = D1 (B) |
| 3 | = OUT-DIS (A) | 11 | = Q3 (A) | 19 | = Q1 (B) |
| 4 | = D0 (A) | 12 | = [GND] | 20 | = D2 (B) |
| 5 | = Q0 (A) | 13 | = RESET (B) | 21 | = Q2 (B) |
| 6 | = D1 (A) | 14 | = STRB (B) | 22 | = D3 (B) |
| 7 | = Q1 (A) | 15 | = OUT-DIS (B) | 23 | = Q3 (B) |
| 8 | = D2 (A) | 16 | = D0 (B) | 24 | = [VCC] |

C4510B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PE | 7 | = COUT' | 12 | = P2 |
| 2 | = Q4 | 8 | = [GND] | 13 | = P3 |
| 3 | = P4 | 9 | = RESET | 14 | = Q3 |
| 4 | = P1 | 10 | = U/D | 15 | = CLK |
| 5 | = CIN' | 11 | = Q2 | 16 | = [VCC] |
| 6 | = Q1 | | | | |

C4511B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INB | 7 | = INA | 12 | = B |
| 2 | = INC | 8 | = [GND] | 13 | = A |
| 3 | = LT' | 9 | = E | 14 | = G |
| 4 | = BL' | 10 | = D | 15 | = F |
| 5 | = LE/STRB | 11 | = C | 16 | = [VCC] |
| 6 | = IND | | | | |

C4512B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D0 | 7 | = D6 | 12 | = B |
| 2 | = D1 | 8 | = [GND] | 13 | = C |
| 3 | = D2 | 9 | = D7 | 14 | = SEL-OUT |
| 4 | = D3 | 10 | = INH | 15 | = 3ST-DIS |
| 5 | = D4 | 11 | = A | 16 | = [VCC] |
| 6 | = D5 | | | | |

C4514B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STRB | 9 | = S1 | 17 | = S9 |
| 2 | = DATA1 | 10 | = S2 | 18 | = S8 |
| 3 | = DATA2 | 11 | = S0 | 19 | = S11 |
| 4 | = S7 | 12 | = [GND] | 20 | = S10 |
| 5 | = S6 | 13 | = S13 | 21 | = DATA3 |
| 6 | = S5 | 14 | = S12 | 22 | = DATA4 |
| 7 | = S4 | 15 | = S15 | 23 | = INH |
| 8 | = S3 | 16 | = S14 | 24 | = [VCC] |

C4515B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STRB | 9 | = S1 | 17 | = S9 |
| 2 | = DATA1 | 10 | = S2 | 18 | = S8 |
| 3 | = DATA2 | 11 | = S0 | 19 | = S11 |
| 4 | = S7 | 12 | = [GND] | 20 | = S10 |
| 5 | = S6 | 13 | = S13 | 21 | = DATA3 |
| 6 | = S5 | 14 | = S12 | 22 | = DATA4 |
| 7 | = S4 | 15 | = S15 | 23 | = INH |
| 8 | = S3 | 16 | = S14 | 24 | = [VCC] |

C4516B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PE | 7 | = COUT' | 12 | = P2 |
| 2 | = Q4 | 8 | = [GND] | 13 | = P3 |
| 3 | = P4 | 9 | = RESET | 14 | = Q3 |
| 4 | = P1 | 10 | = U/D | 15 | = CLK |
| 5 | = CIN' | 11 | = Q2 | 16 | = [VCC] |
| 6 | = Q1 | | | | |

C4517B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q16 (A) | 7 | = D (A) | 12 | = CLK (B) |
| 2 | = Q48 (A) | 8 | = [GND] | 13 | = WE (B) |
| 3 | = WE (A) | 9 | = D (B) | 14 | = Q48 (B) |
| 4 | = CLK (A) | 10 | = Q32 (B) | 15 | = Q16 (B) |
| 5 | = Q64 (A) | 11 | = Q64 (B) | 16 | = [VCC] |
| 6 | = Q32 (A) | | | | |

C4517BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q16A | 7 | = DA | 12 | = CLKB |
| 2 | = Q48A | 8 | = [GND] | 13 | = WEB |
| 3 | = WEA | 9 | = DB | 14 | = Q48B |
| 4 | = CLKA | 10 | = Q32B | 15 | = Q16B |
| 5 | = Q64A | 11 | = Q64B | 16 | = [VCC] |
| 6 | = Q32A | | | | |

C4518B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = RESET (A) | 12 | = Q2 (B) |
| 2 | = EN (A) | 8 | = [GND] | 13 | = Q3 (B) |
| 3 | = Q1 (A) | 9 | = CLK (B) | 14 | = Q4 (B) |
| 4 | = Q2 (A) | 10 | = EN (B) | 15 | = RESET (B) |
| 5 | = Q3 (A) | 11 | = Q1 (B) | 16 | = [VCC] |
| 6 | = Q4 (A) | | | | |

C4518BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLKA | 7 | = RESETA | 12 | = Q2B |
| 2 | = ENA | 8 | = [GND] | 13 | = Q3B |
| 3 | = Q1A | 9 | = CLKB | 14 | = Q4B |
| 4 | = Q2A | 10 | = ENB | 15 | = RESETB |
| 5 | = Q3A | 11 | = Q1B | 16 | = [VCC] |
| 6 | = Q4A | | | | |

C4520B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = RESET (A) | 12 | = Q2 (B) |
| 2 | = EN (A) | 8 | = [GND] | 13 | = Q3 (B) |
| 3 | = Q1 (A) | 9 | = CLK (B) | 14 | = Q4 (B) |
| 4 | = Q2 (A) | 10 | = EN (B) | 15 | = RESET (B) |
| 5 | = Q3 (A) | 11 | = Q1 (B) | 16 | = [VCC] |
| 6 | = Q4 (A) | | | | |

C4520BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLKA | 7 | = RESETA | 12 | = Q2B |
| 2 | = ENA | 8 | = [GND] | 13 | = Q3B |
| 3 | = Q1A | 9 | = CLKB | 14 | = Q4B |
| 4 | = Q2A | 10 | = ENB | 15 | = RESETB |
| 5 | = Q3A | 11 | = Q1B | 16 | = [VCC] |
| 6 | = Q4A | | | | |

C4527B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 9-OUT | 7 | = INH-CO | 12 | = CAS |
| 2 | = C | 8 | = [GND] | 13 | = CLR |
| 3 | = D | 9 | = CLK | 14 | = A |
| 4 | = SET-9 | 10 | = STRB | 15 | = B |
| 5 | = OUT' | 11 | = INH-CIN | 16 | = [VCC] |
| 6 | = OUT | | | | |

C4532B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D4 | 7 | = Q1 | 12 | = D2 |
| 2 | = D5 | 8 | = [GND] | 13 | = D3 |
| 3 | = D6 | 9 | = Q0 | 14 | = GS |
| 4 | = D7 | 10 | = D0 | 15 | = EO |
| 5 | = EI | 11 | = D1 | 16 | = [VCC] |
| 6 | = Q2 | | | | |

C4536B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SET | 7 | = CLK-INH | 12 | = D |
| 2 | = RESET | 8 | = [GND] | 13 | = DEC-OUT |
| 3 | = IN1 | 9 | = A | 14 | = OSC-INH |
| 4 | = OUT1 | 10 | = B | 15 | = MONO-IN |
| 5 | = OUT2 | 11 | = C | 16 | = [VCC] |
| 6 | = 8-BYPAS | | | | |

C4538B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CX (A) | 7 | = Q' (A) | 12 | = TR+ (B) |
| 2 | = RXCX (A) | 8 | = [GND] | 13 | = RESET' (B) |
| 3 | = RESET' (A) | 9 | = Q' (B) | 14 | = RXCX (B) |
| 4 | = TR+ (A) | 10 | = Q (B) | 15 | = CX (B) |
| 5 | = TR- (A) | 11 | = TR- (B) | 16 | = [VCC] |
| 6 | = Q (A) | | | | |

C4538BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CX1 | 7 | = Q1' | 12 | = 2TR+ |
| 2 | = RXCX1 | 8 | = [GND] | 13 | = RESET2 |
| 3 | = RESET1 | 9 | = Q2' | 14 | = RXCX2 |
| 4 | = 1TR+ | 10 | = Q2 | 15 | = CX2 |
| 5 | = 1TR- | 11 | = 2TR- | 16 | = [VCC] |
| 6 | = Q1 | | | | |

C4541B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = RTC | 6 | = MR | 11 | = (n/c) |
| 2 | = CTC | 7 | = [GND] | 12 | = A |
| 3 | = RS | 8 | = Q | 13 | = B |
| 4 | = (n/c) | 9 | = Q-SEL | 14 | = [VCC] |
| 5 | = AR | 10 | = MODE | | |

C4555B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = E' (A) | 7 | = Q3 (A) | 12 | = Q0 (B) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (B) |
| 3 | = B (A) | 9 | = Q3 (B) | 14 | = A (B) |
| 4 | = Q0 (A) | 10 | = Q2 (B) | 15 | = E' (B) |
| 5 | = Q1 (A) | 11 | = Q1 (B) | 16 | = [VCC] |
| 6 | = Q2 (A) | | | | |

C4556B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = E' (A) | 7 | = Q3' (A) | 12 | = Q0' (B) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (B) |
| 3 | = B (A) | 9 | = Q3' (B) | 14 | = A (B) |
| 4 | = Q0' (A) | 10 | = Q2' (B) | 15 | = E' (B) |
| 5 | = Q1' (A) | 11 | = Q1' (B) | 16 | = [VCC] |
| 6 | = Q2' (A) | | | | |

C4585B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B2 | 7 | = A1 | 12 | = A<B |
| 2 | = A2 | 8 | = [GND] | 13 | = A>B |
| 3 | = A=B | 9 | = B1 | 14 | = B3 |
| 4 | = IA>B | 10 | = A0 | 15 | = A3 |
| 5 | = IA<B | 11 | = B0 | 16 | = [VCC] |
| 6 | = IA=B | | | | |

C4724B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Q3 | 12 | = Q7 |
| 2 | = A1 | 8 | = [GND] | 13 | = DATA |
| 3 | = A2 | 9 | = Q4 | 14 | = WR-DIS |
| 4 | = Q0 | 10 | = Q5 | 15 | = RESET |
| 5 | = Q1 | 11 | = Q6 | 16 | = [VCC] |
| 6 | = Q2 | | | | |

C22104A: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 15 | = C3 | 28 | = B1 |
| 2 | = E1 | 16 | = D3 | 29 | = B2 |
| 3 | = G1 | 17 | = E3 | 30 | = B3 |
| 4 | = F1 | 18 | = G3 | 31 | = D1 |
| 5 | = BP-IO | 19 | = F3 | 32 | = D2 |
| 6 | = A2 | 20 | = A4 | 33 | = D3 |
| 7 | = B2 | 21 | = B4 | 34 | = D4 |
| 8 | = C2 | 22 | = C4 | 35 | = [GND] |
| 9 | = D2 | 23 | = D4 | 36 | = OSC-IN |
| 10 | = E2 | 24 | = E4 | 37 | = A1 |
| 11 | = G2 | 25 | = G4 | 38 | = B1 |
| 12 | = F2 | 26 | = F4 | 39 | = C1 |
| 13 | = A3 | 27 | = B0 | 40 | = D1 |
| 14 | = B3 | | | | |

C22105A: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 15 | = C3 | 28 | = B1 |
| 2 | = E1 | 16 | = D3 | 29 | = B2 |
| 3 | = G1 | 17 | = E3 | 30 | = B3 |
| 4 | = F1 | 18 | = G3 | 31 | = DSC1 |
| 5 | = BP-IO | 19 | = F3 | 32 | = DSC2 |
| 6 | = A2 | 20 | = A4 | 33 | = CS1 |
| 7 | = B2 | 21 | = B4 | 34 | = CS2 |
| 8 | = C2 | 22 | = C4 | 35 | = [GND] |
| 9 | = D2 | 23 | = D4 | 36 | = OSC-IN |
| 10 | = E2 | 24 | = E4 | 37 | = A1 |
| 11 | = G2 | 25 | = G4 | 38 | = B1 |
| 12 | = F2 | 26 | = F4 | 39 | = C1 |
| 13 | = A3 | 27 | = B0 | 40 | = D1 |
| 14 | = B3 | | | | |

C22859: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 7 | = OSC1 | 12 | = R3 |
| 2 | = TX | 8 | = OSC2 | 13 | = R2 |
| 3 | = C1 | 9 | = C4 | 14 | = R1 |
| 4 | = C2 | 10 | = RX | 15 | = CD' |
| 5 | = C3 | 11 | = R4 | 16 | = VOUT |
| 6 | = [GND] | | | | |

C40100B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = {n/c} | 7 | = {n/c} | 12 | = SRO |
| 2 | = CINH | 8 | = [GND] | 13 | = L/R |
| 3 | = CLK | 9 | = RC | 14 | = {n/c} |
| 4 | = SLO | 10 | = {n/c} | 15 | = {n/c} |
| 5 | = {n/c} | 11 | = SRI | 16 | = [VCC] |
| 6 | = SLI | | | | |

C40101B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D1 | 6 | = ODD | 11 | = D6 |
| 2 | = D2 | 7 | = [GND] | 12 | = D7 |
| 3 | = D3 | 8 | = INH | 13 | = D8 |
| 4 | = D4 | 9 | = EVEN | 14 | = [VCC] |
| 5 | = D9 | 10 | = D5 | | |

C40102B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 7 | = J3 | 12 | = J6 |
| 2 | = CLR' | 8 | = [GND] | 13 | = J7 |
| 3 | = CI-CE' | 9 | = APE' | 14 | = CO-ZD' |
| 4 | = J0 | 10 | = J4 | 15 | = SPE' |
| 5 | = J1 | 11 | = J5 | 16 | = [VCC] |
| 6 | = J2 | | | | |

C40103B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 7 | = J3 | 12 | = J6 |
| 2 | = CLR' | 8 | = [GND] | 13 | = J7 |
| 3 | = CI-CE' | 9 | = APE' | 14 | = CO-ZD' |
| 4 | = J0 | 10 | = J4 | 15 | = SPE' |
| 5 | = J1 | 11 | = J5 | 16 | = [VCC] |
| 6 | = J2 | | | | |

C40104B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE | 7 | = SLIN | 12 | = Q3 |
| 2 | = SRIN | 8 | = [GND] | 13 | = Q2 |
| 3 | = D0 | 9 | = S0 | 14 | = Q1 |
| 4 | = D1 | 10 | = S1 | 15 | = Q0 |
| 5 | = D2 | 11 | = CLK | 16 | = [VCC] |
| 6 | = D3 | | | | |

C40105B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 7 | = D3 | 12 | = Q1 |
| 2 | = DIR | 8 | = [GND] | 13 | = Q0 |
| 3 | = SHFT-I | 9 | = MR | 14 | = DOR |
| 4 | = D0 | 10 | = Q3 | 15 | = SHFT-O |
| 5 | = D1 | 11 | = Q2 | 16 | = [VCC] |
| 6 | = D2 | | | | |

C40106B: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IN (A) | 6 | = OUT (C) | 11 | = IN (E) |
| 2 | = OUT (A) | 7 | = [GND] | 12 | = OUT (F) |
| 3 | = IN (B) | 8 | = OUT (D) | 13 | = IN (F) |
| 4 | = OUT (B) | 9 | = IN (D) | 14 | = [VCC] |
| 5 | = IN (C) | 10 | = OUT (E) | | |

C40107B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | {n/c} | 6 | {n/c} | 11 | INA (B) |
| 2 | {n/c} | 7 | [GND] | 12 | {n/c} |
| 3 | INA (A) | 8 | {n/c} | 13 | {n/c} |
| 4 | INB (A) | 9 | OUTY (B) | 14 | [VCC] |
| 5 | OUTY (A) | 10 | INB (B) | | |

C40108B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | Q3B | 9 | W1 | 17 | D3 |
| 2 | Q2B | 10 | R1B | 18 | D2 |
| 3 | ENA | 11 | ROB | 19 | D1 |
| 4 | Q0A | 12 | [GND] | 20 | D0 |
| 5 | Q1A | 13 | ROA | 21 | ENB |
| 6 | Q2A | 14 | R1A | 22 | Q0B |
| 7 | Q3A | 15 | WREN | 23 | Q1B |
| 8 | W0 | 16 | CLK | 24 | [VCC] |

C40109B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | VCC (D) | 7 | EN (B) | 12 | {n/c} |
| 2 | EN (A) | 8 | [GND] | 13 | E (D) |
| 3 | A (A) | 9 | EN (C) | 14 | A (D) |
| 4 | E (A) | 10 | A (C) | 15 | EN (D) |
| 5 | E (B) | 11 | E (C) | 16 | [VCC] |
| 6 | A (B) | | | | |

C40109BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | VCC | 7 | ENB | 12 | {n/c} |
| 2 | ENA | 8 | [GND] | 13 | H |
| 3 | A | 9 | ENC | 14 | D |
| 4 | E | 10 | C | 15 | END |
| 5 | F | 11 | G | 16 | [VCC] |
| 6 | B | | | | |

C40110B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A | 7 | = CLK-DN | 12 | = E |
| 2 | = G | 8 | = [GND] | 13 | = D |
| 3 | = F | 9 | = CLK-UP | 14 | = C |
| 4 | = TOG-EN' | 10 | = CARRY | 15 | = B |
| 5 | = RESET | 11 | = BORROW | 16 | = [VCC] |
| 6 | = LE | | | | |

C40115: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 9 | = A8 | 16 | = B6 |
| 2 | = A1 | 10 | = EN | 17 | = B5 |
| 3 | = A2 | 11 | = [GND] | 18 | = B4 |
| 4 | = A3 | 12 | = (n/c) | 19 | = B3 |
| 5 | = A4 | 13 | = DIS | 20 | = B2 |
| 6 | = A5 | 14 | = B8 | 21 | = B1 |
| 7 | = A6 | 15 | = B7 | 22 | = VCC |
| 8 | = A7 | | | | |

C40116: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 9 | = A8 | 16 | = B6 |
| 2 | = A1 | 10 | = EN | 17 | = B5 |
| 3 | = A2 | 11 | = [GND] | 18 | = B4 |
| 4 | = A3 | 12 | = GND2 | 19 | = B3 |
| 5 | = A4 | 13 | = DIS | 20 | = B2 |
| 6 | = A5 | 14 | = B8 | 21 | = B1 |
| 7 | = A6 | 15 | = B7 | 22 | = VCC |
| 8 | = A7 | | | | |

C40117B: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STRB (A) | 6 | = 4 (A) | 11 | = 1 (B) |
| 2 | = STRB (B) | 7 | = [GND] | 12 | = DATA (B) |
| 3 | = 1 (A) | 8 | = 4 (B) | 13 | = DATA (A) |
| 4 | = 2 (A) | 9 | = 3 (B) | 14 | = [VCC] |
| 5 | = 3 (A) | 10 | = 2 (B) | | |

C40147B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = 4 | | 7 = B | | 12 = 2 | |
| 2 = 5 | | 8 = [GND] | | 13 = 3 | |
| 3 = 6 | | 9 = A | | 14 = D | |
| 4 = 7 | | 10 = 9 | | 15 = 0 | |
| 5 = 8 | | 11 = 1 | | 16 = [VCC] | |
| 6 = C | | | | | |

C40160B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = CLR' | | 7 = PE | | 12 = Q3 | |
| 2 = CLK | | 8 = [GND] | | 13 = Q2 | |
| 3 = P1 | | 9 = LOAD' | | 14 = Q1 | |
| 4 = P2 | | 10 = TE | | 15 = COUT | |
| 5 = P3 | | 11 = Q4 | | 16 = [VCC] | |
| 6 = P4 | | | | | |

C40161B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = CLR' | | 7 = PE | | 12 = Q3 | |
| 2 = CLK | | 8 = [GND] | | 13 = Q2 | |
| 3 = P1 | | 9 = LOAD' | | 14 = Q1 | |
| 4 = P2 | | 10 = TE | | 15 = COUT | |
| 5 = P3 | | 11 = Q4 | | 16 = [VCC] | |
| 6 = P4 | | | | | |

C40162B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = CLR' | | 7 = PE | | 12 = Q3 | |
| 2 = CLK | | 8 = [GND] | | 13 = Q2 | |
| 3 = P1 | | 9 = LOAD' | | 14 = Q1 | |
| 4 = P2 | | 10 = TE | | 15 = COUT | |
| 5 = P3 | | 11 = Q4 | | 16 = [VCC] | |
| 6 = P4 | | | | | |

C40163B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = PE | 12 | = Q3 |
| 2 | = CLK | 8 | = [GND] | 13 | = Q2 |
| 3 | = P1 | 9 | = LOAD' | 14 | = Q1 |
| 4 | = P2 | 10 | = TE | 15 | = COUT |
| 5 | = P3 | 11 | = Q4 | 16 | = [VCC] |
| 6 | = P4 | | | | |

C40174B: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' (F) | 7 | = Q (C) | 12 | = Q (E) |
| 2 | = Q (A) | 8 | = [GND] | 13 | = D (E) |
| 3 | = D (A) | 9 | = CLK (F) | 14 | = D (F) |
| 4 | = D (B) | 10 | = Q (D) | 15 | = Q (F) |
| 5 | = Q (B) | 11 | = D (D) | 16 | = [VCC] |
| 6 | = D (C) | | | | |

C40174BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = Q3 | 12 | = Q5 |
| 2 | = Q1 | 8 | = [GND] | 13 | = D5 |
| 3 | = D1 | 9 | = CLK | 14 | = D6 |
| 4 | = D2 | 10 | = Q4 | 15 | = Q6 |
| 5 | = Q2 | 11 | = D4 | 16 | = [VCC] |
| 6 | = D3 | | | | |

C40175B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' (D) | 7 | = Q (B) | 12 | = D (C) |
| 2 | = Q (A) | 8 | = [GND] | 13 | = D (D) |
| 3 | = Q' (A) | 9 | = CLK (D) | 14 | = Q' (D) |
| 4 | = D (A) | 10 | = Q (C) | 15 | = Q (D) |
| 5 | = D (B) | 11 | = Q' (C) | 16 | = [VCC] |
| 6 | = Q' (B) | | | | |

C40175BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = Q2 | 12 | = D3 |
| 2 | = Q1 | 8 | = [GND] | 13 | = D4 |
| 3 | = Q1' | 9 | = CLK | 14 | = Q4' |
| 4 | = D1 | 10 | = Q3 | 15 | = Q4 |
| 5 | = D2 | 11 | = Q3' | 16 | = [VCC] |
| 6 | = Q2' | | | | |

C40181B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B0' | 9 | = F0' | 17 | = G' |
| 2 | = A0' | 10 | = F1' | 18 | = B3' |
| 3 | = S3 | 11 | = F2' | 19 | = A3' |
| 4 | = S2 | 12 | = [GND] | 20 | = B2' |
| 5 | = S1 | 13 | = F3' | 21 | = A2' |
| 6 | = S0 | 14 | = A=B | 22 | = B1' |
| 7 | = CN | 15 | = P' | 23 | = A1' |
| 8 | = M | 16 | = CN+4 | 24 | = [VCC] |

C40182B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = G1' | 7 | = P' | 12 | = CN+X |
| 2 | = P1' | 8 | = [GND] | 13 | = CN |
| 3 | = G0' | 9 | = CN+Z | 14 | = G2' |
| 4 | = P0' | 10 | = G' | 15 | = P2' |
| 5 | = G3' | 11 | = CN+Y | 16 | = [VCC] |
| 6 | = P3' | | | | |

C40192B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = J2 | 7 | = Q4 | 12 | = CARRY' |
| 2 | = Q2 | 8 | = [GND] | 13 | = BORR' |
| 3 | = Q1 | 9 | = J4 | 14 | = RESET |
| 4 | = CLK-D | 10 | = J3 | 15 | = J1 |
| 5 | = CLK-U | 11 | = PE' | 16 | = [VCC] |
| 6 | = Q3 | | | | |

C40193B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = J2 | 7 | = Q4 | 12 | = CARRY' |
| 2 | = Q2 | 8 | = [GND] | 13 | = BORR' |
| 3 | = Q1 | 9 | = J4 | 14 | = RESET |
| 4 | = CLK-D | 10 | = J3 | 15 | = J1 |
| 5 | = CLK-U | 11 | = PE' | 16 | = [VCC] |
| 6 | = Q3 | | | | |

C40194B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = RESET' | 7 | = SLIN | 12 | = Q3 |
| 2 | = SRIN | 8 | = [GND] | 13 | = Q2 |
| 3 | = D0 | 9 | = S0 | 14 | = Q1 |
| 4 | = D1 | 10 | = S1 | 15 | = Q0 |
| 5 | = D2 | 11 | = CLK | 16 | = [VCC] |
| 6 | = D3 | | | | |

C40208B: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q3B | 9 | = W1 | 17 | = D3 |
| 2 | = Q2B | 10 | = R0B | 18 | = D2 |
| 3 | = ENA | 11 | = R1B | 19 | = D1 |
| 4 | = Q0A | 12 | = [GND] | 20 | = D0 |
| 5 | = Q1A | 13 | = R0A | 21 | = ENB |
| 6 | = Q2A | 14 | = R1A | 22 | = Q0B |
| 7 | = Q3A | 15 | = WREN | 23 | = Q1B |
| 8 | = W0 | 16 | = CLK | 24 | = [VCC] |

C40257B: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IN-SEL (D) | 7 | = D (B) | 12 | = D (D) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (D) |
| 3 | = B (A) | 9 | = D (C) | 14 | = A (D) |
| 4 | = D (A) | 10 | = B (C) | 15 | = OUT-DIS (D) |
| 5 | = A (B) | 11 | = A (C) | 16 | = [VCC] |
| 6 | = B (B) | | | | |

C40257BS: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IN-SEL | 7 | = D2 | 12 | = D4 |
| 2 | = A1 | 8 | = [GND] | 13 | = B4 |
| 3 | = B1 | 9 | = D3 | 14 | = A4 |
| 4 | = D1 | 10 | = B3 | 15 | = OUT-DIS |
| 5 | = A2 | 11 | = A3 | 16 | = [VCC] |
| 6 | = B2 | | | | |

H00: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

H02: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = INB (B) | 11 | = INA (D) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = INB (D) |
| 3 | = INB (A) | 8 | = INA (C) | 13 | = OUTY (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

H03: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

H04: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 6 | = O (C) | 11 | = A (E) |
| 2 | = O (A) | 7 | = [GND] | 12 | = O (F) |
| 3 | = A (B) | 8 | = O (D) | 13 | = A (F) |
| 4 | = O (B) | 9 | = A (D) | 14 | = [VCC] |
| 5 | = A (C) | 10 | = O (E) | | |

H05: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 6 | = Y (C) | 11 | = A (E) |
| 2 | = Y (A) | 7 | = [GND] | 12 | = Y (F) |
| 3 | = A (B) | 8 | = Y (D) | 13 | = A (F) |
| 4 | = Y (B) | 9 | = A (D) | 14 | = [VCC] |
| 5 | = A (C) | 10 | = Y (E) | | |

H08: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

H10: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = OUTY (A) |
| 3 | = INA (B) | 8 | = OUTY (C) | 13 | = INC (A) |
| 4 | = INB (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = INB (C) | | |

H11: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = OUTY (A) |
| 3 | = INA (B) | 8 | = OUTY (C) | 13 | = INC (A) |
| 4 | = INB (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = INB (C) | | |

H14: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IN (A) | 6 | = OUT (C) | 11 | = IN (E) |
| 2 | = OUT (A) | 7 | = [GND] | 12 | = OUT (F) |
| 3 | = IN (B) | 8 | = OUT (D) | 13 | = IN (F) |
| 4 | = OUT (B) | 9 | = IN (D) | 14 | = [VCC] |
| 5 | = IN (C) | 10 | = OUT (E) | | |

H20: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (A) | 11 | = {n/c} |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INC (B) |
| 3 | = {n/c} | 8 | = OUTY (B) | 13 | = IND (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

H21: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (A) | 11 | = {n/c} |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INC (B) |
| 3 | = {n/c} | 8 | = OUTY (B) | 13 | = IND (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

H27: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = OUTY (A) |
| 3 | = INA (B) | 8 | = OUTY (C) | 13 | = INC (A) |
| 4 | = INB (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = INB (C) | | |

H30: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA | 6 | = INF | 11 | = ING |
| 2 | = INB | 7 | = [GND] | 12 | = INH |
| 3 | = INC | 8 | = OUTY' | 13 | = {n/c} |
| 4 | = IND | 9 | = {n/c} | 14 | = [VCC] |
| 5 | = INE | 10 | = {n/c} | | |

H32: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

H42: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0' | 7 | = Y6' | 12 | = A3 |
| 2 | = Y1' | 8 | = [GND] | 13 | = A2 |
| 3 | = Y2' | 9 | = Y7' | 14 | = A1 |
| 4 | = Y3' | 10 | = Y8' | 15 | = A0 |
| 5 | = Y4' | 11 | = Y9' | 16 | = [VCC] |
| 6 | = Y5' | | | | |

H44: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 00' | 7 | = 06' | 12 | = A3 |
| 2 | = 01' | 8 | = [GND] | 13 | = A2 |
| 3 | = 02' | 9 | = 07' | 14 | = A1 |
| 4 | = 03' | 10 | = 08' | 15 | = A0 |
| 5 | = 04' | 11 | = 09' | 16 | = [VCC] |
| 6 | = 05' | | | | |

H51: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A1 | 6 | = Y2 | 11 | = F1 |
| 2 | = A2 | 7 | = [GND] | 12 | = B1 |
| 3 | = B2 | 8 | = Y1 | 13 | = C1 |
| 4 | = C2 | 9 | = D1 | 14 | = [VCC] |
| 5 | = D2 | 10 | = E1 | | |

H73: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 6 | = CLR' (B) | 11 | = [GND] |
| 2 | = CLR' (A) | 7 | = J (B) | 12 | = Q (A) |
| 3 | = K (A) | 8 | = Q' (B) | 13 | = Q' (A) |
| 4 | = [VCC] | 9 | = Q (B) | 14 | = J (A) |
| 5 | = CLK (B) | 10 | = K (B) | | |

H74: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' (A) | 6 | = Q' (A) | 11 | = CLK (B) |
| 2 | = D (A) | 7 | = [GND] | 12 | = D (B) |
| 3 | = CLK (A) | 8 | = Q' (B) | 13 | = CLR' (B) |
| 4 | = PR' (A) | 9 | = Q (B) | 14 | = [VCC] |
| 5 | = Q (A) | 10 | = PR' (B) | | |

H75: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q' (A) | 7 | = D (D) | 12 | = [GND] |
| 2 | = D (A) | 8 | = Q' (D) | 13 | = G (A,B) |
| 3 | = D (B) | 9 | = Q (D) | 14 | = Q' (B) |
| 4 | = G (C,D) | 10 | = Q (C) | 15 | = Q (B) |
| 5 | = [VCC] | 11 | = Q' (C) | 16 | = Q (A) |
| 6 | = D (C) | | | | |

H76: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = PR' (B) | 12 | = K (B) |
| 2 | = PR' (A) | 8 | = CLR' (B) | 13 | = [GND] |
| 3 | = CLR' (A) | 9 | = J (B) | 14 | = Q' (A) |
| 4 | = J (A) | 10 | = Q' (B) | 15 | = Q (A) |
| 5 | = [VCC] | 11 | = Q (B) | 16 | = K (A) |
| 6 | = CLK (B) | | | | |

H85: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B3 | 7 | = A<B | 12 | = A1 |
| 2 | = IA<B | 8 | = [GND] | 13 | = A2 |
| 3 | = IA=B | 9 | = B0 | 14 | = B2 |
| 4 | = IA>B | 10 | = A0 | 15 | = A3 |
| 5 | = A>B | 11 | = B1 | 16 | = [VCC] |
| 6 | = A=B | | | | |

H86: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

H93: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CP1 | 6 | = {n/c} | 11 | = Q3 |
| 2 | = MR1 | 7 | = {n/c} | 12 | = Q0 |
| 3 | = MR2 | 8 | = Q2 | 13 | = {n/c} |
| 4 | = {n/c} | 9 | = Q1 | 14 | = CP0 |
| 5 | = [VCC] | 10 | = [GND] | | |

H107: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = J (A) | 6 | = Q' (B) | 11 | = K (B) |
| 2 | = Q' (A) | 7 | = [GND] | 12 | = CLK (A) |
| 3 | = Q (A) | 8 | = J (B) | 13 | = CLR' (A) |
| 4 | = K (A) | 9 | = CLK (B) | 14 | = [VCC] |
| 5 | = Q (B) | 10 | = CLR' (B) | | |

H109: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' (A) | 7 | = Q' (A) | 12 | = CLK (B) |
| 2 | = J (A) | 8 | = [GND] | 13 | = K' (B) |
| 3 | = K' (A) | 9 | = Q' (B) | 14 | = J (B) |
| 4 | = CLK (A) | 10 | = Q (B) | 15 | = CLR' (B) |
| 5 | = PR' (A) | 11 | = PR' (B) | 16 | = [VCC] |
| 6 | = Q (A) | | | | |

H112: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = Q' (B) | 12 | = K (B) |
| 2 | = K (A) | 8 | = [GND] | 13 | = CLK (B) |
| 3 | = J (A) | 9 | = Q (B) | 14 | = CLR' (B) |
| 4 | = PR' (A) | 10 | = PR' (B) | 15 | = CLR' (A) |
| 5 | = Q (A) | 11 | = J (B) | 16 | = [VCC] |
| 6 | = Q' (A) | | | | |

H123: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A' (A) | 7 | = RXCX (B) | 12 | = Q' (B) |
| 2 | = B (A) | 8 | = [GND] | 13 | = Q (A) |
| 3 | = R' (A) | 9 | = A' (B) | 14 | = CX (A) |
| 4 | = Q' (A) | 10 | = B (B) | 15 | = RXCX (A) |
| 5 | = Q (B) | 11 | = R' (B) | 16 | = [VCC] |
| 6 | = CX (B) | | | | |

H125: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = C (A) | 6 | = Y (B) | 11 | = Y (D) |
| 2 | = A (A) | 7 | = [GND] | 12 | = A (D) |
| 3 | = Y (A) | 8 | = Y (C) | 13 | = C (D) |
| 4 | = C (B) | 9 | = A (C) | 14 | = [VCC] |
| 5 | = A (B) | 10 | = C (C) | | |

H126: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = C (A) | 6 | = Y (B) | 11 | = Y (D) |
| 2 | = A (A) | 7 | = [GND] | 12 | = A (D) |
| 3 | = Y (A) | 8 | = Y (C) | 13 | = C (D) |
| 4 | = C (B) | 9 | = A (C) | 14 | = [VCC] |
| 5 | = A (B) | 10 | = C (C) | | |

H132: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

H133: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA | 7 | = ING | 12 | = INJ |
| 2 | = INB | 8 | = [GND] | 13 | = INK |
| 3 | = INC | 9 | = OUTY' | 14 | = INL |
| 4 | = IND | 10 | = INH | 15 | = INM |
| 5 | = INE | 11 | = INI | 16 | = [VCC] |
| 6 | = INF | | | | |

H137: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7' | 12 | = Y3' |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2' |
| 3 | = A2 | 9 | = Y6' | 14 | = Y1' |
| 4 | = LE' | 10 | = Y5' | 15 | = Y0' |
| 5 | = OE1' | 11 | = Y4' | 16 | = [VCC] |
| 6 | = OE0 | | | | |

H138: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7' | 12 | = Y3' |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2' |
| 3 | = A2 | 9 | = Y6' | 14 | = Y1' |
| 4 | = E1' | 10 | = Y5' | 15 | = Y0' |
| 5 | = E2' | 11 | = Y4' | 16 | = [VCC] |
| 6 | = E3 | | | | |

H139: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = EN' (A) | 7 | = Y3' (A) | 12 | = Y0' (B) |
| 2 | = SELA (A) | 8 | = [GND] | 13 | = SELB (B) |
| 3 | = SELB (A) | 9 | = Y3' (B) | 14 | = SELA (B) |
| 4 | = Y0' (A) | 10 | = Y2' (B) | 15 | = EN' (B) |
| 5 | = Y1' (A) | 11 | = Y1' (B) | 16 | = [VCC] |
| 6 | = Y2' (A) | | | | |

H145: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = O0' | 7 | = O6' | 12 | = A3 |
| 2 | = O1' | 8 | = [GND] | 13 | = A2 |
| 3 | = O2' | 9 | = O7' | 14 | = A1 |
| 4 | = O3' | 10 | = O8' | 15 | = A0 |
| 5 | = O4' | 11 | = O9' | 16 | = [VCC] |
| 6 | = O5' | | | | |

H147: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = I4 | 7 | = Y1' | 12 | = I2 |
| 2 | = I5 | 8 | = [GND] | 13 | = I3 |
| 3 | = I6 | 9 | = Y0' | 14 | = Y3' |
| 4 | = I7 | 10 | = I9 | 15 | = I0 |
| 5 | = I8 | 11 | = I1 | 16 | = [VCC] |
| 6 | = Y2' | | | | |

H151: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D3 | 7 | = OE' | 12 | = D7 |
| 2 | = D2 | 8 | = [GND] | 13 | = D6 |
| 3 | = D1 | 9 | = A2 | 14 | = D5 |
| 4 | = D0 | 10 | = A1 | 15 | = D4 |
| 5 | = Y | 11 | = A0 | 16 | = [VCC] |
| 6 | = Y' | | | | |

H153: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' (A) | 7 | = Y (A) | 12 | = C2 (B) |
| 2 | = A1 | 8 | = [GND] | 13 | = C3 (B) |
| 3 | = C3 (A) | 9 | = Y (B) | 14 | = A0 (B) |
| 4 | = C2 (A) | 10 | = C0 (B) | 15 | = OE' |
| 5 | = C1 (A) | 11 | = C1 (B) | 16 | = [VCC] |
| 6 | = C0 (A) | | | | |

H153S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 10E' | 7 | = 1Y | 12 | = 2C2 |
| 2 | = A1 | 8 | = [GND] | 13 | = 2C3 |
| 3 | = 1C3 | 9 | = 2Y | 14 | = A0 |
| 4 | = 1C2 | 10 | = 2C0 | 15 | = 20E' |
| 5 | = 1C1 | 11 | = 2C1 | 16 | = [VCC] |
| 6 | = 1C0 | | | | |

H154: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0' | 9 | = Y8' | 17 | = Y15' |
| 2 | = Y1' | 10 | = Y9' | 18 | = E1' |
| 3 | = Y2' | 11 | = Y10' | 19 | = E2' |
| 4 | = Y3' | 12 | = [GND] | 20 | = A3 |
| 5 | = Y4' | 13 | = Y11' | 21 | = A2 |
| 6 | = Y5' | 14 | = Y12' | 22 | = A1 |
| 7 | = Y6' | 15 | = Y13' | 23 | = A0 |
| 8 | = Y7' | 16 | = Y14' | 24 | = [VCC] |

H157: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = Y (B) | 12 | = Y (D) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (D) |
| 3 | = B (A) | 9 | = Y (C) | 14 | = A (D) |
| 4 | = Y (A) | 10 | = B (C) | 15 | = OE' |
| 5 | = A (B) | 11 | = A (C) | 16 | = [VCC] |
| 6 | = B (B) | | | | |

H157S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y | 12 | = 4Y |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y | 14 | = 4A |
| 4 | = 1Y | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

H158: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = Y' (B) | 12 | = Y' (D) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (D) |
| 3 | = B (A) | 9 | = Y' (C) | 14 | = A (D) |
| 4 | = Y' (A) | 10 | = B (C) | 15 | = OE' |
| 5 | = A (B) | 11 | = A (C) | 16 | = [VCC] |
| 6 | = B (B) | | | | |

H158S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y' | 12 | = 4Y' |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y' | 14 | = 4A |
| 4 | = 1Y' | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

H160: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

H161: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

H162: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

H163: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

H164: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A | 6 | = QD | 11 | = QF |
| 2 | = B | 7 | = [GND] | 12 | = QG |
| 3 | = QA | 8 | = CLK | 13 | = QH |
| 4 | = QB | 9 | = CLR' | 14 | = [VCC] |
| 5 | = QC | 10 | = QE | | |

H165: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PL' | 7 | = Q7' | 12 | = D1 |
| 2 | = CLK | 8 | = [GND] | 13 | = D2 |
| 3 | = D4 | 9 | = Q7 | 14 | = D3 |
| 4 | = D5 | 10 | = DS | 15 | = CE' |
| 5 | = D6 | 11 | = D0 | 16 | = [VCC] |
| 6 | = D7 | | | | |

H166: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DS | 7 | = CLK | 12 | = D6 |
| 2 | = D0 | 8 | = [GND] | 13 | = Q7 |
| 3 | = D1 | 9 | = MR' | 14 | = D7 |
| 4 | = D2 | 10 | = D4 | 15 | = PE' |
| 5 | = D3 | 11 | = D5 | 16 | = [VCC] |
| 6 | = CE' | | | | |

H173: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = CLK | 12 | = D2 |
| 2 | = OE2' | 8 | = [GND] | 13 | = D1 |
| 3 | = Q0 | 9 | = E1' | 14 | = D0 |
| 4 | = Q1 | 10 | = E2' | 15 | = MR |
| 5 | = Q2 | 11 | = D3 | 16 | = [VCC] |
| 6 | = Q3 | | | | |

H174: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = Q (C) | 12 | = Q (E) |
| 2 | = Q (A) | 8 | = [GND] | 13 | = D (E) |
| 3 | = D (A) | 9 | = CLK | 14 | = D (F) |
| 4 | = D (B) | 10 | = Q (D) | 15 | = Q (F) |
| 5 | = Q (B) | 11 | = D (D) | 16 | = [VCC] |
| 6 | = D (C) | | | | |

H174S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = Q3 | 12 | = Q5 |
| 2 | = Q1 | 8 | = [GND] | 13 | = D5 |
| 3 | = D1 | 9 | = CLK | 14 | = D6 |
| 4 | = D2 | 10 | = Q4 | 15 | = Q6 |
| 5 | = Q2 | 11 | = D4 | 16 | = [VCC] |
| 6 | = D3 | | | | |

H175: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = Q (B) | 12 | = D (C) |
| 2 | = Q (A) | 8 | = [GND] | 13 | = D (D) |
| 3 | = Q' (A) | 9 | = CLK | 14 | = Q' (D) |
| 4 | = D (A) | 10 | = Q (C) | 15 | = Q (D) |
| 5 | = D (B) | 11 | = Q' (C) | 16 | = [VCC] |
| 6 | = Q' (B) | | | | |

H175S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = Q2 | 12 | = D3 |
| 2 | = Q1 | 8 | = [GND] | 13 | = D4 |
| 3 | = Q1' | 9 | = CLK | 14 | = Q4' |
| 4 | = D1 | 10 | = Q3 | 15 | = Q4 |
| 5 | = D2 | 11 | = Q3' | 16 | = [VCC] |
| 6 | = Q2' | | | | |

H181: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B0 | 9 | = F0 | 17 | = G |
| 2 | = A0 | 10 | = F1 | 18 | = B3 |
| 3 | = S3 | 11 | = F2 | 19 | = A3 |
| 4 | = S2 | 12 | = [GND] | 20 | = B2 |
| 5 | = S1 | 13 | = F3 | 21 | = A2 |
| 6 | = S0 | 14 | = A=B | 22 | = B1 |
| 7 | = CN' | 15 | = P | 23 | = A1 |
| 8 | = M | 16 | = CN+4' | 24 | = [VCC] |

H182: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = G1' | 7 | = P7' | 12 | = CN+X |
| 2 | = P1' | 8 | = [GND] | 13 | = CN |
| 3 | = G0' | 9 | = CN+Z | 14 | = G2' |
| 4 | = P0' | 10 | = G' | 15 | = P2' |
| 5 | = G3' | 11 | = CN+Y | 16 | = [VCC] |
| 6 | = P3' | | | | |

H190: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TC |
| 2 | = Q1 | 8 | = [GND] | 13 | = RC' |
| 3 | = Q0 | 9 | = P3 | 14 | = CP |
| 4 | = CE' | 10 | = P2 | 15 | = P0 |
| 5 | = D/U' | 11 | = PL' | 16 | = [VCC] |
| 6 | = Q2 | | | | |

H191: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TC |
| 2 | = Q1 | 8 | = [GND] | 13 | = RC' |
| 3 | = Q0 | 9 | = P3 | 14 | = CP |
| 4 | = CE' | 10 | = P2 | 15 | = P0 |
| 5 | = D/U' | 11 | = PL' | 16 | = [VCC] |
| 6 | = Q2 | | | | |

H192: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TCU' |
| 2 | = Q1 | 8 | = [GND] | 13 | = TCD' |
| 3 | = Q0 | 9 | = P3 | 14 | = MR |
| 4 | = CPD | 10 | = P2 | 15 | = P0 |
| 5 | = CPU | 11 | = PL' | 16 | = [VCC] |
| 6 | = Q2 | | | | |

H193: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TCU' |
| 2 | = Q1 | 8 | = [GND] | 13 | = TCD' |
| 3 | = Q0 | 9 | = P3 | 14 | = MR |
| 4 | = CPD | 10 | = P2 | 15 | = P0 |
| 5 | = CPU | 11 | = PL' | 16 | = [VCC] |
| 6 | = Q2 | | | | |

H194: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = SL | 12 | = QD |
| 2 | = SR | 8 | = [GND] | 13 | = QC |
| 3 | = A | 9 | = S0 | 14 | = QB |
| 4 | = B | 10 | = S1 | 15 | = QA |
| 5 | = C | 11 | = CLK | 16 | = [VCC] |
| 6 | = D | | | | |

H195: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = D3 | 12 | = Q3 |
| 2 | = J | 8 | = [GND] | 13 | = Q2 |
| 3 | = K' | 9 | = PE' | 14 | = Q1 |
| 4 | = D0 | 10 | = CLK | 15 | = Q0 |
| 5 | = D1 | 11 | = Q3' | 16 | = [VCC] |
| 6 | = D2 | | | | |

H221: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A' (A) | 7 | = RXCX (B) | 12 | = Q' (B) |
| 2 | = B (A) | 8 | = [GND] | 13 | = Q (A) |
| 3 | = R' (A) | 9 | = A' (B) | 14 | = CX (A) |
| 4 | = Q' (A) | 10 | = B (B) | 15 | = RXCX (A) |
| 5 | = Q (B) | 11 | = R' (B) | 16 | = [VCC] |
| 6 | = CX (B) | | | | |

H237: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7 | 12 | = Y3 |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2 |
| 3 | = A2 | 9 | = Y6 | 14 | = Y1 |
| 4 | = LE' | 10 | = Y5 | 15 | = Y0 |
| 5 | = OE1' | 11 | = Y4 | 16 | = [VCC] |
| 6 | = OE0 | | | | |

H238: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7 | 12 | = Y3 |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2 |
| 3 | = A2 | 9 | = Y6 | 14 | = Y1 |
| 4 | = E1' | 10 | = Y5 | 15 | = Y0 |
| 5 | = E2' | 11 | = Y4 | 16 | = [VCC] |
| 6 | = E3 | | | | |

H240: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-----------------------|------------|-----------------------|------------|-----------------------|
| 1 | = OE ¹ (A) | 8 | = A3 (A) | 15 | = A2 (B) |
| 2 | = A0 (A) | 9 | = Y0 ¹ (B) | 16 | = Y1 ¹ (A) |
| 3 | = Y3 ¹ (B) | 10 | = [GND] | 17 | = A3 (B) |
| 4 | = A1 (A) | 11 | = A0 (B) | 18 | = Y0 ¹ (A) |
| 5 | = Y2 ¹ (B) | 12 | = Y3 ¹ (A) | 19 | = OE ¹ (B) |
| 6 | = A2 (A) | 13 | = A1 (B) | 20 | = [VCC] |
| 7 | = Y1 ¹ (B) | 14 | = Y2 ¹ (A) | | |

H240S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|--------------------|------------|--------------------|
| 1 | = OE1 ¹ | 8 | = 1A3 | 15 | = 2A2 |
| 2 | = 1A0 | 9 | = 2Y0 ¹ | 16 | = 1Y1 ¹ |
| 3 | = 2Y3 ¹ | 10 | = [GND] | 17 | = 2A3 |
| 4 | = 1A1 | 11 | = 2A0 | 18 | = 1Y0 ¹ |
| 5 | = 2Y2 ¹ | 12 | = 1Y3 ¹ | 19 | = OE2 ¹ |
| 6 | = 1A2 | 13 | = 2A1 | 20 | = [VCC] |
| 7 | = 2Y1 ¹ | 14 | = 1Y2 ¹ | | |

H241: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|---------------|------------|---------------|
| 1 | = 1G ¹ | 8 | = 1A4 | 15 | = 2A3 |
| 2 | = 1A1 | 9 | = 2Y1 | 16 | = 1Y2 |
| 3 | = 2Y4 | 10 | = [GND] | 17 | = 2A4 |
| 4 | = 1A2 | 11 | = 2A1 | 18 | = 1Y1 |
| 5 | = 2Y3 | 12 | = 1Y4 | 19 | = 2G |
| 6 | = 1A3 | 13 | = 2A2 | 20 | = [VCC] |
| 7 | = 2Y2 | 14 | = 1Y3 | | |

H242: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|---------------|------------|---------------|
| 1 | = OEB ¹ | 6 | = A3 | 11 | = B0 |
| 2 | = {n/c} | 7 | = [GND] | 12 | = {n/c} |
| 3 | = A0 | 8 | = B3 | 13 | = OEA |
| 4 | = A1 | 9 | = B2 | 14 | = [VCC] |
| 5 | = A2 | 10 | = B1 | | |

H243: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------------|------------|---------------|------------|---------------|
| 1 | = OE ^B ' | 6 | = A3 | 11 | = B0 |
| 2 | = (n/c) | 7 | = [GND] | 12 | = (n/c) |
| 3 | = A0 | 8 | = B3 | 13 | = OEA |
| 4 | = A1 | 9 | = B2 | 14 | = [VCC] |
| 5 | = A2 | 10 | = B1 | | |

H244: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' (A) | 8 | = A3 (A) | 15 | = A2 (B) |
| 2 | = A0 (A) | 9 | = Y0 (B) | 16 | = Y1 (A) |
| 3 | = Y3 (B) | 10 | = [GND] | 17 | = A3 (B) |
| 4 | = A1 (A) | 11 | = A0 (B) | 18 | = Y0 (A) |
| 5 | = Y2 (B) | 12 | = Y3 (A) | 19 | = OE' (B) |
| 6 | = A2 (A) | 13 | = A1 (B) | 20 | = [VCC] |
| 7 | = Y1 (B) | 14 | = Y2 (A) | | |

H244S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 8 | = 1A3 | 15 | = 2A2 |
| 2 | = 1A0 | 9 | = 2Y0 | 16 | = 1Y1 |
| 3 | = 2Y3 | 10 | = [GND] | 17 | = 2A3 |
| 4 | = 1A1 | 11 | = 2A0 | 18 | = 1Y0 |
| 5 | = 2Y2 | 12 | = 1Y3 | 19 | = OE2' |
| 6 | = 1A2 | 13 | = 2A1 | 20 | = [VCC] |
| 7 | = 2Y1 | 14 | = 1Y2 | | |

H245: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DIR | 8 | = A7 | 15 | = B4 |
| 2 | = A1 | 9 | = A8 | 16 | = B3 |
| 3 | = A2 | 10 | = [GND] | 17 | = B2 |
| 4 | = A3 | 11 | = B8 | 18 | = B1 |
| 5 | = A4 | 12 | = B7 | 19 | = OE' |
| 6 | = A5 | 13 | = B6 | 20 | = [VCC] |
| 7 | = A6 | 14 | = B5 | | |

H251: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D3 | 7 | = OE' | 12 | = D7 |
| 2 | = D2 | 8 | = [GND] | 13 | = D6 |
| 3 | = D1 | 9 | = A2 | 14 | = D5 |
| 4 | = D0 | 10 | = A1 | 15 | = D4 |
| 5 | = Y | 11 | = A0 | 16 | = [VCC] |
| 6 | = Y' | | | | |

H253: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' (A) | 7 | = Y (A) | 12 | = 12 (B) |
| 2 | = S1 | 8 | = [GND] | 13 | = 13 (B) |
| 3 | = 13 (A) | 9 | = Y (B) | 14 | = S0 |
| 4 | = 12 (A) | 10 | = 10 (B) | 15 | = OE' (B) |
| 5 | = 11 (A) | 11 | = 11 (B) | 16 | = [VCC] |
| 6 | = 10 (A) | | | | |

H253S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 10E' | 7 | = 1Y | 12 | = 2I2 |
| 2 | = S1 | 8 | = [GND] | 13 | = 2I3 |
| 3 | = 1I3 | 9 | = 2Y | 14 | = S0 |
| 4 | = 1I2 | 10 | = 2I0 | 15 | = 2OE' |
| 5 | = 1I1 | 11 | = 2I1 | 16 | = [VCC] |
| 6 | = 1I0 | | | | |

H257: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y | 12 | = 4Y |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y | 14 | = 4A |
| 4 | = 1Y | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

H258: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y' | 12 | = 4Y' |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y' | 14 | = 4A |
| 4 | = 1Y' | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

H259: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Q3 | 12 | = Q7 |
| 2 | = A1 | 8 | = [GND] | 13 | = D |
| 3 | = A2 | 9 | = Q4 | 14 | = LE |
| 4 | = Q0 | 10 | = Q5 | 15 | = MR' |
| 5 | = Q1 | 11 | = Q6 | 16 | = [VCC] |
| 6 | = Q2 | | | | |

H266: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

H273: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 8 | = D4 | 15 | = Q6 |
| 2 | = Q1 | 9 | = Q4 | 16 | = Q7 |
| 3 | = D1 | 10 | = [GND] | 17 | = D7 |
| 4 | = D2 | 11 | = CLK | 18 | = D8 |
| 5 | = Q2 | 12 | = Q5 | 19 | = Q8 |
| 6 | = Q3 | 13 | = D5 | 20 | = [VCC] |
| 7 | = D3 | 14 | = D6 | | |

H280: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = I6 | 6 | = ODD | 11 | = I3 |
| 2 | = I7 | 7 | = [GND] | 12 | = I4 |
| 3 | = (n/c) | 8 | = I0 | 13 | = I5 |
| 4 | = I8 | 9 | = I1 | 14 | = [VCC] |
| 5 | = EVEN | 10 | = I2 | | |

H283: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = S1 | 7 | = CIN | 12 | = A3 |
| 2 | = B1 | 8 | = [GND] | 13 | = S2 |
| 3 | = A1 | 9 | = COUT | 14 | = A2 |
| 4 | = S0 | 10 | = S3 | 15 | = B2 |
| 5 | = A0 | 11 | = B3 | 16 | = [VCC] |
| 6 | = B0 | | | | |

H297: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B | 7 | = ID/OUT | 12 | = ECPD |
| 2 | = A | 8 | = [GND] | 13 | = FY/A2 |
| 3 | = ENCTR | 9 | = FY/A1 | 14 | = D |
| 4 | = K/CLK | 10 | = FY/B | 15 | = C |
| 5 | = ID/CLK | 11 | = XORPD | 16 | = [VCC] |
| 6 | = D/U' | | | | |

H299: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = S0 | 8 | = Q0 | 15 | = I/05 |
| 2 | = OE1' | 9 | = MR' | 16 | = I/07 |
| 3 | = OE2' | 10 | = [GND] | 17 | = Q7 |
| 4 | = I/06 | 11 | = DS0 | 18 | = DS7 |
| 5 | = I/04 | 12 | = CLK | 19 | = S1 |
| 6 | = I/02 | 13 | = I/01 | 20 | = [VCC] |
| 7 | = I/00 | 14 | = I/03 | | |

H354: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D7 | 8 | = D0 | 15 | = OE1' |
| 2 | = D6 | 9 | = E' | 16 | = OE2' |
| 3 | = D5 | 10 | = [GND] | 17 | = OE3 |
| 4 | = D4 | 11 | = LE' | 18 | = Y' |
| 5 | = D3 | 12 | = S2 | 19 | = Y |
| 6 | = D2 | 13 | = S1 | 20 | = [VCC] |
| 7 | = D1 | 14 | = S0 | | |

H356: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D7 | 8 | = D0 | 15 | = OE1' |
| 2 | = D6 | 9 | = CLK | 16 | = OE2' |
| 3 | = D5 | 10 | = [GND] | 17 | = OE3 |
| 4 | = D4 | 11 | = LE' | 18 | = Y' |
| 5 | = D3 | 12 | = S2 | 19 | = Y |
| 6 | = D2 | 13 | = S1 | 20 | = [VCC] |
| 7 | = D1 | 14 | = S0 | | |

H365: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = Y (C) | 12 | = A (E) |
| 2 | = A (A) | 8 | = [GND] | 13 | = Y (F) |
| 3 | = Y (A) | 9 | = Y (D) | 14 | = A (F) |
| 4 | = A (B) | 10 | = A (D) | 15 | = OE2' |
| 5 | = Y (B) | 11 | = Y (E) | 16 | = [VCC] |
| 6 | = A (C) | | | | |

H365S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y |
| 3 | = 1Y | 9 | = 4Y | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y | 11 | = 5Y | 16 | = [VCC] |
| 6 | = 3A | | | | |

H366: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = Y' (C) | 12 | = A (E) |
| 2 | = A (A) | 8 | = [GND] | 13 | = Y' (F) |
| 3 | = Y' (A) | 9 | = Y' (D) | 14 | = A (F) |
| 4 | = A (B) | 10 | = A (D) | 15 | = OE2' |
| 5 | = Y' (B) | 11 | = Y' (E) | 16 | = [VCC] |
| 6 | = A (C) | | | | |

H366S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y' | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y' |
| 3 | = 1Y' | 9 | = 4Y' | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y' | 11 | = 5Y' | 16 | = [VCC] |
| 6 | = 3A | | | | |

H367: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y |
| 3 | = 1Y | 9 | = 4Y | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y | 11 | = 5Y | 16 | = [VCC] |
| 6 | = 3A | | | | |

H368: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y' | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y' |
| 3 | = 1Y' | 9 | = 4Y' | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y' | 11 | = 5Y' | 16 | = [VCC] |
| 6 | = 3A | | | | |

H373: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D3 | 15 | = O5 |
| 2 | = O0 | 9 | = O3 | 16 | = O6 |
| 3 | = D0 | 10 | = [GND] | 17 | = D6 |
| 4 | = D1 | 11 | = LE' | 18 | = D7 |
| 5 | = O1 | 12 | = O4 | 19 | = O7 |
| 6 | = O2 | 13 | = D4 | 20 | = [VCC] |
| 7 | = D2 | 14 | = D5 | | |

H374: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = 4D | 15 | = 6Q |
| 2 | = 1Q | 9 | = 4Q | 16 | = 7Q |
| 3 | = 1D | 10 | = [GND] | 17 | = 7D |
| 4 | = 2D | 11 | = CLK | 18 | = 8D |
| 5 | = 2Q | 12 | = 5Q | 19 | = 8Q |
| 6 | = 3Q | 13 | = 5D | 20 | = [VCC] |
| 7 | = 3D | 14 | = 6D | | |

H375: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D (A) | 7 | = D (B) | 12 | = EN (C,D) |
| 2 | = Q' (A) | 8 | = [GND] | 13 | = Q (D) |
| 3 | = Q (A) | 9 | = D (C) | 14 | = Q' (D) |
| 4 | = EN (A,B) | 10 | = Q' (C) | 15 | = D (D) |
| 5 | = Q (B) | 11 | = Q (C) | 16 | = [VCC] |
| 6 | = Q' (B) | | | | |

H375S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D1 | 7 | = D2 | 12 | = EN2 |
| 2 | = Q1' | 8 | = [GND] | 13 | = Q4 |
| 3 | = Q1 | 9 | = D3 | 14 | = Q4' |
| 4 | = EN1 | 10 | = Q3' | 15 | = D4 |
| 5 | = Q2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = Q2' | | | | |

H377: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = EN' | 8 | = 4D | 15 | = 6Q |
| 2 | = 1Q | 9 | = 4Q | 16 | = 7Q |
| 3 | = 1D | 10 | = [GND] | 17 | = 7D |
| 4 | = 2D | 11 | = CLK | 18 | = 8D |
| 5 | = 2Q | 12 | = 5Q | 19 | = 8Q |
| 6 | = 3Q | 13 | = 5D | 20 | = [VCC] |
| 7 | = 3D | 14 | = 6D | | |

H390: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 7 | = QD (A) | 12 | = B (B) |
| 2 | = CLR (A) | 8 | = [GND] | 13 | = QA (B) |
| 3 | = QA (A) | 9 | = QD (B) | 14 | = CLR (B) |
| 4 | = B (A) | 10 | = QC (B) | 15 | = A (B) |
| 5 | = QB (A) | 11 | = QB (B) | 16 | = [VCC] |
| 6 | = QC (A) | | | | |

H393: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 6 | = Q3 (A) | 11 | = Q0 (B) |
| 2 | = MR (A) | 7 | = [GND] | 12 | = MR (B) |
| 3 | = Q0 (A) | 8 | = Q3 (B) | 13 | = CLK (B) |
| 4 | = Q1 (A) | 9 | = Q2 (B) | 14 | = [VCC] |
| 5 | = Q2 (A) | 10 | = Q1 (B) | | |

H423: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A' (A) | 7 | = RXCX (B) | 12 | = Q' (B) |
| 2 | = B (A) | 8 | = [GND] | 13 | = Q (A) |
| 3 | = R' (A) | 9 | = A' (B) | 14 | = CX (A) |
| 4 | = Q' (A) | 10 | = B (B) | 15 | = RXCX (A) |
| 5 | = Q (B) | 11 | = R' (B) | 16 | = [VCC] |
| 6 | = CX (B) | | | | |

H533: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|-------------------|------------|-------------------|
| 1 | = OE ¹ | 8 | = D3 | 15 | = Q5 ¹ |
| 2 | = Q0 ¹ | 9 | = Q3 ¹ | 16 | = Q6 ¹ |
| 3 | = D0 | 10 | = [GND] | 17 | = D6 |
| 4 | = D1 | 11 | = LE ¹ | 18 | = D7 |
| 5 | = Q1 ¹ | 12 | = Q4 ¹ | 19 | = Q7 ¹ |
| 6 | = Q2 ¹ | 13 | = D4 | 20 | = [VCC] |
| 7 | = D2 | 14 | = D5 | | |

H534: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|-------------------|------------|-------------------|
| 1 | = OE ¹ | 8 | = D4 | 15 | = Q6 ¹ |
| 2 | = Q1 ¹ | 9 | = Q4 ¹ | 16 | = Q7 ¹ |
| 3 | = D1 | 10 | = [GND] | 17 | = D7 |
| 4 | = D2 | 11 | = CLK | 18 | = D8 |
| 5 | = Q2 ¹ | 12 | = Q5 ¹ | 19 | = Q8 ¹ |
| 6 | = Q3 ¹ | 13 | = D5 | 20 | = [VCC] |
| 7 | = D3 | 14 | = D6 | | |

H540: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|-------------------|------------|--------------------|
| 1 | = OE1 ¹ | 8 | = A6 | 15 | = Y3 ¹ |
| 2 | = A0 | 9 | = A7 | 16 | = Y2 ¹ |
| 3 | = A1 | 10 | = [GND] | 17 | = Y1 ¹ |
| 4 | = A2 | 11 | = Y7 ¹ | 18 | = Y0 ¹ |
| 5 | = A3 | 12 | = Y6 ¹ | 19 | = OE2 ¹ |
| 6 | = A4 | 13 | = Y5 ¹ | 20 | = [VCC] |
| 7 | = A5 | 14 | = Y4 ¹ | | |

H541: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|---------------|------------|--------------------|
| 1 | = OE1 ¹ | 8 | = A6 | 15 | = Y3 |
| 2 | = A0 | 9 | = A7 | 16 | = Y2 |
| 3 | = A1 | 10 | = [GND] | 17 | = Y1 |
| 4 | = A2 | 11 | = Y7 | 18 | = Y0 |
| 5 | = A3 | 12 | = Y6 | 19 | = OE2 ¹ |
| 6 | = A4 | 13 | = Y5 | 20 | = [VCC] |
| 7 | = A5 | 14 | = Y4 | | |

H563: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4' |
| 2 | = D0 | 9 | = D7 | 16 | = Q3' |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2' |
| 4 | = D2 | 11 | = LE' | 18 | = Q1' |
| 5 | = D3 | 12 | = Q7' | 19 | = Q0' |
| 6 | = D4 | 13 | = Q6' | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5' | | |

H564: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4' |
| 2 | = D0 | 9 | = D7 | 16 | = Q3' |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2' |
| 4 | = D2 | 11 | = CLK | 18 | = Q1' |
| 5 | = D3 | 12 | = Q7' | 19 | = Q0' |
| 6 | = D4 | 13 | = Q6' | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5' | | |

H573: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4 |
| 2 | = D0 | 9 | = D7 | 16 | = Q3 |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2 |
| 4 | = D2 | 11 | = LE' | 18 | = Q1 |
| 5 | = D3 | 12 | = Q7 | 19 | = Q0 |
| 6 | = D4 | 13 | = Q6 | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5 | | |

H574: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4 |
| 2 | = D0 | 9 | = D7 | 16 | = Q3 |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2 |
| 4 | = D2 | 11 | = CLK | 18 | = Q1 |
| 5 | = D3 | 12 | = Q7 | 19 | = Q0 |
| 6 | = D4 | 13 | = Q6 | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5 | | |

H583: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B1 | 7 | = S2 | 12 | = B0 |
| 2 | = B2 | 8 | = [GND] | 13 | = A0 |
| 3 | = B3 | 9 | = S3 | 14 | = A1 |
| 4 | = A3 | 10 | = S1 | 15 | = A2 |
| 5 | = CN | 11 | = S0 | 16 | = [VCC] |
| 6 | = CN+4 | | | | |

H597: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B | 7 | = H | 12 | = LT-CLK |
| 2 | = C | 8 | = [GND] | 13 | = SS-PL |
| 3 | = D | 9 | = QH | 14 | = SA |
| 4 | = E | 10 | = RESET | 15 | = A |
| 5 | = F | 11 | = SH-CLK | 16 | = [VCC] |
| 6 | = G | | | | |

H640: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|-------------------|
| 1 | = DIR | 8 | = A6 | 15 | = B3 |
| 2 | = A0 | 9 | = A7 | 16 | = B2 |
| 3 | = A1 | 10 | = [GND] | 17 | = B1 |
| 4 | = A2 | 11 | = B7 | 18 | = B0 |
| 5 | = A3 | 12 | = B6 | 19 | = OE ¹ |
| 6 | = A4 | 13 | = B5 | 20 | = [VCC] |
| 7 | = A5 | 14 | = B4 | | |

H643: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|-------------------|
| 1 | = DIR | 8 | = A6 | 15 | = B3 |
| 2 | = A0 | 9 | = A7 | 16 | = B2 |
| 3 | = A1 | 10 | = [GND] | 17 | = B1 |
| 4 | = A2 | 11 | = B7 | 18 | = B0 |
| 5 | = A3 | 12 | = B6 | 19 | = OE ¹ |
| 6 | = A4 | 13 | = B5 | 20 | = [VCC] |
| 7 | = A5 | 14 | = B4 | | |

H646: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CAB | 9 | = A5 | 17 | = B3 |
| 2 | = SAB | 10 | = A6 | 18 | = B2 |
| 3 | = DIR | 11 | = A7 | 19 | = B1 |
| 4 | = A0 | 12 | = [GND] | 20 | = B0 |
| 5 | = A1 | 13 | = B7 | 21 | = OE' |
| 6 | = A2 | 14 | = B6 | 22 | = SBA |
| 7 | = A3 | 15 | = B5 | 23 | = CBA |
| 8 | = A4 | 16 | = B4 | 24 | = [VCC] |

H648: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CAB | 9 | = A5 | 17 | = B3 |
| 2 | = SAB | 10 | = A6 | 18 | = B2 |
| 3 | = DIR | 11 | = A7 | 19 | = B1 |
| 4 | = A0 | 12 | = [GND] | 20 | = B0 |
| 5 | = A1 | 13 | = B7 | 21 | = OE' |
| 6 | = A2 | 14 | = B6 | 22 | = SBA |
| 7 | = A3 | 15 | = B5 | 23 | = CBA |
| 8 | = A4 | 16 | = B4 | 24 | = [VCC] |

H670: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D1 | 7 | = Q2 | 12 | = WE' |
| 2 | = D2 | 8 | = [GND] | 13 | = WA1 |
| 3 | = D3 | 9 | = Q1 | 14 | = WA0 |
| 4 | = RA1 | 10 | = Q0 | 15 | = D0 |
| 5 | = RA0 | 11 | = RE' | 16 | = [VCC] |
| 6 | = Q3 | | | | |

H688: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = E' | 8 | = A3 | 15 | = A6 |
| 2 | = A0 | 9 | = B3 | 16 | = B6 |
| 3 | = B0 | 10 | = [GND] | 17 | = A7 |
| 4 | = A1 | 11 | = A4 | 18 | = B7 |
| 5 | = B1 | 12 | = B4 | 19 | = Y |
| 6 | = A2 | 13 | = A5 | 20 | = [VCC] |
| 7 | = B2 | 14 | = B5 | | |

H4002: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = {n/c} | 11 | = INC (B) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = IND (B) |
| 3 | = INB (A) | 8 | = {n/c} | 13 | = OUTY (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

H4015: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (B) | 7 | = DATA (A) | 12 | = Q2 (B) |
| 2 | = Q4 (A) | 8 | = [GND] | 13 | = Q1 (B) |
| 3 | = Q3 (A) | 9 | = CLK (A) | 14 | = MR (B) |
| 4 | = Q2 (A) | 10 | = Q4 (B) | 15 | = DATA (B) |
| 5 | = Q1 (A) | 11 | = Q3 (B) | 16 | = [VCC] |
| 6 | = MR (A) | | | | |

H4016: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IO (A) | 6 | = CNTRL (C) | 11 | = IO (D) |
| 2 | = OI (A) | 7 | = [GND] | 12 | = CNTRL (D) |
| 3 | = OI (B) | 8 | = IO (C) | 13 | = CNTRL (A) |
| 4 | = IO (B) | 9 | = OI (C) | 14 | = [VCC] |
| 5 | = CNTRL (B) | 10 | = OI (D) | | |

H4017: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q5 | 7 | = Q3 | 12 | = TC |
| 2 | = Q1 | 8 | = [GND] | 13 | = CE' |
| 3 | = Q0 | 9 | = Q8 | 14 | = CLK |
| 4 | = Q2 | 10 | = Q4 | 15 | = MR |
| 5 | = Q6 | 11 | = Q9 | 16 | = [VCC] |
| 6 | = Q7 | | | | |

H4020: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q4 | 12 | = Q9 |
| 2 | = Q13 | 8 | = [GND] | 13 | = Q8 |
| 3 | = Q14 | 9 | = Q1 | 14 | = Q10 |
| 4 | = Q6 | 10 | = CLK | 15 | = Q11 |
| 5 | = Q5 | 11 | = MR | 16 | = [VCC] |
| 6 | = Q7 | | | | |

H4024: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 6 | = Q4 | 11 | = Q2 |
| 2 | = MR | 7 | = [GND] | 12 | = Q1 |
| 3 | = Q7 | 8 | = {n/c} | 13 | = {n/c} |
| 4 | = Q6 | 9 | = Q3 | 14 | = [VCC] |
| 5 | = Q5 | 10 | = {n/c} | | |

H4040: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q2 | 12 | = Q9 |
| 2 | = Q6 | 8 | = [GND] | 13 | = Q8 |
| 3 | = Q5 | 9 | = Q1 | 14 | = Q10 |
| 4 | = Q7 | 10 | = CLK | 15 | = Q11 |
| 5 | = Q4 | 11 | = MR | 16 | = [VCC] |
| 6 | = Q3 | | | | |

H4046: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P-PULSE | 7 | = C1-2 | 12 | = R2 |
| 2 | = P-COMP1 | 8 | = [GND] | 13 | = P-COMP2 |
| 3 | = COMP | 9 | = VCO-IN | 14 | = SIG-IN |
| 4 | = VCO-OUT | 10 | = DMOD | 15 | = ZENER |
| 5 | = INH | 11 | = R1 | 16 | = [VCC] |
| 6 | = C1-1 | | | | |

H4049: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 7 | = A (C) | 12 | = Y' (E) |
| 2 | = Y' (A) | 8 | = [GND] | 13 | = {n/c} |
| 3 | = A (A) | 9 | = A (D) | 14 | = A (F) |
| 4 | = Y' (B) | 10 | = Y' (D) | 15 | = Y' (F) |
| 5 | = A (B) | 11 | = A (E) | 16 | = {n/c} |
| 6 | = Y' (C) | | | | |

H4050: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 7 | = A (C) | 12 | = Y (E) |
| 2 | = Y (A) | 8 | = [GND] | 13 | = {n/c} |
| 3 | = A (A) | 9 | = A (D) | 14 | = A (F) |
| 4 | = Y (B) | 10 | = Y (D) | 15 | = Y (F) |
| 5 | = A (B) | 11 | = A (E) | 16 | = {n/c} |
| 6 | = Y (C) | | | | |

H4051: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A4 | 7 | = VEE | 12 | = A3 |
| 2 | = A6 | 8 | = [GND] | 13 | = A0 |
| 3 | = AO/I | 9 | = S2 | 14 | = A1 |
| 4 | = A7 | 10 | = S1 | 15 | = A2 |
| 5 | = A5 | 11 | = S0 | 16 | = [VCC] |
| 6 | = E' | | | | |

H4052: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B0 | 7 | = VEE | 12 | = A0 |
| 2 | = B2 | 8 | = [GND] | 13 | = AO/I |
| 3 | = BO/I | 9 | = S1 | 14 | = A1 |
| 4 | = B3 | 10 | = S0 | 15 | = A2 |
| 5 | = B1 | 11 | = A3 | 16 | = [VCC] |
| 6 | = E' | | | | |

H4053: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B1 | 7 | = VEE | 12 | = A0 |
| 2 | = B0 | 8 | = [GND] | 13 | = A1 |
| 3 | = C1 | 9 | = S2 | 14 | = AO/I |
| 4 | = CO/I | 10 | = S1 | 15 | = BO/I |
| 5 | = C0 | 11 | = S0 | 16 | = [VCC] |
| 6 | = E' | | | | |

H4059: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 9 | = J14 | 17 | = J10 |
| 2 | = LE | 10 | = J13 | 18 | = J9 |
| 3 | = J1 | 11 | = S2 | 19 | = J8 |
| 4 | = J2 | 12 | = [GND] | 20 | = J7 |
| 5 | = J3 | 13 | = S1 | 21 | = J6 |
| 6 | = J4 | 14 | = S0 | 22 | = J5 |
| 7 | = J16 | 15 | = J12 | 23 | = Q |
| 8 | = J15 | 16 | = J11 | 24 | = [VCC] |

H4060: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q4 | 12 | = MR |
| 2 | = Q13 | 8 | = [GND] | 13 | = Q9 |
| 3 | = Q14 | 9 | = FYO | 14 | = Q8 |
| 4 | = Q6 | 10 | = FYO' | 15 | = Q10 |
| 5 | = Q5 | 11 | = FYI | 16 | = [VCC] |
| 6 | = Q7 | | | | |

H4066: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IO (A) | 6 | = CNTRL (C) | 11 | = IO (D) |
| 2 | = OI (A) | 7 | = [GND] | 12 | = CNTRL (D) |
| 3 | = OI (B) | 8 | = IO (C) | 13 | = CNTRL (A) |
| 4 | = IO (B) | 9 | = OI (C) | 14 | = [VCC] |
| 5 | = CNTRL (B) | 10 | = OI (D) | | |

H4067: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUT/IN | 9 | = IO | 17 | = I14 |
| 2 | = I7 | 10 | = S0 | 18 | = I13 |
| 3 | = I6 | 11 | = S1 | 19 | = I12 |
| 4 | = I5 | 12 | = [GND] | 20 | = I11 |
| 5 | = I4 | 13 | = S3 | 21 | = I10 |
| 6 | = I3 | 14 | = S2 | 22 | = I9 |
| 7 | = I2 | 15 | = E' | 23 | = I8 |
| 8 | = I1 | 16 | = I15 | 24 | = [VCC] |

H4075: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INA (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INB (C) |
| 3 | = INA (B) | 8 | = INC (A) | 13 | = INC (C) |
| 4 | = INB (B) | 9 | = OUTY (A) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = OUTY (C) | | |

H4078: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = K | 6 | = {n/c} | 11 | = G |
| 2 | = A | 7 | = [GND] | 12 | = H |
| 3 | = B | 8 | = {n/c} | 13 | = Y |
| 4 | = C | 9 | = E | 14 | = [VCC] |
| 5 | = D | 10 | = F | | |

H4094: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STR | 7 | = Q3 | 12 | = Q6 |
| 2 | = DS | 8 | = [GND] | 13 | = Q5 |
| 3 | = CP | 9 | = Q5 | 14 | = Q4 |
| 4 | = Q0 | 10 | = QS' | 15 | = OE |
| 5 | = Q1 | 11 | = Q7 | 16 | = [VCC] |
| 6 | = Q2 | | | | |

H4316: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = X (A) | 7 | = EN | 12 | = Y (D) |
| 2 | = Y (A) | 8 | = [GND] | 13 | = X (D) |
| 3 | = Y (B) | 9 | = VEE | 14 | = CNTRL (D) |
| 4 | = X (B) | 10 | = X (C) | 15 | = CNTRL (A) |
| 5 | = CNTRL (B) | 11 | = Y (C) | 16 | = [VCC] |
| 6 | = CNTRL (C) | | | | |

H4316S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = XA | 7 | = EN | 12 | = YD |
| 2 | = YA | 8 | = [GND] | 13 | = XD |
| 3 | = YB | 9 | = VEE | 14 | = DC |
| 4 | = XB | 10 | = XC | 15 | = AC |
| 5 | = BC | 11 | = YC | 16 | = [VCC] |
| 6 | = CC | | | | |

H4351: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = X4 | 7 | = EN2 | 13 | = A |
| 2 | = X6 | 8 | = VEE | 14 | = X3 |
| 3 | = X | 9 | = [GND] | 15 | = X0 |
| 4 | = X7 | 10 | = LE | 16 | = X1 |
| 5 | = X5 | 11 | = C | 17 | = X2 |
| 6 | = EN1' | 12 | = B | 18 | = [VCC] |

H4352: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0 | 7 | = EN2 | 13 | = X3 |
| 2 | = Y2 | 8 | = VEE | 14 | = X0 |
| 3 | = Y | 9 | = [GND] | 15 | = X |
| 4 | = Y3 | 10 | = LE | 16 | = X1 |
| 5 | = Y1 | 11 | = B | 17 | = X2 |
| 6 | = EN1' | 12 | = A | 18 | = [VCC] |

H4353: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = YI | 7 | = EN1 | 13 | = A |
| 2 | = Y0 | 8 | = VEE | 14 | = X0 |
| 3 | = ZI | 9 | = [GND] | 15 | = XI |
| 4 | = Z | 10 | = LE | 16 | = X |
| 5 | = Z0 | 11 | = C | 17 | = Y |
| 6 | = EN2' | 12 | = B | 18 | = [VCC] |

H4510: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PE | 7 | = COUT' | 12 | = P2 |
| 2 | = Q4 | 8 | = [GND] | 13 | = P3 |
| 3 | = P4 | 9 | = RESET | 14 | = Q3 |
| 4 | = P1 | 10 | = U/D | 15 | = CLK |
| 5 | = CIN' | 11 | = Q2 | 16 | = [VCC] |
| 6 | = Q1 | | | | |

H4511: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INB | 7 | = INA | 12 | = B |
| 2 | = INC | 8 | = [GND] | 13 | = A |
| 3 | = LT' | 9 | = E | 14 | = G |
| 4 | = BL' | 10 | = D | 15 | = F |
| 5 | = LE' | 11 | = C | 16 | = [VCC] |
| 6 | = IND | | | | |

H4514: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | LE' | 9 | Y1 | 17 | Y9 |
| 2 | A0 | 10 | Y2 | 18 | Y8 |
| 3 | A1 | 11 | Y0 | 19 | Y11 |
| 4 | Y7 | 12 | [GND] | 20 | Y10 |
| 5 | Y6 | 13 | Y13 | 21 | A2 |
| 6 | Y5 | 14 | Y12 | 22 | A3 |
| 7 | Y4 | 15 | Y15 | 23 | E' |
| 8 | Y3 | 16 | Y14 | 24 | [VCC] |

H4515: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | LE' | 9 | Y1' | 17 | Y9' |
| 2 | A0 | 10 | Y2' | 18 | Y8' |
| 3 | A1 | 11 | Y0' | 19 | Y11' |
| 4 | Y7' | 12 | [GND] | 20 | Y10' |
| 5 | Y6' | 13 | Y13' | 21 | A2 |
| 6 | Y5' | 14 | Y12' | 22 | A3 |
| 7 | Y4' | 15 | Y15' | 23 | E' |
| 8 | Y3' | 16 | Y14' | 24 | [VCC] |

H4516: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | PE | 7 | COUT' | 12 | P2 |
| 2 | Q4 | 8 | [GND] | 13 | P3 |
| 3 | P4 | 9 | RESET | 14 | Q3 |
| 4 | P1 | 10 | U/D | 15 | CLK |
| 5 | CIN' | 11 | Q2 | 16 | [VCC] |
| 6 | Q1 | | | | |

H4518: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLK (A) | 7 | MR (A) | 12 | Q1 (B) |
| 2 | E (A) | 8 | [GND] | 13 | Q2 (B) |
| 3 | Q0 (A) | 9 | CLK (B) | 14 | Q3 (B) |
| 4 | Q1 (A) | 10 | E (B) | 15 | MR (B) |
| 5 | Q2 (A) | 11 | Q0 (B) | 16 | [VCC] |
| 6 | Q3 (A) | | | | |

H4520: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = MR (A) | 12 | = Q1 (B) |
| 2 | = E (A) | 8 | = [GND] | 13 | = Q2 (B) |
| 3 | = Q0 (A) | 9 | = CLK (B) | 14 | = Q3 (B) |
| 4 | = Q1 (A) | 10 | = E (B) | 15 | = MR (B) |
| 5 | = Q2 (A) | 11 | = Q0 (B) | 16 | = [VCC] |
| 6 | = Q3 (A) | | | | |

H4538: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CX (B) | 7 | = Q' (B) | 12 | = A (A) |
| 2 | = RXCX (B) | 8 | = [GND] | 13 | = R' (A) |
| 3 | = R' (B) | 9 | = Q' (A) | 14 | = RXCX (A) |
| 4 | = A (B) | 10 | = Q (A) | 15 | = CX (A) |
| 5 | = B' (B) | 11 | = B' (A) | 16 | = [VCC] |
| 6 | = Q (B) | | | | |

H7046: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = LOCK-SIG | 7 | = C1-2 | 12 | = R2 |
| 2 | = P-COMP1 | 8 | = [GND] | 13 | = P-COMP2 |
| 3 | = COMP | 9 | = VCO-IN | 14 | = SIG-IN |
| 4 | = VCO-OUT | 10 | = DMOD | 15 | = C2 |
| 5 | = INH | 11 | = R1 | 16 | = [VCC] |
| 6 | = C1-1 | | | | |

H7266: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

H40102: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CP | 7 | = P3 | 12 | = P6 |
| 2 | = MR' | 8 | = [GND] | 13 | = P7 |
| 3 | = TE' | 9 | = PL' | 14 | = TC' |
| 4 | = P0 | 10 | = P4 | 15 | = PE' |
| 5 | = P1 | 11 | = P5 | 16 | = [VCC] |
| 6 | = P2 | | | | |

H40103: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CP | 7 | = P3 | 12 | = P6 |
| 2 | = MR' | 8 | = [GND] | 13 | = P7 |
| 3 | = TE' | 9 | = PL' | 14 | = TC' |
| 4 | = P0 | 10 | = P4 | 15 | = PE' |
| 5 | = P1 | 11 | = P5 | 16 | = [VCC] |
| 6 | = P2 | | | | |

H40104: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE | 7 | = DSL | 12 | = Q3 |
| 2 | = DSR | 8 | = [GND] | 13 | = Q2 |
| 3 | = D0 | 9 | = S0 | 14 | = Q1 |
| 4 | = D1 | 10 | = S1 | 15 | = Q0 |
| 5 | = D2 | 11 | = CLK | 16 | = [VCC] |
| 6 | = D3 | | | | |

H40105: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 7 | = D3 | 12 | = Q1 |
| 2 | = DIR | 8 | = [GND] | 13 | = Q0 |
| 3 | = S1 | 9 | = MR | 14 | = DOR |
| 4 | = D0 | 10 | = Q3 | 15 | = S0 |
| 5 | = D1 | 11 | = Q2 | 16 | = [VCC] |
| 6 | = D2 | | | | |

HT00: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

HT02: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = INB (B) | 11 | = INA (D) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = INB (D) |
| 3 | = INB (A) | 8 | = INA (C) | 13 | = OUTY (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

HT03: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

HT04: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 6 | = O (C) | 11 | = A (E) |
| 2 | = O (A) | 7 | = [GND] | 12 | = O (F) |
| 3 | = A (B) | 8 | = O (D) | 13 | = A (F) |
| 4 | = O (B) | 9 | = A (D) | 14 | = [VCC] |
| 5 | = A (C) | 10 | = O (E) | | |

HT05: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 6 | = Y (C) | 11 | = A (E) |
| 2 | = Y (A) | 7 | = [GND] | 12 | = Y (F) |
| 3 | = A (B) | 8 | = Y (D) | 13 | = A (F) |
| 4 | = Y (B) | 9 | = A (D) | 14 | = [VCC] |
| 5 | = A (C) | 10 | = Y (E) | | |

HT08: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

HT10: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = OUTY (A) |
| 3 | = INA (B) | 8 | = OUTY (C) | 13 | = INC (A) |
| 4 | = INB (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = INB (C) | | |

HT11: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = OUTY (A) |
| 3 | = INA (B) | 8 | = OUTY (C) | 13 | = INC (A) |
| 4 | = INB (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = INB (C) | | |

HT14: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IN (A) | 6 | = OUT (C) | 11 | = IN (E) |
| 2 | = OUT (A) | 7 | = [GND] | 12 | = OUT (F) |
| 3 | = IN (B) | 8 | = OUT (D) | 13 | = IN (F) |
| 4 | = OUT (B) | 9 | = IN (D) | 14 | = [VCC] |
| 5 | = IN (C) | 10 | = OUT (E) | | |

HT20: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (A) | 11 | = {n/c} |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INC (B) |
| 3 | = {n/c} | 8 | = OUTY (B) | 13 | = IND (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

HT21: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (A) | 11 | = {n/c} |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INC (B) |
| 3 | = {n/c} | 8 | = OUTY (B) | 13 | = IND (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

HT27: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INC (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = OUTY (A) |
| 3 | = INA (B) | 8 | = OUTY (C) | 13 | = INC (A) |
| 4 | = INB (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = INB (C) | | |

HT30: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA | 6 | = INF | 11 | = ING |
| 2 | = INB | 7 | = [GND] | 12 | = INH |
| 3 | = INC | 8 | = OUTY' | 13 | = {n/c} |
| 4 | = IND | 9 | = {n/c} | 14 | = [VCC] |
| 5 | = INE | 10 | = {n/c} | | |

HT32: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

HT42: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0' | 7 | = Y6' | 12 | = A3 |
| 2 | = Y1' | 8 | = [GND] | 13 | = A2 |
| 3 | = Y2' | 9 | = Y7' | 14 | = A1 |
| 4 | = Y3' | 10 | = Y8' | 15 | = A0 |
| 5 | = Y4' | 11 | = Y9' | 16 | = [VCC] |
| 6 | = Y5' | | | | |

HT44: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = O0' | 7 | = O6' | 12 | = A3 |
| 2 | = O1' | 8 | = [GND] | 13 | = A2 |
| 3 | = O2' | 9 | = O7' | 14 | = A1 |
| 4 | = O3' | 10 | = O8' | 15 | = A0 |
| 5 | = O4' | 11 | = O9' | 16 | = [VCC] |
| 6 | = O5' | | | | |

HT51: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A1 | 6 | = Y2 | 11 | = F1 |
| 2 | = A2 | 7 | = [GND] | 12 | = B1 |
| 3 | = B2 | 8 | = Y1 | 13 | = C1 |
| 4 | = C2 | 9 | = D1 | 14 | = [VCC] |
| 5 | = D2 | 10 | = E1 | | |

HT73: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 6 | = CLR' (B) | 11 | = [GND] |
| 2 | = CLR' (A) | 7 | = J (B) | 12 | = Q (A) |
| 3 | = K (A) | 8 | = Q' (B) | 13 | = Q' (A) |
| 4 | = [VCC] | 9 | = Q (B) | 14 | = J (A) |
| 5 | = CLK (B) | 10 | = K (B) | | |

HT74: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' (A) | 6 | = Q' (A) | 11 | = CLK (B) |
| 2 | = D (A) | 7 | = [GND] | 12 | = D (B) |
| 3 | = CLK (A) | 8 | = Q' (B) | 13 | = CLR' (B) |
| 4 | = PR' (A) | 9 | = Q (B) | 14 | = [VCC] |
| 5 | = Q (A) | 10 | = PR' (B) | | |

HT75: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q' (A) | 7 | = D (D) | 12 | = [GND] |
| 2 | = D (A) | 8 | = Q' (D) | 13 | = G (A,B) |
| 3 | = D (B) | 9 | = Q (D) | 14 | = Q' (B) |
| 4 | = G (C,D) | 10 | = Q (C) | 15 | = Q (B) |
| 5 | = [VCC] | 11 | = Q' (C) | 16 | = Q (A) |
| 6 | = D (C) | | | | |

HT76: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = PR' (B) | 12 | = K (B) |
| 2 | = PR' (A) | 8 | = CLR' (B) | 13 | = [GND] |
| 3 | = CLR' (A) | 9 | = J (B) | 14 | = Q' (A) |
| 4 | = J (A) | 10 | = Q' (B) | 15 | = Q (A) |
| 5 | = [VCC] | 11 | = Q (B) | 16 | = K (A) |
| 6 | = CLK (B) | | | | |

HT85: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B3 | 7 | = A<B | 12 | = A1 |
| 2 | = IA<B | 8 | = [GND] | 13 | = A2 |
| 3 | = IA=B | 9 | = B0 | 14 | = B2 |
| 4 | = IA>B | 10 | = A0 | 15 | = A3 |
| 5 | = A>B | 11 | = B1 | 16 | = [VCC] |
| 6 | = A=B | | | | |

HT86: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

HT93: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CP1 | 6 | = {n/c} | 11 | = Q3 |
| 2 | = MR1 | 7 | = {n/c} | 12 | = Q0 |
| 3 | = MR2 | 8 | = Q2 | 13 | = {n/c} |
| 4 | = {n/c} | 9 | = Q1 | 14 | = CPO |
| 5 | = [VCC] | 10 | = [GND] | | |

HT107: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = J (A) | 6 | = Q' (B) | 11 | = K (B) |
| 2 | = Q' (A) | 7 | = [GND] | 12 | = CLK (A) |
| 3 | = Q (A) | 8 | = J (B) | 13 | = CLR' (A) |
| 4 | = K (A) | 9 | = CLK (B) | 14 | = [VCC] |
| 5 | = Q (B) | 10 | = CLR' (B) | | |

HT109: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLR' (A) | 7 | Q' (A) | 12 | CLK (B) |
| 2 | J (A) | 8 | [GND] | 13 | K' (B) |
| 3 | K' (A) | 9 | Q' (B) | 14 | J (B) |
| 4 | CLK (A) | 10 | Q (B) | 15 | CLR' (B) |
| 5 | PR' (A) | 11 | PR' (B) | 16 | [VCC] |
| 6 | Q (A) | | | | |

HT112: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLK (A) | 7 | Q' (B) | 12 | K (B) |
| 2 | K (A) | 8 | [GND] | 13 | CLK (B) |
| 3 | J (A) | 9 | Q (B) | 14 | CLR' (B) |
| 4 | PR' (A) | 10 | PR' (B) | 15 | CLR' (A) |
| 5 | Q (A) | 11 | J (B) | 16 | [VCC] |
| 6 | Q' (A) | | | | |

HT123: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | A' (A) | 7 | RXCX (B) | 12 | Q' (B) |
| 2 | B (A) | 8 | [GND] | 13 | Q (A) |
| 3 | R' (A) | 9 | A' (B) | 14 | CX (A) |
| 4 | Q' (A) | 10 | B (B) | 15 | RXCX (A) |
| 5 | Q (B) | 11 | R' (B) | 16 | [VCC] |
| 6 | CX (B) | | | | |

HT125: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | C (A) | 6 | Y (B) | 11 | Y (D) |
| 2 | A (A) | 7 | [GND] | 12 | A (D) |
| 3 | Y (A) | 8 | Y (C) | 13 | C (D) |
| 4 | C (B) | 9 | A (C) | 14 | [VCC] |
| 5 | A (B) | 10 | C (C) | | |

HT126: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = C (A) | 6 | = Y (B) | 11 | = Y (D) |
| 2 | = A (A) | 7 | = [GND] | 12 | = A (D) |
| 3 | = Y (A) | 8 | = Y (C) | 13 | = C (D) |
| 4 | = C (B) | 9 | = A (C) | 14 | = [VCC] |
| 5 | = A (B) | 10 | = C (C) | | |

HT132: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = OUTY (C) | 13 | = INB (D) |
| 4 | = INA (B) | 9 | = INA (C) | 14 | = [VCC] |
| 5 | = INB (B) | 10 | = INB (C) | | |

HT133: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA | 7 | = ING | 12 | = INJ |
| 2 | = INB | 8 | = [GND] | 13 | = INK |
| 3 | = INC | 9 | = OUTY' | 14 | = INL |
| 4 | = IND | 10 | = INH | 15 | = INM |
| 5 | = INE | 11 | = INI | 16 | = [VCC] |
| 6 | = INF | | | | |

HT137: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7' | 12 | = Y3' |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2' |
| 3 | = A2 | 9 | = Y6' | 14 | = Y1' |
| 4 | = LE' | 10 | = Y5' | 15 | = Y0' |
| 5 | = OE1' | 11 | = Y4' | 16 | = [VCC] |
| 6 | = OE0 | | | | |

HT138: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7' | 12 | = Y3' |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2' |
| 3 | = A2 | 9 | = Y6' | 14 | = Y1' |
| 4 | = E1' | 10 | = Y5' | 15 | = Y0' |
| 5 | = E2' | 11 | = Y4' | 16 | = [VCC] |
| 6 | = E3 | | | | |

HT139: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = EN' (A) | 7 | = Y3' (A) | 12 | = Y0' (B) |
| 2 | = SELA (A) | 8 | = [GND] | 13 | = SELB (B) |
| 3 | = SELB (A) | 9 | = Y3' (B) | 14 | = SELA (B) |
| 4 | = Y0' (A) | 10 | = Y2' (B) | 15 | = EN' (B) |
| 5 | = Y1' (A) | 11 | = Y1' (B) | 16 | = [VCC] |
| 6 | = Y2' (A) | | | | |

HT145: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 00' | 7 | = 06' | 12 | = A3 |
| 2 | = 01' | 8 | = [GND] | 13 | = A2 |
| 3 | = 02' | 9 | = 07' | 14 | = A1 |
| 4 | = 03' | 10 | = 08' | 15 | = A0 |
| 5 | = 04' | 11 | = 09' | 16 | = [VCC] |
| 6 | = 05' | | | | |

HT147: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 14 | 7 | = Y1' | 12 | = 12 |
| 2 | = 15 | 8 | = [GND] | 13 | = 13 |
| 3 | = 16 | 9 | = Y0' | 14 | = Y3' |
| 4 | = 17 | 10 | = 19 | 15 | = 10 |
| 5 | = 18 | 11 | = 11 | 16 | = [VCC] |
| 6 | = Y2' | | | | |

HT151: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D3 | 7 | = OE' | 12 | = D7 |
| 2 | = D2 | 8 | = [GND] | 13 | = D6 |
| 3 | = D1 | 9 | = A2 | 14 | = D5 |
| 4 | = D0 | 10 | = A1 | 15 | = D4 |
| 5 | = Y | 11 | = A0 | 16 | = [VCC] |
| 6 | = Y' | | | | |

HT153: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' (A) | 7 | = Y (A) | 12 | = C2 (B) |
| 2 | = A1 | 8 | = [GND] | 13 | = C3 (B) |
| 3 | = C3 (A) | 9 | = Y (B) | 14 | = A0 |
| 4 | = C2 (A) | 10 | = C0 (B) | 15 | = OE' (B) |
| 5 | = C1 (A) | 11 | = C1 (B) | 16 | = [VCC] |
| 6 | = C0 (A) | | | | |

HT153S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 1OE' | 7 | = 1Y | 12 | = 2C2 |
| 2 | = A1 | 8 | = [GND] | 13 | = 2C3 |
| 3 | = 1C3 | 9 | = 2Y | 14 | = A0 |
| 4 | = 1C2 | 10 | = 2C0 | 15 | = 2OE' |
| 5 | = 1C1 | 11 | = 2C1 | 16 | = [VCC] |
| 6 | = 1C0 | | | | |

HT154: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0' | 9 | = Y8' | 17 | = Y15' |
| 2 | = Y1' | 10 | = Y9' | 18 | = E1' |
| 3 | = Y2' | 11 | = Y10' | 19 | = E2' |
| 4 | = Y3' | 12 | = [GND] | 20 | = A3 |
| 5 | = Y4' | 13 | = Y11' | 21 | = A2 |
| 6 | = Y5' | 14 | = Y12' | 22 | = A1 |
| 7 | = Y6' | 15 | = Y13' | 23 | = A0 |
| 8 | = Y7' | 16 | = Y14' | 24 | = [VCC] |

HT157: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = Y (B) | 12 | = Y (D) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (D) |
| 3 | = B (A) | 9 | = Y (C) | 14 | = A (D) |
| 4 | = Y (A) | 10 | = B (C) | 15 | = OE' |
| 5 | = A (B) | 11 | = A (C) | 16 | = [VCC] |
| 6 | = B (B) | | | | |

HT157S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y | 12 | = 4Y |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y | 14 | = 4A |
| 4 | = 1Y | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

HT158: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = Y' (B) | 12 | = Y' (D) |
| 2 | = A (A) | 8 | = [GND] | 13 | = B (D) |
| 3 | = B (A) | 9 | = Y' (C) | 14 | = A (D) |
| 4 | = Y' (A) | 10 | = B (C) | 15 | = OE' |
| 5 | = A (B) | 11 | = A (C) | 16 | = [VCC] |
| 6 | = B (B) | | | | |

HT158S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y' | 12 | = 4Y' |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y' | 14 | = 4A |
| 4 | = 1Y' | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

HT160: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

HT161: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

HT162: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

HT163: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = PE | 12 | = Q2 |
| 2 | = CP | 8 | = [GND] | 13 | = Q1 |
| 3 | = P0 | 9 | = SPE' | 14 | = Q0 |
| 4 | = P1 | 10 | = TE | 15 | = TC |
| 5 | = P2 | 11 | = Q3 | 16 | = [VCC] |
| 6 | = P3 | | | | |

HT164: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A | 6 | = QD | 11 | = QF |
| 2 | = B | 7 | = [GND] | 12 | = QG |
| 3 | = QA | 8 | = CLK | 13 | = QH |
| 4 | = QB | 9 | = CLR' | 14 | = [VCC] |
| 5 | = QC | 10 | = QE | | |

HT165: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PL' | 7 | = Q7' | 12 | = D1 |
| 2 | = CLK | 8 | = [GND] | 13 | = D2 |
| 3 | = D4 | 9 | = Q7 | 14 | = D3 |
| 4 | = D5 | 10 | = DS | 15 | = CE' |
| 5 | = D6 | 11 | = D0 | 16 | = [VCC] |
| 6 | = D7 | | | | |

HT166: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DS | 7 | = CLK | 12 | = D6 |
| 2 | = D0 | 8 | = [GND] | 13 | = Q7 |
| 3 | = D1 | 9 | = MR' | 14 | = D7 |
| 4 | = D2 | 10 | = D4 | 15 | = PE' |
| 5 | = D3 | 11 | = D5 | 16 | = [VCC] |
| 6 | = CE' | | | | |

HT173: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = CLK | 12 | = D2 |
| 2 | = OE2' | 8 | = [GND] | 13 | = D1 |
| 3 | = Q0 | 9 | = E1' | 14 | = D0 |
| 4 | = Q1 | 10 | = E2' | 15 | = MR |
| 5 | = Q2 | 11 | = D3 | 16 | = [VCC] |
| 6 | = Q3 | | | | |

HT174: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLR' | 7 | Q (C) | 12 | Q (E) |
| 2 | Q (A) | 8 | [GND] | 13 | D (E) |
| 3 | D (A) | 9 | CLK | 14 | D (F) |
| 4 | D (B) | 10 | Q (D) | 15 | Q (F) |
| 5 | Q (B) | 11 | D (D) | 16 | [VCC] |
| 6 | D (C) | | | | |

HT174S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLR' | 7 | Q3 | 12 | Q5 |
| 2 | Q1 | 8 | [GND] | 13 | D5 |
| 3 | D1 | 9 | CLK | 14 | D6 |
| 4 | D2 | 10 | Q4 | 15 | Q6 |
| 5 | Q2 | 11 | D4 | 16 | [VCC] |
| 6 | D3 | | | | |

HT175: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLR' | 7 | Q (B) | 12 | D (C) |
| 2 | Q (A) | 8 | [GND] | 13 | D (D) |
| 3 | Q' (A) | 9 | CLK | 14 | Q' (D) |
| 4 | D (A) | 10 | Q (C) | 15 | Q (D) |
| 5 | D (B) | 11 | Q' (C) | 16 | [VCC] |
| 6 | Q' (B) | | | | |

HT175S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | CLR' | 7 | Q2 | 12 | D3 |
| 2 | Q1 | 8 | [GND] | 13 | D4 |
| 3 | Q1' | 9 | CLK | 14 | Q4' |
| 4 | D1 | 10 | Q3 | 15 | Q4 |
| 5 | D2 | 11 | Q3' | 16 | [VCC] |
| 6 | Q2' | | | | |

HT181: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|---------------------|------------|---------------|
| 1 | = B0 | 9 | = F0 | 17 | = G |
| 2 | = A0 | 10 | = F1 | 18 | = B3 |
| 3 | = S3 | 11 | = F2 | 19 | = A3 |
| 4 | = S2 | 12 | = [GND] | 20 | = B2 |
| 5 | = S1 | 13 | = F3 | 21 | = A2 |
| 6 | = S0 | 14 | = A=B | 22 | = B1 |
| 7 | = CN ¹ | 15 | = P | 23 | = A1 |
| 8 | = M | 16 | = CN+4 ¹ | 24 | = [VCC] |

HT182: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|-------------------|------------|-------------------|
| 1 | = G1 ¹ | 7 | = P7 ¹ | 12 | = CN+X |
| 2 | = P1 ¹ | 8 | = [GND] | 13 | = CN |
| 3 | = G0 ¹ | 9 | = CN+Z | 14 | = G2 ¹ |
| 4 | = P0 ¹ | 10 | = G ¹ | 15 | = P2 ¹ |
| 5 | = G3 ¹ | 11 | = CN+Y | 16 | = [VCC] |
| 6 | = P3 ¹ | | | | |

HT190: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|-------------------|------------|-------------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TC |
| 2 | = Q1 | 8 | = [GND] | 13 | = RC ¹ |
| 3 | = Q0 | 9 | = P3 | 14 | = CP |
| 4 | = CE ¹ | 10 | = P2 | 15 | = P0 |
| 5 | = D/U ¹ | 11 | = PL ¹ | 16 | = [VCC] |
| 6 | = Q2 | | | | |

HT191: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|-------------------|------------|-------------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TC |
| 2 | = Q1 | 8 | = [GND] | 13 | = RC ¹ |
| 3 | = Q0 | 9 | = P3 | 14 | = CP |
| 4 | = CE ¹ | 10 | = P2 | 15 | = P0 |
| 5 | = D/U ¹ | 11 | = PL ¹ | 16 | = [VCC] |
| 6 | = Q2 | | | | |

HT192: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TCU' |
| 2 | = Q1 | 8 | = [GND] | 13 | = TCD' |
| 3 | = Q0 | 9 | = P3 | 14 | = MR |
| 4 | = CPD | 10 | = P2 | 15 | = P0 |
| 5 | = CPU | 11 | = PL' | 16 | = [VCC] |
| 6 | = Q2 | | | | |

HT193: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P1 | 7 | = Q3 | 12 | = TCU' |
| 2 | = Q1 | 8 | = [GND] | 13 | = TCD' |
| 3 | = Q0 | 9 | = P3 | 14 | = MR |
| 4 | = CPD | 10 | = P2 | 15 | = P0 |
| 5 | = CPU | 11 | = PL' | 16 | = [VCC] |
| 6 | = Q2 | | | | |

HT194: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 7 | = SL | 12 | = QD |
| 2 | = SR | 8 | = [GND] | 13 | = QC |
| 3 | = A | 9 | = S0 | 14 | = QB |
| 4 | = B | 10 | = S1 | 15 | = QH |
| 5 | = C | 11 | = CLK | 16 | = [VCC] |
| 6 | = D | | | | |

HT195: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = MR' | 7 | = D3 | 12 | = Q3 |
| 2 | = J | 8 | = [GND] | 13 | = Q2 |
| 3 | = K' | 9 | = PE' | 14 | = Q1 |
| 4 | = D0 | 10 | = CLK | 15 | = Q0 |
| 5 | = D1 | 11 | = Q3' | 16 | = [VCC] |
| 6 | = D2 | | | | |

HT221: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A' (A) | 7 | = RXCX (B) | 12 | = Q' (B) |
| 2 | = B (A) | 8 | = [GND] | 13 | = Q (A) |
| 3 | = R' (A) | 9 | = A' (B) | 14 | = CX (A) |
| 4 | = Q' (A) | 10 | = B (B) | 15 | = RXCX (A) |
| 5 | = Q (B) | 11 | = R' (B) | 16 | = [VCC] |
| 6 | = CX (B) | | | | |

HT237: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7 | 12 | = Y3 |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2 |
| 3 | = A2 | 9 | = Y6 | 14 | = Y1 |
| 4 | = LE ¹ | 10 | = Y5 | 15 | = Y0 |
| 5 | = OE1 ¹ | 11 | = Y4 | 16 | = [VCC] |
| 6 | = OE0 | | | | |

HT238: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Y7 | 12 | = Y3 |
| 2 | = A1 | 8 | = [GND] | 13 | = Y2 |
| 3 | = A2 | 9 | = Y6 | 14 | = Y1 |
| 4 | = E1 ¹ | 10 | = Y5 | 15 | = Y0 |
| 5 | = E2 ¹ | 11 | = Y4 | 16 | = [VCC] |
| 6 | = E3 | | | | |

HT240: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-----------------------|------------|-----------------------|------------|-----------------------|
| 1 | = OE ¹ (A) | 8 | = A3 (A) | 15 | = A2 (B) |
| 2 | = A0 (A) | 9 | = Y0 ¹ (B) | 16 | = Y1 ¹ (A) |
| 3 | = Y3 ¹ (B) | 10 | = [GND] | 17 | = A3 (B) |
| 4 | = A1 (A) | 11 | = A0 (B) | 18 | = Y0 ¹ (A) |
| 5 | = Y2 ¹ (B) | 12 | = Y3 ¹ (A) | 19 | = OE ¹ (B) |
| 6 | = A2 (A) | 13 | = A1 (B) | 20 | = [VCC] |
| 7 | = Y1 ¹ (B) | 14 | = Y2 ¹ (A) | | |

HT240S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 8 | = 1A3 | 15 | = 2A2 |
| 2 | = 1A0 | 9 | = 2Y0' | 16 | = 1Y1' |
| 3 | = 2Y3' | 10 | = [GND] | 17 | = 2A3 |
| 4 | = 1A1 | 11 | = 2A0 | 18 | = 1Y0' |
| 5 | = 2Y2' | 12 | = 1Y3' | 19 | = OE2' |
| 6 | = 1A2 | 13 | = 2A1 | 20 | = [VCC] |
| 7 | = 2Y1' | 14 | = 1Y2' | | |

HT241: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 1G' | 8 | = 1A4 | 15 | = 2A3 |
| 2 | = 1A1 | 9 | = 2Y1 | 16 | = 1Y2 |
| 3 | = 2Y4 | 10 | = [GND] | 17 | = 2A4 |
| 4 | = 1A2 | 11 | = 2A1 | 18 | = 1Y1 |
| 5 | = 2Y3 | 12 | = 1Y4 | 19 | = 2G |
| 6 | = 1A3 | 13 | = 2A2 | 20 | = [VCC] |
| 7 | = 2Y2 | 14 | = 1Y3 | | |

HT242: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OEB' | 6 | = A3 | 11 | = B0 |
| 2 | = {n/c} | 7 | = [GND] | 12 | = {n/c} |
| 3 | = A0 | 8 | = B3 | 13 | = OEA |
| 4 | = A1 | 9 | = B2 | 14 | = [VCC] |
| 5 | = A2 | 10 | = B1 | | |

HT243: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OEB' | 6 | = A3 | 11 | = B0 |
| 2 | = {n/c} | 7 | = [GND] | 12 | = {n/c} |
| 3 | = A0 | 8 | = B3 | 13 | = OEA |
| 4 | = A1 | 9 | = B2 | 14 | = [VCC] |
| 5 | = A2 | 10 | = B1 | | |

HT244: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' (A) | 8 | = A3 (A) | 15 | = A2 (B) |
| 2 | = A0 (A) | 9 | = Y0 (B) | 16 | = Y1 (A) |
| 3 | = Y3 (B) | 10 | = [GND] | 17 | = A3 (B) |
| 4 | = A1 (A) | 11 | = A0 (B) | 18 | = Y0 (A) |
| 5 | = Y2 (B) | 12 | = Y3 (A) | 19 | = OE' (B) |
| 6 | = A2 (A) | 13 | = A1 (B) | 20 | = [VCC] |
| 7 | = Y1 (B) | 14 | = Y2 (A) | | |

HT244S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 8 | = 1A3 | 15 | = 2A2 |
| 2 | = 1A0 | 9 | = 2Y0 | 16 | = 1Y1 |
| 3 | = 2Y3 | 10 | = [GND] | 17 | = 2A3 |
| 4 | = 1A1 | 11 | = 2A0 | 18 | = 1Y0 |
| 5 | = 2Y2 | 12 | = 1Y3 | 19 | = OE2' |
| 6 | = 1A2 | 13 | = 2A1 | 20 | = [VCC] |
| 7 | = 2Y1 | 14 | = 1Y2 | | |

HT245: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DIR | 8 | = A7 | 15 | = B4 |
| 2 | = A1 | 9 | = A8 | 16 | = B3 |
| 3 | = A2 | 10 | = [GND] | 17 | = B2 |
| 4 | = A3 | 11 | = B8 | 18 | = B1 |
| 5 | = A4 | 12 | = B7 | 19 | = OE' |
| 6 | = A5 | 13 | = B6 | 20 | = [VCC] |
| 7 | = A6 | 14 | = B5 | | |

HT251: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D3 | 7 | = OE' | 12 | = D7 |
| 2 | = D2 | 8 | = [GND] | 13 | = D6 |
| 3 | = D1 | 9 | = A2 | 14 | = D5 |
| 4 | = D0 | 10 | = A1 | 15 | = D4 |
| 5 | = Y | 11 | = A0 | 16 | = [VCC] |
| 6 | = Y' | | | | |

HT253: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' (A) | 7 | = Y (A) | 12 | = 12 (B) |
| 2 | = S1 | 8 | = [GND] | 13 | = 13 (B) |
| 3 | = 13 (A) | 9 | = Y (B) | 14 | = S0 |
| 4 | = 12 (A) | 10 | = IO (B) | 15 | = OE' (B) |
| 5 | = 11 (A) | 11 | = 11 (B) | 16 | = [VCC] |
| 6 | = IO (A) | | | | |

HT253S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = 10E' | 7 | = 1Y | 12 | = 212 |
| 2 | = S1 | 8 | = [GND] | 13 | = 213 |
| 3 | = 113 | 9 | = 2Y | 14 | = S0 |
| 4 | = 112 | 10 | = 210 | 15 | = 20E' |
| 5 | = 111 | 11 | = 211 | 16 | = [VCC] |
| 6 | = 110 | | | | |

HT257: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y | 12 | = 4Y |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y | 14 | = 4A |
| 4 | = 1Y | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

HT258: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = SEL | 7 | = 2Y' | 12 | = 4Y' |
| 2 | = 1A | 8 | = [GND] | 13 | = 4B |
| 3 | = 1B | 9 | = 3Y' | 14 | = 4A |
| 4 | = 1Y' | 10 | = 3B | 15 | = OE' |
| 5 | = 2A | 11 | = 3A | 16 | = [VCC] |
| 6 | = 2B | | | | |

HT259: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A0 | 7 | = Q3 | 12 | = Q7 |
| 2 | = A1 | 8 | = [GND] | 13 | = D |
| 3 | = A2 | 9 | = Q4 | 14 | = LE |
| 4 | = Q0 | 10 | = Q5 | 15 | = MR' |
| 5 | = Q1 | 11 | = Q6 | 16 | = [VCC] |
| 6 | = Q2 | | | | |

HT266: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

HT273: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLR' | 8 | = D4 | 15 | = Q6 |
| 2 | = Q1 | 9 | = Q4 | 16 | = Q7 |
| 3 | = D1 | 10 | = [GND] | 17 | = D7 |
| 4 | = D2 | 11 | = CLK | 18 | = D8 |
| 5 | = Q2 | 12 | = Q5 | 19 | = Q8 |
| 6 | = Q3 | 13 | = D5 | 20 | = [VCC] |
| 7 | = D3 | 14 | = D6 | | |

HT280: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = I6 | 6 | = ODD | 11 | = I3 |
| 2 | = I7 | 7 | = [GND] | 12 | = I4 |
| 3 | = {n/c} | 8 | = I0 | 13 | = I5 |
| 4 | = I8 | 9 | = I1 | 14 | = [VCC] |
| 5 | = EVEN | 10 | = I2 | | |

HT283: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | S1 | 7 = | CIN | 12 = | A3 |
| 2 = | B1 | 8 = | [GND] | 13 = | S2 |
| 3 = | A1 | 9 = | COU | 14 = | A2 |
| 4 = | S0 | 10 = | S3 | 15 = | B2 |
| 5 = | A0 | 11 = | B3 | 16 = | [VCC] |
| 6 = | B0 | | | | |

HT297: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | B | 7 = | ID/OUT | 12 = | ECPD |
| 2 = | A | 8 = | [GND] | 13 = | FY/A2 |
| 3 = | ENCTR | 9 = | FY/A1 | 14 = | D |
| 4 = | K/CLK | 10 = | FY/B | 15 = | C |
| 5 = | ID/CLK | 11 = | XORPD | 16 = | [VCC] |
| 6 = | D/U' | | | | |

HT299: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | S0 | 8 = | Q0 | 15 = | I/05 |
| 2 = | OE1' | 9 = | MR' | 16 = | I/07 |
| 3 = | OE2' | 10 = | [GND] | 17 = | Q7 |
| 4 = | I/06 | 11 = | DS0 | 18 = | DS7 |
| 5 = | I/04 | 12 = | CLK | 19 = | S1 |
| 6 = | I/02 | 13 = | I/01 | 20 = | [VCC] |
| 7 = | I/00 | 14 = | I/03 | | |

HT354: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 = | D7 | 8 = | D0 | 15 = | OE1' |
| 2 = | D6 | 9 = | E' | 16 = | OE2' |
| 3 = | D5 | 10 = | [GND] | 17 = | OE3 |
| 4 = | D4 | 11 = | LE' | 18 = | Y' |
| 5 = | D3 | 12 = | S2 | 19 = | Y |
| 6 = | D2 | 13 = | S1 | 20 = | [VCC] |
| 7 = | D1 | 14 = | S0 | | |

HT356: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D7 | 8 | = D0 | 15 | = OE1' |
| 2 | = D6 | 9 | = CLK | 16 | = OE2' |
| 3 | = D5 | 10 | = [GND] | 17 | = OE3 |
| 4 | = D4 | 11 | = LE' | 18 | = Y' |
| 5 | = D3 | 12 | = S2 | 19 | = Y |
| 6 | = D2 | 13 | = S1 | 20 | = [VCC] |
| 7 | = D1 | 14 | = S0 | | |

HT365: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = Y (C) | 12 | = A (E) |
| 2 | = A (A) | 8 | = [GND] | 13 | = Y (F) |
| 3 | = Y (A) | 9 | = Y (D) | 14 | = A (F) |
| 4 | = A (B) | 10 | = A (D) | 15 | = OE2' |
| 5 | = Y (B) | 11 | = Y (E) | 16 | = [VCC] |
| 6 | = A (C) | | | | |

HT365S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y |
| 3 | = 1Y | 9 | = 4Y | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y | 11 | = 5Y | 16 | = [VCC] |
| 6 | = 3A | | | | |

HT366: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = Y' (C) | 12 | = A (E) |
| 2 | = A (A) | 8 | = [GND] | 13 | = Y' (F) |
| 3 | = Y' (A) | 9 | = Y' (D) | 14 | = A (F) |
| 4 | = A (B) | 10 | = A (D) | 15 | = OE2' |
| 5 | = Y' (B) | 11 | = Y' (E) | 16 | = [VCC] |
| 6 | = A (C) | | | | |

HT366S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y' | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y' |
| 3 | = 1Y' | 9 | = 4Y' | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y' | 11 | = 5Y' | 16 | = [VCC] |
| 6 | = 3A | | | | |

HT367: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y |
| 3 | = 1Y | 9 | = 4Y | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y | 11 | = 5Y | 16 | = [VCC] |
| 6 | = 3A | | | | |

HT368: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE1' | 7 | = 3Y' | 12 | = 5A |
| 2 | = 1A | 8 | = [GND] | 13 | = 6Y' |
| 3 | = 1Y' | 9 | = 4Y' | 14 | = 6A |
| 4 | = 2A | 10 | = 4A | 15 | = OE2' |
| 5 | = 2Y' | 11 | = 5Y' | 16 | = [VCC] |
| 6 | = 3A | | | | |

HT373: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D3 | 15 | = O5 |
| 2 | = O0 | 9 | = O3 | 16 | = O6 |
| 3 | = D0 | 10 | = [GND] | 17 | = D6 |
| 4 | = D1 | 11 | = LE' | 18 | = D7 |
| 5 | = O1 | 12 | = O4 | 19 | = O7 |
| 6 | = O2 | 13 | = D4 | 20 | = [VCC] |
| 7 | = D2 | 14 | = D5 | | |

HT374: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | OE' | 8 | 4D | 15 | 6Q |
| 2 | 1Q | 9 | 4Q | 16 | 7Q |
| 3 | 1D | 10 | [GND] | 17 | 7D |
| 4 | 2D | 11 | CLK | 18 | 8D |
| 5 | 2Q | 12 | 5Q | 19 | 8Q |
| 6 | 3Q | 13 | 5D | 20 | [VCC] |
| 7 | 3D | 14 | 6D | | |

HT375: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | D (A) | 7 | D (B) | 12 | EN (C,D) |
| 2 | Q' (A) | 8 | [GND] | 13 | Q (D) |
| 3 | Q (A) | 9 | D (C) | 14 | Q' (D) |
| 4 | EN (A,B) | 10 | Q' (C) | 15 | D (D) |
| 5 | Q (B) | 11 | Q (C) | 16 | [VCC] |
| 6 | Q' (B) | | | | |

HT375S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | D1 | 7 | D2 | 12 | EN2 |
| 2 | Q1' | 8 | [GND] | 13 | Q4 |
| 3 | Q1 | 9 | D3 | 14 | Q4' |
| 4 | EN1 | 10 | Q3' | 15 | D4 |
| 5 | Q2 | 11 | Q3 | 16 | [VCC] |
| 6 | Q2' | | | | |

HT377: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | EN' | 8 | 4D | 15 | 6Q |
| 2 | 1Q | 9 | 4Q | 16 | 7Q |
| 3 | 1D | 10 | [GND] | 17 | 7D |
| 4 | 2D | 11 | CLK | 18 | 8D |
| 5 | 2Q | 12 | 5Q | 19 | 8Q |
| 6 | 3Q | 13 | 5D | 20 | [VCC] |
| 7 | 3D | 14 | 6D | | |

HT390: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A (A) | 7 | = QD (A) | 12 | = B (B) |
| 2 | = CLR (A) | 8 | = [GND] | 13 | = QA (B) |
| 3 | = QA (A) | 9 | = QD (B) | 14 | = CLR (B) |
| 4 | = B (A) | 10 | = QC (B) | 15 | = A (B) |
| 5 | = QB (A) | 11 | = QB (B) | 16 | = [VCC] |
| 6 | = QC (A) | | | | |

HT393: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 6 | = Q3 (A) | 11 | = Q0 (B) |
| 2 | = MR (A) | 7 | = [GND] | 12 | = MR (B) |
| 3 | = Q0 (A) | 8 | = Q3 (B) | 13 | = CLK (B) |
| 4 | = Q1 (A) | 9 | = Q2 (B) | 14 | = [VCC] |
| 5 | = Q2 (A) | 10 | = Q1 (B) | | |

HT423: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A' (A) | 7 | = RXCX (B) | 12 | = Q' (B) |
| 2 | = B (A) | 8 | = [GND] | 13 | = Q (A) |
| 3 | = R' (A) | 9 | = A' (B) | 14 | = CX (A) |
| 4 | = Q' (A) | 10 | = B (B) | 15 | = RXCX (A) |
| 5 | = Q (B) | 11 | = R' (B) | 16 | = [VCC] |
| 6 | = CX (B) | | | | |

HT533: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D3 | 15 | = Q5' |
| 2 | = Q0' | 9 | = Q3' | 16 | = Q6' |
| 3 | = D0 | 10 | = [GND] | 17 | = D6 |
| 4 | = D1 | 11 | = LE' | 18 | = D7 |
| 5 | = Q1' | 12 | = Q4' | 19 | = Q7' |
| 6 | = Q2' | 13 | = D4 | 20 | = [VCC] |
| 7 | = D2 | 14 | = D5 | | |

HT534: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|-------------------|------------|-------------------|
| 1 | = OE ¹ | 8 | = D4 | 15 | = Q6 ¹ |
| 2 | = Q1 ¹ | 9 | = Q4 ¹ | 16 | = Q7 ¹ |
| 3 | = D1 | 10 | = [GND] | 17 | = D7 |
| 4 | = D2 | 11 | = CLK | 18 | = D8 |
| 5 | = Q2 ¹ | 12 | = Q5 ¹ | 19 | = Q8 ¹ |
| 6 | = Q3 ¹ | 13 | = D5 | 20 | = [VCC] |
| 7 | = D3 | 14 | = D6 | | |

HT540: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|-------------------|------------|--------------------|
| 1 | = OE1 ¹ | 8 | = A6 | 15 | = Y3 ¹ |
| 2 | = A0 | 9 | = A7 | 16 | = Y2 ¹ |
| 3 | = A1 | 10 | = [GND] | 17 | = Y1 ¹ |
| 4 | = A2 | 11 | = Y7 ¹ | 18 | = Y0 ¹ |
| 5 | = A3 | 12 | = Y6 ¹ | 19 | = OE2 ¹ |
| 6 | = A4 | 13 | = Y5 ¹ | 20 | = [VCC] |
| 7 | = A5 | 14 | = Y4 ¹ | | |

HT541: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|--------------------|------------|---------------|------------|--------------------|
| 1 | = OE1 ¹ | 8 | = A6 | 15 | = Y3 |
| 2 | = A0 | 9 | = A7 | 16 | = Y2 |
| 3 | = A1 | 10 | = [GND] | 17 | = Y1 |
| 4 | = A2 | 11 | = Y7 | 18 | = Y0 |
| 5 | = A3 | 12 | = Y6 | 19 | = OE2 ¹ |
| 6 | = A4 | 13 | = Y5 | 20 | = [VCC] |
| 7 | = A5 | 14 | = Y4 | | |

HT563: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|-------------------|------------|-------------------|------------|-------------------|
| 1 | = OE ¹ | 8 | = D6 | 15 | = Q4 ¹ |
| 2 | = D0 | 9 | = D7 | 16 | = Q3 ¹ |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2 ¹ |
| 4 | = D2 | 11 | = LE ¹ | 18 | = Q1 ¹ |
| 5 | = D3 | 12 | = Q7 ¹ | 19 | = Q0 ¹ |
| 6 | = D4 | 13 | = Q6 ¹ | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5 ¹ | | |

HT564: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4' |
| 2 | = D0 | 9 | = D7 | 16 | = Q3' |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2' |
| 4 | = D2 | 11 | = CLK | 18 | = Q1' |
| 5 | = D3 | 12 | = Q7' | 19 | = Q0' |
| 6 | = D4 | 13 | = Q6' | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5' | | |

HT573: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4 |
| 2 | = D0 | 9 | = D7 | 16 | = Q3 |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2 |
| 4 | = D2 | 11 | = LE' | 18 | = Q1 |
| 5 | = D3 | 12 | = Q7 | 19 | = Q0 |
| 6 | = D4 | 13 | = Q6 | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5 | | |

HT574: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 8 | = D6 | 15 | = Q4 |
| 2 | = D0 | 9 | = D7 | 16 | = Q3 |
| 3 | = D1 | 10 | = [GND] | 17 | = Q2 |
| 4 | = D2 | 11 | = CLK | 18 | = Q1 |
| 5 | = D3 | 12 | = Q7 | 19 | = Q0 |
| 6 | = D4 | 13 | = Q6 | 20 | = [VCC] |
| 7 | = D5 | 14 | = Q5 | | |

HT583: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B1 | 7 | = S2 | 12 | = B0 |
| 2 | = B2 | 8 | = [GND] | 13 | = A0 |
| 3 | = B3 | 9 | = S3 | 14 | = A1 |
| 4 | = A3 | 10 | = S1 | 15 | = A2 |
| 5 | = CN | 11 | = S0 | 16 | = [VCC] |
| 6 | = CN+4 | | | | |

HT597: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B | 7 | = H | 12 | = LT-CLK |
| 2 | = C | 8 | = [GND] | 13 | = SS-PL |
| 3 | = D | 9 | = QH | 14 | = SA |
| 4 | = E | 10 | = RESET | 15 | = A |
| 5 | = F | 11 | = SH-CLK | 16 | = [VCC] |
| 6 | = G | | | | |

HT640: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DIR | 8 | = A6 | 15 | = B3 |
| 2 | = A0 | 9 | = A7 | 16 | = B2 |
| 3 | = A1 | 10 | = [GND] | 17 | = B1 |
| 4 | = A2 | 11 | = B7 | 18 | = B0 |
| 5 | = A3 | 12 | = B6 | 19 | = OE' |
| 6 | = A4 | 13 | = B5 | 20 | = [VCC] |
| 7 | = A5 | 14 | = B4 | | |

HT643: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = DIR | 8 | = A6 | 15 | = B3 |
| 2 | = A0 | 9 | = A7 | 16 | = B2 |
| 3 | = A1 | 10 | = [GND] | 17 | = B1 |
| 4 | = A2 | 11 | = B7 | 18 | = B0 |
| 5 | = A3 | 12 | = B6 | 19 | = OE' |
| 6 | = A4 | 13 | = B5 | 20 | = [VCC] |
| 7 | = A5 | 14 | = B4 | | |

HT646: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CAB | 9 | = A5 | 17 | = B3 |
| 2 | = SAB | 10 | = A6 | 18 | = B2 |
| 3 | = DIR | 11 | = A7 | 19 | = B1 |
| 4 | = A0 | 12 | = [GND] | 20 | = B0 |
| 5 | = A1 | 13 | = B7 | 21 | = OE' |
| 6 | = A2 | 14 | = B6 | 22 | = SBA |
| 7 | = A3 | 15 | = B5 | 23 | = CBA |
| 8 | = A4 | 16 | = B4 | 24 | = [VCC] |

HT648: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CAB | 9 | = A5 | 17 | = B3 |
| 2 | = SAB | 10 | = A6 | 18 | = B2 |
| 3 | = DIR | 11 | = A7 | 19 | = B1 |
| 4 | = A0 | 12 | = [GND] | 20 | = B0 |
| 5 | = A1 | 13 | = B7 | 21 | = OE' |
| 6 | = A2 | 14 | = B6 | 22 | = SBA |
| 7 | = A3 | 15 | = B5 | 23 | = CBA |
| 8 | = A4 | 16 | = B4 | 24 | = [VCC] |

HT670: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = D1 | 7 | = Q2 | 12 | = WE' |
| 2 | = D2 | 8 | = [GND] | 13 | = WA1 |
| 3 | = D3 | 9 | = Q1 | 14 | = WA0 |
| 4 | = RA1 | 10 | = Q0 | 15 | = D0 |
| 5 | = RA0 | 11 | = RE' | 16 | = [VCC] |
| 6 | = Q3 | | | | |

HT688: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = E' | 8 | = A3 | 15 | = A6 |
| 2 | = A0 | 9 | = B3 | 16 | = B6 |
| 3 | = B0 | 10 | = [GND] | 17 | = A7 |
| 4 | = A1 | 11 | = A4 | 18 | = B7 |
| 5 | = B1 | 12 | = B4 | 19 | = Y |
| 6 | = A2 | 13 | = A5 | 20 | = [VCC] |
| 7 | = B2 | 14 | = B5 | | |

HT4002: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUTY (A) | 6 | = {n/c} | 11 | = INC (B) |
| 2 | = INA (A) | 7 | = [GND] | 12 | = IND (B) |
| 3 | = INB (A) | 8 | = {n/c} | 13 | = OUTY (B) |
| 4 | = INC (A) | 9 | = INA (B) | 14 | = [VCC] |
| 5 | = IND (A) | 10 | = INB (B) | | |

HT4015: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (B) | 7 | = DATA (A) | 12 | = Q2 (B) |
| 2 | = Q4 (A) | 8 | = [GND] | 13 | = Q1 (B) |
| 3 | = Q3 (A) | 9 | = CLK (A) | 14 | = MR (B) |
| 4 | = Q2 (A) | 10 | = Q4 (B) | 15 | = DATA (B) |
| 5 | = Q1 (A) | 11 | = Q3 (B) | 16 | = [VCC] |
| 6 | = MR (A) | | | | |

HT4016: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IO (A) | 6 | = CNTRL (C) | 11 | = IO (D) |
| 2 | = OI (A) | 7 | = [GND] | 12 | = CNTRL (D) |
| 3 | = OI (B) | 8 | = IO (C) | 13 | = CNTRL (A) |
| 4 | = IO (B) | 9 | = OI (C) | 14 | = [VCC] |
| 5 | = CNTRL (B) | 10 | = OI (D) | | |

HT4017: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q5 | 7 | = Q3 | 12 | = TC |
| 2 | = Q1 | 8 | = [GND] | 13 | = CE' |
| 3 | = Q0 | 9 | = Q8 | 14 | = CLK |
| 4 | = Q2 | 10 | = Q4 | 15 | = MR |
| 5 | = Q6 | 11 | = Q9 | 16 | = [VCC] |
| 6 | = Q7 | | | | |

HT4020: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q4 | 12 | = Q9 |
| 2 | = Q13 | 8 | = [GND] | 13 | = Q8 |
| 3 | = Q14 | 9 | = Q1 | 14 | = Q10 |
| 4 | = Q6 | 10 | = CLK | 15 | = Q11 |
| 5 | = Q5 | 11 | = MR | 16 | = [VCC] |
| 6 | = Q7 | | | | |

HT4024: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 6 | = Q4 | 11 | = Q2 |
| 2 | = MR | 7 | = [GND] | 12 | = Q1 |
| 3 | = Q7 | 8 | = {n/c} | 13 | = {n/c} |
| 4 | = Q6 | 9 | = Q3 | 14 | = [VCC] |
| 5 | = Q5 | 10 | = {n/c} | | |

HT4040: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q2 | 12 | = Q9 |
| 2 | = Q6 | 8 | = [GND] | 13 | = Q8 |
| 3 | = Q5 | 9 | = Q1 | 14 | = SIG |
| 4 | = Q7 | 10 | = CLK | 15 | = Q11 |
| 5 | = Q4 | 11 | = MR | 16 | = [VCC] |
| 6 | = Q3 | | | | |

HT4046: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = P-PULSE | 7 | = C1-2 | 12 | = R2 |
| 2 | = P-COMP1 | 8 | = [GND] | 13 | = P-COMP2 |
| 3 | = COMP | 9 | = VCO-IN | 14 | = SIG-IN |
| 4 | = VCO-OUT | 10 | = DMOD | 15 | = ZENER |
| 5 | = INH | 11 | = R1 | 16 | = [VCC] |
| 6 | = C1-1 | | | | |

HT4049: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 7 | = A (C) | 12 | = Y' (E) |
| 2 | = Y' (A) | 8 | = [GND] | 13 | = {n/c} |
| 3 | = A (A) | 9 | = A (D) | 14 | = A (F) |
| 4 | = Y' (B) | 10 | = Y' (D) | 15 | = Y' (F) |
| 5 | = A (B) | 11 | = A (E) | 16 | = {n/c} |
| 6 | = Y' (C) | | | | |

HT4050: NUMBER OF GATES PER PACKAGE = 6

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = [VCC] | 7 | = A (C) | 12 | = Y (E) |
| 2 | = Y (A) | 8 | = [GND] | 13 | = {n/c} |
| 3 | = A (A) | 9 | = A (D) | 14 | = A (F) |
| 4 | = Y (B) | 10 | = Y (D) | 15 | = Y (F) |
| 5 | = A (B) | 11 | = A (E) | 16 | = {n/c} |
| 6 | = Y (C) | | | | |

HT4051: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = A4 | 7 | = VEE | 12 | = A3 |
| 2 | = A6 | 8 | = [GND] | 13 | = A0 |
| 3 | = AO/I | 9 | = S2 | 14 | = A1 |
| 4 | = A7 | 10 | = S1 | 15 | = A2 |
| 5 | = A5 | 11 | = S0 | 16 | = [VCC] |
| 6 | = E' | | | | |

HT4052: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B0 | 7 | = VEE | 12 | = A0 |
| 2 | = B2 | 8 | = [GND] | 13 | = AO/I |
| 3 | = BO/I | 9 | = S1 | 14 | = A1 |
| 4 | = B3 | 10 | = S0 | 15 | = A2 |
| 5 | = B1 | 11 | = A3 | 16 | = [VCC] |
| 6 | = E' | | | | |

HT4053: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = B1 | 7 | = VEE | 12 | = A0 |
| 2 | = B0 | 8 | = [GND] | 13 | = A1 |
| 3 | = C1 | 9 | = S2 | 14 | = AO/I |
| 4 | = CO/I | 10 | = S1 | 15 | = BO/I |
| 5 | = C0 | 11 | = S0 | 16 | = [VCC] |
| 6 | = E' | | | | |

HT4059: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK | 9 | = J14 | 17 | = J10 |
| 2 | = LE | 10 | = J13 | 18 | = J9 |
| 3 | = J1 | 11 | = S2 | 19 | = J8 |
| 4 | = J2 | 12 | = [GND] | 20 | = J7 |
| 5 | = J3 | 13 | = S1 | 21 | = J6 |
| 6 | = J4 | 14 | = S0 | 22 | = J5 |
| 7 | = J16 | 15 | = J12 | 23 | = Q |
| 8 | = J15 | 16 | = J11 | 24 | = [VCC] |

HT4060: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Q12 | 7 | = Q4 | 12 | = MR |
| 2 | = Q13 | 8 | = [GND] | 13 | = Q9 |
| 3 | = Q14 | 9 | = FYO | 14 | = Q8 |
| 4 | = Q6 | 10 | = FYO' | 15 | = Q10 |
| 5 | = Q5 | 11 | = FYI | 16 | = [VCC] |
| 6 | = Q7 | | | | |

HT4066: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = IO (A) | 6 | = CNTRL (C) | 11 | = IO (D) |
| 2 | = OI (A) | 7 | = [GND] | 12 | = CNTRL (D) |
| 3 | = OI (B) | 8 | = IO (C) | 13 | = CNTRL (A) |
| 4 | = IO (B) | 9 | = OI (C) | 14 | = [VCC] |
| 5 | = CNTRL (B) | 10 | = OI (D) | | |

HT4067: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OUT/IN | 9 | = IO | 17 | = I14 |
| 2 | = I7 | 10 | = S0 | 18 | = I13 |
| 3 | = I6 | 11 | = S1 | 19 | = I12 |
| 4 | = I5 | 12 | = [GND] | 20 | = I11 |
| 5 | = I4 | 13 | = S3 | 21 | = I10 |
| 6 | = I3 | 14 | = S2 | 22 | = I9 |
| 7 | = I2 | 15 | = E' | 23 | = I8 |
| 8 | = I1 | 16 | = I15 | 24 | = [VCC] |

HT4075: NUMBER OF GATES PER PACKAGE = 3

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = OUTY (B) | 11 | = INA (C) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INB (C) |
| 3 | = INA (B) | 8 | = INC (A) | 13 | = INC (C) |
| 4 | = INB (B) | 9 | = OUTY (A) | 14 | = [VCC] |
| 5 | = INC (B) | 10 | = OUTY (C) | | |

HT4078: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = K | 6 | = {n/c} | 11 | = G |
| 2 | = A | 7 | = [GND] | 12 | = H |
| 3 | = B | 8 | = {n/c} | 13 | = Y |
| 4 | = C | 9 | = E | 14 | = [VCC] |
| 5 | = D | 10 | = F | | |

HT4094: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = STR | 7 | = Q3 | 12 | = Q6 |
| 2 | = DS | 8 | = [GND] | 13 | = Q5 |
| 3 | = CP | 9 | = QS | 14 | = Q4 |
| 4 | = Q0 | 10 | = QS' | 15 | = OE |
| 5 | = Q1 | 11 | = Q7 | 16 | = [VCC] |
| 6 | = Q2 | | | | |

HT4316: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = X (A) | 7 | = EN | 12 | = Y (D) |
| 2 | = Y (A) | 8 | = [GND] | 13 | = X (D) |
| 3 | = Y (B) | 9 | = VEE | 14 | = CNTRL (D) |
| 4 | = X (B) | 10 | = X (C) | 15 | = CNTRL (A) |
| 5 | = CNTRL (B) | 11 | = Y (C) | 16 | = [VCC] |
| 6 | = CNTRL (C) | | | | |

HT4316S: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = XA | 7 | = EN | 12 | = YD |
| 2 | = YA | 8 | = [GND] | 13 | = XD |
| 3 | = YB | 9 | = VEE | 14 | = DC |
| 4 | = XB | 10 | = XC | 15 | = AC |
| 5 | = BC | 11 | = YC | 16 | = [VCC] |
| 6 | = CC | | | | |

HT4351: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = X4 | 7 | = EN2 | 13 | = A |
| 2 | = X6 | 8 | = VEE | 14 | = X3 |
| 3 | = X | 9 | = [GND] | 15 | = X0 |
| 4 | = X7 | 10 | = LE | 16 | = X1 |
| 5 | = X5 | 11 | = C | 17 | = X2 |
| 6 | = EN1' | 12 | = B | 18 | = [VCC] |

HT4352: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = Y0 | 7 | = EN2 | 13 | = X3 |
| 2 | = Y2 | 8 | = VEE | 14 | = X0 |
| 3 | = Y | 9 | = [GND] | 15 | = X |
| 4 | = Y3 | 10 | = LE | 16 | = X1 |
| 5 | = Y1 | 11 | = B | 17 | = X2 |
| 6 | = EN1' | 12 | = A | 18 | = [VCC] |

HT4353: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = YI | 7 | = EN1 | 13 | = A |
| 2 | = Y0 | 8 | = VEE | 14 | = X0 |
| 3 | = ZI | 9 | = [GND] | 15 | = XI |
| 4 | = Z | 10 | = LE | 16 | = X |
| 5 | = Z0 | 11 | = C | 17 | = Y |
| 6 | = EN2' | 12 | = B | 18 | = [VCC] |

HT4510: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PE | 7 | = COUT' | 12 | = P2 |
| 2 | = Q4 | 8 | = [GND] | 13 | = P3 |
| 3 | = P4 | 9 | = RESET | 14 | = Q3 |
| 4 | = P1 | 10 | = U/D | 15 | = CLK |
| 5 | = CIN' | 11 | = Q2 | 16 | = [VCC] |
| 6 | = Q1 | | | | |

HT4511: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INB | 7 | = INA | 12 | = B |
| 2 | = INC | 8 | = [GND] | 13 | = A |
| 3 | = LT' | 9 | = E | 14 | = G |
| 4 | = BL' | 10 | = D | 15 | = F |
| 5 | = LE' | 11 | = C | 16 | = [VCC] |
| 6 | = IND | | | | |

HT4514: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = LE' | 9 | = Y1 | 17 | = Y9 |
| 2 | = A0 | 10 | = Y2 | 18 | = Y8 |
| 3 | = A1 | 11 | = Y0 | 19 | = Y11 |
| 4 | = Y7 | 12 | = [GND] | 20 | = Y10 |
| 5 | = Y6 | 13 | = Y13 | 21 | = A2 |
| 6 | = Y5 | 14 | = Y12 | 22 | = A3 |
| 7 | = Y4 | 15 | = Y15 | 23 | = E' |
| 8 | = Y3 | 16 | = Y14 | 24 | = [VCC] |

HT4515: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = LE' | 9 | = Y1' | 17 | = Y9' |
| 2 | = A0 | 10 | = Y2' | 18 | = Y8' |
| 3 | = A1 | 11 | = Y0' | 19 | = Y11' |
| 4 | = Y7' | 12 | = [GND] | 20 | = Y10' |
| 5 | = Y6' | 13 | = Y13' | 21 | = A2 |
| 6 | = Y5' | 14 | = Y12' | 22 | = A3 |
| 7 | = Y4' | 15 | = Y15' | 23 | = E' |
| 8 | = Y3' | 16 | = Y14' | 24 | = [VCC] |

HT4516: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = PE | 7 | = COUT' | 12 | = P2 |
| 2 | = Q4 | 8 | = [GND] | 13 | = P3 |
| 3 | = P4 | 9 | = RESET | 14 | = Q3 |
| 4 | = P1 | 10 | = U/D | 15 | = CLK |
| 5 | = CIN' | 11 | = Q2 | 16 | = [VCC] |
| 6 | = Q1 | | | | |

HT4518: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = MR (A) | 12 | = Q1 (B) |
| 2 | = E (A) | 8 | = [GND] | 13 | = Q2 (B) |
| 3 | = Q0 (A) | 9 | = CLK (B) | 14 | = Q3 (B) |
| 4 | = Q1 (A) | 10 | = E (B) | 15 | = MR (B) |
| 5 | = Q2 (A) | 11 | = Q0 (B) | 16 | = [VCC] |
| 6 | = Q3 (A) | | | | |

HT4520: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CLK (A) | 7 | = MR (A) | 12 | = Q1 (B) |
| 2 | = E (A) | 8 | = [GND] | 13 | = Q2 (B) |
| 3 | = Q0 (A) | 9 | = CLK (B) | 14 | = Q3 (B) |
| 4 | = Q1 (A) | 10 | = E (B) | 15 | = MR (B) |
| 5 | = Q2 (A) | 11 | = Q0 (B) | 16 | = [VCC] |
| 6 | = Q3 (A) | | | | |

HT4538: NUMBER OF GATES PER PACKAGE = 2

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CX (B) | 7 | = Q' (B) | 12 | = A (A) |
| 2 | = RXCX (B) | 8 | = [GND] | 13 | = R' (A) |
| 3 | = R' (B) | 9 | = Q' (A) | 14 | = RXCX (A) |
| 4 | = A (B) | 10 | = Q (A) | 15 | = CX (A) |
| 5 | = B' (B) | 11 | = B' (A) | 16 | = [VCC] |
| 6 | = Q (B) | | | | |

HT7046: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = LOCK-SIG | 7 | = C1-2 | 12 | = R2 |
| 2 | = P-COMP1 | 8 | = [GND] | 13 | = P-COMP2 |
| 3 | = COMP | 9 | = VCO-IN | 14 | = SIG-IN |
| 4 | = VCO-OUT | 10 | = DMOD | 15 | = C2 |
| 5 | = INH | 11 | = R1 | 16 | = [VCC] |
| 6 | = C1-1 | | | | |

HT7266: NUMBER OF GATES PER PACKAGE = 4

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = INA (A) | 6 | = INB (B) | 11 | = OUTY (D) |
| 2 | = INB (A) | 7 | = [GND] | 12 | = INA (D) |
| 3 | = OUTY (A) | 8 | = INA (C) | 13 | = INB (D) |
| 4 | = OUTY (B) | 9 | = INB (C) | 14 | = [VCC] |
| 5 | = INA (B) | 10 | = OUTY (C) | | |

HT40102: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CP | 7 | = P3 | 12 | = P6 |
| 2 | = MR' | 8 | = [GND] | 13 | = P7 |
| 3 | = TE' | 9 | = PL' | 14 | = TC' |
| 4 | = P0 | 10 | = P4 | 15 | = PE' |
| 5 | = P1 | 11 | = P5 | 16 | = [VCC] |
| 6 | = P2 | | | | |

HT40103: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = CP | 7 | = P3 | 12 | = P6 |
| 2 | = MR' | 8 | = [GND] | 13 | = P7 |
| 3 | = TE' | 9 | = PL' | 14 | = TC' |
| 4 | = P0 | 10 | = P4 | 15 | = PE' |
| 5 | = P1 | 11 | = P5 | 16 | = [VCC] |
| 6 | = P2 | | | | |

HT40104: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE | 7 | = DSL | 12 | = Q3 |
| 2 | = DSR | 8 | = [GND] | 13 | = Q2 |
| 3 | = D0 | 9 | = S0 | 14 | = Q1 |
| 4 | = D1 | 10 | = S1 | 15 | = Q0 |
| 5 | = D2 | 11 | = CLK | 16 | = [VCC] |
| 6 | = D3 | | | | |

HT40105: NUMBER OF GATES PER PACKAGE = 1

| <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> | <u>PIN</u> | <u>SIGNAL</u> |
|------------|---------------|------------|---------------|------------|---------------|
| 1 | = OE' | 7 | = D3 | 12 | = Q1 |
| 2 | = DIR | 8 | = [GND] | 13 | = Q0 |
| 3 | = S1 | 9 | = MR | 14 | = DOR |
| 4 | = D0 | 10 | = Q3 | 15 | = S0 |
| 5 | = D1 | 11 | = Q2 | 16 | = [VCC] |
| 6 | = D2 | | | | |

COMPONENT PLOTS

Plot CD1

| | | | |
|-----------|-----------|-----------|-----------|
| CD4016 | CD4029 | CD4042 | CD4054 |
| CD4015 -# | CD4028 | CD4041U | CD4053 |
| CD4015 | CD4027 | CD4040 | CD4052 |
| CD4014 | CD4026 | CD4038 -# | CD4051 |
| CD4013-# | CD4025 | CD4038 | CD4050 |
| CD4013 | CD4024 | CD4037/# | CD4049U |
| CD4012 | CD4023 | CD4037A | CD4048 |
| CD4011 | CD4022 | CD4035 | CD4047 |
| CD4010 | CD4021 | CD4034 | CD4046 |
| CD4009U | CD4020 | | CD4045 |
| CD4008 | CD4019 -# | CD4033 | CD4044 -# |
| CD4006 | CD4019 | CD4032 -# | CD4044 |
| CD4002 | CD4018 | CD4032 | CD4043 -# |
| CD4001 | CD4017 | CD4031 | CD4043 |
| CD4000 | CD4016-# | CD4030 | CD4042 -# |

COMPONENT PLOTS

Plot CD2

| | | |
|--|---|---|
| <p>▶ CD4072</p> <p>◀ CD4069U</p> <p>▶ CD4071</p> <p>▶ CD4070</p> <p>▶ CD4068</p> | <p>▶ CD4096</p> <p>▶ CD4095</p> <p>▶ CD4094</p> <p>▶ CD4093</p> <p>▶ CD4089</p> | <p>▶ CD4515</p> <p>▶ CD4514</p> <p>▶ CD4512</p> <p>▶ CD4511</p> <p>▶ CD4510</p> |
| <p>▶ CD4067</p> <p>▶ CD4066</p> <p>▶ CD4063</p> <p>▶ CD4060</p> <p>▶ CD4059A</p> | <p>▶ CD4085</p> <p>▶ CD4082</p> <p>▶ CD4081</p> <p>▶ CD4078</p> <p>▶ CD4077</p> <p>▶ CD4076</p> | <p>▶ CD4508</p> <p>▶ CD4503</p> <p>▶ CD4502</p> <p>▶ CD4501</p> <p>▶ CD4500</p> |
| <p>▶ CD4056</p> <p>▶ CD4055</p> | <p>▶ CD4075</p> <p>▶ CD4073</p> | <p>▶ CD4097</p> |

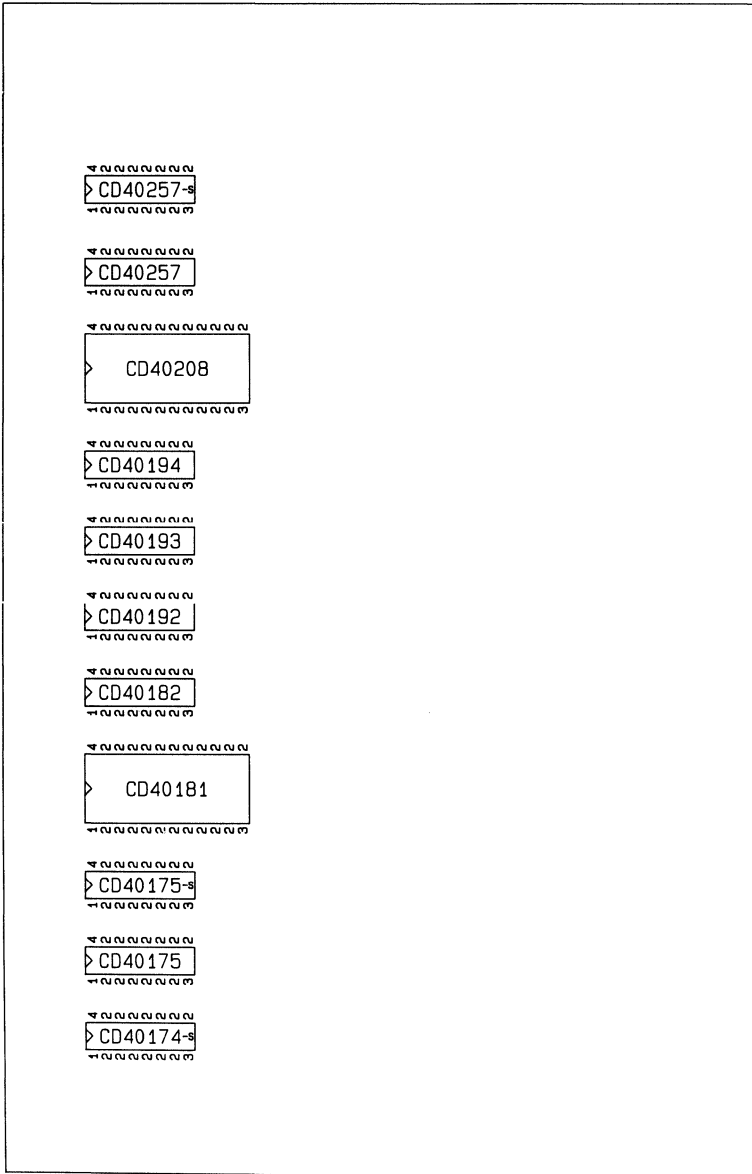
COMPONENT PLOTS

Plot CD3

| | |
|----------|---------|
| CD4556 | CD40174 |
| CD4555 | CD40163 |
| CD4541 | CD40162 |
| CD4538 | CD40161 |
| CD4538 | CD40160 |
| CD4536 | CD40147 |
| CD4532 | CD40117 |
| CD4527 | CD40116 |
| CD4520 | CD40115 |
| CD4520 | CD40109 |
| CD4518 | CD40109 |
| CD4518 | CD40109 |
| CD4517 | CD40109 |
| CD4517 | CD40109 |
| CD4516 | CD40108 |
| CD40107 | CD40108 |
| CD40107 | CD40108 |
| CD40106 | CD40108 |
| CD40106 | CD40108 |
| CD40105 | CD40108 |
| CD40105 | CD40108 |
| CD40104 | CD40108 |
| CD40104 | CD40108 |
| CD40103 | CD40108 |
| CD40103 | CD40108 |
| CD40102 | CD40108 |
| CD40102 | CD40108 |
| CD40101 | CD40108 |
| CD40101 | CD40108 |
| CD40100 | CD40108 |
| CD40100 | CD40108 |
| CD22859 | CD40115 |
| CD22105A | CD40115 |
| CD22104A | CD40109 |
| CD4724 | CD40109 |
| CD4585 | CD40109 |

COMPONENT PLOTS

Plot CD4



COMPONENT PLOTS

Plot HC1

| | | | |
|--------|---------|------------|------------|
| ▶ HC42 | ▶ HC126 | ▶ HC158 | ▶ HC181 |
| ▶ HC32 | ▶ HC125 | ▶ HC157 -8 | |
| ▶ HC30 | ▶ HC123 | ▶ HC157 | ▶ HC175 -8 |
| ▶ HC27 | ▶ HC112 | ▶ HC154 | ▶ HC175 |
| ▶ HC21 | ▶ HC109 | | ▶ HC174 -8 |
| ▶ HC20 | ▶ HC107 | ▶ HC153 -8 | ▶ HC174 |
| ▶ HC14 | ▶ HC93 | ▶ HC153 | ▶ HC173 |
| ▶ HC11 | ▶ HC96 | ▶ HC151 | ▶ HC166 |
| ▶ HC10 | ▶ HC85 | ▶ HC147 | ▶ HC165 |
| ▶ HC08 | ▶ HC76 | ▶ HC145 | ▶ HC164 |
| ▶ HC05 | ▶ HC75 | ▶ HC139 | ▶ HC163 |
| ▶ HC04 | ▶ HC74 | ▶ HC138 | ▶ HC162 |
| ▶ HC03 | ▶ HC73 | ▶ HC137 | ▶ HC161 |
| ▶ HC02 | ▶ HC51 | ▶ HC133 | ▶ HC160 |
| ▶ HC00 | ▶ HC44 | ▶ HC132 | ▶ HC158 -8 |

COMPONENT PLOTS

Plot HC2

| | | | |
|-----------------|-----------------|-----------------|--------------|
| ▶ HC243 ◀ | ▶ HC299 ◀ | ▶ HC393 ◀ | ▶ HC646 ◀ |
| ▶ HC242 ◀ | ▶ HC297 ◀ | ▶ HC390 ◀ | |
| ▶ HC241 ◀ | ▶ HC288 ◀ | ▶ HC377 ◀ | ▶ HC643 ◀ |
| ▶ HC240 -S ◀ | ▶ HC280 ◀ | ▶ HC375 -S ◀ | ▶ HC640 ◀ |
| ▶ HC240 ◀ | ▶ HC273 ◀ | ▶ HC375 ◀ | ▶ HC597 ◀ |
| ▶ HC238 ◀ | ▶ HC266 ◀ | ▶ HC374 ◀ | ▶ HC583 ◀ |
| ▶ HC237 ◀ | ▶ HC259 ◀ | ▶ HC373 ◀ | ▶ HC574 ◀ |
| ▶ HC221 ◀ | ▶ HC258 ◀ | ▶ HC368 ◀ | ▶ HC573 ◀ |
| ▶ HC195 ◀ | ▶ HC257 ◀ | ▶ HC367 ◀ | ▶ HC564 ◀ |
| ▶ HC194 ◀ | ▶ HC253 -S ◀ | ▶ HC366 -S ◀ | ▶ HC563 ◀ |
| ▶ HC193 ◀ | ▶ HC253 ◀ | ▶ HC366 ◀ | ▶ HC541 ◀ |
| ▶ HC192 ◀ | ▶ HC251 ◀ | ▶ HC365 -S ◀ | ▶ HC540 ◀ |
| ▶ HC191 ◀ | ▶ HC245 ◀ | ▶ HC365 ◀ | ▶ HC534 ◀ |
| ▶ HC190 ◀ | ▶ HC244 -S ◀ | ▶ HC356 ◀ | ▶ HC533 ◀ |
| ▶ HC182 ◀ | ▶ HC244 ◀ | ▶ HC354 ◀ | ▶ HC423 ◀ |

COMPONENT PLOTS

Plot HC3

| | | |
|----------|----------|-----------|
| ▶ HC4051 | ▶ HC4353 | ▶ HC40105 |
| ▶ HC4050 | ▶ HC4352 | ▶ HC40104 |
| ▶ HC4049 | ▶ HC4351 | ▶ HC40103 |
| ▶ HC4046 | ▶ HC4316 | ▶ HC40102 |
| ▶ HC4040 | ▶ HC4316 | ▶ HC7256 |
| ▶ HC4024 | ▶ HC4094 | ▶ HC7046 |
| ▶ HC4020 | ▶ HC4078 | ▶ HC4538 |
| ▶ HC4016 | ▶ HC4075 | ▶ HC4520 |
| ▶ HC4015 | ▶ HC4067 | ▶ HC4518 |
| ▶ HC4002 | ▶ HC4066 | ▶ HC4516 |
| ▶ HC6888 | ▶ HC4060 | ▶ HC4515 |
| ▶ HC670 | ▶ HC4059 | ▶ HC4514 |
| ▶ HC648 | ▶ HC4053 | ▶ HC4511 |
| | ▶ HC4052 | ▶ HC4510 |

COMPONENT PLOTS

PLOT HCT1

| | | | |
|--------|---------|------------|------------|
| ▶ HT42 | ▶ HT126 | ▶ HT158 | ▶ HT181 |
| ▶ HT32 | ▶ HT125 | ▶ HT157 -S | |
| ▶ HT30 | ▶ HT123 | ▶ HT157 | ▶ HT175 -S |
| ▶ HT27 | ▶ HT112 | ▶ HT154 | ▶ HT175 |
| ▶ HT21 | ▶ HT109 | | ▶ HT174 -S |
| ▶ HT20 | ▶ HT107 | ▶ HT153 -S | ▶ HT174 |
| ▶ HT14 | ▶ HT93 | ▶ HT153 | ▶ HT173 |
| ▶ HT11 | ▶ HT86 | ▶ HT151 | ▶ HT166 |
| ▶ HT10 | ▶ HT85 | ▶ HT147 | ▶ HT165 |
| ▶ HT08 | ▶ HT76 | ▶ HT145 | ▶ HT164 |
| ▶ HT05 | ▶ HT75 | ▶ HT139 | ▶ HT163 |
| ▶ HT04 | ▶ HT74 | ▶ HT138 | ▶ HT162 |
| ▶ HT03 | ▶ HT73 | ▶ HT137 | ▶ HT161 |
| ▶ HT02 | ▶ HT51 | ▶ HT133 | ▶ HT160 |
| ▶ HT00 | ▶ HT44 | ▶ HT132 | ▶ HT158 -S |

COMPONENT PLOTS

Plot HCT2

| | | | |
|------------|------------|------------|---------|
| ◁ HT243 | ▷ HT299 | ▷ HT393 | ▷ HT646 |
| ▷ HT242 | ▷ HT297 | ▷ HT390 | |
| ▷ HT241 | ▷ HT283 | ▷ HT377 | ▷ HT643 |
| ▷ HT240 -s | ▷ HT280 | ▷ HT375 -s | ▷ HT640 |
| ▷ HT240 | ▷ HT273 | ▷ HT375 | ▷ HT597 |
| ▷ HT238 | ▷ HT266 | ▷ HT374 | ▷ HT583 |
| ▷ HT237 | ▷ HT259 | ▷ HT373 | ▷ HT574 |
| ▷ HT221 | ▷ HT258 | ▷ HT368 | ▷ HT573 |
| ▷ HT195 | ▷ HT257 | ▷ HT367 | ▷ HT564 |
| ▷ HT194 | ▷ HT253 -s | ▷ HT366 -s | ▷ HT563 |
| ▷ HT193 | ▷ HT253 | ▷ HT366 | ▷ HT544 |
| ▷ HT192 | ▷ HT251 | ▷ HT365 -s | ▷ HT540 |
| ▷ HT191 | ▷ HT245 | ▷ HT365 | ▷ HT534 |
| ▷ HT190 | ▷ HT244 -s | ▷ HT356 | ▷ HT533 |
| ▷ HT182 | ▷ HT244 | ▷ HT354 | ▷ HT423 |

COMPONENT PLOTS

Plot ICT3

| | | |
|--|---|---|
| <p>▶ HT4051</p> <p>◀ HT4050</p> <p>▶ HT4049</p> <p>▶ HT4046</p> <p>▶ HT4040</p> <p>▶ HT4024</p> <p>▶ HT4020</p> <p>▶ HT4017</p> <p>▶ HT4016</p> <p>▶ HT4015</p> <p>▶ HT4002</p> <p>▶ HT668</p> <p>▶ HT670</p> <p>▶ HT648</p> | <p>▶ HT4353</p> <p>▶ HT4352</p> <p>▶ HT4351</p> <p>▶ HT4316-8</p> <p>▶ HT4316</p> <p>▶ HT4094</p> <p>▶ HT4078</p> <p>▶ HT4075</p> <p>▶ HT4067</p> <p>▶ HT4066</p> <p>▶ HT4060</p> <p>▶ HT4059</p> <p>▶ HT4053</p> <p>▶ HT4052</p> | <p>▶ HT40105</p> <p>▶ HT40104</p> <p>▶ HT40103</p> <p>▶ HT40102</p> <p>▶ HT7266</p> <p>▶ HT7046</p> <p>▶ HT4538</p> <p>▶ HT4520</p> <p>▶ HT4518</p> <p>▶ HT4516</p> <p>▶ HT4515</p> <p>▶ HT4514</p> <p>▶ HT4511</p> <p>▶ HT4510</p> |
|--|---|---|

GERBER PHOTO PLOTTER APERTURE CHART

| LAYER | TYPE 0 V50R28C.PS | TYPE 2 (N/C) 60R32C.PS | TYPE 3 (N/C) 60R32G.PS | TYPE 4 (N/C) 60R32P.PS |
|--------|---|---|---|------------------------------|
| PADCOM | .050 Circle | .060 Circle | .060 Circle | .060 Circle |
| FLCOMP | Aperture 15 | Aperture 9 | Aperture 9 | Aperture 9 |
| PADSLD | .050 Circle | .060 Circle | .060 Circle | .060 Circle |
| FLSOLD | Aperture 15 | Aperture 9 | Aperture 9 | Aperture 9 |
| PADINT | .050 Circle | .060 Circle | .060 Circle | .060 Circle |
| FLINT | Aperture 15 | Aperture 9 | Aperture 9 | Aperture 9 |
| GNDCON | .020 Ring .060 Inner Diam .100 Outer Diam | .020 Ring .060 Inner Diam .100 Outer Diam | Aperture 9 .025 Width X .100 Outer Diam | .020 Ring .060 Inner Diam |
| FLGCON | Aperture 8 | Aperture 8 | Aperture 22 | Aperture 8 |
| CLEAR | .100 Circle Solid Circle | .125 Circle Solid Circle | .125 Circle Solid Circle | .125 Circle Solid Circle |
| FLCLER | Aperture 20 | Aperture 21 | Aperture 21 | Aperture 21 |
| PWRCON | .020 Ring .060 Inner Diam .100 Outer Diam | .020 Ring .060 Inner Diam .100 Outer Diam | .020 Ring .060 Inner Diam .100 Outer Diam | Aperture 9 .025 Width X |
| FLPCON | Aperture 8 | Aperture 8 | Aperture 8 | Aperture 22 |
| SLDMSK | .060 Circle | .070 Circle | .070 Circle | .070 Circle |
| FLSMSK | Aperture 9 | Aperture 11 | Aperture 11 | Aperture 11 |
| DRILL | +28 | +32 | +32 | +32 |
| FLDRLL | Aperture 23 Text 28 | Aperture 23 Text 32 | Aperture 23 Text 32 | Aperture 23 Text 32 |
| PIN* | .050 | .050 | .050 | .050 |

* The pin layer reflects connectivity (C) with a solid circle or no connectivity (N) with a hollow circle.

GERBER PHOTOPLLOTTER APERTURE CHART Continued

| LAYER | TYPE 1 (N/C) 60S32C.PS | TYPE 5 (N/C) 60S32P.PS | TYPE 6 (N/C) 60S32G.PS |
|--------|---|---|------------------------------|
| PADCOM | .060 Square | .060 Square | .060 Square |
| FLCOMP | Aperture 10 | Aperture 10 | Aperture 10 |
| PADSLD | .060 Square | .060 Square | .060 Square |
| FLSOLD | Aperture 10 | Aperture 10 | Aperture 10 |
| PADINT | .060 Circle | .060 Circle | .060 Circle |
| FLINT | Aperture 9 | Aperture 9 | Aperture 9 |
| GNDCON | .020 Ring .060 Inner Diam .100 Outer Diam | .020 Ring .060 Inner Diam .100 Outer Diam | Aperture 9 .025 Width X |
| FLGCON | Aperture 8 | Aperture 8 | Aperture 22 |
| CLEAR | .125 Circle Solid Circle | .125 Circle Solid Circle | .125 Circle Solid Circle |
| FLCLER | Aperture 21 | Aperture 21 | Aperture 21 |
| PWRCON | .020 Ring .060 Inner Diam .100 Outer Diam | Aperture 9 .025 Width X .100 Outer Diam | .020 Ring .060 Inner Diam |
| FLPCON | Aperture 8 | Aperture 22 | Aperture 8 |
| SLDMSK | .070 Square | .070 Square | .070 Square |
| FLSMSK | Aperture 12 | Aperture 12 | Aperture 12 |
| DRILL | +32 | +32 | +32 |
| FLDRLL | Aperture 23 Text 32 | Aperture 23 Text 32 | Aperture 23 Text 32 |
| PIN* | .050 | .050 | .050 |

* The pin layer reflects connectivity (C) with a solid circle or no connectivity (N) with a hollow circle.