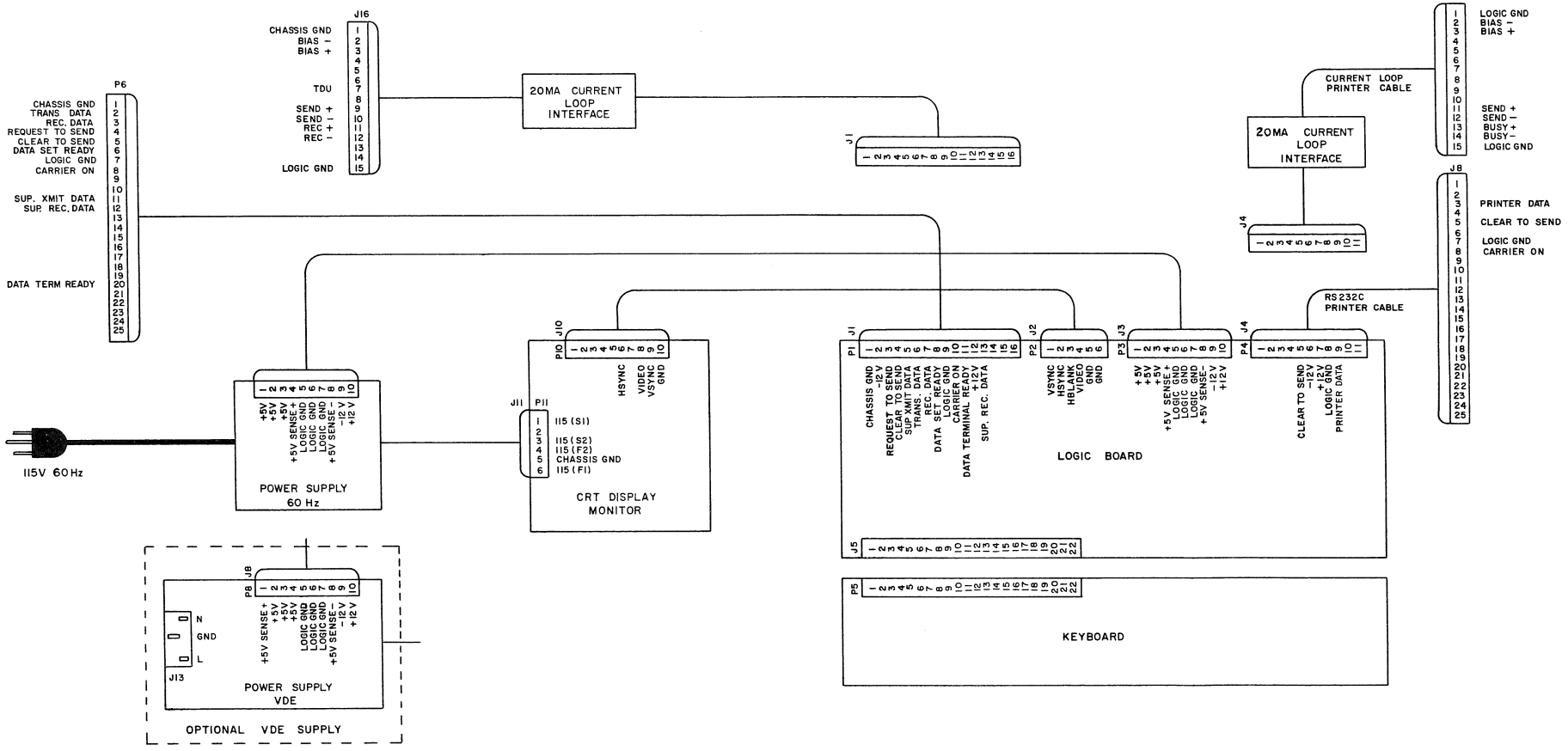
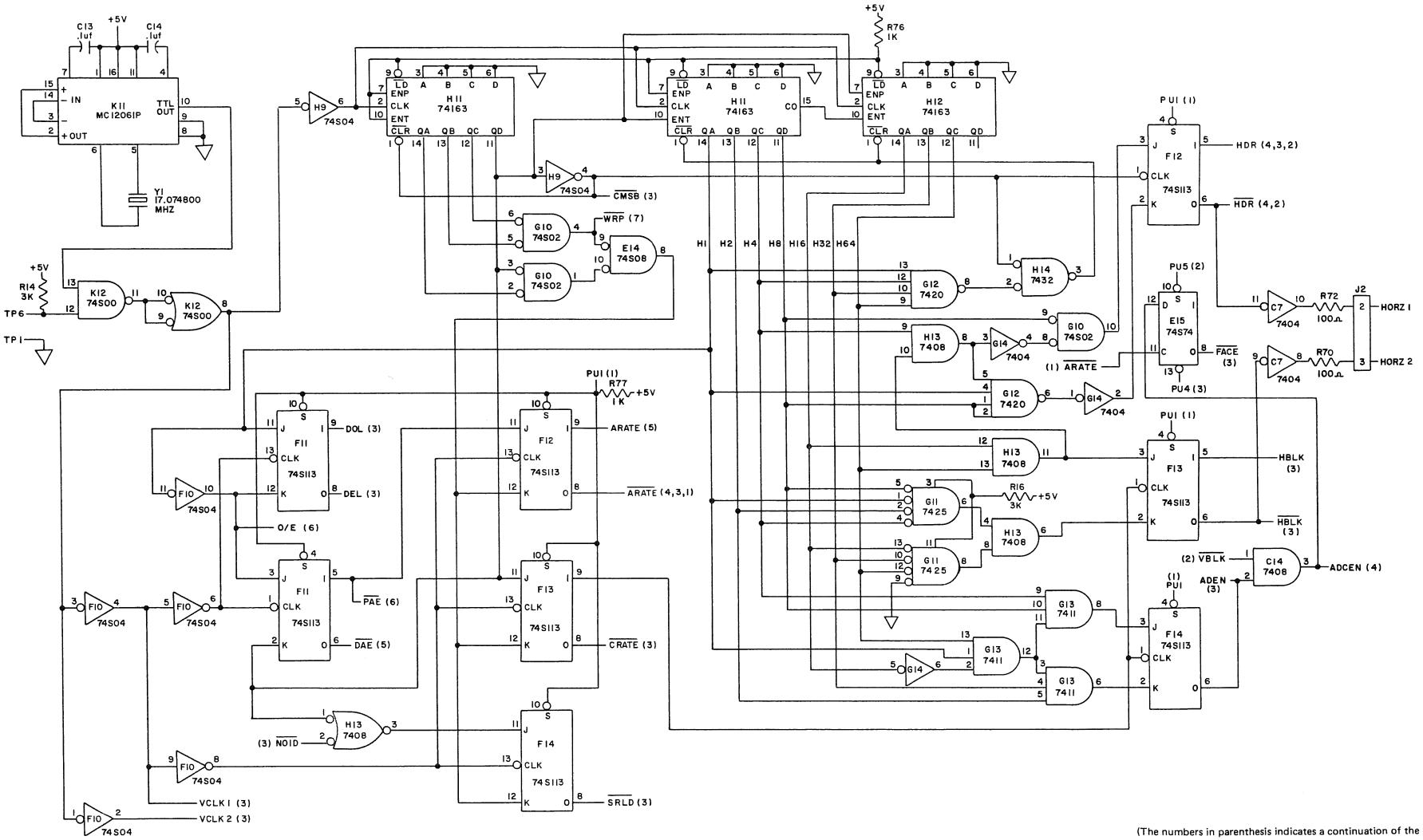
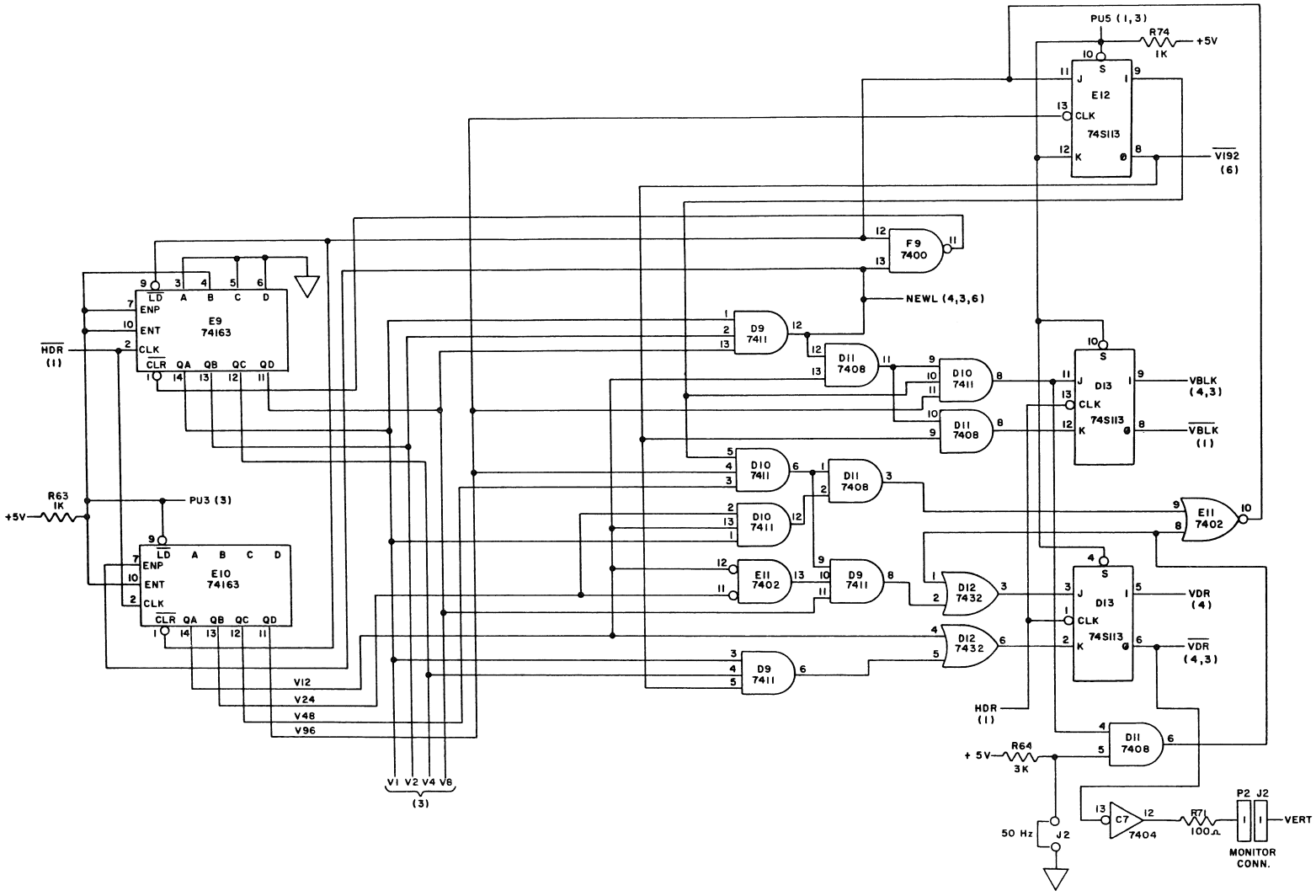


Figure 2-1. Owl-1200 Interconnection Diagram



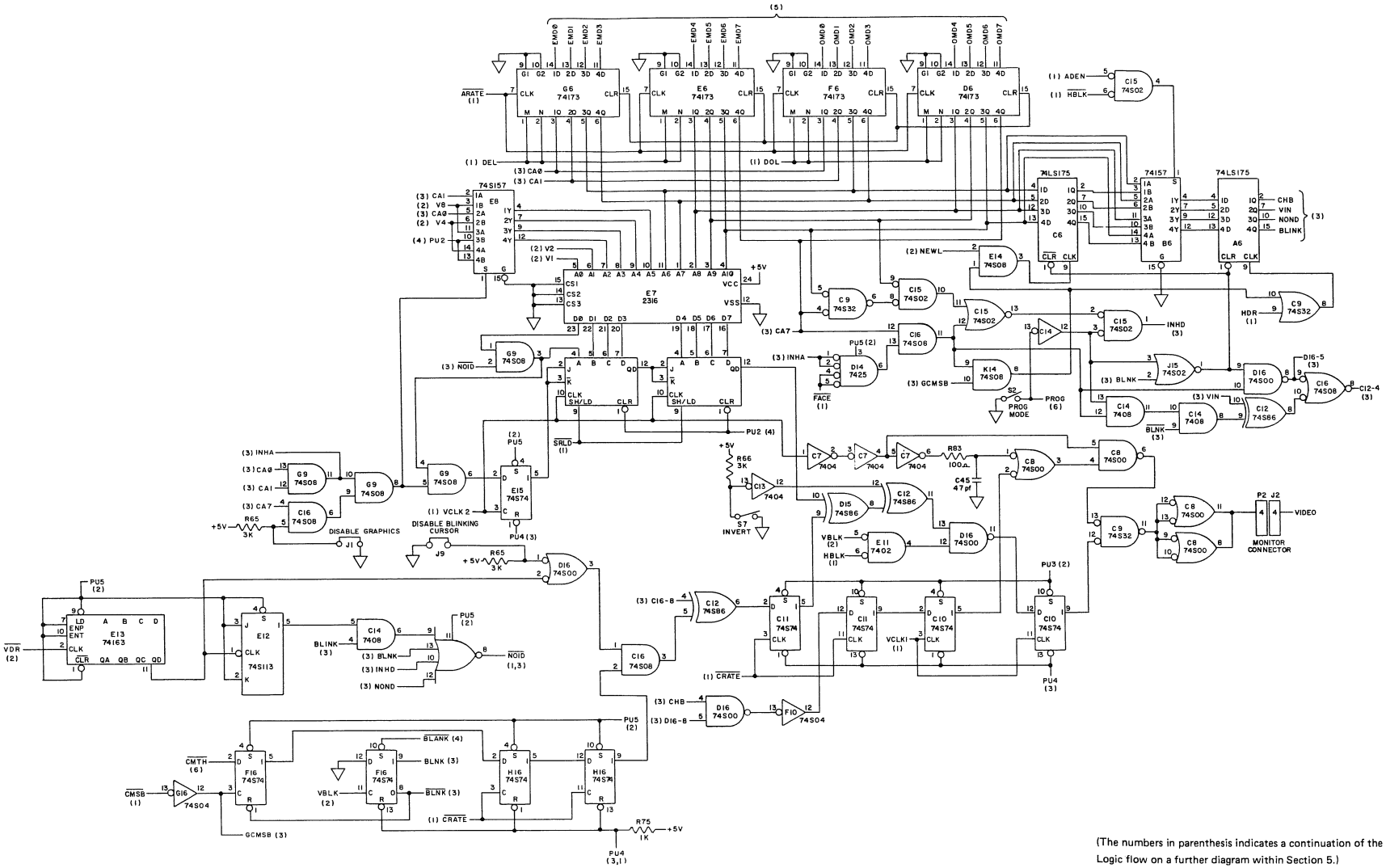
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-1. Horizontal Logic Diagram



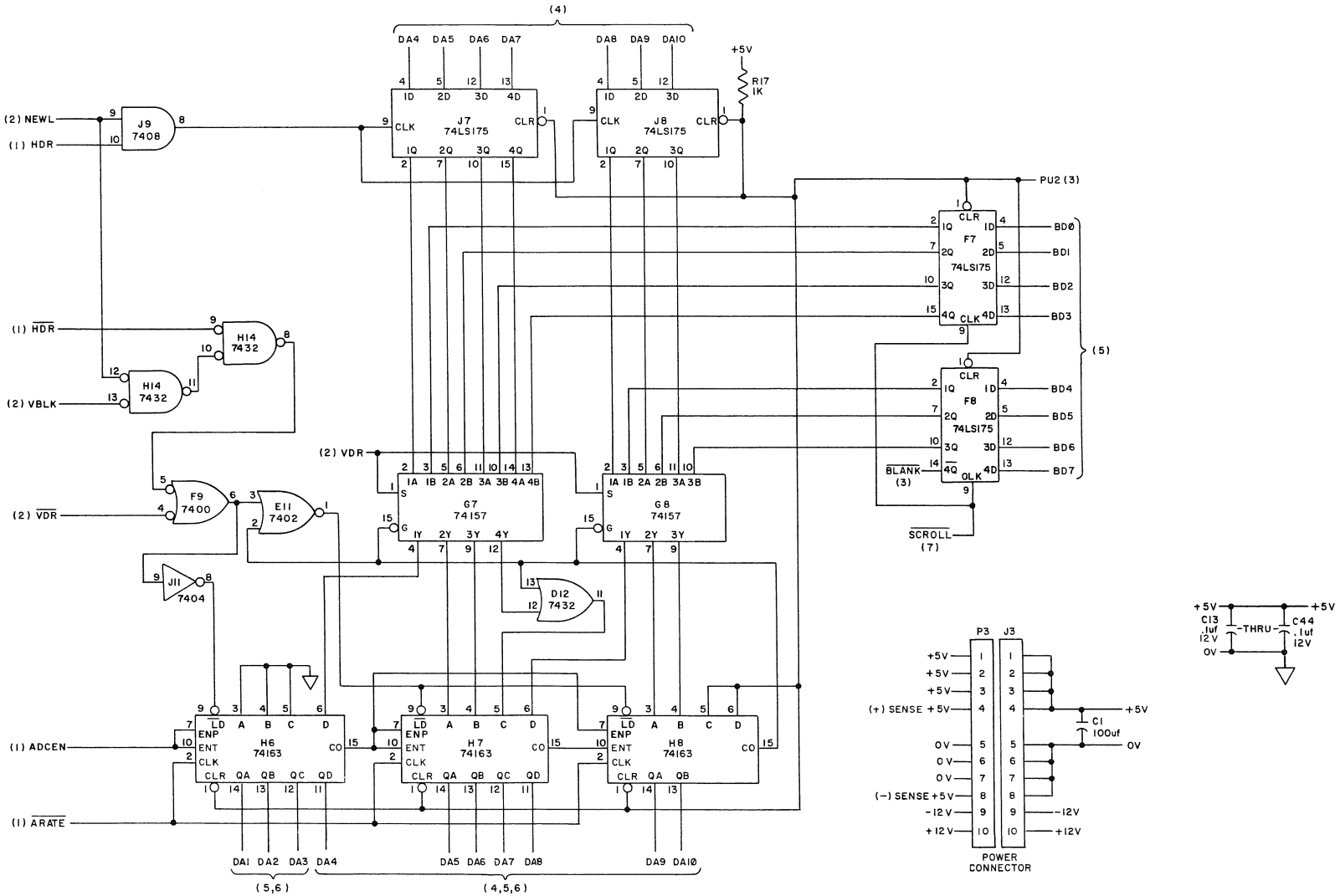
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-2. Vertical Logic Diagram



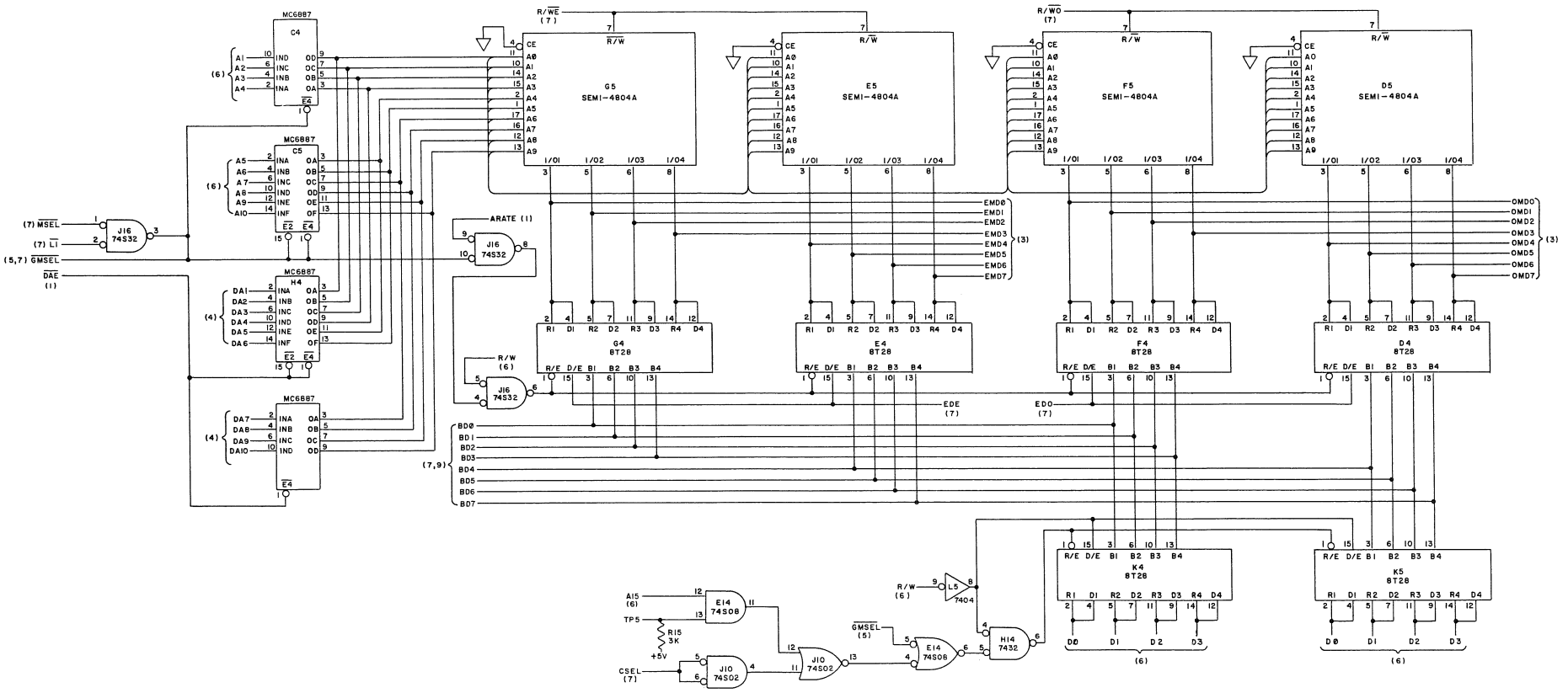
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-3. Video Logic Diagram



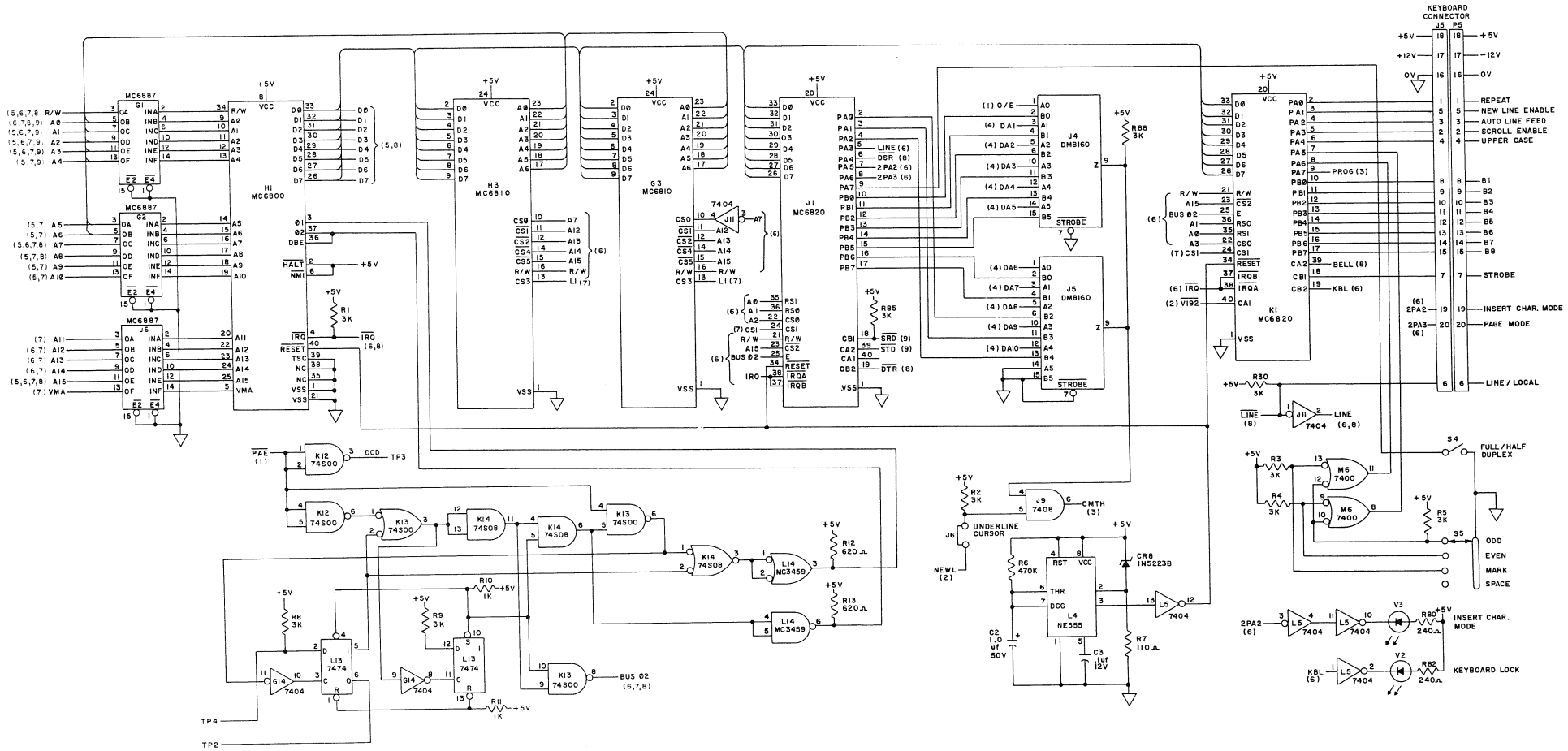
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-4. Scrolling Logic Diagram



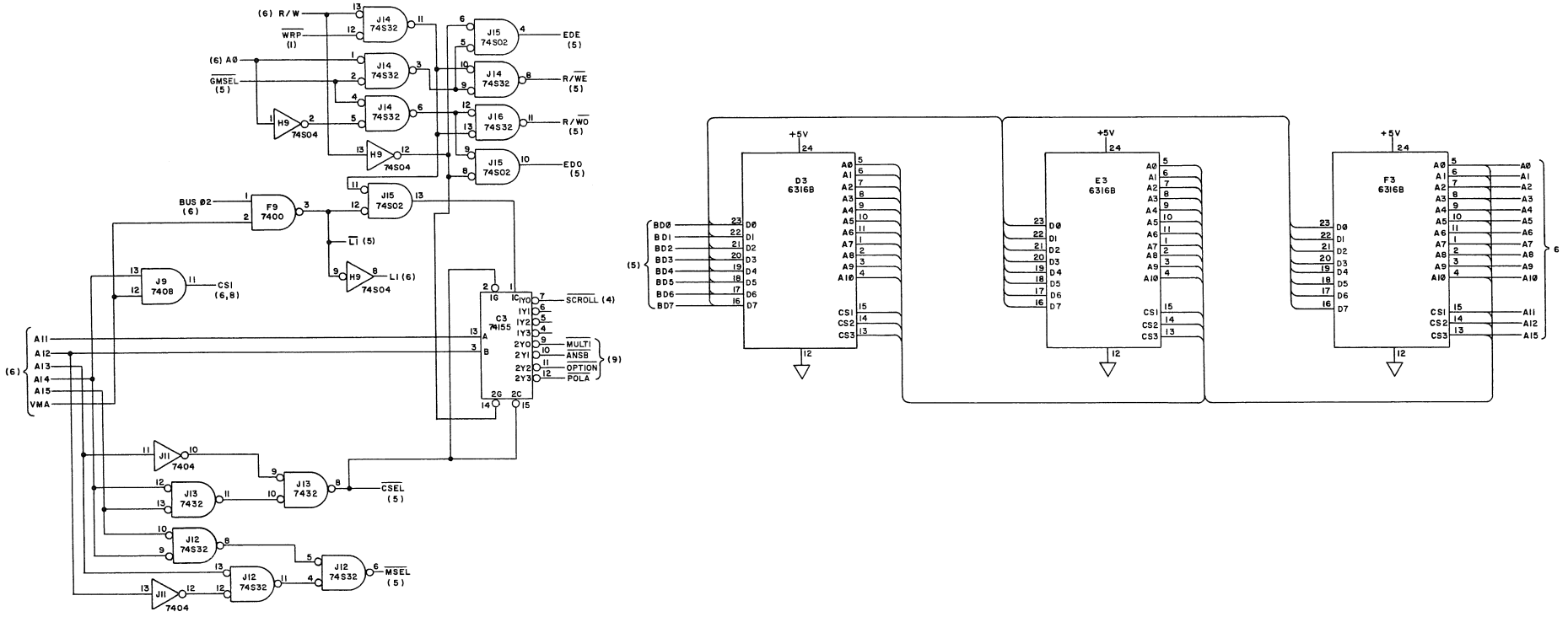
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-5. Display Memory Diagram



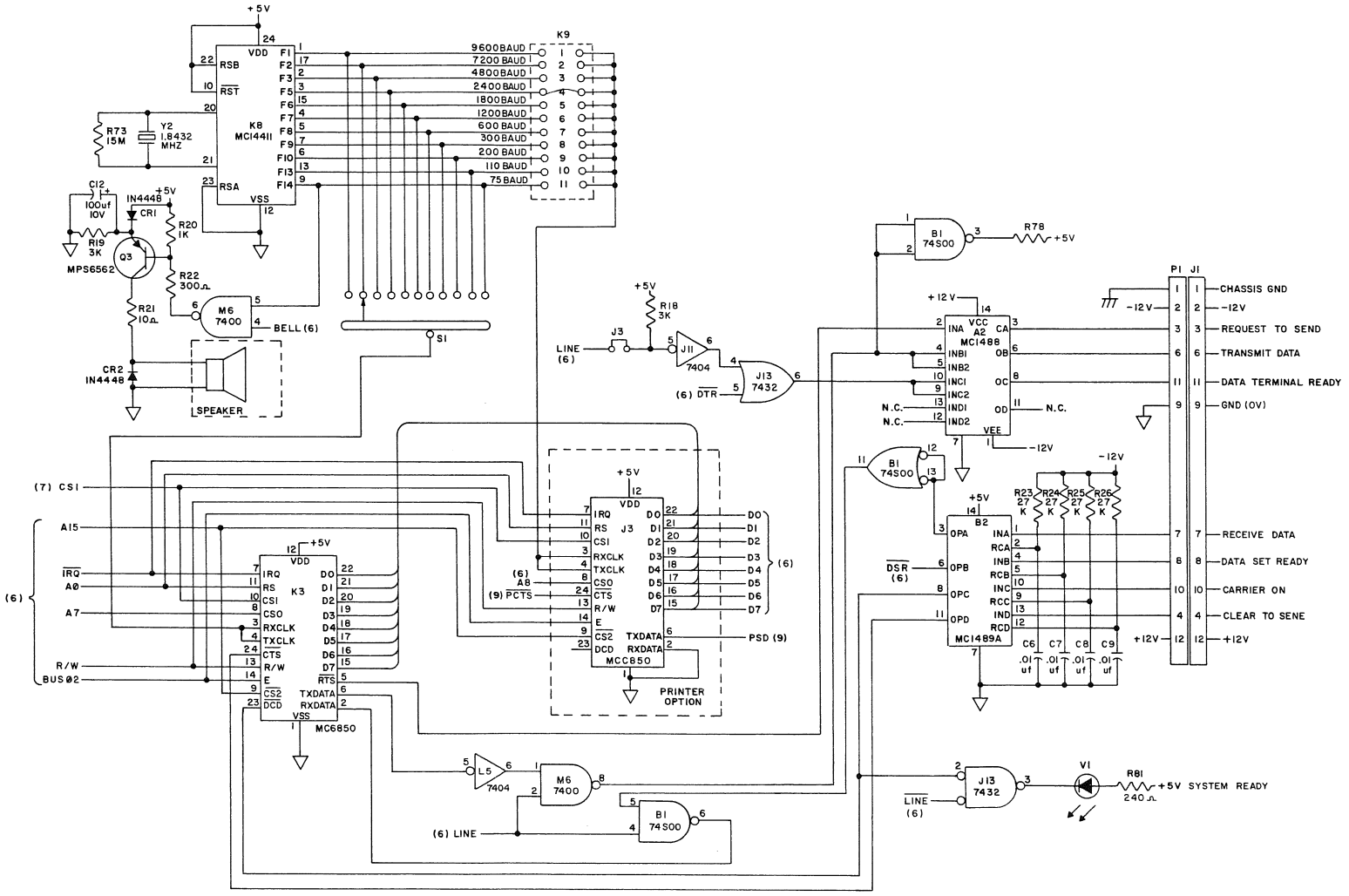
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-6. Processor Logic Diagram



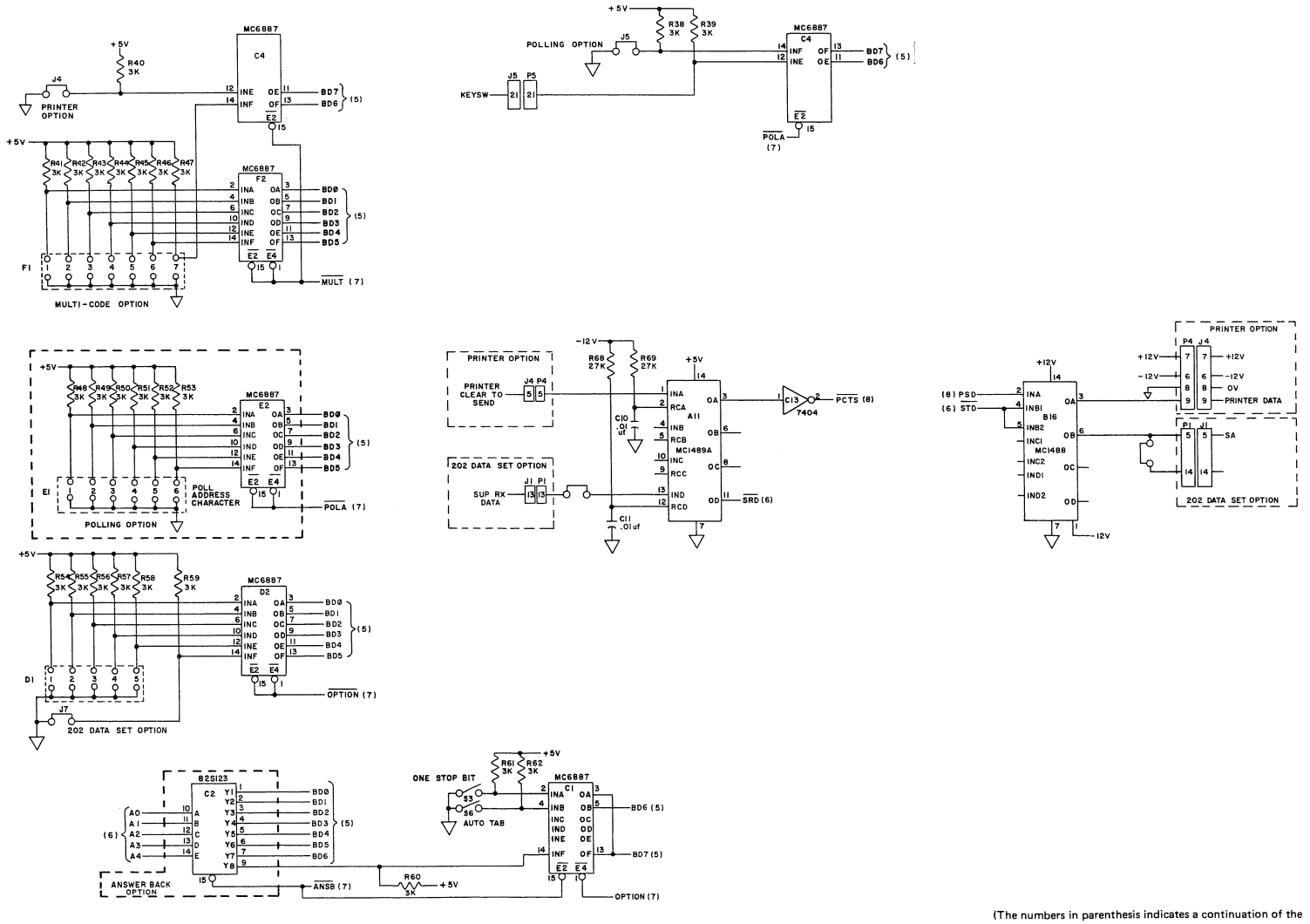
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-7. Decoding and ROM Memory Diagram



(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-8. Communications Interface Diagram



(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-9. Multicode and Answerback Options Diagram

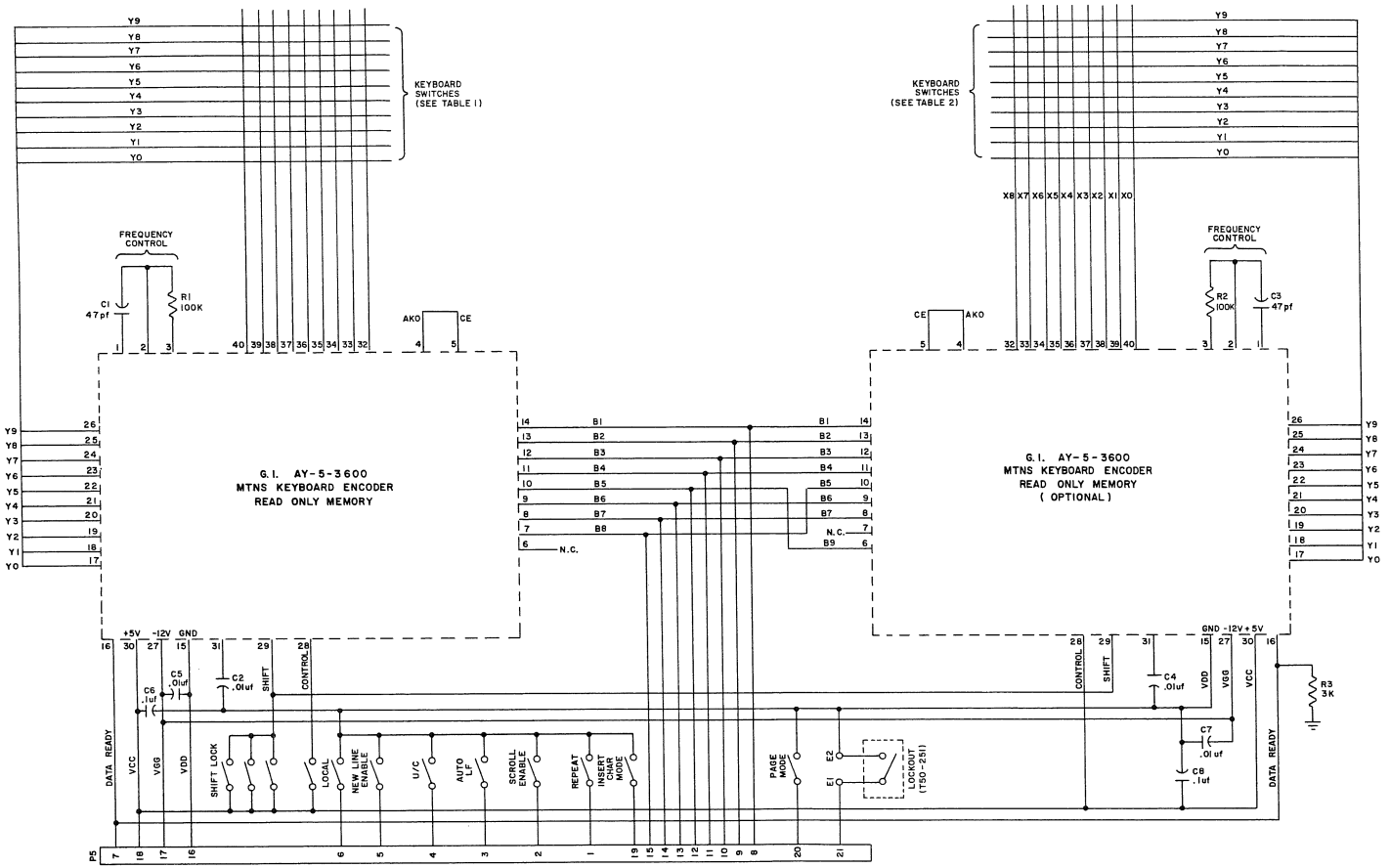


TABLE 1

XY	KEY	XY	KEY
00	SPACE	45	T
01	⇤	46	Y
02	PRINT	47	U
03	,	48	I
04	0	49	O
05	-	50	P
06	SET TAB	51	\ @
07	↓	52	{ [
08	CLEAR TABS	53	DEL
09	→	54	LINE FEED
10	Z	55	4
11	X	56	5
12	C	57	6
13	V	58	PRINT
14	B	59	↑
15	N	60	ESC
16	M	61	! ¡
17	< ,	62	" ²
18	> .	63	# ³
19	? /	64	$ ¤
20	A	65	% ⁵
21	S	66	& ⁶
22	D	67	' ´ ⁷
23	F	68	(⁸
24	BELL G	69	) ⁹
25	H	70	= _
26	J	71	~ ^
27	K	72	* :
28	FF L	73	BACK SPACE
29	+ ;	74	MULT CODE
30	* :	75	7
31	>]	76	8
32	RETURN	77	9
33	1	78	SEND LINE
34	2	79	SEND MSG
35	3	80	BREAK
36	←	81	HERE IS
37	H	82	RESET
38	PRINT PAGE	83	DEL CHAR
39	SEND PAGE	84	INSERT LINE
40	TAB	85	DEL LINE
41	Q	86	SEND LINE
42	W	87	CLEAR ALL
43	E	88	SEND PAGE
44	R	89	o

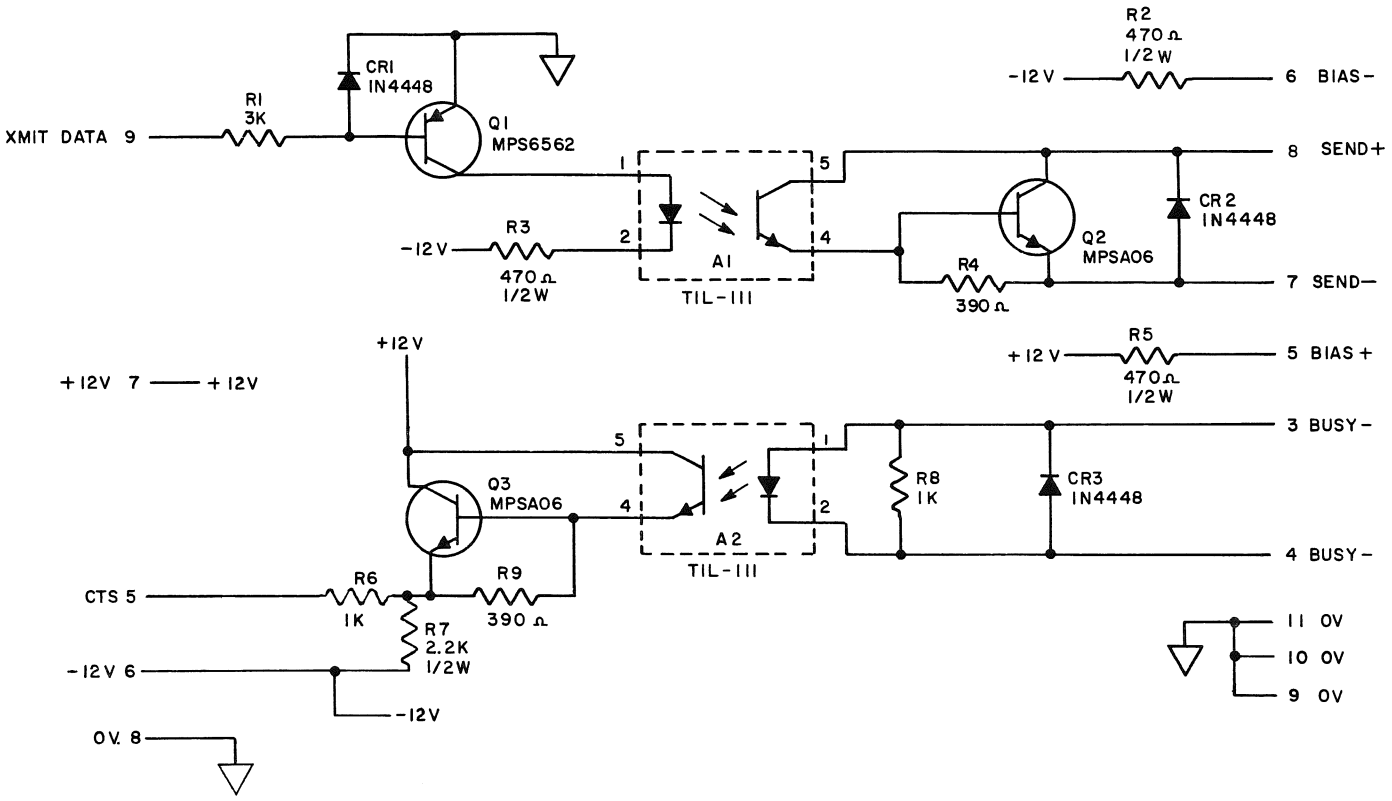
*TABLE 2

XY	KEY	XY	KEY
10	F11	45	F5
11	F9	46	F10
13	F7	47	F6
21	F4	50	F1
31	F14	52	F12
41	F2	53	F16
42	F8	71	F15
44	F3	72	F13

* NOTE KEYS IN TABLE 2 ARE OPTIONAL

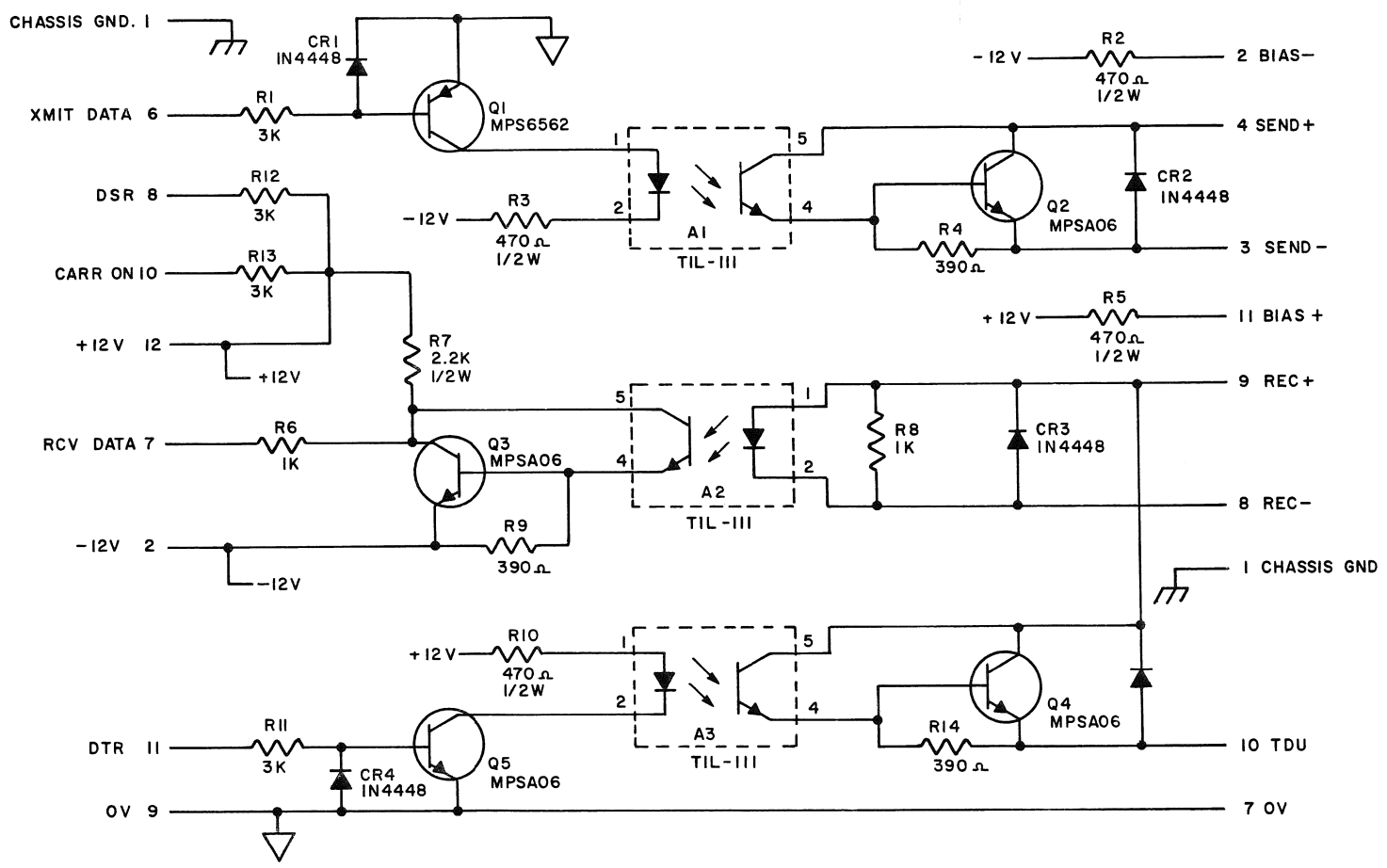
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-10. 1200 Keyboard Diagram



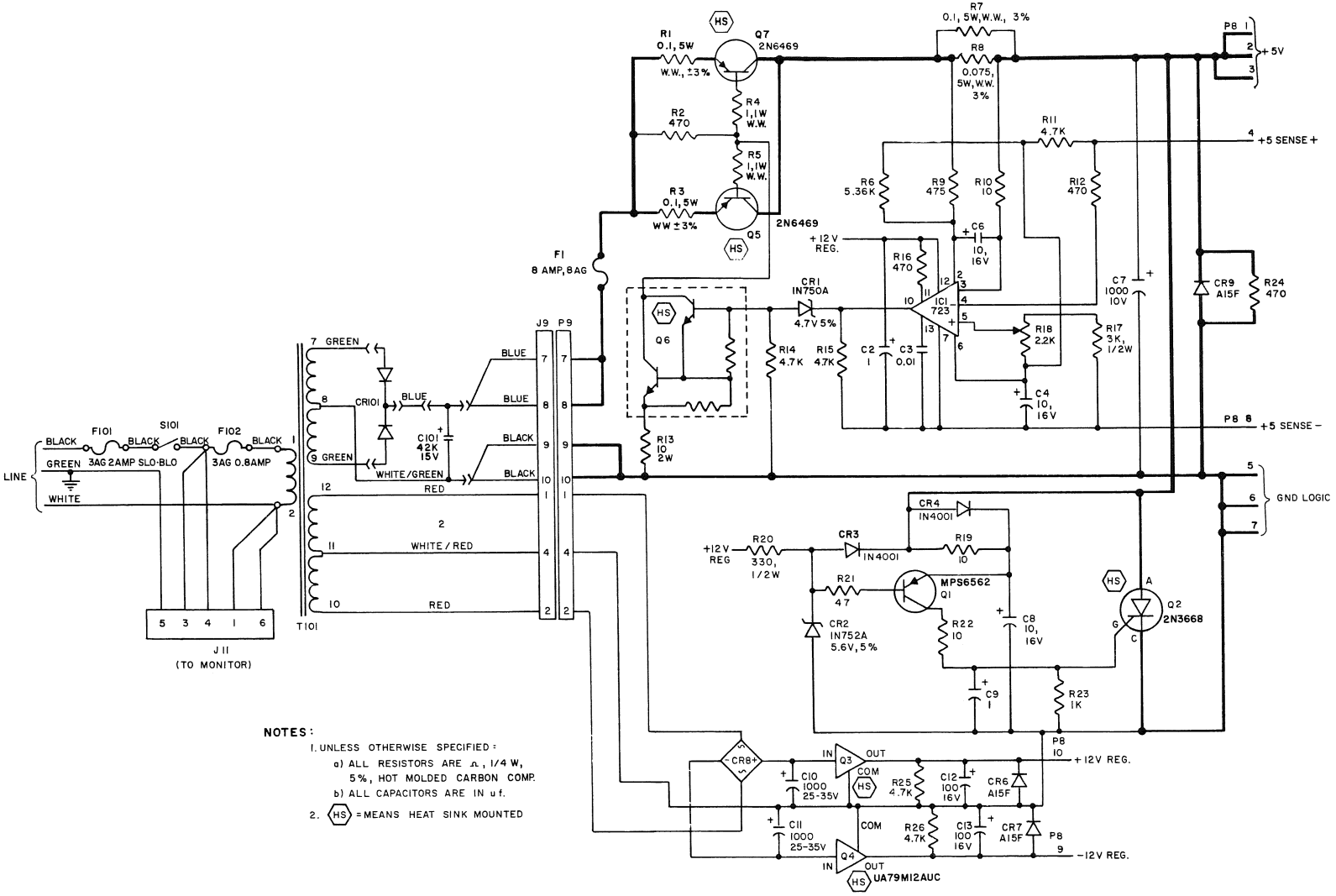
(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-11. Printer Current Loop Option Diagram



(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-12. Communications Current Loop Diagram

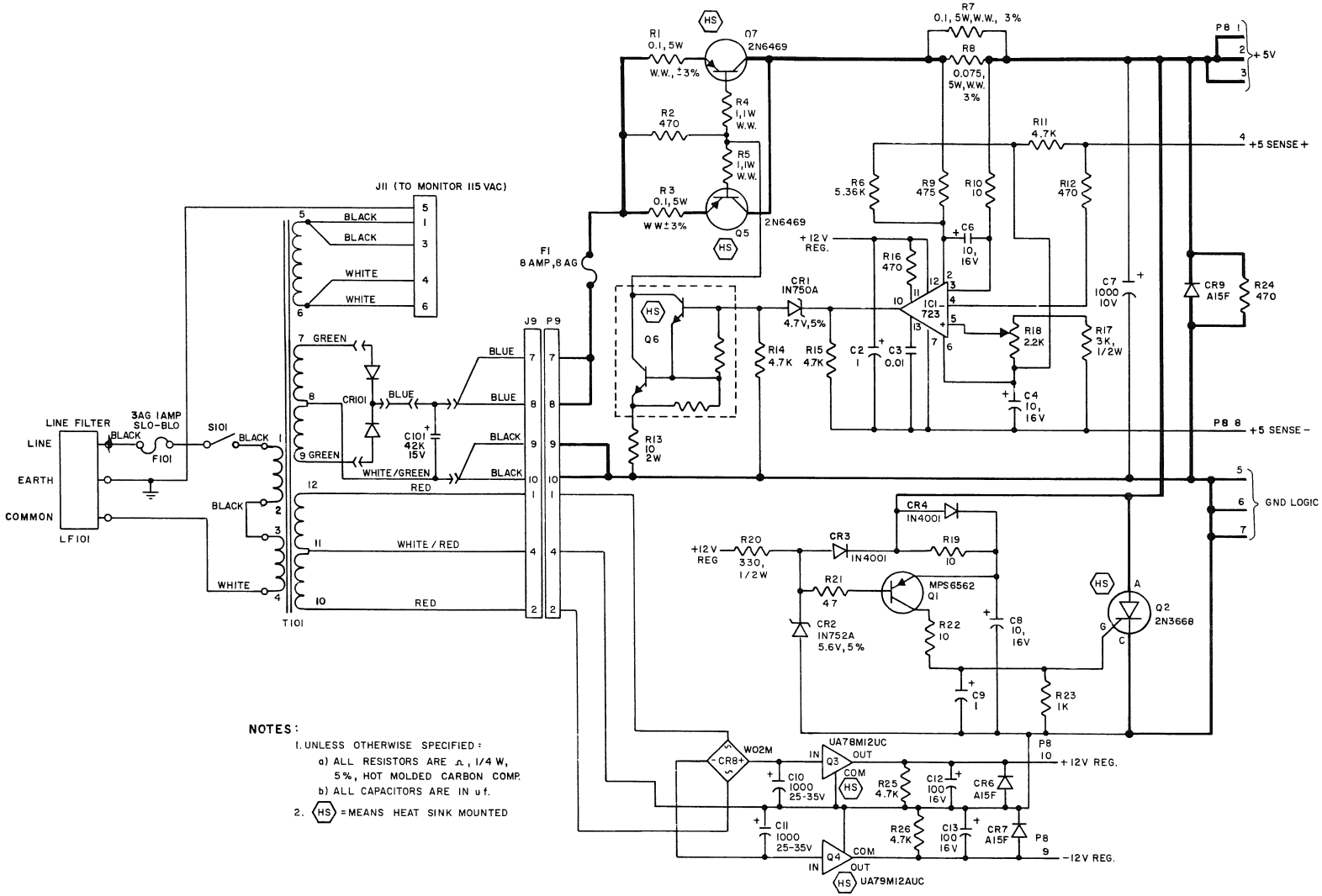


NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   a) ALL RESISTORS ARE Ω, 1/4 W, 5%, HOT MOLDED CARBON COMP.
   b) ALL CAPACITORS ARE IN µf.
2. (HS) = MEANS HEAT SINK MOUNTED

(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-13. Power Supply (115 Volt) Diagram



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   a) ALL RESISTORS ARE Ω, 1/4 W, 5%, HOT MOLDED CARBON COMP.
   b) ALL CAPACITORS ARE IN µf.
2. (HS) = MEANS HEAT SINK MOUNTED

(The numbers in parenthesis indicates a continuation of the Logic flow on a further diagram within Section 5.)

Figure 5-14. Power Supply VDE (230 Volt) Diagram