



# DEVELOPMENT TOOL MATRIX

Mfg.	CPU	Supported Versions	BMD	SIM	HMI Emulator Model No.	Available Interfaces	Max. Speed (MHz)	Overlay Memory	Shadow RAM	Trace Size (Cycles)	ENET	PAC
AMD	8051	8031, 8053, 8751, 8753, 80535, 80515, 80C321			200-8051	D, P	24	128KB, 256KB	No	2@4K	Opt	Std
	8085	8085AH			200-8085	D, P	12	256KB, 1MB	No	2@4K	No	Std
DALLAS	DS5000	DS5000, DS5000T			200-8051	D, P	24	128KB, 256KB	No	2@4K	Opt	Std
HITACHI	64180	64180S, 64180Z			200-64180	D, P	36	256KB, 1MB	No	2@4K	No	Opt
IBM	PPC4xx	PPC401GF, 403GA/GB/GC/GCX	✓		2000-PowerPC	F, X	66	512KB, 1, 2, 4MB	512KB, 2MB	128K	Std	Std
INTEL	8085	8085A, A-2, AH, AH-1, AH-2			200-8085	D, P	12	256KB, 1MB	No	2@4K	No	Std
	8051	8031, 8032, 8044, 8344, 8052, 8751, 8752, 80C152, 80C452, 83C152, 80C51FA/FB/FC, 83C51, 83C252			200-8051	D, P	24	128KB, 256KB	No	2@4K	Opt	Std
	8096/80196	8095BH, 8096BH, 8097BH, M8097, 8098, 80196JR/JQ, 80196KA/KB/KC/KD/KQ/KR, 80196KT/MC/MD/MH/TB, 80194KB, 80198KB			200-8096	D, P, G	20	64KB, 256KB, 1MB	No	2@4K	Opt	Opt
MOTOROLA	6809	68A09, B09, 09E, A09E, B09E			200-6809	D	8	256KB	No	2@4K	No	Std
	68HC11	68HC11A0, A1, A7, A8, B0, B1, B8, D0, D3, EDO, EO, E1, E2, E8, E9, E20, F1, G0, G5, G7, KO, K1, K3, K4, KAO, KA1, KA3, KAA, LO, L1, L5, L6, N4, P2			200-68HC11	D, P	16	64KB, 256K, 1MB	No	2@4K	Opt	Opt
	68HC16	68HC16Z1, Z2			200-68HC16	C, F, K, M, X	25	256KB, 1MB	256KB	2@16K	Opt	Std
		68HC16Y1			200-68HC16	C, F, X	25	256KB, 1MB	256KB	2@16K	Opt	Std
	68000	68000, 68HC000, 68HC001			200-68000	D, G, P	20	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68008			200-68000	D, P	20	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68010			200-68000	D, G, P	20	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68EC000			200-68000	P	20	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
	68020	68020, 68EC020			200-68020	G	33	256KB, 1, 2MB	No	2@16K	Opt	Std
	68030	68030, 68EC030		✓	200-68030	G	40	512KB, 1, 2, 4MB	No	2@16K	Opt	Std
	68040	68040, 68EC040, 68LC040			200-68040	F, G	33	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
	68060	68060, 68EC060, 68LC060			2000-68060	G	66	512KB, 1, 2, 4MB	512KB, 2MB	128K	Std	Std
	68302	68302 (3V & 5V)			200-68302	F, G, K, M, X	25	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68PMB02 (3V & 5V)			200-68302	G, K	25	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68LC302 (3V & 5V)			200-68302	G, K	25	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68EN302			200-68302	G, K	25	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
		68PH302, 68QH302			200-68302	G, K	25	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
	68306	68306			200-68306	C, F, K, M, X	16	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
	68307	68307 (3V & 5V)			200-68307	C, F, K	16	256KB, 1, 2, 4MB	No	2@16K	Opt	Std
	68330	68330	✓		200-CPU32	C, F, M, X	25	256KB, 1, 2MB	256KB	2@16K	Opt	Std
68331	68331	✓	✓	200-CPU32	C, F, G, M, X	25	256KB, 1, 2MB	256KB	2@16K	Opt	Std	
68332	68332	✓	✓	200-CPU32	C, F, G, M, X	25	256KB, 1, 2MB	256KB	2@16K	Opt	Std	
68F333	68F333	✓		200-CPU32	C, F, G, M, X	25	256KB, 1, 2MB	256KB	2@16K	Opt	Std	
68340	68340	✓	✓	200-CPU32	C, F, G	25	256KB, 1, 2MB	256KB	2@16K	Opt	Std	
68349	68349 (3V & 5V)	✓	✓	200-CPU32+	F, X	33	256KB, 1, 2MB	128KB, 512KB	2@16K	Opt	Std	
68360	68EN360, 68360, 68MH360	✓	✓	200-CPU32+	F, G	33	256KB, 1, 2MB	128KB, 512KB	2@16K	Opt	Std	
68356	68356 (68302 Core Only)			200-68302	B	25	512KB, 1, 2, 4MB	512KB, 2MB	2@16K	Opt	Std	
CFire	MCF5202, 5204, 5206	✓		N/A	Debug	40	N/A	N/A	N/A	N/A	N/A	
MPC5xx	MPC505	✓		2000-PowerPC	F, X	44	512KB, 1, 2, 4MB	512KB, 2MB	128K	Std	Std	
MPC8xx	MPC801, 821, 823, 860,	✓		2000-PowerPC	B	40	512KB, 1, 2, 4MB	512KB, 2MB	128K	Std	Std	
	MPC860DC/DE/DH/EN/MI/SAR	✓		2000-PowerPC	B	40	512KB, 1, 2, 4MB	512KB, 2MB	128K	Std	Std	
OKI	8051	80C51, 80C59, 80C154			200-8051	D, P	24	128KB, 256KB	No	2@4K	Opt	Std
PHILIPS	8051	8051, 8052, 8031, 8032, 80C451, 80C552, 80C652, 80C751			200-8051	D, P	24	128KB, 256KB	No	2@4K	Opt	Std
SIEMENS	8051	8051, 8052, 8031, 8032, 80515, 80535, 80537			200-8051	D, P	24	128KB, 256KB	No	2@4K	Opt	Std
	8085	8085A, AH			200-8085	D, P	12	256KB, 1MB	No	2@4K	No	Std
ZILOG	Z80	Z80, Z80A, Z80B, Z80H			200-Z80	D, P	18	256KB, 512KB	No	2@4K	No	Std
	Z180	Z181, Z182, 8S180			200-64180	D, P	18	256KB, 1MB	No	2@4K	No	Opt

**Available Interfaces:**

**B** = Ball Grid Array to PGA adapter    **F** = Solder-In QFP surface mount adapter    **M** = Mates with 3M QFP socket  
**C** = Clip-On for QFP (non-TQFP)    **G** = PGA package interface    **P** = PLCC package interface  
**D** = DIP package interface    **K** = Solder-In TQFP surface mount adapter    **X** = Mates with AMP QFP socket

**BMD** = HMI Background Mode Debugger Available  
**ENET** = Ethernet

**SIM** = HMI CPU Simulator Available  
**PAC** = Performance Analysis Card