

CONTROL DATA[®] 6601-H/6613-A CENTRAL COMPUTER

**WIRE LISTS
CABLE TABS**

CONTROL DATA
CORPORATION

CUSTOMER ENGINEERING MANUAL

This manual lists connections of the coaxial logic cables in three sections:

- 1) Chassis location, source and destination, arranged by chassis number;
- 2) Wire connection, source and destination, arranged by chassis number, chassis location, and color;
- 3) Wire connection, arranged by chassis, module and pin number.

The Dead Start Panel connections are listed only in parts one and two, by connection to the chassis.

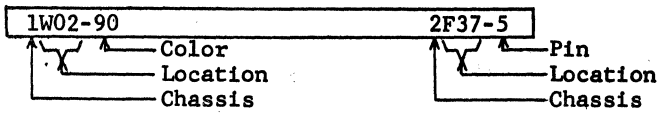
Locations W39 and W40 do not physically exist. These numbers are used for sorting purposes. W39 and W40 are internal chassis connections.

Chassis Locations: 1-16 on Main Frame 6601
 30 on Disk File
 50 on Display Console
 81 on 6681 Data Channel Converter
 82 on 6682 Satellite Coupler

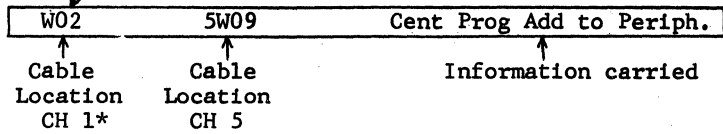
Reference Drawings:

63763600-A	Wire listing - Dead Start Panel
63028800	Chassis Cable Tabs, 6601
63029000	Cable list, inter chassis
63028900	Cable list, inter cabinet
63037300	Cable assembly, wire list, intra chassis

Explanation of Listings

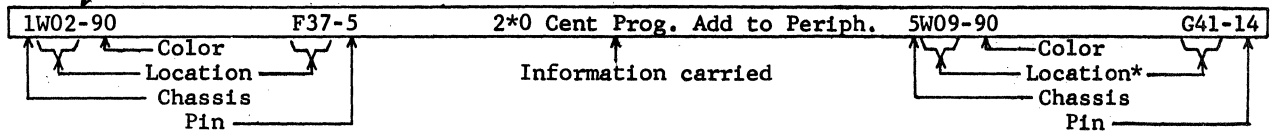


Part 1: Listed by



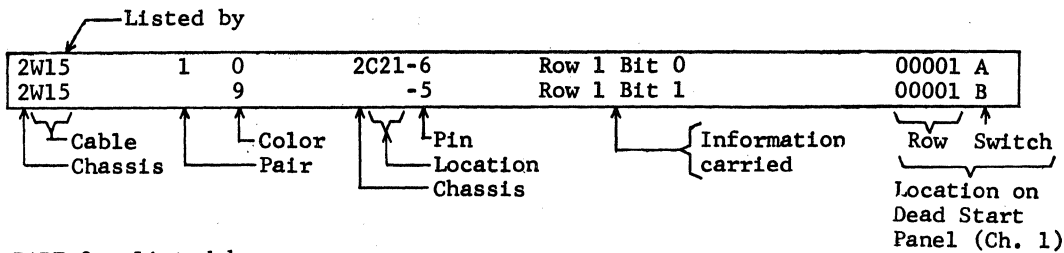
* Chassis number listed at top of each page.

PART 2: Listed by

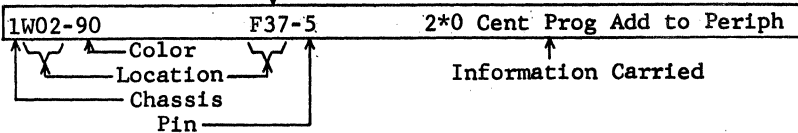


Part 2 (Dead Start)

* Both locations on same chassis



PART 3: Listed by



CABLE CONNECTIONS FOR 6601 CHASSIS 1

ORIGIN	DEST	REMARKS
W01	3W21	CONTROL (1TO 5VIA 3)
W02	5W09	CENTRAL PROGRAM ADDRESS TO PERIPHERAL
W03	3W22	READ-WRITE EXCHANGE ADDRESS
W04	3W03	CENTRAL MEMORY DATA TO PERIPHERAL
W05	4W07	CENTRAL MEMORY DATA TO PERIPHERAL
W06	9W11	CENTRAL MEMORY DATA TO PERIPHERAL
W07	10W11	CENTRAL MEMORY DATA TO PERIPHERAL
W08	2W01	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W09	2W02	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W10	2W03	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W11	2W04	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W12	5W23	CONTROL TO PERIPHERAL
W13		CHANNEL 0
W14		CHANNEL 0
W15		CHANNEL 1
W16		CHANNEL 1
W17		CHANNEL 2
W18		CHANNEL 2
W19		CHANNEL 3
W20		CHANNEL 3
W21		CHANNEL 4
W22		CHANNEL 4
W23		CHANNEL 5
W24		CHANNEL 5
W25		CHANNEL 6
W26		CHANNEL 6
W27		CHANNEL 7
W28		CHANNEL 7
W29		CHANNEL 10
W30		CHANNEL 10
W31		CHANNEL 11
W32		CHANNEL 11
W33		CHANNEL 12
W34		CHANNEL 12
W35		CHANNEL 13
W36		CHANNEL 13
W37	CABLE 5	DEAD START PANEL (SEE 63763600)
W38	W51	MAINTENANCE PANEL

CABLE CONNECTIONS FOR 6601 CHASSIS 2

ORIGIN	DEST	REMARKS
W01	1W08	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W02	1W09	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W03	1W10	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W04	1W11	PERIPHERAL DATA TO MEMORY DISTRIBUTION
W05	13W10	WRITE BUFFER TO DISTRIBUTOR
W06	14W10	WRITE BUFFER TO DISTRIBUTOR
W07	15W10	WRITE BUFFER TO DISTRIBUTOR
W08	16W10	WRITE BUFFER TO DISTRIBUTOR
W09	7W08	LOWER REGISTER TO MEMORY DISTRIBUTION
W10	7W09	LOWER REGISTER TO MEMORY DISTRIBUTION
W11	8W10	UPPER REGISTER TO MEMORY DISTRIBUTION
W12	8W11	UPPER REGISTER TO MEMORY DISTRIBUTION
W13	5W10	CENTRAL CONTROL TO MEMORY DISTRIBUTION
W14	5W27	CENTRAL CONTROL TO MEMORY DISTRIBUTION
W15	CABLE 1	DEAD START PANEL
W16	CABLE 2	DEAD START PANEL
W17	CABLE 3	DEAD START PANEL
W18	CABLE 4	DEAD START PANEL
W19		DEAD START TO CHANNEL (INPUT)
W20		DEAD START TO CHANNEL (OUTPUT)
W21	5W13	CONTROL TO DIVIDE
W22	8W18	REGISTER TO DIVIDE (EXPONENT XK)
W23	8W19	REGISTER TO DIVIDE (EXPONENT XJ)
W24	6W20	DIVIDE TO MULTIPLY
W25	6W21	DIVIDE TO MULTIPLY
W26	6W22	DIVIDE TO MULTIPLY
W27	6W23	MULTIPLY TO DIVIDE
W28	6W24	MULTIPLY TO DIVIDE
W29	6W25	MULTIPLY TO DIVIDE
W30	6W26	MULTIPLY TO DIVIDE
W31	6W27	MULTIPLY TO DIVIDE
W32	6W28	MULTIPLY TO DIVIDE
W33		
W34		
W35		
W36		
W37		
W38	W52	MAINTENANCE PANEL

CABLE CONNECTIONS FOR 6601 CHASSIS 3

ORIGIN	DEST	REMARKS
W01		
W02	9W13	MEMORY TO READ DISTRIBUTOR
W03	1W04	MEMORY TO PERIPHERAL
W04	10W13	MEMORY TO READ DISTRIBUTOR
W05	4W05	MEMORY TO READ DISTRIBUTOR
W06	4W06	MEMORY TO READ DISTRIBUTOR
W07	9W07	MEMORY TO READ DISTRIBUTOR
W08	10W07	MEMORY TO READ DISTRIBUTOR
W09	13W06	MEMORY TO READ DISTRIBUTOR
W10	14W06	MEMORY TO READ DISTRIBUTOR
W11	15W06	MEMORY TO READ DISTRIBUTOR
W12	7W05	READ DISTRIBUTOR TO LOWER REGISTER
W13	16W06	MEMORY TO READ DISTRIBUTOR
W14	5W02	READ DISTRIBUTOR TO CONTROL
W15	13W13	WRITE DISTRIBUTOR TO MEMORY
W16	14W13	WRITE DISTRIBUTOR TO MEMORY
W17	15W13	WRITE DISTRIBUTOR TO MEMORY
W18	16W13	WRITE DISTRIBUTOR TO MEMORY
W19		
W20	5W29	CONTROL TO MEMORY ADDRESS
W21	1W01	CONTROL (1 TO 5 VIA 3)
W22	1W03	READ-WRITE EXCHANGE ADDRESS
W23	5W24	READ-WRITE EXCHANGE ADDRESS
W24	5W21	CONTROL (1 TO 5 VIA 3)
W25		
W26		
W27		
W28		
W29		
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W53	MAINTENANCE PANEL
W39	3W39	MEMORY TO READ DISTRIBUTOR

CABLE CONNECTIONS FOR 6601 CHASSIS 4

ORIGIN	DEST	REMARKS
W01		
W02		
W03	13W12	WRITE DISTRIBUTOR TO MEMORY
W04	14W12	WRITE DISTRIBUTOR TO MEMORY
W05	3W05	MEMORY TO READ DISTRIBUTOR
W06	3W06	MEMORY TO READ DISTRIBUTOR
W07	1W05	MEMORY TO PERIPHERAL
W08	9W14	MEMORY TO READ DISTRIBUTOR
W09	15W12	WRITE DISTRIBUTOR TO MEMORY
W10	16W12	WRITE DISTRIBUTOR TO MEMORY
W11	10W14	MEMORY TO READ DISTRIBUTOR
W12	9W08	MEMORY TO READ DISTRIBUTOR
W13	10W08	MEMORY TO READ DISTRIBUTOR
W14	5W01	READ DISTRIBUTOR TO CONTROL
W15	13W07	MEMORY TO READ DISTRIBUTOR
W16	14W07	MEMORY TO READ DISTRIBUTOR
W17	15W07	MEMORY TO READ DISTRIBUTOR
W18	7W06	READ DISTRIBUTOR TO LOWER REGISTER
W19	5W18	READ DISTRIBUTOR TO CONTROL
W20	5W28	CONTROL TO MEMORY ADDRESS
W21	16W07	MEMORY TO READ DISTRIBUTOR
W22		
W23		
W24		DISK SYNC PASS ON (OUTPUT)
W25		DISK SYNC PASS ON (INPUT)
W26		DISK SYNC TO CHANNEL (INPUT)
W27		DISK SYNC TO CHANNEL (OUTPUT)
W28	30W03	DISK SYNC TO DISK FILE (OUTPUT)
W29	30W04	DISK SYNC TO DISK FILE (INPUT)
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W54	MAINTENANCE PANEL
W39	4W39	MEMORY TO READ DISTRIBUTOR

CABLE CONNECTIONS FOR 6601 CHASSIS 5

ORIGIN	DEST	REMARKS
W01	4W14	READ DISTRIBUTOR TO CONTROL
W02	3W14	READ DISTRIBUTOR TO CONTROL
W03	10W10	READ DISTRIBUTOR TO CONTROL
W04	9W10	READ DISTRIBUTOR TO CONTROL
W05		
W06		
W07		
W08	7W21	INCREMENT RESULT
W09	1W02	PROGRAM ADDRESS TO PERIPHERAL
W10	2W13	CENTRAL CONTROL TO MEMORY DISTRIBUTION
W11	8W13	CONTROL TO UPPER REGISTER
W12	8W12	CONTROL TO LONG ADD, ADD, AND SHIFT
W13	2W21	CONTROL TO DIVIDE
W14	6W16	CONTROL TO MULTIPLY
W15	8W20	CONTROL TO UPPER REGISTER
W16	7W17	CONTROL TO LOWER REGISTER
W17	8W21	CONTROL TO UPPER REGISTER
W18	4W19	READ DISTRIBUTOR TO CONTROL
W19	7W18	CONTROL TO LOWER REGISTER
W20	7W19	CONTROL TO LOWER REGISTER
W21	3W24	CONTROL (1 TO 5 VIA 3)
W22	7W16	CONTROL TO LOWER REGISTER
W23	1W12	CONTROL TO PERIPHERAL
W24	3W23	READ-WRITE EXCHANGE ADDRESS
W25	7W20	CONTROL TO LOWER REGISTER
W26	8W22	CONTROL TO UPPER REGISTER
W27	2W14	CONTROL TO MEMORY DISTRIBUTOR
W28	4W20	CONTROL TO MEMORY ADDRESS
W29	3W20	CONTROL TO MEMORY ADDRESS
W30	9W20	CONTROL TO MEMORY ADDRESS
W31	10W20	CONTROL TO MEMORY ADDRESS
W32	13W18	CONTROL TO MEMORY ADDRESS
W33	14W18	CONTROL TO MEMORY ADDRESS
W34	15W18	CONTROL TO MEMORY ADDRESS
W35	16W18	CONTROL TO MEMORY ADDRESS
W36	7W22	INCREMENT OPERAND
W37	7W23	INCREMENT OPERAND
W38	W51	MAINTENANCE PANEL

CABLE CONNECTIONS FOR 6601 CHASSIS 6

ORIGIN	DEST	REMARKS
W01		
W02		
W03		
W04		
W05		
W06		
W07	8W07	REGISTER TO MULTIPLY XK
W08	8W08	MULTIPLY TO REGISTER
W09	8W09	REGISTER TO MULTIPLY XJ
W10	7W10	MULTIPLY TO REGISTER
W11	7W11	MULTIPLY TO REGISTER
W12	7W12	REGISTER TO MULTIPLY XK
W13	7W13	REGISTER TO MULTIPLY XJ
W14	7W14	REGISTER TO MULTIPLY XJ
W15	7W15	REGISTER TO MULTIPLY XK
W16	5W14	CONTROL TO MULTIPLY
W17		
W18		
W19		
W20	2W24	DIVIDE TO MULTIPLY
W21	2W25	DIVIDE TO MULTIPLY
W22	2W26	DIVIDE TO MULTIPLY
W23	2W27	MULTIPLY TO DIVIDE
W24	2W28	MULTIPLY TO DIVIDE
W25	2W29	MULTIPLY TO DIVIDE
W26	2W30	MULTIPLY TO DIVIDE
W27	2W31	MULTIPLY TO DIVIDE
W28	2W32	MULTIPLY TO DIVIDE
W29		
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W52	MAINTENANCE PANEL

CABLE CONNECTIONS FOR 6601 CHASSIS 7

ORIGIN	DEST	REMARKS
W01		
W02		
W03		
W04		
W05	3W12	READ DISTRIBUTOR TO LOWER REGISTER
W06	4W18	READ DISTRIBUTOR TO LOWER REGISTER
W07	9W12	READ DISTRIBUTOR TO LOWER REGISTER
W08	2W09	LOWER REGISTER TO MEMORY DISTRIBUTION
W09	2W10	LOWER REGISTER TO MEMORY DISTRIBUTION
W10	6W10	MULTIPLY TO REGISTER
W11	6W11	MULTIPLY TO REGISTER
W12	6W12	REGISTER TO MULTIPLY XK
W13	6W13	REGISTER TO MULTIPLY XJ
W14	6W14	REGISTER TO MULTIPLY XJ
W15	6W15	REGISTER TO MULTIPLY XK
W16	5W22	CONTROL TO LOWER REGISTER
W17	5W16	CONTROL TO LOWER REGISTER
W18	5W19	CONTROL TO LOWER REGISTER
W19	5W20	CONTROL TO LOWER REGISTER
W20	5W25	CONTROL TO LOWER REGISTER
W21	5W08	INCREMENT RESULT
W22	5W36	INCREMENT OPERAND
W23	5W37	INCREMENT OPERAND
W24	8W26	REGISTER TO ADD
W25	8W27	REGISTER TO ADD
W26	8W24	ADD TO REGISTER
W27	8W25	ADD TO REGISTER
W28	8W28	REGISTER TO ADD
W29	8W29	REGISTER TO ADD
W30	8W23	D TO X
W31	8W30	SHIFT TO REGISTER
W32		
W33		
W34		
W35		
W36		
W37		
W38	W53	MAINTENANCE PANEL

CABLE CONNECTIONS FOR 6601 CHASSIS 8

ORIGIN	DEST	REMARKS
W01		
W02		
W03		
W04		
W05	9W21	READ DISTRIBUTOR TO UPPER REGISTER
W06	10W12	READ DISTRIBUTOR TO UPPER REGISTER
W07	6W07	REGISTER TO MULTIPLY XK
W08	6W08	MULTIPLY TO REGISTER
W09	6W09	REGISTER TO MULTIPLY XJ
W10	2W11	UPPER REGISTER TO MEMORY DISTRIBUTION
W11	2W12	UPPER REGISTER TO MEMORY DISTRIBUTION
W12	5W12	CONTROL TO LONG ADD, ADD AND SHIFT
W13	5W11	CONTROL TO UPPER REGISTER
W14		
W15		
W16		
W17		
W18	2W22	REGISTER TO DIVIDE (EXPONENT XK)
W19	2W23	REGISTER TO DIVIDE (EXPONENT XJ)
W20	5W15	CONTROL TO UPPER REGISTER
W21	5W17	CONTROL TO UPPER REGISTER
W22	5W26	CONTROL TO UPPER REGISTER
W23	7W30	D TO X
W24	7W26	ADD TO REGISTER
W25	7W27	ADD TO REGISTER
W26	7W24	REGISTER TO ADD
W27	7W25	REGISTER TO ADD
W28	7W28	REGISTER TO ADD
W29	7W29	REGISTER TO ADD
W30	7W31	SHIFT TO REGISTER
W31		
W32		
W33		
W34		
W35		
W36		
W37		VOLTMETER PANEL
W38	W54	MAINTENANCE PANEL

CABLE CONNECTIONS FOR 6601 CHASSIS 9

ORIGIN	DEST	REMARKS
W01		
W02		
W03	13W14	WRITE DISTRIBUTOR TO MEMORY
W04	14W14	WRITE DISTRIBUTOR TO MEMORY
W05	15W14	WRITE DISTRIBUTOR TO MEMORY
W06	16W14	WRITE DISTRIBUTOR TO MEMORY
W07	3W07	MEMORY TO READ DISTRIBUTOR
W08	4W12	MEMORY TO READ DISTRIBUTOR
W09	10W15	MEMORY TO READ DISTRIBUTOR
W10	5W04	READ DISTRIBUTOR TO CONTROL
W11	1W06	MEMORY TO PERIPHERAL
W12	7W07	READ DISTRIBUTOR TO LOWER REGISTER
W13	3W02	MEMORY TO READ DISTRIBUTOR
W14	4W08	MEMORY TO READ DISTRIBUTOR
W15	10W09	MEMORY TO READ DISTRIBUTOR
W16	13W08	MEMORY TO READ DISTRIBUTOR
W17	14W08	MEMORY TO READ DISTRIBUTOR
W18	15W08	MEMORY TO READ DISTRIBUTOR
W19	16W08	MEMORY TO READ DISTRIBUTOR
W20	5W30	CONTROL TO MEMORY ADDRESS
W21	8W05	READ DISTRIBUTOR TO UPPER REGISTER
W22	TUA J11	626 (SEE 63037300)
W23	TUA J09	626 (SEE 63037300)
W24	TUA J10	626 (SEE 63037300)
W25	TUB J11	626 (SEE 63037300)
W26	TUB J09	626 (SEE 63037300)
W27	TUB J10	626 (SEE 63037300)
W28	TUC J11	626 (SEE 63037300)
W29	TUC J09	626 (SEE 63037300)
W30	TUC J10	626 (SEE 63037300)
W31	TUD J11	626 (SEE 63037300)
W32	TUD J09	626 (SEE 63037300)
W33	TUD J10	626 (SEE 63037300)
W34		626 SYNC TO CHANNEL (INPUT)
W35		626 SYNC TO CHANNEL (OUTPUT)
W36		626 SYNC PASS ON (INPUT)
W37		626 SYNC PASS ON (OUTPUT)
W38	W51	MAINTENANCE PANEL
W39	9W39	MEMORY TO READ DISTRIBUTOR

CABLE CONNECTIONS FOR 6601 CHASSIS 10

ORIGIN	DEST	REMARKS
W01		
W02		
W03	13W15	WRITE DISTRIBUTOR
W04	14W15	WRITE DISTRIBUTOR
W05	15W15	WRITE DISTRIBUTOR
W06	16W15	WRITE DISTRIBUTOR
W07	3W08	MEMORY TO READ DISTRIBUTOR
W08	4W13	MEMORY TO READ DISTRIBUTOR
W09	9W15	MEMORY TO READ DISTRIBUTOR
W10	5W03	READ DISTRIBUTOR TO CONTROL
W11	1W07	MEMORY TO PERIPHERAL
W12	8W06	READ DISTRIBUTOR TO UPPER REGISTER
W13	3W04	MEMORY TO READ DISTRIBUTOR
W14	4W11	MEMORY TO READ DISTRIBUTOR
W15	9W09	MEMORY TO READ DISTRIBUTOR
W16	13W09	MEMORY TO READ DISTRIBUTOR
W17	14W09	MEMORY TO READ DISTRIBUTOR
W18	15W09	MEMORY TO READ DISTRIBUTOR
W19	16W09	MEMORY TO READ DISTRIBUTOR
W20	5W31	CONTROL TO MEMORY ADDRESS
W21		
W22		
W23	30W04	DISK SYNC TO DISK FILE (INPUT)
W24	30W03	DISK SYNC TO DISK FILE (OUTPUT)
W25		DISK SYNC PASS ON (OUTPUT)
W26		DISK SYNC PASS ON (INPUT)
W27		DISK SYNC TO CHANNEL (INPUT)
W28		DISK SYNC TO CHANNEL (OUTPUT)
W29		
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W52	MAINTENANCE PANEL
W39	10W39	MEMORY TO READ DISTRIBUTOR

CABLE CONNECTIONS FOR 6601 CHASSIS 12

ORIGIN	DEST	REMARKS
W01		DISPLAY SYNC 1 TO CHANNEL (OUTPUT)
W02		DISPLAY SYNC 1 TO CHANNEL (INPUT)
W03		DISPLAY SYNC 1 PASSON (OUTPUT)
W04		DISPLAY SYNC 1 PASSON (INPUT)
W05	50W01	DISPLAY SYNC 1 TO CONSOLE 0 (OUTPUT)
W06	50W02	DISPLAY SYNC 1 TO CONSOLE 0 (INPUT)
W07	50W01	DISPLAY SYNC 1 TO CONSOLE 1 (OUTPUT)
W08	50W02	DISPLAY SYNC 1 TO CONSOLE 1 (INPUT)
W09		
W10		
W11		
W12		
W13		
W14		DISPLAY SYNC 2 TO CHANNEL (OUTPUT)
W15		DISPLAY SYNC 2 TO CHANNEL (INPUT)
W16		DISPLAY SYNC 2 PASSON (OUTPUT)
W17		DISPLAY SYNC 2 PASSON (INPUT)
W18	50W01	DISPLAY SYNC 2 TO CONSOLE 0 (OUTPUT)
W19	50W02	DISPLAY SYNC 2 TO CONSOLE 0 (INPUT)
W20	50W01	DISPLAY SYNC 2 TO CONSOLE 1 (OUTPUT)
W21	50W02	DISPLAY SYNC 2 TO CONSOLE 1 (INPUT)
W22		
W23		
W24		
W25		
W26		
W27		
W28		
W29		
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38		

CABLE CONNECTIONS FOR 6601 CHASSIS 13

ORIGIN	DEST	REMARKS
W01		
W02		
W03	14W11	WRITE DISTRIBUTOR TO MEMORY
W04	15W11	WRITE DISTRIBUTOR TO MEMORY
W05	16W11	WRITE DISTRIBUTOR TO MEMORY
W06	3W09	MEMORY TO READ DISTRIBUTOR
W07	4W15	MEMORY TO READ DISTRIBUTOR
W08	9W16	MEMORY TO READ DISTRIBUTOR
W09	10W16	MEMORY TO READ DISTRIBUTOR
W10	2W05	WRITE BUFFER TO DISTRIBUTOR
W11	14W03	WRITE DISTRIBUTOR TO MEMORY
W12	4W03	WRITE DISTRIBUTOR TO MEMORY
W13	3W15	WRITE DISTRIBUTOR TO MEMORY
W14	9W03	WRITE DISTRIBUTOR TO MEMORY
W15	10W03	WRITE DISTRIBUTOR TO MEMORY
W16	15W03	WRITE DISTRIBUTOR TO MEMORY
W17	16W03	WRITE DISTRIBUTOR TO MEMORY
W18	5W32	CONTROL TO MEMORY ADDRESS
W19		
W20		
W21		
W22	TUA J11	626 (SEE 63037300)
W23	TUA J09	626 (SEE 63037300)
W24	TUA J10	626 (SEE 63037300)
W25	TUB J11	626
W26	TUB J09	626
W27	TUB J10	626
W28	TUC J11	626
W29	TUC J09	626
W30	TUC J10	626
W31	TUD J11	626
W32	TUD J09	626
W33	TUD J10	626
W34		626 SYNC TO CHANNEL (INPUT)
W35		626 SYNC TO CHANNEL (OUTPUT)
W36		626 SYNC PASS ON (INPUT)
W37		626 SYNC PASS ON (OUTPUT)
W38	W51	MAINTENANCE PANEL
W39	13W39	WRITE DISTRIBUTOR TO MEMORY

CABLE CONNECTIONS FOR 6601 CHASSIS 14

ORIGIN	DEST	REMARKS
W01		
W02		
W03	13W11	WRITE DISTRIBUTOR TO MEMORY
W04	15W16	WRITE DISTRIBUTOR TO MEMORY
W05	16W16	WRITE DISTRIBUTOR TO MEMORY
W06	3W10	MEMORY TO READ DISTRIBUTOR
W07	4W16	MEMORY TO READ DISTRIBUTOR
W08	9W17	MEMORY TO READ DISTRIBUTOR
W09	10W17	MEMORY TO READ DISTRIBUTOR
W10	2W06	WRITE BUFFER TO DISTRIBUTOR
W11	13W03	WRITE DISTRIBUTOR TO MEMORY
W12	4W04	WRITE DISTRIBUTOR TO MEMORY
W13	3W16	WRITE DISTRIBUTOR TO MEMORY
W14	9W04	WRITE DISTRIBUTOR TO MEMORY
W15	10W04	WRITE DISTRIBUTOR TO MEMORY
W16	15W04	WRITE DISTRIBUTOR TO MEMORY
W17	16W04	WRITE DISTRIBUTOR TO MEMORY
W18	5W33	CONTROL TO MEMORY ADDRESS
W19		
W20		
W21		
W22		
W23		
W24		
W25		
W26		
W27		
W28		
W29		
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W52	MAINTENANCE PANEL
W39	14W39	WRITE DISTRIBUTOR TO MEMORY

CABLE CONNECTIONS FOR 6601 CHASSIS 15

ORIGIN	DEST	REMARKS
W01		
W02		
W03	13W16	WRITE DISTRIBUTOR TO MEMORY
W04	14W16	WRITE DISTRIBUTOR TO MEMORY
W05	16W17	WRITE DISTRIBUTOR TO MEMORY
W06	3W11	MEMORY TO READ DISTRIBUTOR
W07	4W17	MEMORY TO READ DISTRIBUTOR
W08	9W18	MEMORY TO READ DISTRIBUTOR
W09	10W18	MEMORY TO READ DISTRIBUTOR
W10	2W07	WRITE BUFFER TO DISTRIBUTOR
W11	13W04	WRITE DISTRIBUTOR TO MEMORY
W12	4W09	WRITE DISTRIBUTOR TO MEMORY
W13	3W17	WRITE DISTRIBUTOR TO MEMORY
W14	9W05	WRITE DISTRIBUTOR TO MEMORY
W15	10W05	WRITE DISTRIBUTOR TO MEMORY
W16	14W04	WRITE DISTRIBUTOR TO MEMORY
W17	16W05	WRITE DISTRIBUTOR TO MEMORY
W18	5W34	CONTROL TO MEMORY ADDRESS
W19		
W20		
W21		
W22		
W23		
W24		
W25		
W26		
W27		
W28		
W29		
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W53	MAINTENANCE PANEL
W39	15W39	WRITE DISTRIBUTOR TO MEMORY

CABLE CONNECTIONS FOR 6601 CHASSIS 16

ORIGIN	DEST	REMARKS
W01		
W02		
W03	13W17	WRITE DISTRIBUTOR TO MEMORY
W04	14W17	WRITE DISTRIBUTOR TO MEMORY
W05	15W17	WRITE DISTRIBUTOR TO MEMORY
W06	3W13	MEMORY TO READ DISTRIBUTOR
W07	4W21	MEMORY TO READ DISTRIBUTOR
W08	9W19	MEMORY TO READ DISTRIBUTOR
W09	10W19	MEMORY TO READ DISTRIBUTOR
W10	2W08	WRITE BUFFER TO DISTRIBUTOR
W11	13W05	WRITE DISTRIBUTOR TO MEMORY
W12	4W10	WRITE DISTRIBUTOR TO MEMORY
W13	3W18	WRITE DISTRIBUTOR TO MEMORY
W14	9W06	WRITE DISTRIBUTOR TO MEMORY
W15	10W06	WRITE DISTRIBUTOR TO MEMORY
W16	14W05	WRITE DISTRIBUTOR TO MEMORY
W17	15W05	WRITE DISTRIBUTOR TO MEMORY
W18	5W35	CONTROL TO MEMORY ADDRESS
W19		
W20		
W21		
W22		
W23		
W24		DISK SYNC PASS ON (OUTPUT)
W25		DISK SYNC PASS ON (INPUT)
W26		DISK SYNC TO CHANNEL (INPUT)
W27		DISK SYNC TO CHANNEL (OUTPUT)
W28	30W03	DISK SYNC TO DISK FILE (OUTPUT)
W29	30W04	DISK SYNC TO DISK FILE (INPUT)
W30		
W31		
W32		
W33		
W34		
W35		
W36		
W37		
W38	W54	MAINTENANCE PANEL
W39	16W39	WRITE DISTRIBUTOR TO MEMORY

1W01	00	CONTROL (1 TO 5 VIA 3)			3W21	00
1W01	90				3W21	90
1W01	91				3W21	91
1W01	92				3W21	92
1W01	93				3W21	93
1W01	94				3W21	94
1W01	95				3W21	95
1W01	96				3W21	96
1W01	97				3W21	97
1W01	98				3W21	98
1W01	99				3W21	99
1W01	900	H02	12	CLOCK	3W21	900 J37 27
1W01	901	I30	27	RD GO	3W21	901 J39 10
1W01	902	I30	25	WR GO	3W21	902 J39 9
1W01	903	I30	8	EXCH GO	3W21	903 J39 8
1W01	904				3W21	904
1W01	905				3W21	905
1W01	906				3W21	906
1W01	907				3W21	907
1W01	908				3W21	908

1W02	00	PERIPHERAL FROM CENTRAL PROGRAM ADDRESS			5W09	00
1W02	90	F37	5	2*0 CENTRAL PROGRAM ADDRESS	5W09	90 G41 4
1W02	91	F37	7	2*1 CENTRAL PROGRAM ADDRESS	5W09	91 G41 6
1W02	92	F37	10	2*2 CENTRAL PROGRAM ADDRESS	5W09	92 G41 8
1W02	93	F37	21	2*3 CENTRAL PROGRAM ADDRESS	5W09	93 G41 10
1W02	94	F37	24	2*4 CENTRAL PROGRAM ADDRESS	5W09	94 G41 12
1W02	95	F37	26	2*5 CENTRAL PROGRAM ADDRESS	5W09	95 G41 19
1W02	96	F38	5	2*6 CENTRAL PROGRAM ADDRESS	5W09	96 G41 21
1W02	97	F38	7	2*7 CENTRAL PROGRAM ADDRESS	5W09	97 G41 23
1W02	98	F38	10	2*8 CENTRAL PROGRAM ADDRESS	5W09	98 G41 25
1W02	99	F38	21	2*9 CENTRAL PROGRAM ADDRESS	5W09	99 G41 27
1W02	900	F38	24	2*10 CENTRAL PROGRAM ADDRESS	5W09	900 G42 4
1W02	901	F38	26	2*11 CENTRAL PROGRAM ADDRESS	5W09	901 G42 6
1W02	902	F39	5	2*12 CENTRAL PROGRAM ADDRESS	5W09	902 G42 8
1W02	903	F39	7	2*13 CENTRAL PROGRAM ADDRESS	5W09	903 G42 10
1W02	904	F39	10	2*14 CENTRAL PROGRAM ADDRESS	5W09	904 G42 12
1W02	905	F39	21	2*15 CENTRAL PROGRAM ADDRESS	5W09	905 G42 19
1W02	906	F39	24	2*16 CENTRAL PROGRAM ADDRESS	5W09	906 G42 21
1W02	907	F39	26	2*17 CENTRAL PROGRAM ADDRESS	5W09	907 G42 23
1W02	908				5W09	908

1W03	00	READWRITE EXCHANGE ADDRESS				3W22	00
1W03	90					3W22	90
1W03	91	F41	27	BIT 9		3W22	91 J41 10
1W03	92	F41	25	BIT 10		3W22	92 J41 9
1W03	93	F41	23	BIT 11		3W22	93 J41 8
1W03	94	F42	8	BIT 12		3W22	94 J42 23
1W03	95	F42	6	BIT 13		3W22	95 J42 22
1W03	96	G42	4	BIT 14		3W22	96 J42 21
1W03	97	F42	27	BIT 15		3W22	97 J42 10
1W03	98	F42	25	BIT 16		3W22	98 J42 9
1W03	99	F42	23	BIT 17		3W22	99 J42 8
1W03	900	F41	4	BIT 8		3W22	900 J41 21
1W03	901	F40	8	BIT 0		3W22	901 J40 23
1W03	902	F40	6	BIT 1		3W22	902 J40 22
1W03	903	F40	4	BIT 2		3W22	903 J40 21
1W03	904	F40	27	BIT 3		3W22	904 J40 10
1W03	905	F40	25	BIT 4		3W22	905 J40 9
1W03	906	F40	23	BIT 5		3W22	906 J40 8
1W03	907	F41	8	BIT 6		3W22	907 J41 23
1W03	908	F41	6	BIT 7		3W22	908 J41 22

1W04	00	PERIPHERAL FROM CENTRAL MEMORY				3W03	00
1W04	90	G33	5	2*0 CENTRAL TO PERIPHERAL DATA		3W03	90 I25 1
1W04	91	G33	7	2*1 CENTRAL TO PERIPHERAL DATA		3W03	91 I26 1
1W04	92	G33	10	2*2 CENTRAL TO PERIPHERAL DATA		3W03	92 I27 1
1W04	93	G33	21	2*3 CENTRAL TO PERIPHERAL DATA		3W03	93 I28 1
1W04	94	G33	24	2*4 CENTRAL TO PERIPHERAL DATA		3W03	94 I29 1
1W04	95	G33	26	2*5 CENTRAL TO PERIPHERAL DATA		3W03	95 I32 1
1W04	96	G34	5	2*6 CENTRAL TO PERIPHERAL DATA		3W03	96 I33 1
1W04	97	G34	7	2*7 CENTRAL TO PERIPHERAL DATA		3W03	97 I34 1
1W04	98	G34	10	2*8 CENTRAL TO PERIPHERAL DATA		3W03	98 I35 1
1W04	99	G34	21	2*9 CENTRAL TO PERIPHERAL DATA		3W03	99 I36 1
1W04	900	G34	24	2*10 CENTRAL TO PERIPHERAL DATA		3W03	900 I38 1
1W04	901	G34	26	2*11 CENTRAL TO PERIPHERAL DATA		3W03	901 I39 1
1W04	902	G35	5	2*12 CENTRAL TO PERIPHERAL DATA		3W03	902 I40 1
1W04	903	G35	7	2*13 CENTRAL TO PERIPHERAL DATA		3W03	903 I41 1
1W04	904	G35	10	2*14 CENTRAL TO PERIPHERAL DATA		3W03	904 I42 1
1W04	905	H29	20	MEMORY MARGIN		3W03	905 C14 14
1W04	906					3W03	906
1W04	907					3W03	907
1W04	908					3W03	908

1W05	00	PERIPHERAL FROM CENTRAL MEMORY				4W07	00		
1W05	90	G35	21	2*15	CENTRAL TO PERIPHERAL DATA	4W07	90	I01	1
1W05	91	G35	24	2*16	CENTRAL TO PERIPHERAL DATA	4W07	91	I02	1
1W05	92	G35	26	2*17	CENTRAL TO PERIPHERAL DATA	4W07	92	I03	1
1W05	93	G36	5	2*18	CENTRAL TO PERIPHERAL DATA	4W07	93	I04	1
1W05	94	G36	7	2*19	CENTRAL TO PERIPHERAL DATA	4W07	94	I05	1
1W05	95	G36	10	2*20	CENTRAL TO PERIPHERAL DATA	4W07	95	I08	1
1W05	96	G36	21	2*21	CENTRAL TO PERIPHERAL DATA	4W07	96	I09	1
1W05	97	G36	24	2*22	CENTRAL TO PERIPHERAL DATA	4W07	97	I10	1
1W05	98	G36	26	2*23	CENTRAL TO PERIPHERAL DATA	4W07	98	I11	1
1W05	99	G37	5	2*24	CENTRAL TO PERIPHERAL DATA	4W07	99	I12	1
1W05	900	G37	7	2*25	CENTRAL TO PERIPHERAL DATA	4W07	900	I14	1
1W05	901	G37	10	2*26	CENTRAL TO PERIPHERAL DATA	4W07	901	I15	1
1W05	902	G37	21	2*27	CENTRAL TO PERIPHERAL DATA	4W07	902	I16	1
1W05	903	G37	24	2*28	CENTRAL TO PERIPHERAL DATA	4W07	903	I17	1
1W05	904	G37	26	2*29	CENTRAL TO PERIPHERAL DATA	4W07	904	I18	1
1W05	905	I25	11	RESUME	CENTRAL READ	4W07	905	I24	7
1W05	906	I29	1	RESUME	CENTRAL READ	4W07	906	I24	9
1W05	907	H29	22	STORAGE	MARGIN	4W07	907	C14	14
1W05	908					4W07	908		

1W06	00	PERIPHERAL FROM CENTRAL MEMORY				9W11	00		
1W06	90	G39	10	2*38	CENTRAL TO PERIPHERAL DATA	9W11	90	I35	1
1W06	91	G39	21	2*39	CENTRAL TO PERIPHERAL DATA	9W11	91	I36	1
1W06	92	G39	24	2*40	CENTRAL TO PERIPHERAL DATA	9W11	92	I38	1
1W06	93	G39	26	2*41	CENTRAL TO PERIPHERAL DATA	9W11	93	I39	1
1W06	94	G40	5	2*42	CENTRAL TO PERIPHERAL DATA	9W11	94	I40	1
1W06	95	G40	7	2*43	CENTRAL TO PERIPHERAL DATA	9W11	95	I41	1
1W06	96	G40	10	2*44	CENTRAL TO PERIPHERAL DATA	9W11	96	I42	1
1W06	97	H29	18	MEMORY	MARGIN	9W11	97	C14	14
1W06	98					9W11	98		
1W06	99					9W11	99		
1W06	900	G38	5	2*30	CENTRAL TO PERIPHERAL DATA	9W11	900	I25	1
1W06	901	G38	7	2*31	CENTRAL TO PERIPHERAL DATA	9W11	901	I26	1
1W06	902	G38	10	2*32	CENTRAL TO PERIPHERAL DATA	9W11	902	I27	1
1W06	903	G38	21	2*33	CENTRAL TO PERIPHERAL DATA	9W11	903	I28	1
1W06	904	G38	24	2*34	CENTRAL TO PERIPHERAL DATA	9W11	904	I29	1
1W06	905	G38	26	2*35	CENTRAL TO PERIPHERAL DATA	9W11	905	I32	1
1W06	906	G39	5	2*36	CENTRAL TO PERIPHERAL DATA	9W11	906	I33	1
1W06	907	G39	7	2*37	CENTRAL TO PERIPHERAL DATA	9W11	907	I34	1
1W06	908					9W11	908		

1W07	00	PERIPHERAL FROM CENTRAL MEMORY				10W11	00
1W07	90	G41	26	2*53	CENTRAL TO PERIPHERAL DATA	10W11	90 I11 1
1W07	91	G42	5	2*54	CENTRAL TO PERIPHERAL DATA	10W11	91 I12 1
1W07	92	G42	7	2*55	CENTRAL TO PERIPHERAL DATA	10W11	92 I14 1
1W07	93	G42	10	2*56	CENTRAL TO PERIPHERAL DATA	10W11	93 I15 1
1W07	94	G42	21	2*57	CENTRAL TO PERIPHERAL DATA	10W11	94 I16 1
1W07	95	G42	24	2*58	CENTRAL TO PERIPHERAL DATA	10W11	95 I17 1
1W07	96	G42	26	2*59	CENTRAL TO PERIPHERAL DATA	10W11	96 I18 1
1W07	97	H29	16		MEMORY MARGIN	10W11	97 C14 14
1W07	98					10W11	98
1W07	99					10W11	99
1W07	900	G40	21	2*45	CENTRAL TO PERIPHERAL DATA	10W11	900 I01 1
1W07	901	G40	24	2*46	CENTRAL TO PERIPHERAL DATA	10W11	901 I02 1
1W07	902	G40	26	2*47	CENTRAL TO PERIPHERAL DATA	10W11	902 I03 1
1W07	903	G41	5	2*48	CENTRAL TO PERIPHERAL DATA	10W11	903 I04 1
1W07	904	G41	7	2*49	CENTRAL TO PERIPHERAL DATA	10W11	904 I05 1
1W07	905	G41	10	2*50	CENTRAL TO PERIPHERAL DATA	10W11	905 I08 1
1W07	906	G41	21	2*51	CENTRAL TO PERIPHERAL DATA	10W11	906 I09 1
1W07	907	G41	24	2*52	CENTRAL TO PERIPHERAL DATA	10W11	907 I10 1
1W07	908					10W11	908

1W08	00	PERIPHERAL DATA TO MEMORY DISTRIBUTION				2W01	00
1W08	90	J33	8	2*48	PERIPHERAL TO CENTRAL DATA	2W01	90 B09 5
1W08	91	J33	6	2*49	PERIPHERAL TO CENTRAL DATA	2W01	91 B09 7
1W08	92	J33	4	2*50	PERIPHERAL TO CENTRAL DATA	2W01	92 B09 10
1W08	93	J33	27	2*51	PERIPHERAL TO CENTRAL DATA	2W01	93 B09 21
1W08	94	J33	25	2*52	PERIPHERAL TO CENTRAL DATA	2W01	94 B09 24
1W08	95	J33	23	2*53	PERIPHERAL TO CENTRAL DATA	2W01	95 B09 26
1W08	96	J34	8	2*54	PERIPHERAL TO CENTRAL DATA	2W01	96 B10 5
1W08	97	J34	6	2*55	PERIPHERAL TO CENTRAL DATA	2W01	97 B10 7
1W08	98	J34	4	2*56	PERIPHERAL TO CENTRAL DATA	2W01	98 B10 10
1W08	99	J34	27	2*57	PERIPHERAL TO CENTRAL DATA	2W01	99 B10 21
1W08	900	J34	25	2*58	PERIPHERAL TO CENTRAL DATA	2W01	900 B10 24
1W08	901	J34	23	2*59	PERIPHERAL TO CENTRAL DATA	2W01	901 B10 26
1W08	902	J35	8	2*36	PERIPHERAL TO CENTRAL DATA	2W01	902 B07 5
1W08	903	J35	6	2*37	PERIPHERAL TO CENTRAL DATA	2W01	903 B07 7
1W08	904	J35	4	2*38	PERIPHERAL TO CENTRAL DATA	2W01	904 B07 10
1W08	905	I29	21		RESUME CENTRAL WRITE	2W01	905 C03 8
1W08	906					2W01	906
1W08	907					2W01	907
1W08	908					2W01	908

1W09	00	PERIPHERAL DATA TO MEMORY DISTRIBUTION				2W02	00
1W09	90	J35	27	2*39	PERIPHERAL TO CENTRAL DATA	2W02	90 B07 21
1W09	91	J35	25	2*40	PERIPHERAL TO CENTRAL DATA	2W02	91 B07 24
1W09	92	J35	23	2*41	PERIPHERAL TO CENTRAL DATA	2W02	92 B07 26
1W09	93	J36	8	2*42	PERIPHERAL TO CENTRAL DATA	2W02	93 B08 5
1W09	94	J36	6	2*43	PERIPHERAL TO CENTRAL DATA	2W02	94 B08 7
1W09	95	J36	4	2*44	PERIPHERAL TO CENTRAL DATA	2W02	95 B08 10
1W09	96	J36	27	2*45	PERIPHERAL TO CENTRAL DATA	2W02	96 B08 21
1W09	97	J36	25	2*46	PERIPHERAL TO CENTRAL DATA	2W02	97 B08 24
1W09	98	J36	23	2*47	PERIPHERAL TO CENTRAL DATA	2W02	98 B08 26
1W09	99	J37	8	2*24	PERIPHERAL TO CENTRAL DATA	2W02	99 B05 5
1W09	900	J37	6	2*25	PERIPHERAL TO CENTRAL DATA	2W02	900 B05 7
1W09	901	J37	4	2*26	PERIPHERAL TO CENTRAL DATA	2W02	901 B05 10
1W09	902	J37	27	2*27	PERIPHERAL TO CENTRAL DATA	2W02	902 B05 21
1W09	903	J37	25	2*28	PERIPHERAL TO CENTRAL DATA	2W02	903 B05 24
1W09	904	J37	23	2*29	PERIPHERAL TO CENTRAL DATA	2W02	904 B05 26
1W09	905					2W02	905
1W09	906					2W02	906
1W09	907					2W02	907
1W09	908					2W02	908

1W10	00	PERIPHERAL DATA TO MEMORY DISTRIBUTION				2W03	00
1W10	90	J38	8	2*30	PERIPHERAL TO CENTRAL DATA	2W03	90 B06 5
1W10	91	J38	6	2*31	PERIPHERAL TO CENTRAL DATA	2W03	91 B06 7
1W10	92	J38	4	2*32	PERIPHERAL TO CENTRAL DATA	2W03	92 B06 10
1W10	93	J38	27	2*33	PERIPHERAL TO CENTRAL DATA	2W03	93 B06 21
1W10	94	J38	25	2*34	PERIPHERAL TO CENTRAL DATA	2W03	94 B06 24
1W10	95	J38	23	2*35	PERIPHERAL TO CENTRAL DATA	2W03	95 B06 26
1W10	96	J39	8	2*12	PERIPHERAL TO CENTRAL DATA	2W03	96 B03 5
1W10	97	J39	6	2*13	PERIPHERAL TO CENTRAL DATA	2W03	97 B03 7
1W10	98	J39	4	2*14	PERIPHERAL TO CENTRAL DATA	2W03	98 B03 10
1W10	99	J39	27	2*15	PERIPHERAL TO CENTRAL DATA	2W03	99 B03 21
1W10	900	J39	25	2*16	PERIPHERAL TO CENTRAL DATA	2W03	900 B03 24
1W10	901	J39	23	2*17	PERIPHERAL TO CENTRAL DATA	2W03	901 B03 26
1W10	902	J40	8	2*18	PERIPHERAL TO CENTRAL DATA	2W03	902 B04 5
1W10	903	J40	6	2*19	PERIPHERAL TO CENTRAL DATA	2W03	903 B04 7
1W10	904	J40	4	2*20	PERIPHERAL TO CENTRAL DATA	2W03	904 B04 10
1W10	905					2W03	905
1W10	906					2W03	906
1W10	907					2W03	907
1W10	908					2W03	908

1W11	00	PERIPHERAL DATA TO MEMORY DISTRIBUTION			2W04	00
1W11	90	J40	27	2*21 PERIPHERAL TO CENTRAL DATA	2W04	90 B04 21
1W11	91	J40	25	2*22 PERIPHERAL TO CENTRAL DATA	2W04	91 B04 24
1W11	92	J40	23	2*23 PERIPHERAL TO CENTRAL DATA	2W04	92 B04 26
1W11	93	J41	8	2*0 PERIPHERAL TO CENTRAL DATA	2W04	93 B01 5
1W11	94	J41	6	2*1 PERIPHERAL TO CENTRAL DATA	2W04	94 B01 7
1W11	95	J41	4	2*2 PERIPHERAL TO CENTRAL DATA	2W04	95 B01 10
1W11	96	J41	27	2*3 PERIPHERAL TO CENTRAL DATA	2W04	96 B01 21
1W11	97	J41	25	2*4 PERIPHERAL TO CENTRAL DATA	2W04	97 B01 24
1W11	98	J41	23	2*5 PERIPHERAL TO CENTRAL DATA	2W04	98 B01 26
1W11	99	J42	8	2*6 PERIPHERAL TO CENTRAL DATA	2W04	99 B02 5
1W11	900	J42	6	2*7 PERIPHERAL TO CENTRAL DATA	2W04	900 B02 7
1W11	901	J42	4	2*8 PERIPHERAL TO CENTRAL DATA	2W04	901 B02 10
1W11	902	J42	27	2*9 PERIPHERAL TO CENTRAL DATA	2W04	902 B02 21
1W11	903	J42	25	2*10 PERIPHERAL TO CENTRAL DATA	2W04	903 B02 24
1W11	904	J42	23	2*11 PERIPHERAL TO CENTRAL DATA	2W04	904 B02 26
1W11	905				2W04	905
1W11	906				2W04	906
1W11	907				2W04	907
1W11	908				2W04	908

1W12	00	PERIPHERAL FROM CONTROL			5W23	00
1W12	90				5W23	90
1W12	91				5W23	91
1W12	92				5W23	92
1W12	93				5W23	93
1W12	94				5W23	94
1W12	95				5W23	95
1W12	96				5W23	96
1W12	97				5W23	97
1W12	98				5W23	98
1W12	99				5W23	99
1W12	900	I29	5	EXCHANGE RESUME OUT	5W23	900 040 2
1W12	901	I30	23	MC	5W23	901 042 26
1W12	902	I30	4	CLOCK	5W23	902 122 12
1W12	903				5W23	903
1W12	904				5W23	904
1W12	905				5W23	905
1W12	906				5W23	906
1W12	907				5W23	907
1W12	908				5W23	908

1W13	00	CHANNEL 0		
1W13	90	M36	6	2*0 INPUT DATA
1W13	91	M36	4	2*1 INPUT DATA
1W13	92	M36	23	2*2 INPUT DATA
1W13	93	M36	27	2*3 INPUT DATA
1W13	94	M37	6	2*4 INPUT DATA
1W13	95	M37	4	2*5 INPUT DATA
1W13	96	M37	23	2*6 INPUT DATA
1W13	97	M37	27	2*7 INPUT DATA
1W13	98	M38	6	2*8 INPUT DATA
1W13	99	M38	4	2*9 INPUT DATA
1W13	900	M38	23	2*10 INPUT DATA
1W13	901	M38	27	2*11 INPUT DATA
1W13	902	M35	28	ACTIVE
1W13	903	M35	14	INACTIVE
1W13	904	M35	15	FULL
1W13	905	M35	8	EMPTY
1W13	906	M36	1	CLOCK (100N SEC)
1W13	907	M37	1	CLOCK (1 US)
1W13	908			

1W14	00	CHANNEL 0		
1W14	90	M36	5	2*0 OUTPUT DATA
1W14	91	M36	11	2*1 OUTPUT DATA
1W14	92	M36	28	2*2 OUTPUT DATA
1W14	93	M36	24	2*3 OUTPUT DATA
1W14	94	M37	5	2*4 OUTPUT DATA
1W14	95	M37	11	2*5 OUTPUT DATA
1W14	96	M37	28	2*6 OUTPUT DATA
1W14	97	M37	24	2*7 OUTPUT DATA
1W14	98	M38	5	2*8 OUTPUT DATA
1W14	99	M38	11	2*9 OUTPUT DATA
1W14	900	M38	28	2*10 OUTPUT DATA
1W14	901	M38	24	2*11 OUTPUT DATA
1W14	902	M35	22	ACTIVE
1W14	903	M35	18	INACTIVE
1W14	904	M35	13	FULL
1W14	905	M35	9	EMPTY
1W14	906	M35	3	FUNCTION
1W14	907	M38	1	MASTER CLEAR
1W14	908			

1W15	00	CHANNEL 1		
1W15	90	M40	6	2*0 INPUT DATA
1W15	91	M40	4	2*1 INPUT DATA
1W15	92	M40	23	2*2 INPUT DATA
1W15	93	M40	27	2*3 INPUT DATA
1W15	94	M41	6	2*4 INPUT DATA
1W15	95	M41	4	2*5 INPUT DATA
1W15	96	M41	23	2*6 INPUT DATA
1W15	97	M41	27	2*7 INPUT DATA
1W15	98	M42	6	2*8 INPUT DATA
1W15	99	M42	4	2*9 INPUT DATA
1W15	900	M42	23	2*10 INPUT DATA
1W15	901	M42	27	2*11 INPUT DATA
1W15	902	M39	25	ACTIVE
1W15	903	M39	14	INACTIVE
1W15	904	M39	15	FULL
1W15	905	M39	8	EMPTY
1W15	906	M40	1	CLOCK (100NSEC)
1W15	907	M41	1	CLOCK (1US)
1W15	908			

1W16	00	CHANNEL 1		
1W16	90	M40	5	2*0 OUTPUT DATA
1W16	91	M40	11	2*1 OUTPUT DATA
1W16	92	M40	28	2*2 OUTPUT DATA
1W16	93	M40	24	2*3 OUTPUT DATA
1W16	94	M41	5	2*4 OUTPUT DATA
1W16	95	M41	11	2*5 OUTPUT DATA
1W16	96	M41	28	2*6 OUTPUT DATA
1W16	97	M41	24	2*7 OUTPUT DATA
1W16	98	M42	5	2*8 OUTPUT DATA
1W16	99	M42	11	2*9 OUTPUT DATA
1W16	900	M42	28	2*10 OUTPUT DATA
1W16	901	M42	24	2*11 OUTPUT DATA
1W16	902	M39	22	ACTIVE
1W16	903	M39	18	INACTIVE
1W16	904	M39	13	FULL
1W16	905	M39	9	EMPTY
1W16	906	M39	3	FUNCTION
1W16	907	M42	1	MASTER CLEAR
1W16	908			


```

1W17  00          CHANNEL 2
1W17  90  N36  6  2*0 INPUT DATA
1W17  91  N36  4  2*1 INPUT DATA
1W17  92  N36 23  2*2 INPUT DATA
1W17  93  N36 27  2*3 INPUT DATA
1W17  94  N37  6  2*4 INPUT DATA
1W17  95  N37  4  2*5 INPUT DATA
1W17  96  N37 23  2*6 INPUT DATA
1W17  97  N37 27  2*7 INPUT DATA
1W17  98  N38  6  2*8 INPUT DATA
1W17  99  N38  4  2*9 INPUT DATA
1W17 900  N38 23  2*10 INPUT DATA
1W17 901  N38 27  2*11 INPUT DATA
1W17 902  N35 25  ACTIVE
1W17 903  N35 14  INACTIVE
1W17 904  N35 15  FULL
1W17 905  N35  8  EMPTY
1W17 906  N36  1  CLOCK (100NSEC)
1W17 907  N37  1  CLOCK (1US)
1W17 908

```

```

1W18  00          CHANNEL 2
1W18  90  N36  5  2*0 OUTPUT DATA
1W18  91  N36 11  2*1 OUTPUT DATA
1W18  92  N36 28  2*2 OUTPUT DATA
1W18  93  N36 24  2*3 OUTPUT DATA
1W18  94  N37  5  2*4 OUTPUT DATA
1W18  95  N37 11  2*5 OUTPUT DATA
1W18  96  N37 28  2*6 OUTPUT DATA
1W18  97  N37 24  2*7 OUTPUT DATA
1W18  98  N38  5  2*8 OUTPUT DATA
1W18  99  N38 11  2*9 OUTPUT DATA
1W18 900  N38 28  2*10 OUTPUT DATA
1W18 901  N38 24  2*11 OUTPUT DATA
1W18 902  N35 22  ACTIVE
1W18 903  N35 18  INACTIVE
1W18 904  N35 13  FULL
1W18 905  N35  9  EMPTY
1W18 906  N35  3  FUNCTION
1W18 907  N38  1  MASTER CLEAR
1W18 908

```

1W19	00	CHANNEL 3		
1W19	90	N40	6	2*0 INPUT DATA
1W19	91	N40	4	2*1 INPUT DATA
1W19	92	N40	23	2*2 INPUT DATA
1W19	93	N40	27	2*3 INPUT DATA
1W19	94	N41	6	2*4 INPUT DATA
1W19	95	N41	4	2*5 INPUT DATA
1W19	96	N41	23	2*6 INPUT DATA
1W19	97	N41	27	2*7 INPUT DATA
1W19	98	N42	6	2*8 INPUT DATA
1W19	99	N42	4	2*9 INPUT DATA
1W19	900	N42	23	2*10 INPUT DATA
1W19	901	N42	27	2*11 INPUT DATA
1W19	902	N39	25	ACTIVE
1W19	903	N39	14	INACTIVE
1W19	904	N39	15	FULL
1W19	905	N39	8	EMPTY
1W19	906	N40	1	CLOCK (100NSEC)
1W19	907	N41	1	CLOCK (1USEC)
1W19	908			

1W20	00	CHANNEL 3		
1W20	90	N40	5	2*0 OUTPUT DATA
1W20	91	N40	11	2*1 OUTPUT DATA
1W20	92	N40	28	2*2 OUTPUT DATA
1W20	93	N40	24	2*3 OUTPUT DATA
1W20	94	N41	5	2*4 OUTPUT DATA
1W20	95	N41	11	2*5 OUTPUT DATA
1W20	96	N41	28	2*6 OUTPUT DATA
1W20	97	N41	24	2*7 OUTPUT DATA
1W20	98	N42	5	2*8 OUTPUT DATA
1W20	99	N42	11	2*9 OUTPUT DATA
1W20	900	N42	28	2*10 OUTPUT DATA
1W20	901	N42	24	2*11 OUTPUT DATA
1W20	902	N39	22	ACTIVE
1W20	903	N39	18	INACTIVE
1W20	904	N39	13	FULL
1W20	905	N39	9	EMPTY
1W20	906	N39	3	FUNCTION
1W20	907	N42	1	MASTER CLEAR
1W20	908			

1W21	00	CHANNEL 4		
1W21	90	036	6	2*0 INPUT DATA
1W21	91	036	4	2*1 INPUT DATA
1W21	92	036	23	2*2 INPUT DATA
1W21	93	036	27	2*3 INPUT DATA
1W21	94	037	6	2*4 INPUT DATA
1W21	95	037	4	2*5 INPUT DATA
1W21	96	037	23	2*6 INPUT DATA
1W21	97	037	27	2*7 INPUT DATA
1W21	98	038	4	2*8 INPUT DATA
1W21	99	038	4	2*9 INPUT DATA
1W21	900	038	23	2*10 INPUT DATA
1W21	901	038	27	2*11 INPUT DATA
1W21	902	035	25	ACTIVE
1W21	903	035	14	INACTIVE
1W21	904	035	15	FULL
1W21	905	035	8	EMPTY
1W21	906	036	1	CLOCK (100NSEC)
1W21	907	037	1	CLOCK (1USEC)
1W21	908			

1W22	00	CHANNEL 4		
1W22	90	036	5	2*0 OUTPUT DATA
1W22	91	036	11	2*1 OUTPUT DATA
1W22	92	036	28	2*2 OUTPUT DATA
1W22	93	036	24	2*3 OUTPUT DATA
1W22	94	037	5	2*4 OUTPUT DATA
1W22	95	037	11	2*5 OUTPUT DATA
1W22	96	037	28	2*6 OUTPUT DATA
1W22	97	037	24	2*7 OUTPUT DATA
1W22	98	038	5	2*8 OUTPUT DATA
1W22	99	038	11	2*9 OUTPUT DATA
1W22	900	038	28	2*10 OUTPUT DATA
1W22	901	038	24	2*11 OUTPUT DATA
1W22	902	035	22	ACTIVE
1W22	903	035	18	INACTIVE
1W22	904	035	13	FULL
1W22	905	035	9	EMPTY
1W22	906	035	3	FUNCTION
1W22	907	038	1	MASTER CLEAR
1W22	908			

1W23	00	CHANNEL 5		
1W23	90	040	6	2*0 INPUT DATA
1W23	91	040	4	2*1 INPUT DATA
1W23	92	040	23	2*2 INPUT DATA
1W23	93	040	27	2*3 INPUT DATA
1W23	94	041	6	2*4 INPUT DATA
1W23	95	041	4	2*5 INPUT DATA
1W23	96	041	23	2*6 INPUT DATA
1W23	97	041	27	2*7 INPUT DATA
1W23	98	042	6	2*8 INPUT DATA
1W23	99	042	4	2*9 INPUT DATA
1W23	900	042	23	2*10 INPUT DATA
1W23	901	042	27	2*11 INPUT DATA
1W23	902	039	25	ACTIVE
1W23	903	039	14	INACTIVE
1W23	904	039	15	FULL
1W23	905	039	8	EMPTY
1W23	906	040	1	CLOCK (100NSEC)
1W23	907	041	1	CLOCK (1US)
1W23	908			

1W24	00	CHANNEL 5		
1W24	90	040	5	2*0 OUTPUT DATA
1W24	91	040	11	2*1 OUTPUT DATA
1W24	92	040	28	2*2 OUTPUT DATA
1W24	93	040	24	2*3 OUTPUT DATA
1W24	94	041	5	2*4 OUTPUT DATA
1W24	95	041	11	2*5 OUTPUT DATA
1W24	96	041	28	2*6 OUTPUT DATA
1W24	97	041	24	2*7 OUTPUT DATA
1W24	98	042	5	2*8 OUTPUT DATA
1W24	99	042	11	2*9 OUTPUT DATA
1W24	900	042	28	2*10 OUTPUT DATA
1W24	901	042	24	2*11 OUTPUT DATA
1W24	902	039	22	ACTIVE
1W24	903	039	18	INACTIVE
1W24	904	039	13	FULL
1W24	905	039	9	EMPTY
1W24	906	039	3	FUNCTION
1W24	907	042	1	MASTER CLEAR
1W24	908			

1W25	00	CHANNEL 6			
1W25	90	P36	6	2*0	INPUT DATA
1W25	91	P36	4	2*1	INPUT DATA
1W25	92	P36	23	2*2	INPUT DATA
1W25	93	P36	27	2*3	INPUT DATA
1W25	94	P37	6	2*4	INPUT DATA
1W25	95	P37	4	2*5	INPUT DATA
1W25	96	P37	23	2*6	INPUT DATA
1W25	97	P37	27	2*7	INPUT DATA
1W25	98	P38	6	2*8	INPUT DATA
1W25	99	P38	4	2*9	INPUT DATA
1W25	900	P38	23	2*10	INPUT DATA
1W25	901	P38	27	2*11	INPUT DATA
1W25	902	P35	25		ACTIVE
1W25	903	P35	14		INACTIVE
1W25	904	P35	15		FULL
1W25	905	P35	8		EMPTY
1W25	906	P36	1		CLOCK (100NSEC)
1W25	907	P37	1		CLOCK (1USEC)
1W25	908				

1W26	00	CHANNEL 6			
1W26	90	P36	5	2*0	OUTPUT DATA
1W26	91	P36	11	2*1	OUTPUT DATA
1W26	92	P36	28	2*2	OUTPUT DATA
1W26	93	P36	24	2*3	OUTPUT DATA
1W26	94	P37	5	2*4	OUTPUT DATA
1W26	95	P37	11	2*5	OUTPUT DATA
1W26	96	P37	28	2*6	OUTPUT DATA
1W26	97	P37	24	2*7	OUTPUT DATA
1W26	98	P38	5	2*8	OUTPUT DATA
1W26	99	P38	11	2*9	OUTPUT DATA
1W26	900	P38	28	2*10	OUTPUT DATA
1W26	901	P38	24	2*11	OUTPUT DATA
1W26	902	P35	22		ACTIVE
1W26	903	P35	18		INACTIVE
1W26	904	P35	13		FULL
1W26	905	P35	9		EMPTY
1W26	906	P35	3		FUNCTION
1W26	907	P38	1		MASTER CLEAR
1W26	908				

1W27	00	CHANNEL 7		
1W27	90	P40	6	2*0 INPUT DATA
1W27	91	P40	4	2*1 INPUT DATA
1W27	92	P40	23	2*2 INPUT DATA
1W27	93	P40	27	2*3 INPUT DATA
1W27	94	P41	6	2*4 INPUT DATA
1W27	95	P41	4	2*5 INPUT DATA
1W27	96	P41	23	2*6 INPUT DATA
1W27	97	P41	27	2*7 INPUT DATA
1W27	98	P42	6	2*8 INPUT DATA
1W27	99	P42	4	2*9 INPUT DATA
1W27	900	P42	23	2*10 INPUT DATA
1W27	901	P42	27	2*11 INPUT DATA
1W27	902	P39	25	ACTIVE
1W27	903	P39	14	INACTIVE
1W27	904	P39	15	FULL
1W27	905	P39	8	EMPTY
1W27	906	P40	1	CLOCK (100NSEC)
1W27	907	P41	1	CLOCK (1US)
1W27	908			

1W28	00	CHANNEL 7		
1W28	90	P40	5	2*0 OUTPUT DATA
1W28	91	P40	11	2*1 OUTPUT DATA
1W28	92	P40	28	2*2 OUTPUT DATA
1W28	93	P40	24	2*3 OUTPUT DATA
1W28	94	P41	5	2*4 OUTPUT DATA
1W28	95	P41	11	2*5 OUTPUT DATA
1W28	96	P41	28	2*6 OUTPUT DATA
1W28	97	P41	24	2*7 OUTPUT DATA
1W28	98	P42	5	2*8 OUTPUT DATA
1W28	99	P42	11	2*9 OUTPUT DATA
1W28	900	P42	28	2*10 OUTPUT DATA
1W28	901	P42	24	2*11 OUTPUT DATA
1W28	902	P39	22	ACTIVE
1W28	903	P39	18	INACTIVE
1W28	904	P39	13	FULL
1W28	905	P39	9	EMPTY
1W28	906	P39	3	FUNCTION
1W28	907	P42	1	MASTER CLEAR
1W28	908			

1W29	00	CHANNEL 10		
1W29	90	Q36	6	2*0 INPUT DATA
1W29	91	Q36	4	2*1 INPUT DATA
1W29	92	Q36	23	2*2 INPUT DATA
1W29	93	Q36	27	2*3 INPUT DATA
1W29	94	Q37	6	2*4 INPUT DATA
1W29	95	Q37	4	2*5 INPUT DATA
1W29	96	Q37	23	2*6 INPUT DATA
1W29	97	Q37	27	2*7 INPUT DATA
1W29	98	Q38	6	2*8 INPUT DATA
1W29	99	Q38	4	2*9 INPUT DATA
1W29	900	Q38	23	2*10 INPUT DATA
1W29	901	Q38	27	2*11 INPUT DATA
1W29	902	Q35	25	ACTIVE
1W29	903	Q35	14	INACTIVE
1W29	904	Q35	13	FULL
1W29	905	Q35	8	EMPTY
1W29	906	Q36	1	CLOCK (100NSEC)
1W29	907	Q37	1	CLOCK (1US)
1W29	908			

1W30	00	CHANNEL 10		
1W30	90	Q36	5	2*0 OUTPUT DATA
1W30	91	Q36	11	2*1 OUTPUT DATA
1W30	92	Q36	28	2*2 OUTPUT DATA
1W30	93	Q36	24	2*3 OUTPUT DATA
1W30	94	Q37	5	2*4 OUTPUT DATA
1W30	95	Q37	11	2*5 OUTPUT DATA
1W30	96	Q37	28	2*6 OUTPUT DATA
1W30	97	Q37	24	2*7 OUTPUT DATA
1W30	98	Q38	5	2*8 OUTPUT DATA
1W30	99	Q38	11	2*9 OUTPUT DATA
1W30	900	Q38	28	2*10 OUTPUT DATA
1W30	901	Q38	24	2*11 OUTPUT DATA
1W30	902	Q35	22	ACTIVE
1W30	903	Q35	18	INACTIVE
1W30	904	Q35	13	FULL
1W30	905	Q35	9	EMPTY
1W30	906	Q35	3	FUNCTION
1W30	907	Q38	1	MASTER CLEAR
1W30	908			

1W31	00	CHANNEL 11		
1W31	90	Q40	6	2*0 INPUT DATA
1W31	91	Q40	4	2*1 INPUT DATA
1W31	92	Q40	23	2*2 INPUT DATA
1W31	93	Q40	27	2*3 INPUT DATA
1W31	94	Q41	6	2*4 INPUT DATA
1W31	95	Q41	4	2*5 INPUT DATA
1W31	96	Q41	23	2*6 INPUT DATA
1W31	97	Q41	27	2*7 INPUT DATA
1W31	98	Q42	6	2*8 INPUT DATA
1W31	99	Q42	4	2*9 INPUT DATA
1W31	900	Q42	23	2*10 INPUT DATA
1W31	901	Q42	27	2*11 INPUT DATA
1W31	902	Q39	25	ACTIVE
1W31	903	Q39	14	INACTIVE
1W31	904	Q39	15	FULL
1W31	905	Q39	8	EMPTY
1W31	906	Q40	1	CLOCK (100US)
1W31	907	Q41	1	CLOCK (1US)
1W31	908			

1W32	00	CHANNEL 11		
1W32	90	Q40	5	2*0 OUTPUT DATA
1W32	91	Q40	11	2*1 OUTPUT DATA
1W32	92	Q40	28	2*2 OUTPUT DATA
1W32	93	Q40	24	2*3 OUTPUT DATA
1W32	94	Q41	5	2*4 OUTPUT DATA
1W32	95	Q41	11	2*5 OUTPUT DATA
1W32	96	Q41	28	2*6 OUTPUT DATA
1W32	97	Q41	24	2*7 OUTPUT DATA
1W32	98	Q42	5	2*8 OUTPUT DATA
1W32	99	Q42	11	2*9 OUTPUT DATA
1W32	900	Q42	28	2*10 OUTPUT DATA
1W32	901	Q42	24	2*11 OUTPUT DATA
1W32	902	Q39	22	ACTIVE
1W32	903	Q39	18	INACTIVE
1W32	904	Q39	13	FULL
1W32	905	Q39	9	EMPTY
1W32	906	Q39	3	FUNCTION
1W32	907	Q42	1	MASTER CLEAR
1W32	908			

1W33	00	CHANNEL 12		
1W33	90	R36	6	2*0 INPUT DATA
1W33	91	R36	4	2*1 INPUT DATA
1W33	92	R36	23	2*2 INPUT DATA
1W33	93	R36	27	2*3 INPUT DATA
1W33	94	R37	6	2*4 INPUT DATA
1W33	95	R37	4	2*5 INPUT DATA
1W33	96	R37	23	2*6 INPUT DATA
1W33	97	R37	27	2*7 INPUT DATA
1W33	98	R38	6	2*8 INPUT DATA
1W33	99	R38	4	2*9 INPUT DATA
1W33	900	R38	23	2*10 INPUT DATA
1W33	901	R38	27	2*11 INPUT DATA
1W33	902	R35	25	ACTIVE
1W33	903	R35	14	INACTIVE
1W33	904	R35	15	FULL
1W33	905	R35	8	EMPTY
1W33	906	R36	1	CLOCK (100 NSEC)
1W33	907	R37	1	CLOCK (1US)
1W33	908			

1W34	00	CHANNEL 12		
1W34	90	R36	5	2*0 OUTPUT DATA
1W34	91	R36	11	2*1 OUTPUT DATA
1W34	92	R36	28	2*2 OUTPUT DATA
1W34	93	R36	24	2*3 OUTPUT DATA
1W34	94	R37	5	2*4 OUTPUT DATA
1W34	95	R37	11	2*5 OUTPUT DATA
1W34	96	R37	28	2*6 OUTPUT DATA
1W34	97	R37	24	2*7 OUTPUT DATA
1W34	98	R38	5	2*8 OUTPUT DATA
1W34	99	R38	11	2*9 OUTPUT DATA
1W34	900	R38	28	2*10 OUTPUT DATA
1W34	901	R38	24	2*11 OUTPUT DATA
1W34	902	R35	22	ACTIVE
1W34	903	R35	18	INACTIVE
1W34	904	R35	13	FULL
1W34	905	R35	9	EMPTY
1W34	906	R35	3	FUNCTION
1W34	907	R38	1	MASTER CLEAR
1W34	908			

1W35	00	CHANNEL 13		
1W35	90	R40	6	2*0 INPUT DATA
1W35	91	R40	4	2*1 INPUT DATA
1W35	92	R40	23	2*2 INPUT DATA
1W35	93	R40	27	2*3 INPUT DATA
1W35	94	R41	6	2*4 INPUT DATA
1W35	95	R41	4	2*5 INPUT DATA
1W35	96	R41	23	2*6 INPUT DATA
1W35	97	R41	27	2*7 INPUT DATA
1W35	98	R42	6	2*8 INPUT DATA
1W35	99	R42	4	2*9 INPUT DATA
1W35	900	R42	23	2*10 INPUT DATA
1W35	901	R42	27	2*11 INPUT DATA
1W35	902	R39	25	ACTIVE
1W35	903	R39	14	INACTIVE
1W35	904	R39	15	FULL
1W35	905	R39	8	EMPTY
1W35	906	R40	1	CLOCK (100NS)
1W35	907	R41	1	CLOCK (1US)
1W35	908			

1W36	00	CHANNEL 13		
1W36	90	R40	5	2*0 OUTPUT DATA
1W36	91	R40	11	2*1 OUTPUT DATA
1W36	92	R40	28	2*2 OUTPUT DATA
1W36	93	R40	24	2*3 OUTPUT DATA
1W36	94	R41	5	2*4 OUTPUT DATA
1W36	95	R41	11	2*5 OUTPUT DATA
1W36	96	R41	28	2*6 OUTPUT DATA
1W36	97	R41	24	2*7 OUTPUT DATA
1W36	98	R42	5	2*8 OUTPUT DATA
1W36	99	R42	11	2*9 OUTPUT DATA
1W36	900	R42	28	2*10 OUTPUT DATA
1W36	901	R42	24	2*11 OUTPUT DATA
1W36	902	R39	22	ACTIVE
1W36	903	R39	18	INACTIVE
1W36	904	R39	13	FULL
1W36	905	R39	9	EMPTY
1W36	906	R39	3	FUNCTION
1W36	907	R42	1	MASTER CLEAR
1W36	908			

1W37 CABLE 5 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.	SOURCE	
1W37	1	0	1I29-GND	S01-ON	S01-1
1W37		9	1I29-GND	S01-ON	S01-1
1W37	2	2	1I29-16	S01-OFF	S01-2
1W37		9			
1W37	3	4	1I29-2	S02-SWEEP	S02-1
1W37		9			
1W37	4	5	1I30-GND	S02-LOAD	S02-2
1W37		9	1I30-GND	S02-LOAD	S02-2
1W37	5	6	1I29-4	S02-DUMP	S02-3
1W37		9			
1W37	6	90	1 +6V	S03-HIGH	S03-1
1W37		9	1 +6V	S03-HIGH	S03-1
1W37	7	91	1G10-16	S01-NORMAL	S03-2
1W37		9			
1W37	8	92	1I30-GND	S03-LOW	S03-3
1W37		9	1I30-GND	S03-LOW	S03-3
1W37	9	93	1 +6V	S04-HIGH	S04-1
1W37		9	1 +6V	S04-HIGH	S04-1
1W37	10	94	1H29-26	S04-NORMAL	S04-2
1W37		9	24	S04-NORMAL	S04-2
1W37	11	95	GND	S04-LOW	S04-3
1W37		9	GND	S04-LOW	S04-3
1W37	12	96			
1W37		9			
1W37	13	97			
1W37		9			
1W37	14	98			
1W37		9			
1W37	15	900			
1W37		9			
1W37	16	910			
1W37		9			
1W37	17	920			
1W37		9			
1W37	18	930			
1W37		9			
1W37	19	940			
1W37		9			
1W37	20	950			
1W37		9			
1W37	21	960			
1W37		9			
1W37	22	970			
1W37		9			
1W37	23	980			
1W37		9			
1W37	24	990			
1W37		9			



2W01	00	MEMORY DISTRIBUTION FROM PERIPHERAL				1W08	00
2W01	90	B09	5	2+48	PERIPHERAL TO CENTRAL DATA	1W08	90 J33 8
2W01	91	B09	7	2+49	PERIPHERAL TO CENTRAL DATA	1W08	91 J33 6
2W01	92	B09	10	2+50	PERIPHERAL TO CENTRAL DATA	1W08	92 J33 4
2W01	93	B09	21	2+51	PERIPHERAL TO CENTRAL DATA	1W08	93 J33 27
2W01	94	B09	24	2+52	PERIPHERAL TO CENTRAL DATA	1W08	94 J33 25
2W01	95	B09	26	2+53	PERIPHERAL TO CENTRAL DATA	1W08	95 J33 23
2W01	96	B10	5	2+54	PERIPHERAL TO CENTRAL DATA	1W08	96 J34 8
2W01	97	B10	7	2+55	PERIPHERAL TO CENTRAL DATA	1W08	97 J34 6
2W01	98	B10	10	2+56	PERIPHERAL TO CENTRAL DATA	1W08	98 J34 4
2W01	99	B10	21	2+57	PERIPHERAL TO CENTRAL DATA	1W08	99 J34 27
2W01	900	B10	24	2+58	PERIPHERAL TO CENTRAL DATA	1W08	900 J34 25
2W01	901	B10	26	2+59	PERIPHERAL TO CENTRAL DATA	1W08	901 J34 23
2W01	902	B07	5	2+36	PERIPHERAL TO CENTRAL DATA	1W08	902 J35 8
2W01	903	B07	7	2+37	PERIPHERAL TO CENTRAL DATA	1W08	903 J35 6
2W01	904	B07	10	2+38	PERIPHERAL TO CENTRAL DATA	1W08	904 J35 4
2W01	905	C03	8		RESUME CENTRAL WRITE	1W08	905 I29 21
2W01	906					1W08	906
2W01	907					1W08	907
2W01	908					1W08	908

2W02	00	MEMORY DISTRIBUTION FROM PERIPHERAL				1W09	00
2W02	90	B07	21	2+39	PERIPHERAL TO CENTRAL DATA	1W09	90 J35 27
2W02	91	B07	24	2+40	PERIPHERAL TO CENTRAL DATA	1W09	91 J35 25
2W02	92	B07	26	2+41	PERIPHERAL TO CENTRAL DATA	1W09	92 J35 23
2W02	93	B08	5	2+42	PERIPHERAL TO CENTRAL DATA	1W09	93 J36 8
2W02	94	B08	7	2+43	PERIPHERAL TO CENTRAL DATA	1W09	94 J36 6
2W02	95	B08	10	2+44	PERIPHERAL TO CENTRAL DATA	1W09	95 J36 4
2W02	96	B08	21	2+45	PERIPHERAL TO CENTRAL DATA	1W09	96 J36 27
2W02	97	B08	24	2+46	PERIPHERAL TO CENTRAL DATA	1W09	97 J36 25
2W02	98	B08	26	2+47	PERIPHERAL TO CENTRAL DATA	1W09	98 J36 23
2W02	99	B05	5	2+24	PERIPHERAL TO CENTRAL DATA	1W09	99 J37 8
2W02	900	B05	7	2+25	PERIPHERAL TO CENTRAL DATA	1W09	900 J37 6
2W02	901	B05	10	2+26	PERIPHERAL TO CENTRAL DATA	1W09	901 J37 4
2W02	902	B05	21	2+27	PERIPHERAL TO CENTRAL DATA	1W09	902 J37 27
2W02	903	B05	24	2+28	PERIPHERAL TO CENTRAL DATA	1W09	903 J37 25
2W02	904	B05	26	2+29	PERIPHERAL TO CENTRAL DATA	1W09	904 J37 23
2W02	905					1W09	905
2W02	906					1W09	906
2W02	907					1W09	907
2W02	908					1W09	908

2W03	00	MEMORY DISTRIBUTION FROM PERIPHERAL				1W10	00
2W03	90	B06	5	2*30 PERIPHERAL TO CENTRAL DATA	1W10	90	J38 8
2W03	91	B06	7	2*31 PERIPHERAL TO CENTRAL DATA	1W10	91	J38 6
2W03	92	B06	10	2*32 PERIPHERAL TO CENTRAL DATA	1W10	92	J38 4
2W03	93	B06	21	2*33 PERIPHERAL TO CENTRAL DATA	1W10	93	J38 27
2W03	94	B06	24	2*34 PERIPHERAL TO CENTRAL DATA	1W10	94	J38 25
2W03	95	B06	26	2*35 PERIPHERAL TO CENTRAL DATA	1W10	95	J38 23
2W03	96	B03	5	2*12 PERIPHERAL TO CENTRAL DATA	1W10	96	J39 8
2W03	97	B03	7	2*13 PERIPHERAL TO CENTRAL DATA	1W10	97	J39 6
2W03	98	B03	10	2*14 PERIPHERAL TO CENTRAL DATA	1W10	98	J39 4
2W03	99	B03	21	2*15 PERIPHERAL TO CENTRAL DATA	1W10	99	J39 27
2W03	900	B03	24	2*16 PERIPHERAL TO CENTRAL DATA	1W10	900	J39 25
2W03	901	B03	26	2*17 PERIPHERAL TO CENTRAL DATA	1W10	901	J39 23
2W03	902	B04	5	2*18 PERIPHERAL TO CENTRAL DATA	1W10	902	J40 8
2W03	903	B04	7	2*19 PERIPHERAL TO CENTRAL DATA	1W10	903	J40 6
2W03	904	B04	10	2*20 PERIPHERAL TO CENTRAL DATA	1W10	904	J40 4
2W03	905				1W10	905	
2W03	906				1W10	906	
2W03	907				1W10	907	
2W03	908				1W10	908	

2W04	00	MEMORY DISTRIBUTION FROM PERIPHERAL				1W11	00
2W04	90	B04	21	2*21 PERIPHERAL TO CENTRAL DATA	1W11	90	J40 27
2W04	91	B04	24	2*22 PERIPHERAL TO CENTRAL DATA	1W11	91	J40 25
2W04	92	B04	26	2*23 PERIPHERAL TO CENTRAL DATA	1W11	92	J40 23
2W04	93	B01	5	2*0 PERIPHERAL TO CENTRAL DATA	1W11	93	J41 8
2W04	94	B01	7	2*1 PERIPHERAL TO CENTRAL DATA	1W11	94	J41 6
2W04	95	B01	10	2*2 PERIPHERAL TO CENTRAL DATA	1W11	95	J41 4
2W04	96	B01	21	2*3 PERIPHERAL TO CENTRAL DATA	1W11	96	J41 27
2W04	97	B01	24	2*4 PERIPHERAL TO CENTRAL DATA	1W11	97	J41 25
2W04	98	B01	26	2*5 PERIPHERAL TO CENTRAL DATA	1W11	98	J41 23
2W04	99	B02	5	2*6 PERIPHERAL TO CENTRAL DATA	1W11	99	J42 8
2W04	900	B02	7	2*7 PERIPHERAL TO CENTRAL DATA	1W11	900	J42 6
2W04	901	B02	10	2*8 PERIPHERAL TO CENTRAL DATA	1W11	901	J42 4
2W04	902	B02	21	2*9 PERIPHERAL TO CENTRAL DATA	1W11	902	J42 27
2W04	903	B02	24	2*10 PERIPHERAL TO CENTRAL DATA	1W11	903	J42 25
2W04	904	B02	26	2*11 PERIPHERAL TO CENTRAL DATA	1W11	904	J42 23
2W04	905				1W11	905	
2W04	906				1W11	906	
2W04	907				1W11	907	
2W04	908				1W11	908	

2W05	00	WRITE BUFFER TO DISTRIBUTOR						13W10	00	
2W05	90	B12	8	2*0	DATA TO MEMORY FAN OUT		13W10	90	139	4
2W05	91	B12	6	2*1	DATA TO MEMORY FAN OUT		13W10	91	139	6
2W05	92	B12	4	2*2	DATA TO MEMORY FAN OUT		13W10	92	139	7
2W05	93	B12	27	2*3	DATA TO MEMORY FAN OUT		13W10	93	139	27
2W05	94	B12	25	2*4	DATA TO MEMORY FAN OUT		13W10	94	139	25
2W05	95	B12	23	2*5	DATA TO MEMORY FAN OUT		13W10	95	139	22
2W05	96	B13	8	2*6	DATA TO MEMORY FAN OUT		13W10	96	140	4
2W05	97	B13	6	2*7	DATA TO MEMORY FAN OUT		13W10	97	140	6
2W05	98	B13	4	2*8	DATA TO MEMORY FAN OUT		13W10	98	140	7
2W05	99	B13	27	2*9	DATA TO MEMORY FAN OUT		13W10	99	140	27
2W05	900	B13	25	2*10	DATA TO MEMORY FAN OUT		13W10	900	140	25
2W05	901	B13	23	2*11	DATA TO MEMORY FAN OUT		13W10	901	140	22
2W05	902	B14	8	2*12	DATA TO MEMORY FAN OUT		13W10	902	141	4
2W05	903	B14	6	2*13	DATA TO MEMORY FAN OUT		13W10	903	141	6
2W05	904	B14	4	2*14	DATA TO MEMORY FAN OUT		13W10	904	141	7
2W05	905	B22	22		MEMORY MARGIN		13W10	905	C14	14
2W05	906						13W10	906		
2W05	907						13W10	907		
2W05	908						13W10	908		

2W06	00	WRITE BUFFER TO DISTRIBUTOR						14W10	00	
2W06	90	B14	27	2*15	DATA TO MEMORY FAN OUT		14W10	90	102	4
2W06	91	B14	25	2*16	DATA TO MEMORY FAN OUT		14W10	91	102	6
2W06	92	B14	23	2*17	DATA TO MEMORY FAN OUT		14W10	92	102	7
2W06	93	B15	8	2*18	DATA TO MEMORY FAN OUT		14W10	93	102	27
2W06	94	B15	6	2*19	DATA TO MEMORY FAN OUT		14W10	94	102	25
2W06	95	B15	4	2*20	DATA TO MEMORY FAN OUT		14W10	95	102	22
2W06	96	B15	27	2*21	DATA TO MEMORY FAN OUT		14W10	96	103	4
2W06	97	B15	25	2*22	DATA TO MEMORY FAN OUT		14W10	97	103	6
2W06	98	B15	23	2*23	DATA TO MEMORY FAN OUT		14W10	98	103	7
2W06	99	B16	8	2*24	DATA TO MEMORY FAN OUT		14W10	99	103	27
2W06	900	B16	6	2*25	DATA TO MEMORY FAN OUT		14W10	900	103	25
2W06	901	B16	4	2*26	DATA TO MEMORY FAN OUT		14W10	901	103	22
2W06	902	B16	27	2*27	DATA TO MEMORY FAN OUT		14W10	902	104	4
2W06	903	B16	25	2*28	DATA TO MEMORY FAN OUT		14W10	903	104	6
2W06	904	B16	23	2*29	DATA TO MEMORY FAN OUT		14W10	904	104	7
2W06	905	B22	20		MEMORY MARGIN		14W10	905	C14	14
2W06	906						14W10	906		
2W06	907						14W10	907		
2W06	908						14W10	908		

2W07	00	WRITE BUFFER TO DISTRIBUTOR				15W10	00
2W07	90	B17	8	2*30 DATA TO MEMORY FAN OUT	15W10	90	I39 4
2W07	91	B17	6	2*31 DATA TO MEMORY FAN OUT	15W10	91	I39 6
2W07	92	B17	4	2*32 DATA TO MEMORY FAN OUT	15W10	92	I39 7
2W07	93	B17	27	2*33 DATA TO MEMORY FAN OUT	15W10	93	I39 27
2W07	94	B17	25	2*34 DATA TO MEMORY FAN OUT	15W10	94	I39 25
2W07	95	B17	23	2*35 DATA TO MEMORY FAN OUT	15W10	95	I39 22
2W07	96	B18	8	2*36 DATA TO MEMORY FAN OUT	15W10	96	I40 4
2W07	97	B18	6	2*37 DATA TO MEMORY FAN OUT	15W10	97	I40 6
2W07	98	B18	4	2*38 DATA TO MEMORY FAN OUT	15W10	98	I40 7
2W07	99	B18	27	2*39 DATA TO MEMORY FAN OUT	15W10	99	I40 27
2W07	900	B18	25	2*40 DATA TO MEMORY FAN OUT	15W10	900	I40 25
2W07	901	B18	23	2*41 DATA TO MEMORY FAN OUT	15W10	901	I40 22
2W07	902	B19	8	2*42 DATA TO MEMORY FAN OUT	15W10	902	I41 4
2W07	903	B19	6	2*43 DATA TO MEMORY FAN OUT	15W10	903	I41 6
2W07	904	B19	4	2*44 DATA TO MEMORY FAN OUT	15W10	904	I41 7
2W07	905	B22	18	MEMORY MARGIN	15W10	905	C14 14
2W07	906				15W10	906	
2W07	907				15W10	907	
2W07	908				15W10	908	

2W08	00	WRITE BUFFER TO DISTRIBUTOR				16W10	00
2W08	90	B19	27	2*45 DATA TO MEMORY FAN OUT	16W10	90	I02 4
2W08	91	B19	25	2*46 DATA TO MEMORY FAN OUT	16W10	91	I02 6
2W08	92	B19	23	2*47 DATA TO MEMORY FAN OUT	16W10	92	I02 7
2W08	93	B20	8	2*48 DATA TO MEMORY FAN OUT	16W10	93	I02 27
2W08	94	B20	6	2*49 DATA TO MEMORY FAN OUT	16W10	94	I02 25
2W08	95	B20	4	2*50 DATA TO MEMORY FAN OUT	16W10	95	I02 22
2W08	96	B20	27	2*51 DATA TO MEMORY FAN OUT	16W10	96	I03 4
2W08	97	B20	25	2*52 DATA TO MEMORY FAN OUT	16W10	97	I03 6
2W08	98	B20	23	2*53 DATA TO MEMORY FAN OUT	16W10	98	I03 7
2W08	99	B21	8	2*54 DATA TO MEMORY FAN OUT	16W10	99	I03 27
2W08	900	B21	6	2*55 DATA TO MEMORY FAN OUT	16W10	900	I03 25
2W08	901	B21	4	2*56 DATA TO MEMORY FAN OUT	16W10	901	I03 22
2W08	902	B21	27	2*57 DATA TO MEMORY FAN OUT	16W10	902	I04 4
2W08	903	B21	25	2*58 DATA TO MEMORY FAN OUT	16W10	903	I04 6
2W08	904	B21	23	2*59 DATA TO MEMORY FAN OUT	16W10	904	I04 7
2W08	905	B22	16	MEMORY MARGIN	16W10	905	C14 14
2W08	906				16W10	906	
2W08	907				16W10	907	
2W08	908				16W10	908	

2W09	00	MEMORY DISTRIBUTION FROM LOWER REGISTER				7W08	00		
2W09	90	A09	10	DATA 2*8	REG. TO MEM.	7W08	90	F28	21
2W09	91	A09	21	DATA 2*9	REG. TO MEM.	7W08	91	F29	3
2W09	92	A09	24	DATA 2*10	REG. TO MEM.	7W08	92	F29	25
2W09	93	A09	26	DATA 2*11	REG. TO MEM.	7W08	93	F29	21
2W09	94	A10	5	DATA 2*12	REG. TO MEM.	7W08	94	F30	3
2W09	95	A10	7	DATA 2*13	REG. TO MEM.	7W08	95	F30	25
2W09	96	A10	10	DATA 2*14	REG. TO MEM.	7W08	96	F30	21
2W09	97	A10	21	DATA 2*15	REG. TO MEM.	7W08	97	F31	3
2W09	98	A10	24	DATA 2*16	REG. TO MEM.	7W08	98	F31	25
2W09	99	A10	26	DATA 2*17	REG. TO MEM.	7W08	99	F31	21
2W09	900	A08	5	DATA 2*0	REG. TO MEM.	7W08	900	F26	3
2W09	901	A08	7	DATA 2*1	REG. TO MEM.	7W08	901	F26	25
2W09	902	A08	10	DATA 2*2	REG. TO MEM.	7W08	902	F26	21
2W09	903	A08	21	DATA 2*3	REG. TO MEM.	7W08	903	F27	3
2W09	904	A08	24	DATA 2*4	REG. TO MEM.	7W08	904	F27	25
2W09	905	A08	26	DATA 2*5	REG. TO MEM.	7W08	905	F27	21
2W09	906	A09	5	DATA 2*6	REG. TO MEM.	7W08	906	F28	3
2W09	907	A09	7	DATA 2*7	REG. TO MEM.	7W08	907	F28	25
2W09	908					7W08	908		

2W10	00	MEMORY DISTRIBUTION FROM LOWER REGISTER				7W09	00		
2W10	90	A12	10	DATA 2*26	REG. TO MEM.	7W09	90	F34	21
2W10	91	A12	21	DATA 2*27	REG. TO MEM.	7W09	91	F35	3
2W10	92	A12	24	DATA 2*28	REG. TO MEM.	7W09	92	F35	25
2W10	93	A12	26	DATA 2*29	REG. TO MEM.	7W09	93	F35	21
2W10	94	A13	5	DATA 2*30	REG. TO MEM.	7W09	94	E26	3
2W10	95	A13	7	DATA 2*31	REG. TO MEM.	7W09	95	E26	25
2W10	96	A13	10	DATA 2*32	REG. TO MEM.	7W09	96	E26	21
2W10	97	A13	21	DATA 2*33	REG. TO MEM.	7W09	97	E27	3
2W10	98	A13	24	DATA 2*34	REG. TO MEM.	7W09	98	E27	25
2W10	99	A13	26	DATA 2*35	REG. TO MEM.	7W09	99	E27	21
2W10	900	A11	5	DATA 2*18	REG. TO MEM.	7W09	900	F32	3
2W10	901	A11	7	DATA 2*19	REG. TO MEM.	7W09	901	F32	25
2W10	902	A11	10	DATA 2*20	REG. TO MEM.	7W09	902	F32	21
2W10	903	A11	21	DATA 2*21	REG. TO MEM.	7W09	903	F33	3
2W10	904	A11	24	DATA 2*22	REG. TO MEM.	7W09	904	F33	25
2W10	905	A11	26	DATA 2*23	REG. TO MEM.	7W09	905	F33	21
2W10	906	A12	5	DATA 2*24	REG. TO MEM.	7W09	906	F34	3
2W10	907	A12	7	DATA 2*25	REG. TO MEM.	7W09	907	F34	25
2W10	908					7W09	908		

2W11	00	MEMORY DISTRIBUTION FROM UPPER REGISTER				8W10	00
2W11	90	A14	10	2*38	REGISTER TO DISTRIBUTOR	8W10	90 A09 25
2W11	91	A14	21	2*39	REGISTER TO DISTRIBUTOR	8W10	91 A10 3
2W11	92	A14	24	2*40	REGISTER TO DISTRIBUTOR	8W10	92 A10 21
2W11	93	A14	26	2*41	REGISTER TO DISTRIBUTOR	8W10	93 A10 25
2W11	94	A15	5	2*42	REGISTER TO DISTRIBUTOR	8W10	94 A11 3
2W11	95	A15	7	2*43	REGISTER TO DISTRIBUTOR	8W10	95 A11 21
2W11	96	A15	10	2*44	REGISTER TO DISTRIBUTOR	8W10	96 A11 25
2W11	97					8W10	97
2W11	98					8W10	98
2W11	99					8W10	99
2W11	900					8W10	900
2W11	901					8W10	901
2W11	902					8W10	902
2W11	903					8W10	903
2W11	904					8W10	904
2W11	905					8W10	905
2W11	906	A14	5	2*36	REGISTER TO DISTRIBUTOR	8W10	906 A09 3
2W11	907	A14	7	2*37	REGISTER TO DISTRIBUTOR	8W10	907 A09 21
2W11	908					8W10	908

2W12	00	MEMORY DISTRIBUTION FROM UPPER REGISTER				8W11	00
2W12	90	A16	26	2*53	REGISTER TO DISTRIBUTOR	8W11	90 B10 25
2W12	91	A17	5	2*54	REGISTER TO DISTRIBUTOR	8W11	91 B11 3
2W12	92	A17	7	2*55	REGISTER TO DISTRIBUTOR	8W11	92 B11 21
2W12	93	A17	10	2*56	REGISTER TO DISTRIBUTOR	8W11	93 B11 25
2W12	94	A17	21	2*57	REGISTER TO DISTRIBUTOR	8W11	94 B12 3
2W12	95	A17	24	2*58	REGISTER TO DISTRIBUTOR	8W11	95 B12 21
2W12	96	A17	26	2*59	REGISTER TO DISTRIBUTOR	8W11	96 B12 25
2W12	97					8W11	97
2W12	98					8W11	98
2W12	99					8W11	99
2W12	900	A15	21	2*45	REGISTER TO DISTRIBUTOR	8W11	900 A12 3
2W12	901	A15	24	2*46	REGISTER TO DISTRIBUTOR	8W11	901 A12 21
2W12	902	A15	26	2*47	REGISTER TO DISTRIBUTOR	8W11	902 A12 25
2W12	903	A16	5	2*48	REGISTER TO DISTRIBUTOR	8W11	903 B09 3
2W12	904	A16	7	2*49	REGISTER TO DISTRIBUTOR	8W11	904 B09 21
2W12	905	A16	10	2*50	REGISTER TO DISTRIBUTOR	8W11	905 B09 25
2W12	906	A16	21	2*51	REGISTER TO DISTRIBUTOR	8W11	906 B10 3
2W12	907	A16	24	2*52	REGISTER TO DISTRIBUTOR	8W11	907 B10 21
2W12	908					8W11	908

2W13	00	MEMORY DISTRIBUTION FROM CENTRAL CONTROL				5W10	00
2W13	90	A03	10	STORE CONTROL BIT 2*44	5W10	90	I39 4
2W13	91	A03	21	STORE CONTROL BIT 2*45	5W10	91	I39 27
2W13	92	A03	24	STORE CONTROL BIT 2*46	5W10	92	I39 25
2W13	93	A03	26	STORE CONTROL BIT 2*47	5W10	93	I39 23
2W13	94	A04	5	STORE CONTROL BIT 2*48	5W10	94	I39 21
2W13	95	A04	7	STORE CONTROL BIT 2*49	5W10	95	I39 19
2W13	96	A04	10	STORE CONTROL BIT 2*50	5W10	96	I40 4
2W13	97	A04	21	STORE CONTROL BIT 2*51	5W10	97	I40 27
2W13	98	A04	24	STORE CONTROL BIT 2*52	5W10	98	I40 23
2W13	99	A04	26	STORE CONTROL BIT 2*53	5W10	99	I40 23
2W13	900	A03	7	STORE CONTROL BIT 2*43	5W10	900	I39 6
2W13	901	A02	5	STORE CONTROL BIT 2*36	5W10	901	I38 25
2W13	902	A02	7	STORE CONTROL BIT 2*37	5W10	902	I38 23
2W13	903	A02	10	STORE CONTROL BIT 2*38	5W10	903	I38 21
2W13	904	A02	21	STORE CONTROL BIT 2*39	5W10	904	I38 19
2W13	905	A02	24	STORE CONTROL BIT 2*40	5W10	905	I39 12
2W13	906	A02	26	STORE CONTROL BIT 2*41	5W10	906	I39 10
2W13	907	A03	5	STORE CONTROL BIT 2*42	5W10	907	I39 8
2W13	908				5W10	908	

2W14	00	MEMORY DISTRIBUTION FROM CENTRAL CONTROL				5W27	00
2W14	90	A01	5	2*30	5W27	90	I38 12
2W14	91	A01	7	2*31	5W27	91	I38 10
2W14	92	A01	10	2*32	5W27	92	I38 8
2W14	93	A01	21	2*33	5W27	93	I38 6
2W14	94	A01	24	2*34	5W27	94	I38 4
2W14	95	A01	26	2*35	5W27	95	I38 27
2W14	96	A05	10	2*56	5W27	96	I40 19
2W14	97				5W27	97	
2W14	98				5W27	98	
2W14	99				5W27	99	
2W14	900				5W27	900	
2W14	901	I22	12	CLOCK	5W27	901	J39 8
2W14	902	A07	7	STORE CONTROL	5W27	902	K38 23
2W14	903	A07	10	STORE REGISTERS	5W27	903	K38 25
2W14	904	A07	5	STORE PERIPHERAL	5W27	904	K38 27
2W14	905	E01	25	EXP = 1777 ERROR	5W27	905	N23 10
2W14	906	E01	23	EXP = 3777 ERROR	5W27	906	N23 7
2W14	907				5W27	907	
2W14	908				5W27	908	

2W15 CABLE 1 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.				SOURCE
2W15	1	0	2C21- 6	Row 1	Bit	0	0001 A
2W15		9	5			1	B
2W15	2	2	7			2	C
2W15		9	22			3	D
2W15	3	4	24			4	E
2W15		9	23			5	F
2W15	4	5	2C22- 6			6	G
2W15		9	5			7	H
2W15	5	6	7			8	I
2W15		9	22			9	J
2W15	6	90	24			10	K
2W15		9	23			11	L
2W15	7	91	2C23- 6	Row 2	Bit	0	0002 A
2W15		9	5			1	B
2W15	8	92	7			2	C
2W15		9	22			3	D
2W15	9	93	24			4	E
2W15		9	23			5	F
2W15	10	94	2C24- 6			6	G
2W15		9	5			7	H
2W15	11	95	7			8	I
2W15		9	22			9	J
2W15	12	96	24			10	K
2W15		9	23			11	L
2W15	13	97	2C25- 6	Row 3	Bit	0	0003 A
2W15		9	5			1	B
2W15	14	98	7			2	C
2W15		9	22			3	D
2W15	15	900	24			4	E
2W15		9	23			5	F
2W15	16	910	2C26- 6			6	G
2W15		9	5			7	H
2W15	17	920	7			8	I
2W15		9	22			9	J
2W15	18	930	24			10	K
2W15		9	23			11	L
2W15	19	940	2C27- 6	Row 4	Bit	0	0004 A
2W15		9	5			1	B
2W15	20	950	7			2	C
2W15		9	22			3	D
2W15	21	960	24			4	E
2W15		9	23			5	F
2W15	22	970	2C28- 6			6	G
2W15		9	5			7	H
2W15	23	980	7			8	I
2W15		9	22			9	J
2W15	24	990	24			10	K
2W15		9	23			11	L

2W16 CABLE 2 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.				SOURCE
2W16	1	0	2C21- 4	Row 5	Bit	0	0005 A
2W16		9	3			1	B
2W16	2	2	13			2	C
2W16		9	18			3	D
2W16	3	4	26			4	E
2W16		9	25			5	F
2W16	4	5	2C22- 4			6	G
2W16		9	3			7	H
2W16	5	6	13			8	I
2W16		9	18			9	J
2W16	6	90	26			10	K
2W16		9	25			11	L
2W16	7	91	2C23- 4	Row 6	Bit	0	0006 A
2W16		9	3			1	B
2W16	8	92	13			2	C
2W16		9	18			3	D
2W16	9	93	26			4	E
2W16		9	25			5	F
2W16	10	94	2C24- 4			6	G
2W16		9	3			7	H
2W16	11	95	13			8	I
2W16		9	18			9	J
2W16	12	96	26			10	K
2W16		9	25			11	L
2W16	13	97	2C25- 4	Row 7	Bit	0	0007 A
2W16		9	3			1	B
2W16	14	98	13			2	C
2W16		9	18			3	D
2W16	15	900	26			4	E
2W16		9	25			5	F
2W16	16	910	2C26- 4			6	G
2W16		9	3			7	H
2W16	17	920	13			8	I
2W16		9	18			9	J
2W16	18	930	26			10	K
2W16		9	25			11	L
2W16	19	940	2C27- 4	Row 10	Bit	0	0010 A
2W16		9	3			1	B
2W16	20	950	13			2	C
2W16		9	18			3	D
2W16	21	960	26			4	E
2W16		9	25			5	F
2W16	22	970	2C28- 4			6	G
2W16		9	3			7	H
2W16	23	980	13			8	I
2W16		9	18			9	J
2W16	24	990	26			10	K
2W16		9	25			11	L

2W17 CABLE 3 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.			SOURCE
2W17	1	0	2C21-	2	Row 11 Bit	0 0011 A
2W17		9		1		1 B
2W17	2	2		12		2 C
2W17		9		17		3 D
2W17	3	4		28		4 E
2W17		9		27		5 F
2W17	4	5	2C22-	2		6 G
2W17		9		1		7 H
2W17	5	6		12		8 I
2W17		9		17		9 J
2W17	6	90		28		10 K
2W17		9		27		11 L
2W17	7	91	2C23-	2	Row 12 Bit	0 0012 A
2W17		9		1		1 B
2W17	8	92		12		2 C
2W17		9		17		3 D
2W17	9	93		28		4 E
2W17		9		27		5 F
2W17	10	94	2C24-	2		6 G
2W17		9		1		7 H
2W17	11	95		12		8 I
2W17		9		17		9 J
2W17	12	96		28		10 K
2W17		9		27		11 L
2W17	13	97	2C25-	2	Row 13 Bit	0 0013 A
2W17		9		1		1 B
2W17	14	98		12		2 C
2W17		9		17		3 D
2W17	15	900		28		4 E
2W17		9		27		5 F
2W17	16	910	2C26-	2		6 G
2W17		9		1		7 H
2W17	17	920		12		8 I
2W17		9		17		9 J
2W17	18	930		28		10 K
2W17		9		27		11 L
2W17	19	940	2C27-	2	Row 14 Bit	0 0014 A
2W17		9		1		1 B
2W17	20	950		12		2 C
2W17		9		17		3 D
2W17	21	960		28		4 E
2W17		9		27		5 F
2W17	22	970	2C28-	2		6 G
2W17		9		1		7 H
2W17	23	980		12		8 I
2W17		9		17		9 J
2W17	24	990		28		10 K
2W17		9		27		11 L

2W18 CABLE 4 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.	SOURCE
2W18	1	0	2A01 GND	GND BUS
2W18		9		
2W18	2	2	2A01 GND	
2W18		9		
2W18	3	4	2A02 GND	
2W18		9		
2W18	4	5	2A02 GND	
2W18		9		
2W18	5	6	2A03 GND	
2W18		9		
2W18	6	90	2A03 GND	
2W18		9		
2W18	7	91	2A04 GND	
2W18		9		
2W18	8	92	2A04 GND	
2W18		9		
2W18	9	93	2A05 GND	
2W18		9		
2W18	10	94	2A05 GND	
2W18		9		
2W18	11	95	2A05 GND	
2W18		9		
2W18	12	96	2 +6V	+6V BUS
2W18		9		
2W18	13	97	2 +6V	
2W18		9		
2W18	14	98	2 +6V	
2W18		9		
2W18	15	900	2 +6V	
2W18		9		
2W18	16	910	2 +6V	
2W18		9		
2W18	17	920	2 +6V	
2W18		9		
2W18	18	930	2 +6V	
2W18		9		
2W18	19	940	2 +6V	
2W18		9		
2W18	20	950	2 +6V	
2W18		9		
2W18	21	960	2 +6V	
2W18		9		
2W18	22	970	2 +6V	
2W18		9		
2W18	23	980	2B 22-26	S04-2
2W18		9		
2W18	24	990	2B 22-24	S04-2
2W18		9		

2W19 00 DEAD START TO CHANNEL (INPUT)

2W19	90	C33	8	2*0	INPUT DATA
2W19	91	C33	6	2*1	INPUT DATA
2W19	92	C33	4	2*2	INPUT DATA
2W19	93	C33	27	2*3	INPUT DATA
2W19	94	C33	25	2*4	INPUT DATA
2W19	95	C33	23	2*5	INPUT DATA
2W19	96	C34	8	2*6	INPUT DATA
2W19	97	C34	6	2*7	INPUT DATA
2W19	98	C34	4	2*8	INPUT DATA
2W19	99	C34	27	2*9	INPUT DATA
2W19	900	C34	25	2*10	INPUT DATA
2W19	901	C34	23	2*11	INPUT DATA
2W19	902				ACTIVE
2W19	903	C32	6		INACTIVE
2W19	904	C32	8		FULL
2W19	905				EMPTY
2W19	906				
2W19	907				
2W19	908				

2W20 00 DEAD START TO CHANNEL (OUTPUT)

2W20	90			2*0	OUTPUT DATA
2W20	91			2*1	OUTPUT DATA
2W20	92			2*2	OUTPUT DATA
2W20	93			2*3	OUTPUT DATA
2W20	94			2*4	OUTPUT DATA
2W20	95			2*5	OUTPUT DATA
2W20	96			2*6	OUTPUT DATA
2W20	97			2*7	OUTPUT DATA
2W20	98			2*8	OUTPUT DATA
2W20	99			2*9	OUTPUT DATA
2W20	900			2*10	OUTPUT DATA
2W20	901			2*11	OUTPUT DATA
2W20	902				ACTIVE
2W20	903				INACTIVE
2W20	904				FULL
2W20	905	C17	12		EMPTY
2W20	906				FUNCTION
2W20	907	C16	12		MASTER CLEAR
2W20	908				

2W21	00	DIVIDE FROM CONTROL				5W13	00
2W21	90					5W13	90
2W21	91	J02	7	ROUND		5W13	91 E40 12
2W21	92			BOOLEAN		5W13	92
2W21	93	J01	5	SUM		5W13	93 E38 8
2W21	94	J01	7	DIFFERENCE		5W13	94 E38 10
2W21	95	J01	10	PRODUCT		5W13	95 E38 12
2W21	96	J02	10	POP. COUNT		5W13	96 E40 19
2W21	97	J01	24	COMPLEMENT		5W13	97 E38 6
2W21	98	J01	21	TRANSFER		5W13	98 E38 4
2W21	99	L11	18	TRANSMIT BOOLEAN		5W13	99 J40 10
2W21	900	L11	14	TRANSMIT DIVIDE		5W13	900 J40 27
2W21	901					5W13	901
2W21	902	L11	16	GO BOOLEAN		5W13	902 E40 21
2W21	903	L11	12	GO DIVIDE		5W13	903 J40 8
2W21	904			CLEAR		5W13	904
2W21	905	E01	8	DIVIDE REQUEST RELEASE		5W13	905 J24 19
2W21	906	E01	6	BOOLEAN REQUEST RELEASE		5W13	906 J23 19
2W21	907	009	7	TRANSMIT MUIT 1		5W13	907 035 10
2W21	908	009	21	TRANSMIT MUIT 2		5W13	908 035 12

2W22	00	DIVIDE FROM REGISTER				8W18	00
2W22	90	M08	12	2*48 X**K (EXPONENT)		8W18	90 D09 3
2W22	91	M08	14	2*49 X**K (EXPONENT)		8W18	91 D09 21
2W22	92	M08	16	2*50 X**K (EXPONENT)		8W18	92 D09 25
2W22	93	M08	18	2*51 X**K (EXPONENT)		8W18	93 D10 3
2W22	94	M09	12	2*52 X**K (EXPONENT)		8W18	94 D10 21
2W22	95	M09	14	2*53 X**K (EXPONENT)		8W18	95 D10 25
2W22	96	M09	16	2*54 X**K (EXPONENT)		8W18	96 D11 3
2W22	97	M09	18	2*55 X**K (EXPONENT)		8W18	97 D11 21
2W22	98	M10	12	2*56 X**K (EXPONENT)		8W18	98 D11 25
2W22	99	M10	14	2*57 X**K (EXPONENT)		8W18	99 D12 3
2W22	900	M10	16	2*58 X**K (EXPONENT)		8W18	900 D12 21
2W22	901	M10	18	2*59 X**K (EXPONENT)		8W18	901 D12 23
2W22	902	K13	1	2*48 EXPONENT RESULT-REG.		8W18	902 C03 2
2W22	903	K13	6	2*49 EXPONENT RESULT-REG.		8W18	903 C03 26
2W22	904	K13	23	2*50 EXPONENT RESULT-REG.		8W18	904 C04 2
2W22	905	K13	28	2*51 EXPONENT RESULT-REG.		8W18	905 C04 26
2W22	906	K14	1	2*52 EXPONENT RESULT-REG.		8W18	906 C05 2
2W22	907	K14	6	2*53 EXPONENT RESULT-REG.		8W18	907 C05 26
2W22	908	E02	5	ERROR (EXP = 1777 FROM CHASSIS 7)		8W18	908 H06 19

2W23	00	DIVIDE FROM REGISTER				8W19	00
2W23	90	L08	12	2*48 X**J (EXPONENT)	8W19	90	F09 3
2W23	91	L08	14	2*49 X**J (EXPONENT)	8W19	91	F09 21
2W23	92	L08	16	2*50 X**J (EXPONENT)	8W19	92	F09 25
2W23	93	L08	18	2*51 X**J (EXPONENT)	8W19	93	F10 3
2W23	94	L09	12	2*52 X**J (EXPONENT)	8W19	94	F10 21
2W23	95	L09	14	2*53 X**J (EXPONENT)	8W19	95	F10 25
2W23	96	L09	16	2*54 X**J (EXPONENT)	8W19	96	F11 3
2W23	97	L09	18	2*55 X**J (EXPONENT)	8W19	97	F11 21
2W23	98	L10	12	2*56 X**J (EXPONENT)	8W19	98	F11 25
2W23	99	L10	14	2*57 X**J (EXPONENT)	8W19	99	F12 3
2W23	900	L10	16	2*58 X**J (EXPONENT)	8W19	900	F12 21
2W23	901	L10	18	2*59 X**J (EXPONENT)	8W19	901	F12 23
2W23	902	K14	23	2*54 EXPONENT RESULT-REG	8W19	902	C06 2
2W23	903	K14	28	2*55 EXPONENT RESULT-REG	8W19	903	C06 26
2W23	904	K15	1	2*56 EXPONENT RESULT-REG	8W19	904	C07 2
2W23	905	K15	6	2*57 EXPONENT RESULT-REG	8W19	905	C07 26
2W23	906	K15	23	2*58 EXPONENT RESULT-REG	8W19	906	C08 2
2W23	907	K15	28	2*59 EXPONENT RESULT-REG	8W19	907	C08 26
2W23	908	E02	7	ERROR (EXP = 3777 FROM CHASSIS 8)	8W19	908	H06 21

2W24	00	DIVIDE TO MULTIPLY				6W20	00
2W24	90	K01	1	2*0 DIVIDE TO MULTIPLY RESULT	6W20	90	K01 11
2W24	91	K01	6	2*1 DIVIDE TO MULTIPLY RESULT	6W20	91	K01 10
2W24	92	K01	23	2*2 DIVIDE TO MULTIPLY RESULT	6W20	92	K02 11
2W24	93	K01	28	2*3 DIVIDE TO MULTIPLY RESULT	6W20	93	K02 10
2W24	94	K02	1	2*4 DIVIDE TO MULTIPLY RESULT	6W20	94	K03 11
2W24	95	K02	6	2*5 DIVIDE TO MULTIPLY RESULT	6W20	95	K03 10
2W24	96	K02	23	2*6 DIVIDE TO MULTIPLY RESULT	6W20	96	K04 11
2W24	97	K02	28	2*7 DIVIDE TO MULTIPLY RESULT	6W20	97	K04 10
2W24	98	K03	1	2*8 DIVIDE TO MULTIPLY RESULT	6W20	98	K05 11
2W24	99	K03	6	2*9 DIVIDE TO MULTIPLY RESULT	6W20	99	K05 10
2W24	900	K03	23	2*10 DIVIDE TO MULTIPLY RESULT	6W20	900	K06 11
2W24	901	K03	28	2*11 DIVIDE TO MULTIPLY RESULT	6W20	901	K06 10
2W24	902	K04	1	2*12 DIVIDE TO MULTIPLY RESULT	6W20	902	K07 11
2W24	903	K04	6	2*13 DIVIDE TO MULTIPLY RESULT	6W20	903	K07 10
2W24	904	K04	23	2*14 DIVIDE TO MULTIPLY RESULT	6W20	904	L01 11
2W24	905	K04	28	2*15 DIVIDE TO MULTIPLY RESULT	6W20	905	L01 10
2W24	906	K05	1	2*16 DIVIDE TO MULTIPLY RESULT	6W20	906	L02 11
2W24	907	K05	6	2*17 DIVIDE TO MULTIPLY RESULT	6W20	907	L02 10
2W24	908				6W20	908	

2W25	00	DIVIDE TO MULTIPLY				6W21	00
2W25	90	K05	23	2*18	DIVIDE TO MULTIPLY RESULT	6W21	90 L03 11
2W25	91	K05	28	2*19	DIVIDE TO MULTIPLY RESULT	6W21	91 L03 10
2W25	92	K06	1	2*20	DIVIDE TO MULTIPLY RESULT	6W21	92 L04 11
2W25	93	K06	6	2*21	DIVIDE TO MULTIPLY RESULT	6W21	93 L04 10
2W25	94	K06	23	2*22	DIVIDE TO MULTIPLY RESULT	6W21	94 L05 11
2W25	95	K06	28	2*23	DIVIDE TO MULTIPLY RESULT	6W21	95 L05 10
2W25	96	K07	1	2*24	DIVIDE TO MULTIPLY RESULT	6W21	96 L06 11
2W25	97	K07	6	2*25	DIVIDE TO MULTIPLY RESULT	6W21	97 L06 10
2W25	98	K07	23	2*26	DIVIDE TO MULTIPLY RESULT	6W21	98 L07 11
2W25	99	K07	28	2*27	DIVIDE TO MULTIPLY RESULT	6W21	99 L07 10
2W25	900	K08	1	2*28	DIVIDE TO MULTIPLY RESULT	6W21	900 M01 11
2W25	901	K08	6	2*29	DIVIDE TO MULTIPLY RESULT	6W21	901 M01 10
2W25	902	K08	23	2*30	DIVIDE TO MULTIPLY RESULT	6W21	902 M02 11
2W25	903	K08	28	2*31	DIVIDE TO MULTIPLY RESULT	6W21	903 M02 10
2W25	904	K09	1	2*32	DIVIDE TO MULTIPLY RESULT	6W21	904 M03 11
2W25	905	K09	6	2*33	DIVIDE TO MULTIPLY RESULT	6W21	905 M03 10
2W25	906	K09	23	2*34	DIVIDE TO MULTIPLY RESULT	6W21	906 M04 11
2W25	907	K09	28	2*35	DIVIDE TO MULTIPLY RESULT	6W21	907 M04 10
2W25	908	Q11	7	REDUCE MULT. 1		6W21	908 F07 1

2W26	00	DIVIDE TO MULTIPLY				6W22	00
2W26	90	K10	1	2*36	DIVIDE TO MULTIPLY RESULT	6W22	90 M05 11
2W26	91	K10	6	2*37	DIVIDE TO MULTIPLY RESULT	6W22	91 M05 10
2W26	92	K10	23	2*38	DIVIDE TO MULTIPLY RESULT	6W22	92 M06 11
2W26	93	K10	28	2*39	DIVIDE TO MULTIPLY RESULT	6W22	93 M06 10
2W26	94	K11	1	2*40	DIVIDE TO MULTIPLY RESULT	6W22	94 M07 11
2W26	95	K11	6	2*41	DIVIDE TO MULTIPLY RESULT	6W22	95 M07 10
2W26	96	K11	23	2*42	DIVIDE TO MULTIPLY RESULT	6W22	96 N01 11
2W26	97	K11	28	2*43	DIVIDE TO MULTIPLY RESULT	6W22	97 N01 10
2W26	98	K12	1	2*44	DIVIDE TO MULTIPLY RESULT	6W22	98 N02 11
2W26	99	K12	6	2*45	DIVIDE TO MULTIPLY RESULT	6W22	99 N02 10
2W26	900	K12	23	2*46	DIVIDE TO MULTIPLY RESULT	6W22	900 N03 11
2W26	901	K12	28	2*47	DIVIDE TO MULTIPLY RESULT	6W22	901 N03 10
2W26	902	Q12	7	REDUCE MULT 2		6W22	902 F06 1
2W26	903					6W22	903
2W26	904					6W22	904
2W26	905	M11	14	GO MULT 1		6W22	905 H10 1
2W26	906	M11	16	GO MULT 2		6W22	906 K10 1
2W26	907	Q11	5	D P MULT 1		6W22	907 N07 12
2W26	908	Q12	5	D P MULT 2		6W22	908 N07 19

2W27	00	DIVIDE FROM MULTIPLY				6W23	00
2W27	90	R06	12	2*0 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	90	K01 1
2W27	91	R06	14	2*1 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	91	K01 3
2W27	92	R06	16	2*2 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	92	K01 2
2W27	93	R06	18	2*3 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	93	K02 1
2W27	94	R07	12	2*4 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	94	K02 3
2W27	95	R07	14	2*5 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	95	K02 2
2W27	96	R07	16	2*6 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	96	K03 1
2W27	97	R07	18	2*7 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	97	K03 3
2W27	98	R08	12	2*8 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	98	K03 2
2W27	99	R08	14	2*9 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	99	K04 1
2W27	900	R08	16	2*10 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	900	K04 3
2W27	901	R08	18	2*11 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	901	K04 2
2W27	902	R09	12	2*12 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	902	K05 1
2W27	903	R09	14	2*13 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	903	K05 3
2W27	904	R09	16	2*14 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	904	K05 2
2W27	905	R09	18	2*15 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	905	K06 1
2W27	906	R10	12	2*16 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	906	K06 3
2W27	907	R10	14	2*17 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	907	K06 2
2W27	908				6W23	908	

2W28	00	DIVIDE FROM MULTIPLY				6W24	00
2W28	90	R10	16		6W24	90	K07 1
2W28	91	R10	18		6W24	91	K07 3
2W28	92	P06	12	2*20 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	92	K07 2
2W28	93	P06	14	2*21 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	93	L01 1
2W28	94	P06	16	2*22 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	94	L01 3
2W28	95	P06	18	2*23 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	95	L01 2
2W28	96	P07	12	2*24 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	96	L02 1
2W28	97	P07	14	2*25 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	97	L02 3
2W28	98	P07	16	2*26 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	98	L02 2
2W28	99	P07	18	2*27 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	99	L03 1
2W28	900	P08	12	2*28 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	900	L03 3
2W28	901	P08	14	2*29 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	901	L03 2
2W28	902	P08	16	2*30 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	902	L04 1
2W28	903	P08	18	2*31 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	903	L04 3
2W28	904	P09	12	2*32 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	904	L04 2
2W28	905	P09	14	2*33 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	905	L05 1
2W28	906	P09	16	2*34 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	906	L05 3
2W28	907	P09	18	2*35 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	907	L05 2
2W28	908				6W24	908	

2W29	00	DIVIDE FROM MULTIPLY						6W25	00	
2W29	90	P10	12	2*36	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	90	L06	1
2W29	91	P10	14	2*37	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	91	L06	3
2W29	92	P10	16	2*38	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	92	L06	2
2W29	93	P10	18	2*39	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	93	L07	1
2W29	94	M06	12	2*40	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	94	L07	3
2W29	95	M06	14	2*41	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	95	L07	2
2W29	96	M06	16	2*42	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	96	M01	1
2W29	97	M06	18	2*43	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	97	M01	3
2W29	98	M07	12	2*44	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	98	M01	2
2W29	99	M07	14	2*45	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	99	M02	1
2W29	900	M07	16	2*46	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	900	M02	3
2W29	901	M07	18	2*47	MULTIPLY TO DIVIDE (OPERAND)	X**K	6W25	901	M02	2
2W29	902			2*48	SIGN		6W25	902		
2W29	903	Q06	12	2*0	MULTIPLY TO DIVIDE (OPERAND)	X**J	6W25	903	M03	1
2W29	904	Q06	14	2*1	MULTIPLY TO DIVIDE (OPERAND)	X**J	6W25	904	M03	3
2W29	905	Q06	16	2*2	MULTIPLY TO DIVIDE (OPERAND)	X**J	6W25	905	M03	2
2W29	906	Q06	18	2*3	MULTIPLY TO DIVIDE (OPERAND)	X**J	6W25	906	M04	1
2W29	907	Q07	12	2*4	MULTIPLY TO DIVIDE (OPERAND)	X**J	6W25	907	M04	3
2W29	908						6W25	908		

2W30	00	DIVIDE FROM MULTIPLY						6W26	00	
2W30	90	Q07	14	2*5	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	90	M04	2
2W30	91	Q07	16	2*6	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	91	M05	1
2W30	92	Q07	18	2*7	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	92	M05	3
2W30	93	Q08	12	2*8	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	93	M05	2
2W30	94	Q08	14	2*9	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	94	M06	1
2W30	95	Q08	16	2*10	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	95	M06	3
2W30	96	Q08	18	2*11	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	96	M06	2
2W30	97	Q09	12	2*12	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	97	M07	1
2W30	98	Q09	14	2*13	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	98	M07	3
2W30	99	Q09	16	2*14	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	99	M07	2
2W30	900	Q09	18	2*15	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	900	N01	1
2W30	901	Q10	12	2*16	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	901	N01	3
2W30	902	Q10	14	2*17	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	902	N01	2
2W30	903	Q10	16	2*18	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	903	N02	1
2W30	904	Q10	18	2*19	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	904	N02	3
2W30	905	N06	12	2*20	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	905	N02	2
2W30	906	N06	14	2*21	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	906	N03	1
2W30	907	N06	16	2*22	MULTIPLY TO DIVIDE OPERAND	X**J	6W26	907	N03	3
2W30	908						6W26	908		

2W31	00	DIVIDE FROM MULTIPLY				6W27	00
2W31	90	N06	18	2*23 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	90 N03 2
2W31	91	N07	12	2*24 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	91 N04 8
2W31	92	N07	14	2*25 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	92 N04 6
2W31	93	N07	16	2*26 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	93 N04 4
2W31	94	N07	18	2*27 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	94 N04 27
2W31	95	N08	12	2*28 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	95 N04 25
2W31	96	N08	14	2*29 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	96 N04 23
2W31	97	N08	16	2*30 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	97 N05 8
2W31	98	N08	18	2*31 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	98 N05 6
2W31	99	N09	12	2*32 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	99 N05 4
2W31	900	N09	14	2*33 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	900 N05 27
2W31	901	N09	16	2*34 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	901 N05 25
2W31	902	N09	18	2*35 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	902 N05 23
2W31	903	N10	12	2*36 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	903 N06 8
2W31	904	N10	14	2*37 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	904 N06 6
2W31	905	N10	16	2*38 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	905 N06 4
2W31	906	N10	18	2*39 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	906 N06 27
2W31	907	L06	12	2*40 MULTIPLY TO DIVIDE	OPERAND X**J	6W27	907 N06 25
2W31	908					6W27	908

2W32	00	DIVIDE FROM MULTIPLY				6W28	00
2W32	90	L06	14	2*41 MULTIPLY TO DIVIDE	X**J	6W28	90 N06 23
2W32	91	L06	16	2*42 MULTIPLY TO DIVIDE	X**J	6W28	91 N07 8
2W32	92	L06	18	2*43 MULTIPLY TO DIVIDE	X**J	6W28	92 N07 6
2W32	93	L07	12	2*44 MULTIPLY TO DIVIDE	X**J	6W28	93 N07 4
2W32	94	L07	14	2*45 MULTIPLY TO DIVIDE	X**J	6W28	94 N07 27
2W32	95	L07	16	2*46 MULTIPLY TO DIVIDE	X**J	6W28	95 N07 25
2W32	96	L07	18	2*47 MULTIPLY TO DIVIDE	X**J	6W28	96 N07 23
2W32	97			2*48 MULTIPLY TO DIVIDE	X**J	6W28	97
2W32	98	J03	1	ERROR MULT. 1		6W28	98 I07 23
2W32	99	J03	23	ERROR MULT. 2		6W28	99 J07 23
2W32	900	J03	6	ERROR MULT. 1		6W28	900 I08 27
2W32	901	J03	28	ERROR MULT. 2		6W28	901 J08 27
2W32	902					6W28	902
2W32	903					6W28	903
2W32	904					6W28	904
2W32	905					6W28	905
2W32	906					6W28	906
2W32	907					6W28	907
2W32	908					6W28	908

3W02	00	MEMORY TO READ DISTRIBUTOR				9W13	00
3W02	90	H34	28	2*38	9W13	90	I35 5
3W02	91	H35	1	2*39	9W13	91	I36 5
3W02	92	H35	8	2*40	9W13	92	I38 5
3W02	93	H35	28	2*41	9W13	93	I39 5
3W02	94	H36	1	2*42	9W13	94	I40 5
3W02	95	H36	8	2*43	9W13	95	I41 5
3W02	96	H36	28	2*44	9W13	96	I42 5
3W02	97				9W13	97	
3W02	98				9W13	98	
3W02	99				9W13	99	
3W02	900	H31	1	2*30	9W13	900	I25 5
3W02	901	H31	8	2*31	9W13	901	I26 5
3W02	902	H31	28	2*32	9W13	902	I27 5
3W02	903	H32	1	2*33	9W13	903	I28 5
3W02	904	H32	8	2*34	9W13	904	I29 5
3W02	905	H32	28	2*35	9W13	905	I32 5
3W02	906	H34	1	2*36	9W13	906	I33 5
3W02	907	H34	8	2*37	9W13	907	I34 5
3W02	908				9W13	908	

3W03	00	CENTRAL MEMORY TO PERIPHERAL				1W04	00
3W03	90	I25	1	2*0 CENTRAL TO PERIPHERAL DATA	1W04	90	G33 5
3W03	91	I26	1	2*1 CENTRAL TO PERIPHERAL DATA	1W04	91	G33 7
3W03	92	I27	1	2*2 CENTRAL TO PERIPHERAL DATA	1W04	92	G33 10
3W03	93	I28	1	2*3 CENTRAL TO PERIPHERAL DATA	1W04	93	G33 21
3W03	94	I29	1	2*4 CENTRAL TO PERIPHERAL DATA	1W04	94	G33 24
3W03	95	I32	1	2*5 CENTRAL TO PERIPHERAL DATA	1W04	95	G33 26
3W03	96	I33	1	2*6 CENTRAL TO PERIPHERAL DATA	1W04	96	G34 5
3W03	97	I34	1	2*7 CENTRAL TO PERIPHERAL DATA	1W04	97	G34 7
3W03	98	I35	1	2*8 CENTRAL TO PERIPHERAL DATA	1W04	98	G34 10
3W03	99	I36	1	2*9 CENTRAL TO PERIPHERAL DATA	1W04	99	G34 21
3W03	900	I38	1	2*10 CENTRAL TO PERIPHERAL DATA	1W04	900	G34 24
3W03	901	I39	1	2*11 CENTRAL TO PERIPHERAL DATA	1W04	901	G34 26
3W03	902	I40	1	2*12 CENTRAL TO PERIPHERAL DATA	1W04	902	G35 5
3W03	903	I41	1	2*13 CENTRAL TO PERIPHERAL DATA	1W04	903	G35 7
3W03	904	I42	1	2*14 CENTRAL TO PERIPHERAL DATA	1W04	904	G35 10
3W03	905	C14	14	MEMORY MARGIN	1W04	905	H29 20
3W03	906				1W04	906	
3W03	907				1W04	907	
3W03	908				1W04	908	

3W04	00	MEMORY TO READ DISTRIBUTOR				10W13	00		
3W04	90	H40	28	2*53	10W13	90	I11	5	
3W04	91	H41	1	2*54	10W13	91	I12	5	
3W04	92	H41	8	2*55	10W13	92	I14	5	
3W04	93	H41	28	2*56	10W13	93	I15	5	
3W04	94	H42	1	2*57	10W13	94	I16	5	
3W04	95	H42	8	2*58	10W13	95	I17	5	
3W04	96	H42	28	2*59	10W13	96	I18	5	
3W04	97	I23	9	ACCEPT FROM CHAS. 10	10W13	97	G05	28	
3W04	98				10W13	98			
3W04	99				10W13	99			
3W04	900	H37	1	2*45	10W13	900	I01	5	
3W04	901	H37	8	2*46	10W13	901	I02	5	
3W04	902	H37	28	2*47	10W13	902	I03	5	
3W04	903	H39	1	2*48	10W13	903	I04	5	
3W04	904	H39	8	2*49	10W13	904	I05	5	
3W04	905	H39	28	2*50	10W13	905	I08	5	
3W04	906	H40	1	2*51	10W13	906	I09	5	
3W04	907	H40	8	2*52	10W13	907	I10	5	
3W04	908				10W13	908			

3W05	00	READ DISTRIBUTOR FROM MEMORY				4W05	00		
3W05	90	I25	3	2*0	4W05	90	H01	1	
3W05	91	I26	3	2*1	4W05	91	H01	8	
3W05	92	I27	3	2*2	4W05	92	H01	28	
3W05	93	I28	3	2*3	4W05	93	H02	1	
3W05	94	I29	3	2*4	4W05	94	H02	8	
3W05	95	I32	3	2*5	4W05	95	H02	28	
3W05	96	I33	3	2*6	4W05	96	H03	1	
3W05	97	I34	3	2*7	4W05	97	H03	8	
3W05	98	I35	3	2*8	4W05	98	H03	28	
3W05	99	I36	3	2*9	4W05	99	H04	1	
3W05	900	I38	3	2*10	4W05	900	H04	8	
3W05	901	I39	3	2*11	4W05	901	H04	28	
3W05	902	I40	3	2*12	4W05	902	H06	1	
3W05	903	I41	3	2*13	4W05	903	H06	8	
3W05	904	I42	3	2*14	4W05	904	H06	28	
3W05	905	G38	21	GO	4W05	905	I21	22	
3W05	906	G38	20	WRITE	4W05	906	I24	26	
3W05	907				4W05	907			
3W05	908				4W05	908			

3W06 00 MEMORY TO READ DISTRIBUTOR

3W06	90	H27	28	2*23
3W06	91	H29	1	2*24
3W06	92	H29	8	2*25
3W06	93	H29	28	2*26
3W06	94	H30	1	2*27
3W06	95	H30	8	2*28
3W06	96	H30	28	2*29
3W06	97	I24	12	READ PERIPHERAL
3W06	98	I23	3	ACCEPT
3W06	99			
3W06	900	H25	1	2*15
3W06	901	H25	8	2*16
3W06	902	H25	28	2*17
3W06	903	H26	1	2*18
3W06	904	H26	8	2*19
3W06	905	H26	28	2*20
3W06	906	H27	1	2*21
3W06	907	H27	8	2*22
3W06	908			

4W06 00

4W06	90	I11	5
4W06	91	I12	5
4W06	92	I14	5
4W06	93	I15	5
4W06	94	I16	5
4W06	95	I17	5
4W06	96	I18	5
4W06	97	I21	11
4W06	98	G05	28
4W06	99		
4W06	900	I01	5
4W06	901	I02	5
4W06	902	I03	5
4W06	903	I04	5
4W06	904	I05	5
4W06	905	I08	5
4W06	906	I09	5
4W06	907	I10	5
4W06	908		

3W07 00 READ DISTRIBUTOR FROM MEMORY

3W07	90	I35	7	2*8
3W07	91	I36	7	2*9
3W07	92	I38	7	2*10
3W07	93	I39	7	2*11
3W07	94	I40	7	2*12
3W07	95	I41	7	2*13
3W07	96	I42	7	2*14
3W07	97			
3W07	98			
3W07	99	I23	7	ACCEPT
3W07	900	I25	7	2*0
3W07	901	I26	7	2*1
3W07	902	I27	7	2*2
3W07	903	I28	7	2*3
3W07	904	I29	7	2*4
3W07	905	I32	7	2*5
3W07	906	I33	7	2*6
3W07	907	I34	7	2*7
3W07	908			

9W07 00

9W07	90	H21	28
9W07	91	H22	1
9W07	92	H22	8
9W07	93	H22	28
9W07	94	H24	1
9W07	95	H24	8
9W07	96	H24	28
9W07	97		
9W07	98		
9W07	99	G39	28
9W07	900	H19	1
9W07	901	H19	8
9W07	902	H19	28
9W07	903	H20	1
9W07	904	H20	8
9W07	905	H20	28
9W07	906	H21	1
9W07	907	H21	8
9W07	908		

3W08	00	READ DISTRIBUTOR FROM MEMORY				10W07	00
3W08	90	135	9	2*8	10W07	90	H03 28
3W08	91	136	9	2*9	10W07	91	H04 1
3W08	92	138	9	2*10	10W07	92	H04 8
3W08	93	139	9	2*11	10W07	93	H04 28
3W08	94	140	9	2*12	10W07	94	H06 1
3W08	95	141	9	2*13	10W07	95	H06 8
3W08	96	142	9	2*14	10W07	96	H06 28
3W08	97				10W07	97	
3W08	98				10W07	98	
3W08	99				10W07	99	
3W08	900	125	9	2*0	10W07	900	H01 1
3W08	901	126	9	2*1	10W07	901	H01 8
3W08	902	127	9	2*2	10W07	902	H01 28
3W08	903	128	9	2*3	10W07	903	H02 1
3W08	904	129	9	2*4	10W07	904	H02 8
3W08	905	132	9	2*5	10W07	905	H02 28
3W08	906	133	9	2*6	10W07	906	H03 1
3W08	907	134	9	2*7	10W07	907	H03 8
3W08	908				10W07	908	

3W09	00	READ DISTRIBUTOR FROM MEMORY				13W06	00
3W09	90	135	11	2*8	13W06	90	H21 28
3W09	91	136	11	2*9	13W06	91	H22 1
3W09	92	138	11	2*10	13W06	92	H22 8
3W09	93	139	11	2*11	13W06	93	H22 28
3W09	94	140	11	2*12	13W06	94	H24 1
3W09	95	141	11	2*13	13W06	95	H24 8
3W09	96	142	11	2*14	13W06	96	H24 28
3W09	97				13W06	97	
3W09	98				13W06	98	
3W09	99				13W06	99	
3W09	900	125	11	2*0	13W06	900	H19 1
3W09	901	126	11	2*1	13W06	901	H19 8
3W09	902	127	11	2*2	13W06	902	H19 28
3W09	903	128	11	2*3	13W06	903	H20 1
3W09	904	129	11	2*4	13W06	904	H20 8
3W09	905	132	11	2*5	13W06	905	H20 28
3W09	906	133	11	2*6	13W06	906	H21 1
3W09	907	134	11	2*7	13W06	907	H21 8
3W09	908				13W06	908	

3W10	00	READ DISTRIBUTOR FROM MEMORY				14W06	00
3W10	90	135	19	2*8	14W06	90	H03 28
3W10	91	136	19	2*9	14W06	91	H04 1
3W10	92	138	19	2*10	14W06	92	H04 8
3W10	93	139	19	2*11	14W06	93	H04 28
3W10	94	140	19	2*12	14W06	94	H06 1
3W10	95	141	19	2*13	14W06	95	H06 8
3W10	96	142	19	2*14	14W06	96	H06 28
3W10	97				14W06	97	
3W10	98				14W06	98	
3W10	99				14W06	99	
3W10	900	125	19	2*0	14W06	900	H01 1
3W10	901	126	19	2*1	14W06	901	H01 8
3W10	902	127	19	2*2	14W06	902	H01 28
3W10	903	128	19	2*3	14W06	903	H02 1
3W10	904	129	19	2*4	14W06	904	H02 8
3W10	905	132	19	2*5	14W06	905	H02 28
3W10	906	133	19	2*6	14W06	906	H03 1
3W10	907	134	19	2*7	14W06	907	H03 8
3W10	908				14W06	908	

3W11	00	READ DISTRIBUTOR FROM MEMORY				15W06	00
3W11	90	135	21	2*8	15W06	90	H21 28
3W11	91	136	21	2*9	15W06	91	H22 1
3W11	92	138	21	2*10	15W06	92	H22 8
3W11	93	139	21	2*11	15W06	93	H22 28
3W11	94	140	21	2*12	15W06	94	H24 1
3W11	95	141	21	2*13	15W06	95	H24 8
3W11	96	142	21	2*14	15W06	96	H24 28
3W11	97				15W06	97	
3W11	98				15W06	98	
3W11	99				15W06	99	
3W11	900	125	21	2*0	15W06	900	H19 1
3W11	901	126	21	2*1	15W06	901	H19 8
3W11	902	127	21	2*2	15W06	902	H19 28
3W11	903	128	21	2*3	15W06	903	H20 1
3W11	904	129	21	2*4	15W06	904	H20 8
3W11	905	132	21	2*5	15W06	905	H20 28
3W11	906	133	21	2*6	15W06	906	H21 1
3W11	907	134	21	2*7	15W06	907	H21 8
3W11	908				15W06	908	

3W12	00	READ DISTRIBUTOR TO LOWER REGISTER				7W05	00
3W12	90	135	28	2*8	MEMORY TO REGISTER	7W05	90 A41 2
3W12	91	136	28	2*9	MEMORY TO REGISTER	7W05	91 A41 26
3W12	92	138	28	2*10	MEMORY TO REGISTER	7W05	92 A42 2
3W12	93	139	28	2*11	MEMORY TO REGISTER	7W05	93 A42 26
3W12	94	140	28	2*12	MEMORY TO REGISTER	7W05	94 B37 2
3W12	95	141	28	2*13	MEMORY TO REGISTER	7W05	95 B37 26
3W12	96	142	28	2*14	MEMORY TO REGISTER	7W05	96 B38 2
3W12	97					7W05	97
3W12	98					7W05	98
3W12	99					7W05	99
3W12	900	125	28	2*0	MEMORY TO REGISTER	7W05	900 A37 2
3W12	901	126	28	2*1	MEMORY TO REGISTER	7W05	901 A37 26
3W12	902	127	28	2*2	MEMORY TO REGISTER	7W05	902 A38 2
3W12	903	128	28	2*3	MEMORY TO REGISTER	7W05	903 A38 26
3W12	904	129	28	2*4	MEMORY TO REGISTER	7W05	904 A39 2
3W12	905	132	28	2*5	MEMORY TO REGISTER	7W05	905 A39 26
3W12	906	133	28	2*6	MEMORY TO REGISTER	7W05	906 A40 2
3W12	907	134	28	2*7	MEMORY TO REGISTER	7W05	907 A40 26
3W12	908					7W05	908

3W13	00	READ DISTRIBUTOR FROM MEMORY				16W06	00
3W13	90	135	23	2*8		16W06	90 H03 28
3W13	91	136	23	2*9		16W06	91 H04 1
3W13	92	138	23	2*10		16W06	92 H04 8
3W13	93	139	23	2*11		16W06	93 H04 28
3W13	94	140	23	2*12		16W06	94 H06 1
3W13	95	141	23	2*13		16W06	95 H06 8
3W13	96	142	23	2*14		16W06	96 H06 28
3W13	97					16W06	97
3W13	98					16W06	98
3W13	99					16W06	99
3W13	900	125	23	2*0		16W06	900 H01 1
3W13	901	126	23	2*1		16W06	901 H01 8
3W13	902	127	23	2*2		16W06	902 H01 28
3W13	903	128	23	2*3		16W06	903 H02 1
3W13	904	129	23	2*4		16W06	904 H02 8
3W13	905	132	23	2*5		16W06	905 H02 28
3W13	906	133	23	2*6		16W06	906 H03 1
3W13	907	134	23	2*7		16W06	907 H03 8
3W13	908					16W06	908

3W14	00	READ DISTRIBUTOR TO CONTROL				5W02	00		
3W14	90	132	26	2*5	5W02	90	E41	7	
3W14	91	133	26	2*6	5W02	91	E41	22	
3W14	92	134	26	2*7	5W02	92	E41	25	
3W14	93	135	26	2*8	5W02	93	E41	27	
3W14	94	136	26	2*9	5W02	94	E42	4	
3W14	95	138	26	2*10	5W02	95	E42	6	
3W14	96	139	26	2*11	5W02	96	E42	7	
3W14	97	140	26	2*12	5W02	97	E42	22	
3W14	98	141	26	2*13	5W02	98	E42	25	
3W14	99	142	26	2*14	5W02	99	E42	27	
3W14	900	129	26	2*4	5W02	900	E41	6	
3W14	901				5W02	901			
3W14	902				5W02	902			
3W14	903				5W02	903			
3W14	904	125	26	2*0	5W02	904	D42	22	
3W14	905	126	26	2*1	5W02	905	D42	25	
3W14	906	127	26	2*2	5W02	906	D42	27	
3W14	907	128	26	2*3	5W02	907	E41	4	
3W14	908				5W02	908			

3W15	00	MEMORY FROM WRITE DISTRIBUTOR				13W13	00		
3W15	90	H21	24	2*8 FAN OUT TO MEMORY	13W13	90	I33	3	
3W15	91	H22	2	2*9 FAN OUT TO MEMORY	13W13	91	I34	18	
3W15	92	H22	19	2*10 FAN OUT TO MEMORY	13W13	92	I34	13	
3W15	93	H22	24	2*11 FAN OUT TO MEMORY	13W13	93	I35	3	
3W15	94	H24	2	2*12 FAN OUT TO MEMORY	13W13	94	I36	18	
3W15	95	H24	19	2*13 FAN OUT TO MEMORY	13W13	95	I36	13	
3W15	96	H24	24	2*14 FAN OUT TO MEMORY	13W13	96	I37	3	
3W15	97	I23	19	MEMORY ACCEPT	13W13	97	G39	28	
3W15	98				13W13	98			
3W15	99				13W13	99			
3W15	900	H19	2	2*0 FAN OUT TO MEMORY	13W13	900	I28	18	
3W15	901	H19	19	2*1 FAN OUT TO MEMORY	13W13	901	I28	13	
3W15	902	H19	24	2*2 FAN OUT TO MEMORY	13W13	902	I29	3	
3W15	903	H20	2	2*3 FAN OUT TO MEMORY	13W13	903	I30	18	
3W15	904	H20	19	2*4 FAN OUT TO MEMORY	13W13	904	I30	13	
3W15	905	H20	24	2*5 FAN OUT TO MEMORY	13W13	905	I31	3	
3W15	906	H21	2	2*6 FAN OUT TO MEMORY	13W13	906	I32	18	
3W15	907	H21	19	2*7 FAN OUT TO MEMORY	13W13	907	I32	13	
3W15	908				13W13	908			

JW16	00	MEMORY FROM WRITE DISTRIBUTOR			14W13	00
JW16	90	H27	24	2*23 FAN OUT TO MEMORY	14W13	90 111 28
JW16	91	H29	2	2*24 FAN OUT TO MEMORY	14W13	91 112 24
JW16	92	H29	19	2*25 FAN OUT TO MEMORY	14W13	92 112 7
JW16	93	H29	24	2*26 FAN OUT TO MEMORY	14W13	93 113 28
JW16	94	H30	2	2*27 FAN OUT TO MEMORY	14W13	94 114 24
JW16	95	H30	19	2*28 FAN OUT TO MEMORY	14W13	95 114 7
JW16	96	H30	24	2*29 FAN OUT TO MEMORY	14W13	96 115 28
JW16	97	I23	21	ACCEPT	14W13	97 005 28
JW16	98				14W13	98
JW16	99				14W13	99
JW16	900	H25	2	2*15 FAN OUT TO MEMORY	14W13	900 106 24
JW16	901	H25	19	2*16 FAN OUT TO MEMORY	14W13	901 106 7
JW16	902	H25	24	2*17 FAN OUT TO MEMORY	14W13	902 107 28
JW16	903	H26	2	2*18 FAN OUT TO MEMORY	14W13	903 108 24
JW16	904	H26	19	2*19 FAN OUT TO MEMORY	14W13	904 108 7
JW16	905	H26	24	2*20 FAN OUT TO MEMORY	14W13	905 109 28
JW16	906	H27	2	2*21 FAN OUT TO MEMORY	14W13	906 110 24
JW16	907	H27	19	2*22 FAN OUT TO MEMORY	14W13	907 110 7
JW16	908				14W13	908

JW17	00	MEMORY FROM WRITE DISTRIBUTOR			15W13	00
JW17	90	H34	24	2*38 FAN OUT TO MEMORY	15W13	90 133 28
JW17	91	H35	2	2*39 FAN OUT TO MEMORY	15W13	91 134 24
JW17	92	H35	19	2*40 FAN OUT TO MEMORY	15W13	92 134 7
JW17	93	H35	24	2*41 FAN OUT TO MEMORY	15W13	93 135 28
JW17	94	H36	2	2*42 FAN OUT TO MEMORY	15W13	94 136 24
JW17	95	H36	19	2*43 FAN OUT TO MEMORY	15W13	95 136 7
JW17	96	H36	24	2*44 FAN OUT TO MEMORY	15W13	96 137 28
JW17	97	I23	23	ACCEPT	15W13	97 039 28
JW17	98				15W13	98
JW17	99				15W13	99
JW17	900	H31	2	2*30 FAN OUT TO MEMORY	15W13	900 128 24
JW17	901	H31	19	2*31 FAN OUT TO MEMORY	15W13	901 128 7
JW17	902	H31	24	2*32 FAN OUT TO MEMORY	15W13	902 129 28
JW17	903	H32	2	2*33 FAN OUT TO MEMORY	15W13	903 130 24
JW17	904	H32	19	2*34 FAN OUT TO MEMORY	15W13	904 130 7
JW17	905	H32	24	2*35 FAN OUT TO MEMORY	15W13	905 131 28
JW17	906	H34	2	2*36 FAN OUT TO MEMORY	15W13	906 132 24
JW17	907	H34	19	2*37 FAN OUT TO MEMORY	15W13	907 132 7
JW17	908				15W13	908

3W18	00	MEMORY FROM WRITE DISTRIBUTOR				16W13	00		
3W18	90	H40	24	2*53	FAN OUT TO MEMORY	16W13	90	I11	28
3W18	91	H41	2	2*54	FAN OUT TO MEMORY	16W13	91	I12	24
3W18	92	H41	19	2*55	FAN OUT TO MEMORY	16W13	92	I12	7
3W18	93	H41	24	2*56	FAN OUT TO MEMORY	16W13	93	I13	28
3W18	94	H42	2	2*57	FAN OUT TO MEMORY	16W13	94	I14	24
3W18	95	H42	19	2*58	FAN OUT TO MEMORY	16W13	95	I14	7
3W18	96	H42	24	2*59	FAN OUT TO MEMORY	16W13	96	I15	28
3W18	97	I23	25		ACCEPT	16W13	97	Q05	28
3W18	98					16W13	98		
3W18	99					16W13	99		
3W18	900	H37	2	2*45	FAN OUT TO MEMORY	16W13	900	I06	24
3W18	901	H37	19	2*46	FAN OUT TO MEMORY	16W13	901	I06	7
3W18	902	H37	24	2*47	FAN OUT TO MEMORY	16W13	902	I07	28
3W18	903	H39	2	2*48	FAN OUT TO MEMORY	16W13	903	I08	24
3W18	904	H39	19	2*49	FAN OUT TO MEMORY	16W13	904	I08	7
3W18	905	H39	24	2*50	FAN OUT TO MEMORY	16W13	905	I09	28
3W18	906	H40	2	2*51	FAN OUT TO MEMORY	16W13	906	I10	24
3W18	907	H40	19	2*52	FAN OUT TO MEMORY	16W13	907	I10	7
3W18	908					16W13	908		

3W20	00	MEMORY ADDRESS FROM CONTROL				5W29	00		
3W20	90	G40	18	2*8	ADDRESS TO MEMORY	5W29	90	Q36	20
3W20	91	G41	11	2*9	ADDRESS TO MEMORY	5W29	91	Q37	9
3W20	92	G41	14	2*10	ADDRESS TO MEMORY	5W29	92	Q37	5
3W20	93	G41	17	2*11	ADDRESS TO MEMORY	5W29	93	Q37	20
3W20	94	G41	18	2*12	ADDRESS TO MEMORY	5W29	94	Q38	9
3W20	95	G42	11	2*13	ADDRESS TO MEMORY	5W29	95	Q38	5
3W20	96	G42	14	2*14	ADDRESS TO MEMORY	5W29	96	Q38	20
3W20	97	G42	17	2*15	ADDRESS TO MEMORY	5W29	97	Q39	9
3W20	98	G42	18	2*16	ADDRESS TO MEMORY	5W29	98	Q39	5
3W20	99	I23	28		ACCEPT	5W29	99	Q40	17
3W20	900	G35	12		CLOCK	5W29	900	J39	23
3W20	901	G39	18	2*0	BANK SELECT	5W29	901	Q34	9
3W20	902	G39	22	2*1	BANK SELECT	5W29	902	Q34	5
3W20	903	G38	9	2*2	CHASSIS 3 SELECT (000)	5W29	903	Q34	20
3W20	904	G38	4	2*3	CHASSIS 3 SELECT (000)	5W29	904	Q35	9
3W20	905	G38	14	2*4		5W29	905	Q35	5
3W20	906	G40	11	2*5	ADDRESS TO MEMORY	5W29	906	Q35	20
3W20	907	G40	14	2*6	ADDRESS TO MEMORY	5W29	907	Q36	9
3W20	908	G40	17	2*7	ADDRESS TO MEMORY	5W29	908	Q36	5

3W21	00	CONTROL (1 TO 9 VIA 3)			1W01	00
3W21	90				1W01	90
3W21	91				1W01	91
3W21	92				1W01	92
3W21	93				1W01	93
3W21	94				1W01	94
3W21	95				1W01	95
3W21	96				1W01	96
3W21	97				1W01	97
3W21	98				1W01	98
3W21	99				1W01	99
3W21	900	J37	27	CLOCK	1W01	900 H02 12
3W21	901	J39	10	READ GO	1W01	901 I30 27
3W21	902	J39	9	WRITE GO	1W01	902 I30 25
3W21	903	J39	8	EXCHANGE GO	1W01	903 I30 8
3W21	904				1W01	904
3W21	905				1W01	905
3W21	906				1W01	906
3W21	907				1W01	907
3W21	908				1W01	908

3W22	00	READWRITE EXCHANGE ADDRESS			1W03	00
3W22	90				1W03	90
3W22	91	J41	10	BIT 9	1W03	91 F41 27
3W22	92	J41	9	BIT 10	1W03	92 F41 25
3W22	93	J41	8	BIT 11	1W03	93 F41 23
3W22	94	J42	23	BIT 12	1W03	94 F42 8
3W22	95	J42	22	BIT 13	1W03	95 F42 6
3W22	96	J42	21	BIT 14	1W03	96 G42 4
3W22	97	J42	10	BIT 15	1W03	97 F42 27
3W22	98	J42	9	BIT 16	1W03	98 F42 25
3W22	99	J42	8	BIT 17	1W03	99 F42 23
3W22	900	J41	21	BIT 8	1W03	900 F41 4
3W22	901	J40	23	BIT 0	1W03	901 F40 8
3W22	902	J40	22	BIT 1	1W03	902 F40 6
3W22	903	J40	21	BIT 2	1W03	903 F40 4
3W22	904	J40	10	BIT 3	1W03	904 F40 27
3W22	905	J40	9	BIT 4	1W03	905 F40 25
3W22	906	J40	8	BIT 5	1W03	906 F40 23
3W22	907	J41	23	BIT 6	1W03	907 F41 8
3W22	908	J41	22	BIT 7	1W03	908 F41 6

3W23	00	READWRITE EXCHANGE ADDRESS				5W24	00
3W23	90					5W24	90
3W23	91	J41	28	BIT 9	5W24	91	N41 21
3W23	92	J41	26	BIT 10	5W24	92	N41 24
3W23	93	J41	24	BIT 11	5W24	93	N41 26
3W23	94	J42	7	BIT 12	5W24	94	N42 5
3W23	95	J42	5	BIT 13	5W24	95	N42 7
3W23	96	J42	3	BIT 14	5W24	96	N42 10
3W23	97	J42	28	BIT 15	5W24	97	N42 21
3W23	98	J42	26	BIT 16	5W24	98	N42 24
3W23	99	J42	24	BIT 17	5W24	99	N42 26
3W23	900	J41	3	BIT 8	5W24	900	N41 10
3W23	901	J40	7	BIT 0	5W24	901	N40 5
3W23	902	J40	5	BIT 1	5W24	902	N40 7
3W23	903	J40	3	BIT 2	5W24	903	N40 10
3W23	904	J40	28	BIT 3	5W24	904	N40 21
3W23	905	J40	26	BIT 4	5W24	905	N40 24
3W23	906	J40	24	BIT 5	5W24	906	N40 26
3W23	907	J41	7	BIT 6	5W24	907	N41 5
3W23	908	J41	5	BIT 7	5W24	908	N41 7

3W24	00	CONTROL (1 TO 5 VIA 3)				5W21	00
3W24	90					5W21	90
3W24	91					5W21	91
3W24	92					5W21	92
3W24	93					5W21	93
3W24	94					5W21	94
3W24	95					5W21	95
3W24	96					5W21	96
3W24	97					5W21	97
3W24	98					5W21	98
3W24	99					5W21	99
3W24	900	J39	28	READ GO (PERIPH)	5W21	900	041 18
3W24	901	J39	26	WRITE GO (PERIPH)	5W21	901	041 20
3W24	902	J39	24	EXCHANGE GO (PERIPH)	5W21	902	040 15
3W24	903				5W21	903	
3W24	904				5W21	904	
3W24	905				5W21	905	
3W24	906				5W21	906	
3W24	907				5W21	907	
3W24	908				5W21	908	

JW39 00 MEMORY TO READ DISTRIBUTOR

JW39 90 H21 28 2+8
 JW39 91 H22 1 2+9
 JW39 92 H22 8 2+10
 JW39 93 H22 28 2+11
 JW39 94 H24 1 2+12
 JW39 95 H24 8 2+13
 JW39 96 H24 28 2+14
 JW39 97 G39 28 ACCEPT
 JW39 98
 JW39 99
 JW39 900 H19 1 2+0
 JW39 901 H19 8 2+1
 JW39 902 H19 28 2+2
 JW39 903 H20 1 2+3
 JW39 904 H20 8 2+4
 JW39 905 H20 28 2+5
 JW39 906 H21 1 2+6
 JW39 907 H21 8 2+7
 JW39 908

JW39 00

JW39 90 I35 5
 JW39 91 I36 5
 JW39 92 I38 5
 JW39 93 I39 5
 JW39 94 I40 5
 JW39 95 I41 5
 JW39 96 I42 5
 JW39 97 I23 5
 JW39 98
 JW39 99
 JW39 900 I25 5
 JW39 901 I26 5
 JW39 902 I27 5
 JW39 903 I28 5
 JW39 904 I29 5
 JW39 905 I32 5
 JW39 906 I33 5
 JW39 907 I34 5
 JW39 908

JW39 00 READ DISTRIBUTOR FROM MEMORY

JW39 90 I35 5 2+8
 JW39 91 I36 5 2+9
 JW39 92 I38 5 2+10
 JW39 93 I39 5 2+11
 JW39 94 I40 5 2+12
 JW39 95 I41 5 2+13
 JW39 96 I42 5 2+14
 JW39 97 I23 5 ACCEPT
 JW39 98
 JW39 99
 JW39 900 I25 5 2+0
 JW39 901 I26 5 2+1
 JW39 902 I27 5 2+2
 JW39 903 I28 5 2+3
 JW39 904 I29 5 2+4
 JW39 905 I32 5 2+5
 JW39 906 I33 5 2+6
 JW39 907 I34 5 2+7
 JW39 908

JW39 00

JW39 90 H21 28
 JW39 91 H22 1
 JW39 92 H22 8
 JW39 93 H22 28
 JW39 94 H24 1
 JW39 95 H24 8
 JW39 96 H24 28
 JW39 97 G39 28
 JW39 98
 JW39 99
 JW39 900 H19 1
 JW39 901 H19 8
 JW39 902 H19 28
 JW39 903 H20 1
 JW39 904 H20 8
 JW39 905 H20 28
 JW39 906 H21 1
 JW39 907 H21 8
 JW39 908

4W03	00	MEMORY FROM WRITE DISTRIBUTOR				13W12	00
4W03	90	H03	24	2*8	FAN OUT TO MEMORY	13W12	90 133 5
4W03	91	H04	2	2*9	FAN OUT TO MEMORY	13W12	91 134 20
4W03	92	H04	19	2*10	FAN OUT TO MEMORY	13W12	92 134 11
4W03	93	H04	24	2*11	FAN OUT TO MEMORY	13W12	93 135 5
4W03	94	H06	2	2*12	FAN OUT TO MEMORY	13W12	94 136 20
4W03	95	H06	19	2*13	FAN OUT TO MEMORY	13W12	95 136 11
4W03	96	H06	24	2*14	FAN OUT TO MEMORY	13W12	96 137 5
4W03	97					13W12	97
4W03	98					13W12	98
4W03	99					13W12	99
4W03	900	H01	2	2*0	FAN OUT TO MEMORY	13W12	900 128 20
4W03	901	H01	19	2*1	FAN OUT TO MEMORY	13W12	901 128 11
4W03	902	H01	24	2*2	FAN OUT TO MEMORY	13W12	902 129 5
4W03	903	H02	2	2*3	FAN OUT TO MEMORY	13W12	903 130 20
4W03	904	H02	19	2*4	FAN OUT TO MEMORY	13W12	904 130 11
4W03	905	H02	24	2*5	FAN OUT TO MEMORY	13W12	905 131 5
4W03	906	H03	2	2*6	FAN OUT TO MEMORY	13W12	906 132 20
4W03	907	H03	19	2*7	FAN OUT TO MEMORY	13W12	907 132 11
4W03	908					13W12	908

4W04	00	MEMORY FROM WRITE DISTRIBUTOR				14W12	00
4W04	90	H09	24	2*23	FAN OUT TO MEMORY	14W12	90 111 5
4W04	91	H11	2	2*24	FAN OUT TO MEMORY	14W12	91 112 20
4W04	92	H11	19	2*25	FAN OUT TO MEMORY	14W12	92 112 11
4W04	93	H11	24	2*26	FAN OUT TO MEMORY	14W12	93 113 5
4W04	94	H12	2	2*27	FAN OUT TO MEMORY	14W12	94 114 20
4W04	95	H12	19	2*28	FAN OUT TO MEMORY	14W12	95 114 11
4W04	96	H12	24	2*29	FAN OUT TO MEMORY	14W12	96 115 5
4W04	97					14W12	97
4W04	98					14W12	98
4W04	99					14W12	99
4W04	900	H07	2	2*15	FAN OUT TO MEMORY	14W12	900 106 20
4W04	901	H07	19	2*16	FAN OUT TO MEMORY	14W12	901 106 11
4W04	902	H07	24	2*17	FAN OUT TO MEMORY	14W12	902 107 5
4W04	903	H08	2	2*18	FAN OUT TO MEMORY	14W12	903 108 20
4W04	904	H08	19	2*19	FAN OUT TO MEMORY	14W12	904 108 11
4W04	905	H08	24	2*20	FAN OUT TO MEMORY	14W12	905 109 5
4W04	906	H09	2	2*21	FAN OUT TO MEMORY	14W12	906 110 20
4W04	907	H09	19	2*22	FAN OUT TO MEMORY	14W12	907 110 11
4W04	908					14W12	908

4W05 00 MEMORY TO READ DISTRIBUTOR

4W05 90 H01 1 2*0
 4W05 91 H01 8 2*1
 4W05 92 H01 28 2*2
 4W05 93 H02 1 2*3
 4W05 94 H02 8 2*4
 4W05 95 H02 28 2*5
 4W05 96 H03 1 2*6
 4W05 97 H03 8 2*7
 4W05 98 H03 28 2*8
 4W05 99 H04 1 2*9
 4W05 900 H04 8 2*10
 4W05 901 H04 28 2*11
 4W05 902 H06 1 2*12
 4W05 903 H06 8 2*13
 4W05 904 H06 28 2*14
 4W05 905 I21 22 00
 4W05 906 I24 26 WRITE
 4W05 907
 4W05 908

3W05 00

3W05 90 I25 3
 3W05 91 I26 3
 3W05 92 I27 3
 3W05 93 I28 3
 3W05 94 I29 3
 3W05 95 I32 3
 3W05 96 I33 3
 3W05 97 I34 3
 3W05 98 I35 3
 3W05 99 I36 3
 3W05 900 I38 3
 3W05 901 I39 3
 3W05 902 I40 3
 3W05 903 I41 3
 3W05 904 I42 3
 3W05 905 Q38 21
 3W05 906 Q38 20
 3W05 907
 3W05 908

4W06 00 READ DISTRIBUTOR FROM MEMORY

4W06 90 I11 5 2*23
 4W06 91 I12 5 2*24
 4W06 92 I14 5 2*25
 4W06 93 I15 5 2*26
 4W06 94 I16 5 2*27
 4W06 95 I17 5 2*28
 4W06 96 I18 5 2*29
 4W06 97 I21 11 READ PERIPHERAL
 4W06 98 Q05 28 ACCEPT
 4W06 99
 4W06 900 I01 5 2*15
 4W06 901 I02 5 2*16
 4W06 902 I03 5 2*17
 4W06 903 I04 5 2*18
 4W06 904 I05 5 2*19
 4W06 905 I08 5 2*20
 4W06 906 I09 5 2*21
 4W06 907 I10 5 2*22
 4W06 908

3W06 00

3W06 90 H27 28
 3W06 91 H29 1
 3W06 92 H29 8
 3W06 93 H29 28
 3W06 94 H30 1
 3W06 95 H30 8
 3W06 96 H30 28
 3W06 97 I24 12
 3W06 98 I23 3
 3W06 99
 3W06 900 H25 1
 3W06 901 H25 8
 3W06 902 H25 28
 3W06 903 H26 1
 3W06 904 H26 8
 3W06 905 H26 28
 3W06 906 H27 1
 3W06 907 H27 8
 3W06 908

4W07	00	CENTRAL MEMORY TO PERIPHERAL				1W05	00		
4W07	90	I01	1	2*15	CENTRAL TO PERIPHERAL DATA	1W05	90	G35	21
4W07	91	I02	1	2*16	CENTRAL TO PERIPHERAL DATA	1W05	91	G35	24
4W07	92	I03	1	2*17	CENTRAL TO PERIPHERAL DATA	1W05	92	G35	26
4W07	93	I04	1	2*18	CENTRAL TO PERIPHERAL DATA	1W05	93	G36	5
4W07	94	I05	1	2*19	CENTRAL TO PERIPHERAL DATA	1W05	94	G36	7
4W07	95	I08	1	2*20	CENTRAL TO PERIPHERAL DATA	1W05	95	G36	10
4W07	96	I09	1	2*21	CENTRAL TO PERIPHERAL DATA	1W05	96	G36	21
4W07	97	I10	1	2*22	CENTRAL TO PERIPHERAL DATA	1W05	97	G36	24
4W07	98	I11	1	2*23	CENTRAL TO PERIPHERAL DATA	1W05	98	G36	26
4W07	99	I12	1	2*24	CENTRAL TO PERIPHERAL DATA	1W05	99	G37	5
4W07	900	I14	1	2*25	CENTRAL TO PERIPHERAL DATA	1W05	900	G37	7
4W07	901	I15	1	2*26	CENTRAL TO PERIPHERAL DATA	1W05	901	G37	10
4W07	902	I16	1	2*27	CENTRAL TO PERIPHERAL DATA	1W05	902	G37	21
4W07	903	I17	1	2*28	CENTRAL TO PERIPHERAL DATA	1W05	903	G37	24
4W07	904	I18	1	2*29	CENTRAL TO PERIPHERAL DATA	1W05	904	G37	26
4W07	905	I24	7	RESUME CENTRAL READ		1W05	905	I25	11
4W07	906	I24	9	RESUME CENTRAL READ		1W05	906	I29	1
4W07	907	C14	14	STORAGE MARGIN		1W05	907	H29	22
4W07	908					1W05	908		

4W08	00	MEMORY TO READ DISTRIBUTOR				9W14	00		
4W08	90	H16	28	2*38		9W14	90	I35	3
4W08	91	H17	1	2*39		9W14	91	I36	3
4W08	92	H17	8	2*40		9W14	92	I38	3
4W08	93	H17	28	2*41		9W14	93	I39	3
4W08	94	H18	1	2*42		9W14	94	I40	3
4W08	95	H18	8	2*43		9W14	95	I41	3
4W08	96	H18	28	2*44		9W14	96	I42	3
4W08	97	I21	20	GO		9W14	97	G38	21
4W08	98	I24	5	WRITE		9W14	98	G38	20
4W08	99	I21	9	READ PERIPHERAL		9W14	99	I24	12
4W08	900	H13	1	2*30		9W14	900	I25	3
4W08	901	H13	8	2*31		9W14	901	I26	3
4W08	902	H13	28	2*32		9W14	902	I27	3
4W08	903	H14	1	2*33		9W14	903	I28	3
4W08	904	H14	8	2*34		9W14	904	I29	3
4W08	905	H14	28	2*35		9W14	905	I32	3
4W08	906	H16	1	2*36		9W14	906	I33	3
4W08	907	H16	8	2*37		9W14	907	I34	3
4W08	908					9W14	908		

4W09	00	MEMORY FROM WRITE DISTRIBUTOR						15W12	00
4W09	90	H16	24	2*38	FAN OUT	TO MEMORY	15W12	90 133 5	
4W09	91	H17	2	2*39	FAN OUT	TO MEMORY	15W12	91 134 20	
4W09	92	H17	19	2*40	FAN OUT	TO MEMORY	15W12	92 134 11	
4W09	93	H17	24	2*41	FAN OUT	TO MEMORY	15W12	93 135 5	
4W09	94	H18	2	2*42	FAN OUT	TO MEMORY	15W12	94 136 20	
4W09	95	H18	19	2*43	FAN OUT	TO MEMORY	15W12	95 136 11	
4W09	96	H18	24	2*44	FAN OUT	TO MEMORY	15W12	96 137 5	
4W09	97						15W12	97	
4W09	98						15W12	98	
4W09	99						15W12	99	
4W09	900	H13	2	2*30	FAN OUT	TO MEMORY	15W12	900 128 20	
4W09	901	H13	19	2*31	FAN OUT	TO MEMORY	15W12	901 128 11	
4W09	902	H13	24	2*32	FAN OUT	TO MEMORY	15W12	902 129 5	
4W09	903	H14	2	2*33	FAN OUT	TO MEMORY	15W12	903 130 20	
4W09	904	H14	19	2*34	FAN OUT	TO MEMORY	15W12	904 130 11	
4W09	905	H14	24	2*35	FAN OUT	TO MEMORY	15W12	905 131 5	
4W09	906	H16	2	2*36	FAN OUT	TO MEMORY	15W12	906 132 20	
4W09	907	H16	19	2*37	FAN OUT	TO MEMORY	15W12	907 132 11	
4W09	908						15W12	908	

4W10	00	MEMORY FROM WRITE DISTRIBUTOR						16W12	00
4W10	90	H22	24	2*53	FAN OUT	TO MEMORY	16W12	90 111 5	
4W10	91	H23	2	2*54	FAN OUT	TO MEMORY	16W12	91 112 20	
4W10	92	H23	19	2*55	FAN OUT	TO MEMORY	16W12	92 112 11	
4W10	93	H23	24	2*56	FAN OUT	TO MEMORY	16W12	93 113 5	
4W10	94	H24	2	2*57	FAN OUT	TO MEMORY	16W12	94 114 20	
4W10	95	H24	19	2*58	FAN OUT	TO MEMORY	16W12	95 114 11	
4W10	96	H24	24	2*59	FAN OUT	TO MEMORY	16W12	96 115 5	
4W10	97						16W12	97	
4W10	98	I24	24	WRITE			16W12	98 004 20	
4W10	99	I21	3	GO			16W12	99 004 21	
4W10	900	H19	2	2*45	FAN OUT	TO MEMORY	16W12	900 106 20	
4W10	901	H19	19	2*46	FAN OUT	TO MEMORY	16W12	901 106 11	
4W10	902	H19	24	2*47	FAN OUT	TO MEMORY	16W12	902 107 5	
4W10	903	H21	2	2*48	FAN OUT	TO MEMORY	16W12	903 108 20	
4W10	904	H21	19	2*49	FAN OUT	TO MEMORY	16W12	904 108 11	
4W10	905	H21	24	2*50	FAN OUT	TO MEMORY	16W12	905 109 5	
4W10	906	H22	2	2*51	FAN OUT	TO MEMORY	16W12	906 110 20	
4W10	907	H22	19	2*52	FAN OUT	TO MEMORY	16W12	907 110 11	
4W10	908						16W12	908	

4W11	00	MEMORY TO READ DISTRIBUTOR				10W14	00		
4W11	90	H22	28	2*53	10W14	90	I11	3	
4W11	91	H23	1	2*54	10W14	91	I12	3	
4W11	92	H23	8	2*55	10W14	92	I14	3	
4W11	93	H23	28	2*56	10W14	93	I15	3	
4W11	94	H24	1	2*57	10W14	94	I16	3	
4W11	95	H24	8	2*58	10W14	95	I17	3	
4W11	96	H24	28	2*59	10W14	96	I18	3	
4W11	97	I21	18	GO	10W14	97	G04	21	
4W11	98	I24	3	WRITE	10W14	98	G04	20	
4W11	99	I21	7	READ PERIPHERAL	10W14	99	I19	12	
4W11	900	H19	1	2*45	10W14	900	I01	3	
4W11	901	H19	8	2*46	10W14	901	I02	3	
4W11	902	H19	28	2*47	10W14	902	I03	3	
4W11	903	H21	1	2*48	10W14	903	I04	3	
4W11	904	H21	8	2*49	10W14	904	I05	3	
4W11	905	H21	28	2*50	10W14	905	I08	3	
4W11	906	H22	1	2*51	10W14	906	I09	3	
4W11	907	H22	8	2*52	10W14	907	I10	3	
4W11	908				10W14	908			

4W12	00	READ DISTRIBUTOR FROM MEMORY				9W08	00		
4W12	90	I11	7	2*23	9W08	90	H27	28	
4W12	91	I12	7	2*24	9W08	91	H29	1	
4W12	92	I14	7	2*25	9W08	92	H29	8	
4W12	93	I15	7	2*26	9W08	93	H29	28	
4W12	94	I16	7	2*27	9W08	94	H30	1	
4W12	95	I17	7	2*28	9W08	95	H30	8	
4W12	96	I18	7	2*29	9W08	96	H30	28	
4W12	97				9W08	97			
4W12	98				9W08	98			
4W12	99				9W08	99			
4W12	900	I01	7	2*15	9W08	900	H25	1	
4W12	901	I02	7	2*16	9W08	901	H25	8	
4W12	902	I03	7	2*17	9W08	902	H25	28	
4W12	903	I04	7	2*18	9W08	903	H26	1	
4W12	904	I05	7	2*19	9W08	904	H26	8	
4W12	905	I08	7	2*20	9W08	905	H26	28	
4W12	906	I09	7	2*21	9W08	906	H27	1	
4W12	907	I10	7	2*22	9W08	907	H27	8	
4W12	908				9W08	908			

4W13	00	READ DISTRIBUTOR FROM MEMORY				10W08	00
4W13	90	I11	9	2*23	10W08	90	H09 28
4W13	91	I12	9	2*24	10W08	91	H11 1
4W13	92	I14	9	2*25	10W08	92	H11 8
4W13	93	I15	9	2*26	10W08	93	H11 28
4W13	94	I16	9	2*27	10W08	94	H12 1
4W13	95	I17	9	2*28	10W08	95	H12 8
4W13	96	I18	9	2*29	10W08	96	H12 28
4W13	97				10W08	97	
4W13	98				10W08	98	
4W13	99				10W08	99	
4W13	900	I01	9	2*15	10W08	900	H07 1
4W13	901	I02	9	2*16	10W08	901	H07 8
4W13	902	I03	9	2*17	10W08	902	H07 28
4W13	903	I04	9	2*18	10W08	903	H08 1
4W13	904	I05	9	2*19	10W08	904	H08 8
4W13	905	I08	9	2*20	10W08	905	H08 28
4W13	906	I09	9	2*21	10W08	906	H09 1
4W13	907	I10	9	2*22	10W08	907	H09 8
4W13	908				10W08	908	

4W14	00	READ DISTRIBUTOR TO CONTROL				5W01	00
4W14	90	I08	26	2*20	5W01	90	C42 27
4W14	91	I09	26	2*21	5W01	91	D41 4
4W14	92	I10	26	2*22	5W01	92	D41 6
4W14	93	I11	26	2*23	5W01	93	D41 7
4W14	94	I12	26	2*24	5W01	94	D41 22
4W14	95	I14	26	2*25	5W01	95	D41 25
4W14	96	I15	26	2*26	5W01	96	D41 27
4W14	97	I16	26	2*27	5W01	97	D42 4
4W14	98	I17	26	2*28	5W01	98	D42 6
4W14	99	I18	26	2*29	5W01	99	D42 7
4W14	900	I05	26	2*19	5W01	900	C42 25
4W14	901				5W01	901	
4W14	902				5W01	902	
4W14	903				5W01	903	
4W14	904	I01	26	2*15	5W01	904	C42 4
4W14	905	I02	26	2*16	5W01	905	C42 6
4W14	906	I03	26	2*17	5W01	906	C42 7
4W14	907	I04	26	2*18	5W01	907	C42 22
4W14	908				5W01	908	

4W15	00	READ DISTRIBUTOR FROM MEMORY				13W07	00
4W15	90	I11	11	2*23	13W07	90	H27 28
4W15	91	I12	11	2*24	13W07	91	H29 1
4W15	92	I14	11	2*25	13W07	92	H29 8
4W15	93	I15	11	2*26	13W07	93	H29 28
4W15	94	I16	11	2*27	13W07	94	H30 1
4W15	95	I17	11	2*28	13W07	95	H30 8
4W15	96	I18	11	2*29	13W07	96	H30 28
4W15	97	I21	28	MEMORY GO	13W07	97	G38 21
4W15	98	I24	18	MEMORY WRITE	13W07	98	G38 20
4W15	99				13W07	99	
4W15	900	I01	11	2*15	13W07	900	H25 1
4W15	901	I02	11	2*16	13W07	901	H25 8
4W15	902	I03	11	2*17	13W07	902	H25 28
4W15	903	I04	11	2*18	13W07	903	H26 1
4W15	904	I05	11	2*19	13W07	904	H26 8
4W15	905	I08	11	2*20	13W07	905	H26 28
4W15	906	I09	11	2*21	13W07	906	H27 1
4W15	907	I10	11	2*22	13W07	907	H27 8
4W15	908				13W07	908	

4W16	00	READ DISTRIBUTOR FROM MEMORY				14W07	00
4W16	90	I11	19	2*23	14W07	90	H09 28
4W16	91	I12	19	2*24	14W07	91	H11 1
4W16	92	I14	19	2*25	14W07	92	H11 8
4W16	93	I15	19	2*26	14W07	93	H11 28
4W16	94	I16	19	2*27	14W07	94	H12 1
4W16	95	I17	19	2*28	14W07	95	H12 8
4W16	96	I18	19	2*29	14W07	96	H12 28
4W16	97	I21	26	GO MEMORY	14W07	97	G04 21
4W16	98	I24	20	WRITE MEMORY	14W07	98	G04 20
4W16	99				14W07	99	
4W16	900	I01	19	2*15	14W07	900	H07 1
4W16	901	I02	19	2*16	14W07	901	H07 8
4W16	902	I03	19	2*17	14W07	902	H07 28
4W16	903	I04	19	2*18	14W07	903	H08 1
4W16	904	I05	19	2*19	14W07	904	H08 8
4W16	905	I08	19	2*20	14W07	905	H08 28
4W16	906	I09	19	2*21	14W07	906	H09 1
4W16	907	I10	19	2*22	14W07	907	H09 8
4W16	908				14W07	908	

4W17 00 READ DISTRIBUTOR FROM MEMORY

4W17 90 I11 21 2*23
 4W17 91 I12 21 2*24
 4W17 92 I14 21 2*25
 4W17 93 I15 21 2*26
 4W17 94 I16 21 2*27
 4W17 95 I17 21 2*28
 4W17 96 I18 21 2*29
 4W17 97 I21 5 00
 4W17 98 I24 22 WRITE
 4W17 99
 4W17 900 I01 21 2*15
 4W17 901 I02 21 2*16
 4W17 902 I03 21 2*17
 4W17 903 I04 21 2*18
 4W17 904 I05 21 2*19
 4W17 905 I08 21 2*20
 4W17 906 I09 21 2*21
 4W17 907 I10 21 2*22
 4W17 908

1BW07 00

1BW07 90 H27 28
 1BW07 91 H29 1
 1BW07 92 H29 8
 1BW07 93 H29 28
 1BW07 94 H30 1
 1BW07 95 H30 8
 1BW07 96 H30 28
 1BW07 97 G38 21
 1BW07 98 G38 20
 1BW07 99
 1BW07 900 H25 1
 1BW07 901 H25 8
 1BW07 902 H25 28
 1BW07 903 H26 1
 1BW07 904 H26 8
 1BW07 905 H26 28
 1BW07 906 H27 1
 1BW07 907 H27 8
 1BW07 908

4W18 00 READ DISTRIBUTOR TO LOWER REGISTER

4W18 90 I11 28 2*23 MEMORY TO REGISTER
 4W18 91 I12 28 2*24 MEMORY TO REGISTER
 4W18 92 I14 28 2*25 MEMORY TO REGISTER
 4W18 93 I15 28 2*26 MEMORY TO REGISTER
 4W18 94 I16 28 2*27 MEMORY TO REGISTER
 4W18 95 I17 28 2*28 MEMORY TO REGISTER
 4W18 96 I18 28 2*29 MEMORY TO REGISTER
 4W18 97
 4W18 98
 4W18 99
 4W18 900 I01 28 2*15 MEMORY TO REGISTER
 4W18 901 I02 28 2*16 MEMORY TO REGISTER
 4W18 902 I03 28 2*17 MEMORY TO REGISTER
 4W18 903 I04 28 2*18 MEMORY TO REGISTER
 4W18 904 I05 28 2*19 MEMORY TO REGISTER
 4W18 905 I08 28 2*20 MEMORY TO REGISTER
 4W18 906 I09 28 2*21 MEMORY TO REGISTER
 4W18 907 I10 28 2*22 MEMORY TO REGISTER
 4W18 908

7W06 00

7W06 90 B42 26
 7W06 91 C37 2
 7W06 92 C37 26
 7W06 93 C38 2
 7W06 94 C38 26
 7W06 95 C39 2
 7W06 96 C39 26
 7W06 97
 7W06 98
 7W06 99
 7W06 900 B38 26
 7W06 901 B39 2
 7W06 902 B39 26
 7W06 903 B40 2
 7W06 904 B40 26
 7W06 905 B41 2
 7W06 906 B41 26
 7W06 907 B42 2
 7W06 908

4W19	00	READ DISTRIBUTOR TO CONTROL				5W18	00
4W19	90					5W18	90
4W19	91					5W18	91
4W19	92					5W18	92
4W19	93					5W18	93
4W19	94					5W18	94
4W19	95					5W18	95
4W19	96					5W18	96
4W19	97					5W18	97
4W19	98					5W18	98
4W19	99					5W18	99
4W19	900					5W18	900
4W19	901	I20	5	MEMORY GO		5W18	901 F42 27
4W19	902	I20	7	MEMORY WRITE		5W18	902 K38 6
4W19	903					5W18	903
4W19	904	I20	24	READ TO PERIPHERAL		5W18	904 K38 4
4W19	905					5W18	905
4W19	906					5W18	906
4W19	907					5W18	907
4W19	908					5W18	908

4W20	00	MEMORY ADDRESS FROM CONTROL				5W28	00
4W20	90	G01	18	2*8 ADDRESS TO MEMORY		5W28	90 Q36 18
4W20	91	G02	11	2*9 ADDRESS TO MEMORY		5W28	91 Q37 7
4W20	92	G02	14	2*10 ADDRESS TO MEMORY		5W28	92 Q37 3
4W20	93	G02	17	2*11 ADDRESS TO MEMORY		5W28	93 Q37 18
4W20	94	G02	18	2*12 ADDRESS TO MEMORY		5W28	94 Q38 7
4W20	95	G03	11	2*13 ADDRESS TO MEMORY		5W28	95 Q38 3
4W20	96	G03	14	2*14 ADDRESS TO MEMORY		5W28	96 Q38 18
4W20	97	G03	17	2*15 ADDRESS TO MEMORY		5W28	97 Q39 7
4W20	98	G03	18	2*16 ADDRESS TO MEMORY		5W28	98 Q39 3
4W20	99					5W28	99
4W20	900	G13	12	CLOCK		5W28	900 J39 6
4W20	901	G05	18	2*0 BANK SELECT		5W28	901 Q34 7
4W20	902	G05	22	2*1 BANK SELECT		5W28	902 Q34 3
4W20	903	G04	8			5W28	903 Q34 18
4W20	904	G04	4	2*3 CHASSIS 4 SELECT (001)		5W28	904 Q35 7
4W20	905	G04	14	2*4		5W28	905 Q35 3
4W20	906	G01	11	2*5 ADDRESS TO MEMORY		5W28	906 Q35 18
4W20	907	G01	14	2*6 ADDRESS TO MEMORY		5W28	907 Q36 7
4W20	908	G01	17	2*7 ADDRESS TO MEMORY		5W28	908 Q36 3

4W21	00	READ DISTRIBUTOR FROM MEMORY				16W07	00
4W21	90	I11	23	2*23	16W07	90	H09 28
4W21	91	I12	23	2*24	16W07	91	H11 1
4W21	92	I14	23	2*25	16W07	92	H11 8
4W21	93	I15	23	2*26	16W07	93	H11 28
4W21	94	I16	23	2*27	16W07	94	H12 1
4W21	95	I17	23	2*28	16W07	95	H12 8
4W21	96	I18	23	2*29	16W07	96	H12 28
4W21	97				16W07	97	
4W21	98				16W07	98	
4W21	99				16W07	99	
4W21	900	I01	23	2*15	16W07	900	H07 1
4W21	901	I02	23	2*16	16W07	901	H07 8
4W21	902	I03	23	2*17	16W07	902	H07 28
4W21	903	I04	23	2*18	16W07	903	H08 1
4W21	904	I05	23	2*19	16W07	904	H08 8
4W21	905	I08	23	2*20	16W07	905	H08 28
4W21	906	I09	23	2*21	16W07	906	H09 1
4W21	907	I10	23	2*22	16W07	907	H09 8
4W21	908				16W07	908	

4W24	00	DISK SYNC PASSON (OUTPUT)			
4W24	90	K02	8	2*0	OUTPUT DATA
4W24	91	K02	6	2*1	OUTPUT DATA
4W24	92	K02	4	2*2	OUTPUT DATA
4W24	93	K02	27	2*3	OUTPUT DATA
4W24	94	K02	25	2*4	OUTPUT DATA
4W24	95	K02	23	2*5	OUTPUT DATA
4W24	96	K03	8	2*6	OUTPUT DATA
4W24	97	K03	6	2*7	OUTPUT DATA
4W24	98	K03	4	2*8	OUTPUT DATA
4W24	99	K03	27	2*9	OUTPUT DATA
4W24	900	K03	25	2*10	OUTPUT DATA
4W24	901	K03	23	2*11	OUTPUT DATA
4W24	902	K01	23		ACTIVE
4W24	903	K01	8		INACTIVE
4W24	904	K01	6		FULL
4W24	905	K01	27		EMPTY
4W24	906	K01	4		FUNCTION
4W24	907	K01	25		MASTER CLEAR
4W24	908				

4W25 00 DISK SYNC PABSON (INPUT)

4W25	90	L09	5	2*0	INPUT DATA
4W25	91	L09	7	2*1	INPUT DATA
4W25	92	L09	10	2*2	INPUT DATA
4W25	93	L09	21	2*3	INPUT DATA
4W25	94	L09	24	2*4	INPUT DATA
4W25	95	L09	26	2*5	INPUT DATA
4W25	96	L08	5	2*6	INPUT DATA
4W25	97	L08	7	2*7	INPUT DATA
4W25	98	L08	10	2*8	INPUT DATA
4W25	99	L08	21	2*9	INPUT DATA
4W25	900	L08	24	2*10	INPUT DATA
4W25	901	L08	26	2*11	INPUT DATA
4W25	902				ACTIVE
4W25	903	L07	5		INACTIVE
4W25	904	L07	7		FULL
4W25	905				EMPTY
4W25	906	L26	1		
4W25	907	L26	15		
4W25	908				

4W26 00 DISK SYNC TO CHANNEL (INPUT)

4W26	90	K05	8	2*0	INPUT DATA
4W26	91	K05	6	2*1	INPUT DATA
4W26	92	K05	4	2*2	INPUT DATA
4W26	93	K05	27	2*3	INPUT DATA
4W26	94	K05	25	2*4	INPUT DATA
4W26	95	K05	23	2*5	INPUT DATA
4W26	96	K06	8	2*6	INPUT DATA
4W26	97	K06	6	2*7	INPUT DATA
4W26	98	K06	4	2*8	INPUT DATA
4W26	99	K06	27	2*9	INPUT DATA
4W26	900	K06	25	2*10	INPUT DATA
4W26	901	K06	23	2*11	INPUT DATA
4W26	902	K04	4		ACTIVE
4W26	903	K04	8		INACTIVE
4W26	904	K04	6		FULL
4W26	905	K04	27		EMPTY
4W26	906	L26	3		
4W26	907	L26	13		
4W26	908				

4W27 00 DISK SYNC TO CHANNEL (OUTPUT)

4W27	90	L01	5	2*0	OUTPUT DATA
4W27	91	L01	7	2*1	OUTPUT DATA
4W27	92	L01	10	2*2	OUTPUT DATA
4W27	93	L01	21	2*3	OUTPUT DATA
4W27	94	L01	24	2*4	OUTPUT DATA
4W27	95	L01	26	2*5	OUTPUT DATA
4W27	96	L02	5	2*6	OUTPUT DATA
4W27	97	L02	7	2*7	OUTPUT DATA
4W27	98	L02	10	2*8	OUTPUT DATA
4W27	99	L02	21	2*9	OUTPUT DATA
4W27	900	L02	24	2*10	OUTPUT DATA
4W27	901	L02	26	2*11	OUTPUT DATA
4W27	902	L03	7		ACTIVE
4W27	903	L03	21		INACTIVE
4W27	904	L03	10		FULL
4W27	905	L03	26		EMPTY
4W27	906	L03	5		FUNCTION
4W27	907	L03	24		MASTER CLEAR
4W27	908				

4W28 00 DISK SYNC TO DISK FILE (OUTPUT)

4W28	90	J06	8	2*0	WRITE DATA
4W28	91	J06	6	2*1	WRITE DATA
4W28	92	J06	4	2*2	WRITE DATA
4W28	93	J06	27	2*3	WRITE DATA
4W28	94	J06	25	2*4	WRITE DATA
4W28	95	J06	23	2*5	WRITE DATA
4W28	96	J05	8	2*6	WRITE DATA
4W28	97	J05	6	2*7	WRITE DATA
4W28	98	J05	4	2*8	WRITE DATA
4W28	99	J05	27	2*9	WRITE DATA
4W28	900	J05	25	2*10	WRITE DATA
4W28	901	J05	23	2*11	WRITE DATA
4W28	902	J07	8		
4W28	903	J07	6		
4W28	904	J07	4		
4W28	905	J07	27		
4W28	906				
4W28	907				
4W28	908				

4W29 00 DISK SYNC TO DISK FILE (INPUT)

4W29	90	J01	12	2*0	READ DATA ANALOG
4W29	91	J01	5	2*1	READ DATA ANALOG
4W29	92	J01	26	2*2	READ DATA ANALOG
4W29	93	J01	19	2*3	READ DATA ANALOG
4W29	94	J02	12	2*4	READ DATA ANALOG
4W29	95	J02	5	2*5	READ DATA ANALOG
4W29	96	J02	26	2*6	READ DATA ANALOG
4W29	97	J02	19	2*7	READ DATA ANALOG
4W29	98	J03	12	2*8	READ DATA ANALOG
4W29	99	J03	5	2*9	READ DATA ANALOG
4W29	900	J03	26	2*10	READ DATA ANALOG
4W29	901	J03	19	2*11	READ DATA ANALOG
4W29	902	J04	12		REV. MARK
4W29	903	J04	5		CLOCK
4W29	905				
4W29	906				
4W29	907				
4W29	908				

4W39 00 MEMORY TO READ DISTRIBUTOR

4W39	90	H07	1	2*15	
4W39	91	H07	8	2*16	
4W39	92	H07	28	2*17	
4W39	93	H08	1	2*18	
4W39	94	H08	8	2*19	
4W39	95	H08	28	2*20	
4W39	96	H09	1	2*21	
4W39	97	H09	8	2*22	
4W39	98	H09	28	2*23	
4W39	99	H11	1	2*24	
4W39	900	H11	8	2*25	
4W39	901	H11	28	2*26	
4W39	902	H12	1	2*27	
4W39	903	H12	8	2*28	
4W39	904	H12	28	2*29	
4W39	905	G04	21	GO	
4W39	906	G04	20	WRITE	
4W39	907	I21	13	READ PERIPHERAL	
4W39	908				

4W39 00

4W39	90	I01	3	
4W39	91	I02	3	
4W39	92	I03	3	
4W39	93	I04	3	
4W39	94	I05	3	
4W39	95	I08	3	
4W39	96	I09	3	
4W39	97	I10	3	
4W39	98	I11	3	
4W39	99	I12	3	
4W39	900	I14	3	
4W39	901	I15	3	
4W39	902	I16	3	
4W39	903	I17	3	
4W39	904	I18	3	
4W39	905	I21	24	
4W39	906	I24	28	
4W39	907	I19	12	
4W39	908			

4W39 00 READ DISTRIBUTOR FROM MEMORY

4W39 00

4W39 90 I01 3 2*15
4W39 91 I02 3 2*16
4W39 92 I03 3 2*17
4W39 93 I04 3 2*18
4W39 94 I05 3 2*19
4W39 95 I08 3 2*20
4W39 96 I09 3 2*21
4W39 97 I10 3 2*22
4W39 98 I11 3 2*23
4W39 99 I12 3 2*24
4W39 900 I14 3 2*25
4W39 901 I15 3 2*26
4W39 902 I16 3 2*27
4W39 903 I17 3 2*28
4W39 904 I18 3 2*29
4W39 905 I21 24 GO
4W39 906 I24 28 WRITE
4W39 907 I19 12 READ PERIPHERAL
4W39 908

4W39 90 H07 1
4W39 91 H07 8
4W39 92 H07 28
4W39 93 H08 1
4W39 94 H08 8
4W39 95 H08 28
4W39 96 H09 1
4W39 97 H09 8
4W39 98 H09 28
4W39 99 H11 1
4W39 900 H11 8
4W39 901 H11 28
4W39 902 H12 1
4W39 903 H12 8
4W39 904 H12 28
4W39 905 G04 21
4W39 906 G04 20
4W39 907 I21 13
4W39 908

5W01	00	CONTROL FROM READ DISTRIBUTOR				4W14	00
5W01	90	C42	27	2*20	4W14	90	108 26
5W01	91	D41	4	2*21	4W14	91	109 26
5W01	92	D41	6	2*22	4W14	92	110 26
5W01	93	D41	7	2*23	4W14	93	111 26
5W01	94	D41	22	2*24	4W14	94	112 26
5W01	95	D41	25	2*25	4W14	95	114 26
5W01	96	D41	27	2*26	4W14	96	115 26
5W01	97	D42	4	2*27	4W14	97	116 26
5W01	98	D42	6	2*28	4W14	98	117 26
5W01	99	D42	7	2*29	4W14	99	118 26
5W01	900	C42	25	2*19	4W14	900	105 26
5W01	901				4W14	901	
5W01	902				4W14	902	
5W01	903				4W14	903	
5W01	904	C42	4	2*15	4W14	904	101 26
5W01	905	C42	6	2*16	4W14	905	102 26
5W01	906	C42	7	2*17	4W14	906	103 26
5W01	907	C42	22	2*18	4W14	907	104 26
5W01	908				4W14	908	

5W02	00	CONTROL FROM READ DISTRIBUTOR				3W14	00
5W02	90	E41	7	2*5	3W14	90	132 26
5W02	91	E41	22	2*6	3W14	91	133 26
5W02	92	E41	25	2*7	3W14	92	134 26
5W02	93	E41	27	2*8	3W14	93	135 26
5W02	94	E42	4	2*9	3W14	94	136 26
5W02	95	E42	6	2*10	3W14	95	138 26
5W02	96	E42	7	2*11	3W14	96	139 26
5W02	97	E42	22	2*12	3W14	97	140 26
5W02	98	E42	25	2*13	3W14	98	141 26
5W02	99	E42	27	2*14	3W14	99	142 26
5W02	900	E41	6	2*4	3W14	900	129 26
5W02	901				3W14	901	
5W02	902				3W14	902	
5W02	903				3W14	903	
5W02	904	D42	22	2*0	3W14	904	125 26
5W02	905	D42	25	2*1	3W14	905	126 26
5W02	906	D42	27	2*2	3W14	906	127 26
5W02	907	E41	4	2*3	3W14	907	128 26
5W02	908				3W14	908	

5W03	00	CONTROL FROM READ DISTRIBUTOR				10W10	00
5W03	90	A42	7	2*53	10W10	90	I11 26
5W03	91	A42	22	2*54	10W10	91	I12 26
5W03	92	A42	25	2*55	10W10	92	I14 26
5W03	93	A42	27	2*56	10W10	93	I15 26
5W03	94	B41	4	2*57	10W10	94	I16 26
5W03	95	B41	6	2*58	10W10	95	I17 26
5W03	96	B41	7	2*59	10W10	96	I18 26
5W03	97				10W10	97	
5W03	98				10W10	98	
5W03	99				10W10	99	
5W03	900	A41	4	2*45	10W10	900	I01 26
5W03	901	A41	6	2*46	10W10	901	I02 26
5W03	902	A41	7	2*47	10W10	902	I03 26
5W03	903	A41	22	2*48	10W10	903	I04 26
5W03	904	A41	25	2*49	10W10	904	I05 26
5W03	905	A41	27	2*50	10W10	905	I08 26
5W03	906	A42	4	2*51	10W10	906	I09 26
5W03	907	A42	6	2*52	10W10	907	I10 26
5W03	908				10W10	908	

5W04	00	CONTROL FROM READ DISTRIBUTOR				9W10	00
5W04	90	B42	27	2*38 BUFFER TO CONTROL	9W10	90	I35 26
5W04	91	C41	4	2*39 BUFFER TO CONTROL	9W10	91	I36 26
5W04	92	C41	6	2*40 BUFFER TO CONTROL	9W10	92	I38 26
5W04	93	C41	7	2*41 BUFFER TO CONTROL	9W10	93	I39 26
5W04	94	C41	22	2*42 BUFFER TO CONTROL	9W10	94	I40 26
5W04	95	C41	25	2*43 BUFFER TO CONTROL	9W10	95	I41 26
5W04	96	C41	27	2*44 BUFFER TO CONTROL	9W10	96	I42 26
5W04	97				9W10	97	
5W04	98				9W10	98	
5W04	99				9W10	99	
5W04	900	B41	22	2*30 BUFFER TO CONTROL	9W10	900	I25 26
5W04	901	B41	25	2*31 BUFFER TO CONTROL	9W10	901	I26 26
5W04	902	B41	27	2*32 BUFFER TO CONTROL	9W10	902	I27 26
5W04	903	B42	4	2*33 BUFFER TO CONTROL	9W10	903	I28 26
5W04	904	B42	6	2*34 BUFFER TO CONTROL	9W10	904	I29 26
5W04	905	B42	7	2*35 BUFFER TO CONTROL	9W10	905	I32 26
5W04	906	B42	22	2*36 BUFFER TO CONTROL	9W10	906	I33 26
5W04	907	B42	25	2*37 BUFFER TO CONTROL	9W10	907	I34 26
5W04	908				9W10	908	

5W08	00	INCREMENT RESULT				7W21	00
5W08	90	F41	27	BIT 9	7W21	90	F42 26
5W08	91	F42	4	BIT 10	7W21	91	G38 2
5W08	92	F42	6	BIT 11	7W21	92	G38 26
5W08	93	F42	8	BIT 12	7W21	93	G39 2
5W08	94	F42	10	BIT 13	7W21	94	G39 26
5W08	95	F42	12	BIT 14	7W21	95	G40 2
5W08	96	F42	19	BIT 15	7W21	96	G40 26
5W08	97	F42	21	BIT 16	7W21	97	G41 2
5W08	98	F42	23	BIT 17	7W21	98	G41 26
5W08	99				7W21	99	
5W08	900	F41	4	BIT 0	7W21	900	F38 2
5W08	901	F41	6	BIT 1	7W21	901	F38 26
5W08	902	F41	8	BIT 2	7W21	902	F39 2
5W08	903	F41	10	BIT 3	7W21	903	F39 26
5W08	904	F41	12	BIT 4	7W21	904	F40 2
5W08	905	F41	19	BIT 5	7W21	905	F40 26
5W08	906	F41	21	BIT 6	7W21	906	F41 2
5W08	907	F41	23	BIT 7	7W21	907	F41 26
5W08	908	F41	25	BIT 8	7W21	908	F42 2

5W09	00	CENTRAL PROGRAM ADDRESS TO PERIPHERAL				1W02	00
5W09	90	G41	4	2*0 CENTRAL PROGRAM ADDRESS	1W02	90	F37 5
5W09	91	G41	6	2*1 CENTRAL PROGRAM ADDRESS	1W02	91	F37 7
5W09	92	G41	8	2*2 CENTRAL PROGRAM ADDRESS	1W02	92	F37 10
5W09	93	G41	10	2*3 CENTRAL PROGRAM ADDRESS	1W02	93	F37 21
5W09	94	G41	12	2*4 CENTRAL PROGRAM ADDRESS	1W02	94	F37 24
5W09	95	G41	19	2*5 CENTRAL PROGRAM ADDRESS	1W02	95	F37 26
5W09	96	G41	21	2*6 CENTRAL PROGRAM ADDRESS	1W02	96	F38 5
5W09	97	G41	23	2*7 CENTRAL PROGRAM ADDRESS	1W02	97	F38 7
5W09	98	G41	25	2*8 CENTRAL PROGRAM ADDRESS	1W02	98	F38 10
5W09	99	G41	27	2*9 CENTRAL PROGRAM ADDRESS	1W02	99	F38 21
5W09	900	G42	4	2*10 CENTRAL PROGRAM ADDRESS	1W02	900	F38 24
5W09	901	G42	6	2*11 CENTRAL PROGRAM ADDRESS	1W02	901	F38 26
5W09	902	G42	8	2*12 CENTRAL PROGRAM ADDRESS	1W02	902	F39 5
5W09	903	G42	10	2*13 CENTRAL PROGRAM ADDRESS	1W02	903	F39 7
5W09	904	G42	12	2*14 CENTRAL PROGRAM ADDRESS	1W02	904	F39 10
5W09	905	G42	19	2*15 CENTRAL PROGRAM ADDRESS	1W02	905	F39 21
5W09	906	G42	21	2*16 CENTRAL PROGRAM ADDRESS	1W02	906	F39 24
5W09	907	G42	23	2*17 CENTRAL PROGRAM ADDRESS	1W02	907	F39 26
5W09	908				1W02	908	

5W10	00	CENTRAL CONTROL TO MEMORY DISTRIBUTION				2W13	00
5W10	90	I39	4	STORE CONTROL BIT 2*44	2W13	90	A03 10
5W10	91	I39	27	STORE CONTROL BIT 2*45	2W13	91	A03 21
5W10	92	I39	25	STORE CONTROL BIT 2*46	2W13	92	A03 24
5W10	93	I39	23	STORE CONTROL BIT 2*47	2W13	93	A03 26
5W10	94	I39	21	STORE CONTROL BIT 2*48	2W13	94	A04 5
5W10	95	I39	19	STORE CONTROL BIT 2*49	2W13	95	A04 7
5W10	96	I40	4	STORE CONTROL BIT 2*50	2W13	96	A04 10
5W10	97	I40	27	STORE CONTROL BIT 2*51	2W13	97	A04 21
5W10	98	I40	23	STORE CONTROL BIT 2*52	2W13	98	A04 24
5W10	99	I40	23	STORE CONTROL BIT 2*53	2W13	99	A04 26
5W10	900	I39	6	STORE CONTROL BIT 2*43	2W13	900	A03 7
5W10	901	I38	25	STORE CONTROL BIT 2*36	2W13	901	A02 5
5W10	902	I38	23	STORE CONTROL BIT 2*37	2W13	902	A02 7
5W10	903	I38	21	STORE CONTROL BIT 2*38	2W13	903	A02 10
5W10	904	I38	19	STORE CONTROL BIT 2*39	2W13	904	A02 21
5W10	905	I39	12	STORE CONTROL BIT 2*40	2W13	905	A02 24
5W10	906	I39	10	STORE CONTROL BIT 2*41	2W13	906	A02 26
5W10	907	I39	8	STORE CONTROL BIT 2*42	2W13	907	A03 5
5W10	908				2W13	908	

5W11	00	CONTROL TO UPPER REGISTER				8W13	00
5W11	90	F42	25	INCR SIGN	8W13	90	E06 26
5W11	91				8W13	91	
5W11	92	E39	23	ADD PLUS	8W13	92	G01 26
5W11	93				8W13	93	
5W11	94				8W13	94	
5W11	95				8W13	95	
5W11	96				8W13	96	
5W11	97				8W13	97	
5W11	98				8W13	98	
5W11	99				8W13	99	
5W11	900	042	24	LONG ADD INDEFINITE	8W13	900	H06 12
5W11	901	042	21	LONG ADD INFINITE	8W13	901	H06 10
5W11	902				8W13	902	
5W11	903				8W13	903	
5W11	904				8W13	904	
5W11	905				8W13	905	
5W11	906				8W13	906	
5W11	907				8W13	907	
5W11	908				8W13	908	

5W12	00	CONTROL TO LONG ADD, ADD AND SHIFT			8W12	00		
5W12	90	E40	27	LONG ADD IN	8W12	90	H01	5
5W12	91	E40	25	ADD IN	8W12	91	H01	10
5W12	92	E39	12	MASK	8W12	92	I19	9
5W12	93	E39	21	ROUND ADD	8W12	93	H01	21
5W12	94	E39	19	ADD	8W12	94	H01	24
5W12	95	E39	8	PACK	8W12	95	G16	5
5W12	96	E39	10	UN-PACK	8W12	96	G16	10
5W12	97	E38	19	SHIFT (JK)	8W12	97	G16	21
5W12	98	E38	23	SHIFT NOMINAL	8W12	98	G16	26
5W12	99	E38	21	SHIFT LEFT	8W12	99	G16	24
5W12	900	E39	6	ROUND SHIFT	8W12	900	H01	7
5W12	901	J40	21	TRANSMIT LONG ADD	8W12	901	F01	24
5W12	902	J40	12	TRANSMIT SHIFT	8W12	902	G01	24
5W12	903	J40	19	TRANSMIT ADD	8W12	903	G02	24
5W12	904	J22	19	REQUEST RELEASE SHIFT	8W12	904	H06	4
5W12	905	E39	27	LONG ADD - PLUS	8W12	905	I19	5
5W12	906	K22	19	REQUEST RELEASE LONG ADD	8W12	906	H06	8
5W12	907	E40	23	SHIFT IN	8W12	907	H01	26
5W12	908	E39	4	NORM. (SHIFT)	8W12	908	G16	7

5W13	00	CONTROL TO DIVIDE			2W21	00		
5W13	90				2W21	90		
5W13	91	E40	12	ROUND	2W21	91	J02	7
5W13	92			BOCLEAN	2W21	92		
5W13	93	E38	8	SUM	2W21	93	J01	5
5W13	94	E38	10	DIFFERENCE	2W21	94	J01	7
5W13	95	E38	12	PRODUCT	2W21	95	J01	10
5W13	96	E40	19	POP. COUNT	2W21	96	J02	10
5W13	97	E38	6	COMPLEMENT	2W21	97	J01	24
5W13	98	E38	4	TRANSFER	2W21	98	J01	21
5W13	99	J40	10	TRANSMIT BCOLEAN	2W21	99	L11	18
5W13	900	J40	27	TRANSMIT DIVIDE	2W21	900	L11	14
5W13	901				2W21	901		
5W13	902	E40	21	GO BOOLEAN	2W21	902	L11	16
5W13	903	J40	8	GO DIVIDE	2W21	903	L11	12
5W13	904			CLEAR	2W21	904		
5W13	905	J24	19	DIVIDE REQUEST RELEASE	2W21	905	E01	8
5W13	906	J23	19	BOOLEAN REQUEST RELEASE	2W21	906	E01	6
5W13	907	O35	10	TRANSMIT MUIT 1	2W21	907	O09	7
5W13	908	O35	12	TRANSMIT MUIT 2	2W21	908	O09	21

5W14 00 CONTROL TO MULTIPLY

5W14 90 J40 4 GO UNIT NO. 1.
 5W14 91 E40 4 ROUND NO. 1
 5W14 92 E40 8 DOUBLE NO. 1
 5W14 93 J40 23 XMIT RESULT NO. 1
 5W14 94 J40 25 XMIT RESULT NO. 2
 5W14 95 J25 19 REQUEST RELEASE
 5W14 96 J39 27 CLOCK
 5W14 97 CLEAR
 5W14 98
 5W14 99
 5W14 900 J40 6 GO UNIT NO. 2
 5W14 901 E40 6 ROUND
 5W14 902 E40 10 DOUBLE
 5W14 903 XMIT RESULT
 5W14 904 XMIT RESULT
 5W14 905 K21 19 REQUEST RELEASE
 5W14 906
 5W14 907
 5W14 908

6W16 00

6W16 90 I09 23
 6W16 91 I09 3
 6W16 92 I09 23
 6W16 93 L08 26
 6W16 94 L08 2
 6W16 95 H09 1
 6W16 96 I22 12
 6W16 97
 6W16 98
 6W16 99
 6W16 900 J09 23
 6W16 901 J09 3
 6W16 902 J08 23
 6W16 903
 6W16 904
 6W16 905 K09 1
 6W16 906
 6W16 907
 6W16 908

5W15 00 CONTROL TO UPPER REGISTER

5W15 90 I42 7 MEMORY TO D GO
 5W15 91 I42 3 MEMORY TO D 0
 5W15 92 I42 28 MEMORY TO D 1
 5W15 93 I42 24 MEMORY TO D 2
 5W15 94 J42 7 K TO MULTIPLY GO
 5W15 95 J42 3 K TO MULTIPLY 0
 5W15 96 J42 28 K TO MULTIPLY 1
 5W15 97 J42 24 K TO MULTIPLY 2
 5W15 98 H39 24 ADD TO X 2
 5W15 99
 5W15 900 J39 25 CLOCK
 5W15 901 H38 7 MULTIPLY TO X GO
 5W15 902 H38 3 MULTIPLY TO X 0
 5W15 903 H38 28 MULTIPLY TO X 1
 5W15 904 H38 24 MULTIPLY TO X 2
 5W15 905 H39 7 ADD TO X GO
 5W15 906 H39 3 ADD TO X 0
 5W15 907 H39 28 ADD TO X 1
 5W15 908

8W20 00

8W20 90 G05 17
 8W20 91 G05 5
 8W20 92 G05 4
 8W20 93 G05 16
 8W20 94 F02 21
 8W20 95 F02 5
 8W20 96 F02 7
 8W20 97 F02 10
 8W20 98 G07 16
 8W20 99
 8W20 900 I21 12
 8W20 901 G06 17
 8W20 902 G06 5
 8W20 903 G06 4
 8W20 904 G06 16
 8W20 905 G07 17
 8W20 906 G07 5
 8W20 907 G07 4
 8W20 908

5W16	00	CONTROL TO LOWER REGISTER				7W17	00		
5W16	90	I42	1	MEMORY TO D	0	7W17	90	Q28	4
5W16	91	I42	26	MEMORY TO D	1	7W17	91	Q28	5
5W16	92	I42	22	MEMORY TO D	2	7W17	92	Q28	16
5W16	93	J42	5	K TO MULTIPLY (1)	GO	7W17	93	I26	21
5W16	94	J42	1	K TO MULTIPLY (1)	0	7W17	94	I26	24
5W16	95	J42	26	K TO MULTIPLY (1)	1	7W17	95	I27	5
5W16	96	J42	22	K TO MULTIPLY (1)	2	7W17	96	I27	10
5W16	97	J41	1	J TO MULTIPLY (2)	0	7W17	97	I27	26
5W16	98	J41	26	J TO MULTIPLY (2)	1	7W17	98	I27	21
5W16	99	J41	22	J TO MULTIPLY (2)	2	7W17	99	I27	24
5W16	900	H42	5	INCREMENT TO A	GO	7W17	900	H27	17
5W16	901	H42	1	INCREMENT TO A	0	7W17	901	H27	16
5W16	902	H42	26	INCREMENT TO A	1	7W17	902	H27	4
5W16	903	H42	22	INCREMENT TO A	2	7W17	903	H27	5
5W16	904	I41	5	SHIFT TO B	GO	7W17	904	K34	17
5W16	905	I41	1	SHIFT TO B	0	7W17	905	K34	16
5W16	906	I41	26	SHIFT TO B	1	7W17	906	K34	4
5W16	907	I41	22	SHIFT TO B	2	7W17	907	K34	5
5W16	908					7W17	908		

5W17	00	CONTROL TO UPPER REGISTER				8W21	00		
5W17	90	K42	3	X TO ADD	0	8W21	90	F01	5
5W17	91	K42	28	X TO ADD	1	8W21	91	F01	7
5W17	92	K42	24	X TO ADD	2	8W21	92	F01	10
5W17	93	K42	7	X TO ADD	GO	8W21	93	F01	21
5W17	94	H40	7	INCR TO X	GO	8W21	94	Q08	17
5W17	95	H40	3	INCR TO X	0	8W21	95	Q08	5
5W17	96	H40	28	INCR TO X	1	8W21	96	Q08	4
5W17	97	H40	24	INCR TO X	2	8W21	97	Q08	16
5W17	98	O42	10	LONG ADD ZERO	CH 5	8W21	98	H06	23
5W17	99	O42	7	LONG ADD SIGN	CH 5	8W21	99	H06	25
5W17	900	J41	7	J TO MULTIPLY	GO	8W21	900	Q01	21
5W17	901	J41	3	J TO MULTIPLY	0	8W21	901	Q01	5
5W17	902	J41	28	J TO MULTIPLY	1	8W21	902	Q01	7
5W17	903	J41	24	J TO MULTIPLY	2	8W21	903	Q01	10
5W17	904	K41	7	X TO ADD	GO	8W21	904	E01	21
5W17	905	K41	3	X TO ADD	0	8W21	905	E01	5
5W17	906	K41	28	X TO ADD	1	8W21	906	E01	7
5W17	907	K41	24	X TO ADD	2	8W21	907	E01	10
5W17	908					8W21	908		

5W18	00	CONTROL FROM READ DISTRIBUTOR				4W19	00
5W18	90					4W19	90
5W18	91					4W19	91
5W18	92					4W19	92
5W18	93					4W19	93
5W18	94					4W19	94
5W18	95					4W19	95
5W18	96					4W19	96
5W18	97					4W19	97
5W18	98					4W19	98
5W18	99					4W19	99
5W18	900					4W19	900
5W18	901	F42	27	MEMORY GO		4W19	901 I20 5
5W18	902	K38	6	MEMORY WRITE		4W19	902 I20 7
5W18	903					4W19	903
5W18	904	K38	4	READ TO PERIPHERAL		4W19	904 I20 24
5W18	905					4W19	905
5W18	906					4W19	906
5W18	907					4W19	907
5W18	908					4W19	908

5W19	00	CONTROL TO LOWER REGISTER				7W18	00
5W19	90	K40	1	X TO INCREMENT	0	7W18	90 J27 24
5W19	91	K40	26	X TO INCREMENT	1	7W18	91 J28 5
5W19	92	K40	22	X TO INCREMENT	2	7W18	92 J28 10
5W19	93	M41	5	B TO ADD (1)	GO	7W18	93 M34 7
5W19	94	M41	1	B TO ADD (1)	0	7W18	94 M34 26
5W19	95	M41	26	B TO ADD (1)	1	7W18	95 M34 21
5W19	96	M41	22	B TO ADD (1)	2	7W18	96 M34 24
5W19	97					7W18	97
5W19	98					7W18	98
5W19	99					7W18	99
5W19	900	K41	5	XJ TO ADD (1)	GO	7W18	900 J28 7
5W19	901	K41	1	XJ TO ADD (1)	0	7W18	901 J28 26
5W19	902	K41	26	XJ TO ADD (1)	1	7W18	902 J28 21
5W19	903	K41	22	XJ TO ADD (1)	2	7W18	903 J28 24
5W19	904	K42	5	XL TO ADD (2)	GO	7W18	904 I26 5
5W19	905	K42	1	XL TO ADD (2)	0	7W18	905 I26 10
5W19	906	K42	26	XL TO ADD (2)	1	7W18	906 I26 7
5W19	907	K42	22	XL TO ADD (2)	2	7W18	907 I26 26
5W19	908					7W18	908

5W20	00	CONTROL TO LOWER REGISTER				7W19	00		
5W20	90	M42	1	A TO INCREMENT	0	7W19	90	G26	10
5W20	91	M42	26	A TO INCREMENT	1	7W19	91	G26	7
5W20	92	M42	22	A TO INCREMENT	2	7W19	92	G26	26
5W20	93	H39	26	ADD TO X	1	7W19	93	I28	4
5W20	94	I42	5	MEM. TO D	GO	7W19	94	G28	17
5W20	95	K40	5	X TO INCR.	GO	7W19	95	J27	21
5W20	96	M42	5	A TO INCR.	GO	7W19	96	G26	5
5W20	97					7W19	97		
5W20	98					7W19	98		
5W20	99					7W19	99		
5W20	900	L41	5	BJ TO INCREMENT	GO	7W19	900	L34	5
5W20	901	L41	1	BJ TO INCREMENT	0	7W19	901	L34	10
5W20	902	L41	26	BJ TO INCREMENT	1	7W19	902	L34	7
5W20	903	L41	22	BJ TO INCREMENT	2	7W19	903	L34	26
5W20	904	L42	5	BK TO INCREMENT	GO	7W19	904	L34	21
5W20	905	L42	1	BK TO INCREMENT	0	7W19	905	L34	24
5W20	906	L42	26	BK TO INCREMENT	1	7W19	906	M34	5
5W20	907	L42	22	BK TO INCREMENT	2	7W19	907	M34	10
5W20	908					7W19	908		

5W21	00	CONTROL (1 TO 5 VIA 3)				3W24	00		
5W21	90					3W24	90		
5W21	91					3W24	91		
5W21	92					3W24	92		
5W21	93					3W24	93		
5W21	94					3W24	94		
5W21	95					3W24	95		
5W21	96					3W24	96		
5W21	97					3W24	97		
5W21	98					3W24	98		
5W21	99					3W24	99		
5W21	900	041	18	READ/GO (PERIPH)		3W24	900	J39	28
5W21	901	041	20	WRITE GO (PERIPH)		3W24	901	J39	26
5W21	902	040	15	EXCHANGE GO (PERIPH)		3W24	902	J39	24
5W21	903					3W24	903		
5W21	904					3W24	904		
5W21	905					3W24	905		
5W21	906					3W24	906		
5W21	907					3W24	907		
5W21	908					3W24	908		

5W22	00	CONTROL TO LOWER REGISTER				7W16	00
5W22	90	H39	22	ADD TO X	2	7W16	90 I28 5
5W22	91	H40	5	INCREMENT TO X	GO	7W16	91 H28 17
5W22	92	H40	1	INCREMENT TO X	0	7W16	92 H28 16
5W22	93	H40	26	INCREMENT TO X	1	7W16	93 H28 4
5W22	94	H40	22	INCREMENT TO X	2	7W16	94 H28 5
5W22	95	H41	5	INCREMENT TO B	GO	7W16	95 K33 17
5W22	96	H41	1	INCREMENT TO B	0	7W16	96 K33 16
5W22	97	H41	26	INCREMENT TO B	1	7W16	97 K33 4
5W22	98	H41	22	INCREMENT TO B	2	7W16	98 K33 5
5W22	99	J41	5	XJ TO MULTIPLY (2)	GO	7W16	99 I27 7
5W22	900					7W16	900
5W22	901	J39	4	CLOCK		7W16	901 I22 12
5W22	902	H38	5	MULTIPLY TO X	GO	7W16	902 Q27 17
5W22	903	H38	1	MULTIPLY TO X	0	7W16	903 Q27 16
5W22	904	H38	26	MULTIPLY TO X	1	7W16	904 Q27 4
5W22	905	H38	22	MULTIPLY TO X	2	7W16	905 Q27 5
5W22	906	H39	5	ADD TO X	GO	7W16	906 I28 17
5W22	907	H39	1	ADD TO X	0	7W16	907 I28 16
5W22	908					7W16	908

5W23	00	CONTROL TO PERIPHERAL				1W12	00
5W23	90					1W12	90
5W23	91					1W12	91
5W23	92					1W12	92
5W23	93					1W12	93
5W23	94					1W12	94
5W23	95					1W12	95
5W23	96					1W12	96
5W23	97					1W12	97
5W23	98					1W12	98
5W23	99					1W12	99
5W23	900	040	2	EXCHANGE RESUME OUT		1W12	900 I29 5
5W23	901	042	26	MC		1W12	901 I30 23
5W23	902	I22	12	CLOCK		1W12	902 I30 4
5W23	903					1W12	903
5W23	904					1W12	904
5W23	905					1W12	905
5W23	906					1W12	906
5W23	907					1W12	907
5W23	908					1W12	908

5W24 00 READWRITE EXCHANGE ADDRESS

5W24	90			
5W24	91	N41	21	BIT 9
5W24	92	N41	24	BIT 10
5W24	93	N41	26	BIT 11
5W24	94	N42	5	BIT 12
5W24	95	N42	7	BIT 13
5W24	96	N42	10	BIT 14
5W24	97	N42	21	BIT 15
5W24	98	N42	24	BIT 16
5W24	99	N42	26	BIT 17
5W24	900	N41	10	BIT 8
5W24	901	N40	5	BIT 0
5W24	902	N40	7	BIT 1
5W24	903	N40	10	BIT 2
5W24	904	N40	21	BIT 3
5W24	905	N40	24	BIT 4
5W24	906	N40	26	BIT 5
5W24	907	N41	5	BIT 6
5W24	908	N41	7	BIT 7

3W23 00

3W23	90			
3W23	91	J41	28	
3W23	92	J41	26	
3W23	93	J41	24	
3W23	94	J42	7	
3W23	95	J42	5	
3W23	96	J42	3	
3W23	97	J42	28	
3W23	98	J42	26	
3W23	99	J42	24	
3W23	900	J41	3	
3W23	901	J40	7	
3W23	902	J40	5	
3W23	903	J40	3	
3W23	904	J40	28	
3W23	905	J40	26	
3W23	906	J40	24	
3W23	907	J41	7	
3W23	908	J41	5	

5W25 00 CONTROL TO LOWER REGISTER

5W25	90			
5W25	91			
5W25	92			
5W25	93	033	6	
5W25	94			
5W25	95			
5W25	96			
5W25	97			
5W25	98	034	4	
5W25	99			
5W25	900	034	12	
5W25	901	034	10	
5W25	902	034	8	
5W25	903	034	6	
5W25	904	031	6	
5W25	905	031	5	
5W25	906	031	26	X TO MEMORY
5W25	907	031	23	
5W25	908			

7W20 00

7W20	90			
7W20	91			
7W20	92			
7W20	93	G37	5	
7W20	94			
7W20	95			
7W20	96			
7W20	97			
7W20	98	J26	24	
7W20	99			
7W20	900	J26	5	
7W20	901	J26	7	
7W20	902	J26	10	
7W20	903	J26	21	
7W20	904	H26	4	
7W20	905	H26	6	
7W20	906	H26	7	
7W20	907	H26	27	
7W20	908			

5W26	00	CONTROL TO UPPER REGISTER				8W22	00
5W26	91	033	8		8W22	91	E01 24
5W26	92	035	8	ENTER (JK) 2*0	8W22	92	H02 4
5W26	93	035	6	ENTER (JK) 2*1	8W22	93	H02 6
5W26	94	035	4	ENTER (JK) 2*2	8W22	94	H02 7
5W26	95	035	27	ENTER (JK) 2*3	8W22	95	H02 22
5W26	96	035	25	ENTER (JK) 2*4	8W22	96	H02 25
5W26	97	035	23	ENTER (JK) 2*5	8W22	97	H02 27
5W26	98	K23	19	REQUEST RELEASE ADD	8W22	98	H06 4
5W26	99	042	5	LONG ADD SER CONTROL	8W22	99	H06 27
5W26	904	031	8		8W22	904	G02 21
5W26	905	031	3	X TO MEMORY 0	8W22	905	G02 5
5W26	906	031	28	X TO MEMORY 1	8W22	906	G02 7
5W26	907	031	25		8W22	907	G02 10
5W26	908				8W22	908	

5W27	00	CENTRAL CONTROL TO MEMORY DISTRIBUTION				2W14	00
5W27	90	I38	12	2*30	2W14	90	A01 5
5W27	91	I38	10	2*31	2W14	91	A01 7
5W27	92	I38	8	2*32	2W14	92	A01 10
5W27	93	I38	6	2*33	2W14	93	A01 21
5W27	94	I38	4	2*34	2W14	94	A01 24
5W27	95	I38	27	2*35	2W14	95	A01 26
5W27	96	I40	19	2*56	2W14	96	A05 10
5W27	97				2W14	97	
5W27	98				2W14	98	
5W27	99				2W14	99	
5W27	900				2W14	900	
5W27	901	J39	8	CLOCK	2W14	901	I22 12
5W27	902	K38	23	STORE CONTROL	2W14	902	A07 7
5W27	903	K38	25	STORE REGISTERS	2W14	903	A07 10
5W27	904	K38	27	STORE PERIPHERAL	2W14	904	A07 5
5W27	905	N23	10	EXP = 1777 ERROR	2W14	905	E01 25
5W27	906	N23	7	EXP = 3777 ERROR	2W14	906	E01 23
5W27	907				2W14	907	
5W27	908				2W14	908	

5W28	00	CONTROL TO MEMORY ADDRESS			4W20	00
5W28	90	Q36	18	2*8 ADDRESS TO MEMORY	4W20	90 G01 18
5W28	91	Q37	7	2*9 ADDRESS TO MEMORY	4W20	91 G02 11
5W28	92	Q37	3	2*10 ADDRESS TO MEMORY	4W20	92 G02 14
5W28	93	Q37	18	2*11 ADDRESS TO MEMORY	4W20	93 G02 17
5W28	94	Q38	7	2*12 ADDRESS TO MEMORY	4W20	94 G02 18
5W28	95	Q38	3	2*13 ADDRESS TO MEMORY	4W20	95 G03 11
5W28	96	Q38	18	2*14 ADDRESS TO MEMORY	4W20	96 G03 14
5W28	97	Q39	7	2*15 ADDRESS TO MEMORY	4W20	97 G03 17
5W28	98	Q39	3	2*16 ADDRESS TO MEMORY	4W20	98 G03 18
5W28	99				4W20	99
5W28	900	J39	6	CLOCK	4W20	900 G13 12
5W28	901	Q34	7	2*0 BANK SELECT	4W20	901 G05 18
5W28	902	Q34	3	2*1 BANK SELECT	4W20	902 G05 22
5W28	903	Q34	18	2*2 CHASSIS 4 SELECT (001)	4W20	903 G04 8
5W28	904	Q35	7	2*3 CHASSIS 4 SELECT (001)	4W20	904 G04 4
5W28	905	Q35	3	2*4 CHASSIS 4 SELECT (001)	4W20	905 G04 14
5W28	906	Q35	18	2*5 ADDRESS TO MEMORY	4W20	906 G01 11
5W28	907	Q36	7	2*6 ADDRESS TO MEMORY	4W20	907 G01 14
5W28	908	Q36	3	2*7 ADDRESS TO MEMORY	4W20	908 G01 17

5W29	00	CONTROL TO MEMORY ADDRESS			3W20	00
5W29	90	Q36	20	2*8 ADDRESS TO MEMORY	3W20	90 G40 18
5W29	91	Q37	9	2*9 ADDRESS TO MEMORY	3W20	91 G41 11
5W29	92	Q37	5	2*10 ADDRESS TO MEMORY	3W20	92 G41 14
5W29	93	Q37	20	2*11 ADDRESS TO MEMORY	3W20	93 G41 17
5W29	94	Q38	9	2*12 ADDRESS TO MEMORY	3W20	94 G41 18
5W29	95	Q38	5	2*13 ADDRESS TO MEMORY	3W20	95 G42 11
5W29	96	Q38	20	2*14 ADDRESS TO MEMORY	3W20	96 G42 14
5W29	97	Q39	9	2*15 ADDRESS TO MEMORY	3W20	97 G42 17
5W29	98	Q39	5	2*16	3W20	98 G42 18
5W29	99	Q40	17	ACCEPT	3W20	99 I23 28
5W29	900	J39	23	CLOCK	3W20	900 G35 12
5W29	901	Q34	9	2*0 BANK SELECT	3W20	901 G39 18
5W29	902	Q34	5	2*1 BANK SELECT	3W20	902 G39 22
5W29	903	Q34	20	2*2 CHASSIS 3 SELECT (000)	3W20	903 G38 9
5W29	904	Q35	9	2*3 CHASSIS 3 SELECT (000)	3W20	904 G38 4
5W29	905	Q35	5	2*4 CHASSIS 3 SELECT (000)	3W20	905 G38 14
5W29	906	Q35	20	2*5 ADDRESS TO MEMORY	3W20	906 G40 11
5W29	907	Q36	9	2*6 ADDRESS TO MEMORY	3W20	907 G40 14
5W29	908	Q36	5	2*7 ADDRESS TO MEMORY	3W20	908 G40 17

5W30	00	CONTROL TO MEMORY ADDRESS				9W20	00
5W30	90	Q36	22	2*8	ADDRESS TO MEMORY	9W20	90 G40 18
5W30	91	Q37	11	2*9	ADDRESS TO MEMORY	9W20	91 G41 11
5W30	92	Q37	26	2*10	ADDRESS TO MEMORY	9W20	92 G41 14
5W30	93	Q37	22	2*11	ADDRESS TO MEMORY	9W20	93 G41 17
5W30	94	Q38	11	2*12	ADDRESS TO MEMORY	9W20	94 G41 18
5W30	95	Q38	26	2*13	ADDRESS TO MEMORY	9W20	95 G42 11
5W30	96	Q38	22	2*14	ADDRESS TO MEMORY	9W20	96 G42 14
5W30	97	Q39	11	2*15	ADDRESS TO MEMORY	9W20	97 G42 17
5W30	98	Q39	26	2*16	ADDRESS TO MEMORY	9W20	98 G42 18
5W30	99					9W20	99
5W30	900	M32	8		CLOCK	9W20	900 G35 12
5W30	901	Q34	11	2*0	BANK SELECT	9W20	901 G39 18
5W30	902	Q34	26	2*1	BANK SELECT	9W20	902 G39 22
5W30	903	Q34	22	2*2	CHASSIS 9 SELECT (010)	9W20	903 G38 9
5W30	904	Q35	11	2*3	CHASSIS 9 SELECT (010)	9W20	904 G38 5
5W30	905	Q35	26	2*4	CHASSIS 9 SELECT (010)	9W20	905 G38 14
5W30	906	Q35	22	2*5	ADDRESS TO MEMORY	9W20	906 G40 11
5W30	907	Q36	11	2*6	ADDRESS TO MEMORY	9W20	907 G40 14
5W30	908	Q36	26	2*7	ADDRESS TO MEMORY	9W20	908 G40 17

5W31	00	CONTROL TO MEMORY ADDRESS				10W20	00
5W31	90	Q36	24	2*8	ADDRESS TO MEMORY	10W20	90 G01 18
5W31	91	Q37	13	2*9	ADDRESS TO MEMORY	10W20	91 G02 11
5W31	92	Q37	28	2*10	ADDRESS TO MEMORY	10W20	92 G02 14
5W31	93	Q37	24	2*11	ADDRESS TO MEMORY	10W20	93 G02 17
5W31	94	Q38	13	2*12	ADDRESS TO MEMORY	10W20	94 G02 18
5W31	95	Q38	28	2*13	ADDRESS TO MEMORY	10W20	95 G03 11
5W31	96	Q38	24	2*14	ADDRESS TO MEMORY	10W20	96 G03 14
5W31	97	Q39	13	2*15	ADDRESS TO MEMORY	10W20	97 G03 17
5W31	98	Q39	28	2*16	ADDRESS TO MEMORY	10W20	98 G03 18
5W31	99					10W20	99
5W31	900	M32	6		CLOCK	10W20	900 G13 12
5W31	901	Q34	13	2*0	BANK SELECT	10W20	901 G05 18
5W31	902	Q34	28	2*1	BANK SELECT	10W20	902 G05 22
5W31	903	Q34	24	2*2	CHASSIS 10 SELECT (011)	10W20	903 G04 8
5W31	904	Q35	13	2*3	CHASSIS 10 SELECT (011)	10W20	904 G04 5
5W31	905	Q35	28	2*4	CHASSIS 10 SELECT (011)	10W20	905 G04 14
5W31	906	Q35	24	2*5	ADDRESS TO MEMORY	10W20	906 G01 11
5W31	907	Q36	13	2*6	ADDRESS TO MEMORY	10W20	907 G01 14
5W31	908	Q36	28	2*7	ADDRESS TO MEMORY	10W20	908 G01 17

5W32	00	CONTROL TO MEMORY ADDRESS				13W18	00
5W32	90	Q42	18	2*8	ADDRESS TO MEMORY	13W18	90 G40 18
5W32	91	R40	7	2*9	ADDRESS TO MEMORY	13W18	91 G41 11
5W32	92	R40	3	2*10	ADDRESS TO MEMORY	13W18	92 G41 14
5W32	93	R40	18	2*11	ADDRESS TO MEMORY	13W18	93 G41 17
5W32	94	R41	7	2*12	ADDRESS TO MEMORY	13W18	94 G41 18
5W32	95	R41	3	2*13	ADDRESS TO MEMORY	13W18	95 G42 11
5W32	96	R41	18	2*14	ADDRESS TO MEMORY	13W18	96 G42 14
5W32	97	R42	7	2*15	ADDRESS TO MEMORY	13W18	97 G42 17
5W32	98	R42	3	2*16	ADDRESS TO MEMORY	13W18	98 G42 18
5W32	99					13W18	99
5W32	900	M32	4		CLOCK	13W18	900 G35 12
5W32	901	Q40	7	2*0	BANK SELECT	13W18	901 G39 18
5W32	902	Q40	3	2*1	BANK SELECT	13W18	902 G39 22
5W32	903	Q40	18	2*2	CHASSIS 13 SELECT (100)	13W18	903 G38 9
5W32	904	Q41	7	2*3	CHASSIS 13 SELECT (100)	13W18	904 G38 4
5W32	905	Q41	3	2*4	CHASSIS 13 SELECT (100)	13W18	905 G38 13
5W32	906	Q41	18	2*5	ADDRESS TO MEMORY	13W18	906 G40 11
5W32	907	Q42	7	2*6	ADDRESS TO MEMORY	13W18	907 G40 14
5W32	908	Q42	3	2*7	ADDRESS TO MEMORY	13W18	908 G40 17

5W33	00	CONTROL TO MEMORY ADDRESS				14W18	00
5W33	90	Q42	20	2*8	ADDRESS TO MEMORY	14W18	90 G01 18
5W33	91	R40	9	2*9	ADDRESS TO MEMORY	14W18	91 G02 11
5W33	92	R40	5	2*10	ADDRESS TO MEMORY	14W18	92 G02 14
5W33	93	R40	20	2*11	ADDRESS TO MEMORY	14W18	93 G02 17
5W33	94	R41	9	2*12	ADDRESS TO MEMORY	14W18	94 G02 18
5W33	95	R41	5	2*13	ADDRESS TO MEMORY	14W18	95 G03 11
5W33	96	R41	20	2*14	ADDRESS TO MEMORY	14W18	96 G03 14
5W33	97	R42	9	2*15	ADDRESS TO MEMORY	14W18	97 G03 17
5W33	98	R42	5	2*16	ADDRESS TO MEMORY	14W18	98 G03 18
5W33	99					14W18	99
5W33	900	M32	27		CLOCK	14W18	900 G13 12
5W33	901	Q40	9	2*0	BANK SELECT	14W18	901 G05 18
5W33	902	Q40	5	2*1	BANK SELECT	14W18	902 G05 22
5W33	903	Q40	20	2*2	CHASSIS 14 SELECT (101)	14W18	903 G04 8
5W33	904	Q41	9	2*3	CHASSIS 14 SELECT (101)	14W18	904 G04 4
5W33	905	Q41	5	2*4	CHASSIS 14 SELECT (101)	14W18	905 G04 13
5W33	906	Q41	20	2*5	ADDRESS TO MEMORY	14W18	906 G01 11
5W33	907	Q42	5	2*6	ADDRESS TO MEMORY	14W18	907 G01 17
5W33	908	Q42	9	2*7	ADDRESS TO MEMORY	14W18	908 G01 14

5W34	00	CONTROL TO MEMORY ADDRESS			15W18	00
5W34	90	G42	22	2+8 ADDRESS TO MEMORY	15W18	90 G40 18
5W34	91	R40	11	2+9 ADDRESS TO MEMORY	15W18	91 G41 11
5W34	92	R40	26	2+10 ADDRESS TO MEMORY	15W18	92 G41 14
5W34	93	R40	22	2+11 ADDRESS TO MEMORY	15W18	93 G41 17
5W34	94	R41	11	2+12 ADDRESS TO MEMORY	15W18	94 G41 18
5W34	95	R41	26	2+13 ADDRESS TO MEMORY	15W18	95 G42 11
5W34	96	R41	22	2+14 ADDRESS TO MEMORY	15W18	96 G42 14
5W34	97	R42	11	2+15 ADDRESS TO MEMORY	15W18	97 G42 17
5W34	98	R42	26	2+16 ADDRESS TO MEMORY	15W18	98 G42 18
5W34	99				15W18	99
5W34	900	M32	25	CLOCK	15W18	900 G35 12
5W34	901	G40	11	2+0 BANK SELECT	15W18	901 G39 18
5W34	902	G40	26	2+1 BANK SELECT	15W18	902 G39 22
5W34	903	G40	22	2+2 CHASSIS 15 SELECT (110)	15W18	903 G38 9
5W34	904	G41	11	2+3 CHASSIS 15 SELECT (110)	15W18	904 G38 5
5W34	905	G41	26	2+4 CHASSIS 15 SELECT (110)	15W18	905 G38 13
5W34	906	G41	22	2+5 ADDRESS TO MEMORY	15W18	906 G40 11
5W34	907	G42	11	2+6 ADDRESS TO MEMORY	15W18	907 G40 14
5W34	908	G42	26	2+7 ADDRESS TO MEMORY	15W18	908 G40 17

5W35	00	CONTROL TO MEMORY ADDRESS			16W18	00
5W35	90	G42	24	2+8 ADDRESS TO MEMORY	16W18	90 G01 18
5W35	91	R40	13	2+9 ADDRESS TO MEMORY	16W18	91 G02 11
5W35	92	R40	28	2+10 ADDRESS TO MEMORY	16W18	92 G02 14
5W35	93	R40	24	2+11 ADDRESS TO MEMORY	16W18	93 G02 17
5W35	94	R41	13	2+12 ADDRESS TO MEMORY	16W18	94 G02 18
5W35	95	R41	28	2+13 ADDRESS TO MEMORY	16W18	95 G03 11
5W35	96	R41	24	2+14 ADDRESS TO MEMORY	16W18	96 G03 14
5W35	97	R42	13	2+15 ADDRESS TO MEMORY	16W18	97 G03 17
5W35	98	R42	28	2+16 ADDRESS TO MEMORY	16W18	98 G03 18
5W35	99				16W18	99
5W35	900	M32	23	CLOCK	16W18	900 G13 12
5W35	901	G40	13	2+0 BANK SELECT	16W18	901 G05 18
5W35	902	G40	28	2+1 BANK SELECT	16W18	902 G05 22
5W35	903	G40	24	2+2 CHASSIS 16 SELECT (111)	16W18	903 G04 8
5W35	904	G41	13	2+3 CHASSIS 16 SELECT (111)	16W18	904 G04 5
5W35	905	G41	28	2+4 CHASSIS 16 SELECT (111)	16W18	905 G04 13
5W35	906	G41	24	2+5 ADDRESS TO MEMORY	16W18	906 G01 11
5W35	907	G42	13	2+6 ADDRESS TO MEMORY	16W18	907 G01 14
5W35	908	G42	28	2+7 ADDRESS TO MEMORY	16W18	908 G01 17

5W36	00	INCREMENT OPERAND TO REGISTER			
5W36	90	026	7	INCREMENT OPERAND J BIT 2*9	
5W36	91	027	23	INCREMENT OPERAND J BIT 2*10	
5W36	92	027	7	INCREMENT OPERAND J BIT 2*11	
5W36	93	028	23	INCREMENT OPERAND J BIT 2*12	
5W36	94	028	7	INCREMENT OPERAND J BIT 2*13	
5W36	95	029	23	INCREMENT OPERAND J BIT 2*14	
5W36	96	029	7	INCREMENT OPERAND J BIT 2*15	
5W36	97	030	7		
5W36	98	030	24		
5W36	99				
5W36	900	022	23	INCREMENT OPERAND J BIT 2*0	
5W36	901	022	7	INCREMENT OPERAND J BIT 2*1	
5W36	902	023	23	INCREMENT OPERAND J BIT 2*2	
5W36	903	023	7	INCREMENT OPERAND J BIT 2*3	
5W36	904	024	23	INCREMENT OPERAND J BIT 2*4	
5W36	905	024	7	INCREMENT OPERAND J BIT 2*5	
5W36	906	025	23	INCREMENT OPERAND J BIT 2*6	
5W36	907	025	7	INCREMENT OPERAND J BIT 2*7	
5W36	908	026	23	INCREMENT OPERAND J BIT 2*8	

7W22	00		
7W22	90	K30	3
7W22	91	K30	25
7W22	92	K30	21
7W22	93	K31	3
7W22	94	K31	25
7W22	95	K31	21
7W22	96	K32	3
7W22	97	K32	25
7W22	98	K32	21
7W22	99		
7W22	900	K27	3
7W22	901	K27	25
7W22	902	K27	21
7W22	903	K28	3
7W22	904	K28	25
7W22	905	K28	21
7W22	906	K29	3
7W22	907	K29	25
7W22	908	K29	21

5W37	00	INCREMENT OPERAND TO REGISTER			
5W37	90	026	3	INCREMENT OPERAND K BIT 2*9	
5W37	91	027	27	INCREMENT OPERAND K BIT 2*10	
5W37	92	027	3	INCREMENT OPERAND K BIT 2*11	
5W37	93	028	27	INCREMENT OPERAND K BIT 2*12	
5W37	94	028	3	INCREMENT OPERAND K BIT 2*13	
5W37	95	029	27	INCREMENT OPERAND K BIT 2*14	
5W37	96	029	3	INCREMENT OPERAND K BIT 2*15	
5W37	97	030	3		
5W37	98	030	25		
5W37	99				
5W37	900	022	27	INCREMENT OPERAND K BIT 2*0	
5W37	901	022	3	INCREMENT OPERAND K BIT 2*1	
5W37	902	023	27	INCREMENT OPERAND K BIT 2*2	
5W37	903	023	3	INCREMENT OPERAND K BIT 2*3	
5W37	904	024	27	INCREMENT OPERAND K BIT 2*4	
5W37	905	024	3	INCREMENT OPERAND K BIT 2*5	
5W37	906	025	27	INCREMENT OPERAND K BIT 2*6	
5W37	907	025	3	INCREMENT OPERAND K BIT 2*7	
5W37	908	026	27	INCREMENT OPERAND K BIT 2*8	

7W23	00		
7W23	90	L30	3
7W23	91	L30	25
7W23	92	L30	21
7W23	93	L31	3
7W23	94	L31	25
7W23	95	L31	21
7W23	96	L32	3
7W23	97	L32	25
7W23	98	L32	21
7W23	99		
7W23	900	L27	3
7W23	901	L27	25
7W23	902	L27	21
7W23	903	L28	3
7W23	904	L28	25
7W23	905	L28	21
7W23	906	L29	3
7W23	907	L29	25
7W23	908	L29	21

Small vertical text or markings along the right edge of the page, possibly a page number or reference code.

6W07 00				MULTIPLY FROM REGISTER				8W07 00			
6W07	90	H04	12					8W07	90	C09	3
6W07	91	H04	14					8W07	91	C09	21
6W07	92	H04	16					8W07	92	C09	25
6W07	93	H04	18					8W07	93	C10	3
6W07	94	H05	12	2*40	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	94	C10	21
6W07	95	H05	14	2*41	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	95	C10	25
6W07	96	H05	16	2*42	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	96	C11	3
6W07	97	H05	18	2*43	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	97	C11	21
6W07	98	H06	12	2*44	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	98	C11	25
6W07	99	H06	14	2*45	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	99	C12	3
6W07	900	H06	16	2*46	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	900	C12	21
6W07	901	H06	18	2*47	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	901	C12	25
6W07	902	F07	15	2*59	REGISTER TO MULTIPLY,	OPERAND 1(XK)		8W07	902	D12	25
6W07	903							8W07	903		
6W07	904							8W07	904		
6W07	905							8W07	905		
6W07	906							8W07	906		
6W07	907							8W07	907		
6W07	908							8W07	908		

6W08 00				MULTIPLY TO REGISTER				8W08 00			
6W08	90	F03	1	2*36	MULTIPLY TO REGISTER			8W08	90	B05	2
6W08	91	F03	5	2*37	MULTIPLY TO REGISTER			8W08	91	B05	26
6W08	92	F03	25	2*38	MULTIPLY TO REGISTER			8W08	92	B06	2
6W08	93	F03	27	2*39	MULTIPLY TO REGISTER			8W08	93	B06	26
6W08	94	F04	1	2*40	MULTIPLY TO REGISTER			8W08	94	B07	2
6W08	95	F04	5	2*41	MULTIPLY TO REGISTER			8W08	95	B07	26
6W08	96	F04	25	2*42	MULTIPLY TO REGISTER			8W08	96	B08	2
6W08	97	F04	27	2*43	MULTIPLY TO REGISTER			8W08	97	B08	26
6W08	98	F05	1	2*44	MULTIPLY TO REGISTER			8W08	98	C01	2
6W08	99	F05	5	2*45	MULTIPLY TO REGISTER			8W08	99	C01	26
6W08	900	F05	25	2*46	MULTIPLY TO REGISTER			8W08	900	C02	2
6W08	901	F05	27	2*47	MULTIPLY TO REGISTER			8W08	901	C02	26
6W08	902							8W08	902		
6W08	903							8W08	903		
6W08	904							8W08	904		
6W08	905							8W08	905		
6W08	906							8W08	906		
6W08	907							8W08	907		
6W08	908							8W08	908		

6W09	00	MULTIPLY FROM REGISTER				8W09	00
6W09	90	J04	12	2*36 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	90	E09 3
6W09	91	J04	14	2*37 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	91	E09 21
6W09	92	J04	16	2*38 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	92	E09 25
6W09	93	J04	18	2*39 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	93	E10 3
6W09	94	J05	12	2*40 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	94	E10 21
6W09	95	J05	14	2*41 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	95	E10 25
6W09	96	J05	16	2*42 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	96	E11 3
6W09	97	J05	18	2*43 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	97	E11 21
6W09	98	J06	12	2*44 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	98	E11 25
6W09	99	J06	14	2*45 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	99	E12 3
6W09	900	J06	16	2*46 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	900	E12 21
6W09	901	J06	18	2*47 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	901	E12 25
6W09	902	F06	15	2*59 REGISTER TO MULTIPLY, OPERAND (XJ)	8W09	902	F12 25
6W09	903				8W09	903	
6W09	904				8W09	904	
6W09	905				8W09	905	
6W09	906				8W09	906	
6W09	907				8W09	907	
6W09	908				8W09	908	

6W10	00	MULTIPLY TO REGISTER				7W10	00
6W10	90	E01	1	2*0 MULTIPLY TO REGISTER	7W10	90	D34 2
6W10	91	E01	5	2*1 MULTIPLY TO REGISTER	7W10	91	D34 26
6W10	92	E01	25	2*2 MULTIPLY TO REGISTER	7W10	92	D35 2
6W10	93	E01	27	2*3 MULTIPLY TO REGISTER	7W10	93	D35 26
6W10	94	E02	1	2*4 MULTIPLY TO REGISTER	7W10	94	D36 2
6W10	95	E02	5	2*5 MULTIPLY TO REGISTER	7W10	95	D36 26
6W10	96	E02	25	2*6 MULTIPLY TO REGISTER	7W10	96	D37 2
6W10	97	E02	27	2*7 MULTIPLY TO REGISTER	7W10	97	D37 26
6W10	98	E03	1	2*8 MULTIPLY TO REGISTER	7W10	98	D38 2
6W10	99	E03	5	2*9 MULTIPLY TO REGISTER	7W10	99	D38 26
6W10	900	E03	25	2*10 MULTIPLY TO REGISTER	7W10	900	D39 2
6W10	901	E03	27	2*11 MULTIPLY TO REGISTER	7W10	901	D39 26
6W10	902	E04	1	2*12 MULTIPLY TO REGISTER	7W10	902	D40 2
6W10	903	E04	5	2*13 MULTIPLY TO REGISTER	7W10	903	D40 26
6W10	904	E04	25	2*14 MULTIPLY TO REGISTER	7W10	904	D41 2
6W10	905	E04	27	2*15 MULTIPLY TO REGISTER	7W10	905	D41 26
6W10	906	E05	1	2*16 MULTIPLY TO REGISTER	7W10	906	D42 2
6W10	907	E05	5	2*17 MULTIPLY TO REGISTER	7W10	907	D42 26
6W10	908				7W10	908	

6W11	00	MULTIPLY TO REGISTER					7W11	00	
6W11	90	E05	25	2*18	MULTIPLY TO REGISTER	7W11	90	E34	2
6W11	91	E05	27	2*19	MULTIPLY TO REGISTER	7W11	91	E34	26
6W11	92	E06	1	2*20	MULTIPLY TO REGISTER	7W11	92	E35	2
6W11	93	E06	5	2*21	MULTIPLY TO REGISTER	7W11	93	E35	26
6W11	94	E06	25	2*22	MULTIPLY TO REGISTER	7W11	94	E36	2
6W11	95	E06	27	2*23	MULTIPLY TO REGISTER	7W11	95	E36	26
6W11	96	E07	1	2*24	MULTIPLY TO REGISTER	7W11	96	E37	2
6W11	97	E07	5	2*25	MULTIPLY TO REGISTER	7W11	97	E37	26
6W11	98	E07	25	2*26	MULTIPLY TO REGISTER	7W11	98	E38	2
6W11	99	E07	27	2*27	MULTIPLY TO REGISTER	7W11	99	E38	26
6W11	900	F01	1	2*28	MULTIPLY TO REGISTER	7W11	900	E39	2
6W11	901	F01	5	2*29	MULTIPLY TO REGISTER	7W11	901	E39	26
6W11	902	F01	25	2*30	MULTIPLY TO REGISTER	7W11	902	E40	2
6W11	903	F01	27	2*31	MULTIPLY TO REGISTER	7W11	903	E40	26
6W11	904	F02	1	2*32	MULTIPLY TO REGISTER	7W11	904	E41	2
6W11	905	F02	5	2*33	MULTIPLY TO REGISTER	7W11	905	E41	26
6W11	906	F02	25	2*34	MULTIPLY TO REGISTER	7W11	906	E42	2
6W11	907	F02	27	2*35	MULTIPLY TO REGISTER	7W11	907	E42	26
6W11	908					7W11	908		

6W12	00	MULTIPLY FROM REGISTER					7W12	00	
6W12	90	G01	12	2*0	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	90	G29	3
6W12	91	G01	14	2*1	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	91	G29	25
6W12	92	G01	16	2*2	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	92	G29	21
6W12	93	G01	18	2*3	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	93	G30	3
6W12	94	G02	12	2*4	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	94	G30	25
6W12	95	G02	14	2*5	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	95	G30	21
6W12	96	G02	16	2*6	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	96	G31	3
6W12	97	G02	18	2*7	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	97	G31	25
6W12	98	G03	12	2*8	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	98	G31	21
6W12	99	G03	14	2*9	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	99	G32	3
6W12	900	G03	16	2*10	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	900	G32	25
6W12	901	G03	18	2*11	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	901	G32	21
6W12	902	G04	12	2*12	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	902	G33	3
6W12	903	G04	14	2*13	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	903	G33	25
6W12	904	G04	16	2*14	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	904	G33	21
6W12	905	G04	18	2*15	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	905	G34	3
6W12	906	G05	12	2*16	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	906	G34	25
6W12	907	G05	14	2*17	REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	907	G34	21
6W12	908					7W12	908		

6W13	00	MULTIPLY FROM REGISTER						7W13	00	
6W13	90	I05	16	2*18	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	90	J29	3
6W13	91	I05	18	2*19	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	91	J29	25
6W13	92	I06	12	2*20	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	92	J29	21
6W13	93	I06	14	2*21	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	93	J30	3
6W13	94	I06	16	2*22	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	94	J30	25
6W13	95	I06	18	2*23	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	95	J30	21
6W13	96	J01	12	2*24	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	96	J31	3
6W13	97	J01	14	2*25	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	97	J31	25
6W13	98	J01	16	2*26	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	98	J31	21
6W13	99	J01	18	2*27	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	99	J32	3
6W13	900	J02	12	2*28	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	900	J32	25
6W13	901	J02	14	2*29	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	901	J32	21
6W13	902	J02	16	2*30	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	902	J33	3
6W13	903	J02	18	2*31	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	903	J33	25
6W13	904	J03	12	2*32	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	904	J33	21
6W13	905	J03	14	2*33	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	905	J34	3
6W13	906	J03	16	2*34	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	906	J34	25
6W13	907	J03	18	2*35	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W13	907	J34	21
6W13	908						7W13	908		

6W14	00	MULTIPLY FROM REGISTER						7W14	00	
6W14	90	I01	12	2*0	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	90	I29	3
6W14	91	I01	14	2*1	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	91	I29	25
6W14	92	I01	16	2*2	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	92	I29	21
6W14	93	I01	18	2*3	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	93	I30	3
6W14	94	I02	12	2*4	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	94	I30	25
6W14	95	I02	14	2*5	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	95	I30	21
6W14	96	I02	16	2*6	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	96	I31	3
6W14	97	I02	18	2*7	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	97	I31	25
6W14	98	I03	12	2*8	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	98	I31	21
6W14	99	I03	14	2*9	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	99	I32	3
6W14	900	I03	16	2*10	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	900	I32	25
6W14	901	I03	18	2*11	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	901	I32	21
6W14	902	I04	12	2*12	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	902	I33	3
6W14	903	I04	14	2*13	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	903	I33	25
6W14	904	I04	16	2*14	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	904	I33	21
6W14	905	I04	18	2*15	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	905	I34	3
6W14	906	I05	12	2*16	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	906	I34	25
6W14	907	I05	14	2*17	REGISTER TO MULTIPLY,	OPERAND 2(XJ)	7W14	907	I34	21
6W14	908						7W14	908		

6W15	00	MULTIPLY FROM REGISTER						7W15	00	
6W15	90	G05	16	2*18	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	90	H29	3
6W15	91	G05	18	2*19	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	91	H29	25
6W15	92	G06	12	2*20	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	92	H29	21
6W15	93	G06	14	2*21	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	93	H30	3
6W15	94	G06	16	2*22	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	94	H30	25
6W15	95	G06	18	2*23	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	95	H30	21
6W15	96	H01	12	2*24	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	96	H31	3
6W15	97	H01	14	2*25	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	97	H31	25
6W15	98	H01	16	2*26	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	98	H31	21
6W15	99	H01	18	2*27	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	99	H32	3
6W15	900	H02	12	2*28	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	900	H32	25
6W15	901	H02	14	2*29	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	901	H32	21
6W15	902	H02	16	2*30	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	902	H33	3
6W15	903	H02	18	2*31	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	903	H33	25
6W15	904	H03	12	2*32	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	904	H33	21
6W15	905	H03	14	2*33	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	905	H34	3
6W15	906	H03	16	2*34	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	906	H34	25
6W15	907	H03	18	2*35	REGISTER TO MULTIPLY, OPERAND 1(XK)		7W15	907	H34	21
6W15	908						7W15	908		

6W16	00	MULTIPLY FROM CONTROL						5W14	00	
6W16	90	I09	23	GO UNIT NO. 1			5W14	90	J40	4
6W16	91	I09	3	ROUND NO. 1			5W14	91	E40	4
6W16	92	I08	23	DOUBLE NO. 1			5W14	92	E40	6
6W16	93	L08	26	XMIT RESULT NO. 1			5W14	93	J40	23
6W16	94	L08	2	XMIT RESULT NO. 2			5W14	94	J40	25
6W16	95	H09	1	REQUEST RELEASE			5W14	95	J25	19
6W16	96	I22	12	CLOCK			5W14	96	J39	27
6W16	97			CLEAR			5W14	97		
6W16	98						5W14	98		
6W16	99						5W14	99		
6W16	900	J09	23	GO UNIT NO. 2			5W14	900	J40	6
6W16	901	J09	3	ROUND			5W14	901	E40	6
6W16	902	J08	23	DOUBLE			5W14	902	E40	10
6W16	903			XMIT RESULT			5W14	903		
6W16	904			XMIT RESULT			5W14	904		
6W16	905	K09	1	REQUEST RELEASE			5W14	905	K21	19
6W16	906						5W14	906		
6W16	907						5W14	907		
6W16	908						5W14	908		

6W20	00	MULTIPLY FROM DIVIDE				2W24	00
6W20	90	K01	11	2*0	DIVIDE TO MULTIPLY RESULT	2W24	90 K01 1
6W20	91	K01	10	2*1	DIVIDE TO MULTIPLY RESULT	2W24	91 K01 6
6W20	92	K02	11	2*2	DIVIDE TO MULTIPLY RESULT	2W24	92 K01 23
6W20	93	K02	10	2*3	DIVIDE TO MULTIPLY RESULT	2W24	93 K01 28
6W20	94	K03	11	2*4	DIVIDE TO MULTIPLY RESULT	2W24	94 K02 1
6W20	95	K03	10	2*5	DIVIDE TO MULTIPLY RESULT	2W24	95 K02 6
6W20	96	K04	11	2*6	DIVIDE TO MULTIPLY RESULT	2W24	96 K02 23
6W20	97	K04	10	2*7	DIVIDE TO MULTIPLY RESULT	2W24	97 K02 28
6W20	98	K05	11	2*8	DIVIDE TO MULTIPLY RESULT	2W24	98 K03 1
6W20	99	K05	10	2*9	DIVIDE TO MULTIPLY RESULT	2W24	99 K03 6
6W20	900	K06	11	2*10	DIVIDE TO MULTIPLY RESULT	2W24	900 K03 23
6W20	901	K06	10	2*11	DIVIDE TO MULTIPLY RESULT	2W24	901 K03 28
6W20	902	K07	11	2*12	DIVIDE TO MULTIPLY RESULT	2W24	902 K04 1
6W20	903	K07	10	2*13	DIVIDE TO MULTIPLY RESULT	2W24	903 K04 6
6W20	904	L01	11	2*14	DIVIDE TO MULTIPLY RESULT	2W24	904 K04 23
6W20	905	L01	10	2*15	DIVIDE TO MULTIPLY RESULT	2W24	905 K04 28
6W20	906	L02	11	2*16	DIVIDE TO MULTIPLY RESULT	2W24	906 K05 1
6W20	907	L02	10	2*17	DIVIDE TO MULTIPLY RESULT	2W24	907 K05 6
6W20	908					2W24	908

6W21	00	MULTIPLY FROM DIVIDE				2W25	00
6W21	90	L03	11	2*18	DIVIDE TO MULTIPLY RESULT	2W25	90 K05 23
6W21	91	L03	10	2*19	DIVIDE TO MULTIPLY RESULT	2W25	91 K05 28
6W21	92	L04	11	2*20	DIVIDE TO MULTIPLY RESULT	2W25	92 K06 1
6W21	93	L04	10	2*21	DIVIDE TO MULTIPLY RESULT	2W25	93 K06 6
6W21	94	L05	11	2*22	DIVIDE TO MULTIPLY RESULT	2W25	94 K06 23
6W21	95	L05	10	2*23	DIVIDE TO MULTIPLY RESULT	2W25	95 K06 28
6W21	96	L06	11	2*24	DIVIDE TO MULTIPLY RESULT	2W25	96 K07 1
6W21	97	L06	10	2*25	DIVIDE TO MULTIPLY RESULT	2W25	97 K07 6
6W21	98	L07	11	2*26	DIVIDE TO MULTIPLY RESULT	2W25	98 K07 23
6W21	99	L07	10	2*27	DIVIDE TO MULTIPLY RESULT	2W25	99 K07 28
6W21	900	M01	11	2*28	DIVIDE TO MULTIPLY RESULT	2W25	900 K08 1
6W21	901	M01	10	2*29	DIVIDE TO MULTIPLY RESULT	2W25	901 K08 6
6W21	902	M02	11	2*30	DIVIDE TO MULTIPLY RESULT	2W25	902 K08 23
6W21	903	M02	10	2*31	DIVIDE TO MULTIPLY RESULT	2W25	903 K08 28
6W21	904	M03	11	2*32	DIVIDE TO MULTIPLY RESULT	2W25	904 K09 1
6W21	905	M03	10	2*33	DIVIDE TO MULTIPLY RESULT	2W25	905 K09 6
6W21	906	M04	11	2*34	DIVIDE TO MULTIPLY RESULT	2W25	906 K09 23
6W21	907	M04	10	2*35	DIVIDE TO MULTIPLY RESULT	2W25	907 K09 28
6W21	908	F07	1	REDUCE MULT.	1	2W25	908 Q11 7

6W22	00	MULTIPLY FROM DIVIDE			
6W22	90	M05	11	2*36	DIVIDE TO MULTIPLY RESULT
6W22	91	M05	10	2*37	DIVIDE TO MULTIPLY RESULT
6W22	92	M06	11	2*38	DIVIDE TO MULTIPLY RESULT
6W22	93	M06	10	2*39	DIVIDE TO MULTIPLY RESULT
6W22	94	M07	11	2*40	DIVIDE TO MULTIPLY RESULT
6W22	95	M07	10	2*41	DIVIDE TO MULTIPLY RESULT
6W22	96	N01	11	2*42	DIVIDE TO MULTIPLY RESULT
6W22	97	N01	10	2*43	DIVIDE TO MULTIPLY RESULT
6W22	98	N02	11	2*44	DIVIDE TO MULTIPLY RESULT
6W22	99	N02	10	2*45	DIVIDE TO MULTIPLY RESULT
6W22	900	N03	11	2*46	DIVIDE TO MULTIPLY RESULT
6W22	901	N03	10	2*47	DIVIDE TO MULTIPLY RESULT
6W22	902	F06	1	REDUCE MULT. 2	
6W22	903				
6W22	904				
6W22	905	M10	1	GO MULT 1	
6W22	906	K10	1		
6W22	907	N07	12	D P MULT 1	
6W22	908	N07	19	D P MULT 2	

2W26	00		
2W26	90	K10	1
2W26	91	K10	6
2W26	92	K10	23
2W26	93	K10	28
2W26	94	K11	1
2W26	95	K11	6
2W26	96	K11	23
2W26	97	K11	28
2W26	98	K12	1
2W26	99	K12	6
2W26	900	K12	23
2W26	901	K12	28
2W26	902	Q12	7
2W26	903		
2W26	904		
2W26	905	M11	14
2W26	906	M11	16
2W26	907	Q11	5
2W26	908	Q12	5

6W23	00	MULTIPLY TO DIVIDE			
6W23	90	K01	1	2*0	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	91	K01	3	2*1	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	92	K01	2	2*2	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	93	K02	1	2*3	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	94	K02	3	2*4	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	95	K02	2	2*5	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	96	K03	1	2*6	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	97	K03	3	2*7	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	98	K03	2	2*8	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	99	K04	1	2*9	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	900	K04	3	2*10	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	901	K04	2	2*11	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	902	K05	1	2*12	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	903	K05	3	2*13	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	904	K05	2	2*14	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	905	K06	1	2*15	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	906	K06	3	2*16	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	907	K06	2	2*17	MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	908				

2W27	00		
2W27	90	R06	12
2W27	91	R06	14
2W27	92	R06	16
2W27	93	R06	18
2W27	94	R07	12
2W27	95	R07	14
2W27	96	R07	16
2W27	97	R07	18
2W27	98	R08	12
2W27	99	R08	14
2W27	900	R08	16
2W27	901	R08	18
2W27	902	R09	12
2W27	903	R09	14
2W27	904	R09	16
2W27	905	R09	18
2W27	906	R10	12
2W27	907	R10	14
2W27	908		

6W24	00	MULTIPLY TO DIVIDE						2W28	00		
6W24	90	K07	1	2*18	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	90	R10	16
6W24	91	K07	3	2*19	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	91	R10	18
6W24	92	K07	2	2*20	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	92	P06	12
6W24	93	L01	1	2*21	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	93	P06	14
6W24	94	L01	3	2*22	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	94	P06	16
6W24	95	L01	2	2*23	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	95	P06	18
6W24	96	L02	1	2*24	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	96	P07	12
6W24	97	L02	3	2*25	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	97	P07	14
6W24	98	L02	2	2*26	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	98	P07	16
6W24	99	L03	1	2*27	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	99	P07	18
6W24	900	L03	3	2*28	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	900	P08	12
6W24	901	L03	2	2*29	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	901	P08	14
6W24	902	L04	1	2*30	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	902	P08	16
6W24	903	L04	3	2*31	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	903	P08	18
6W24	904	L04	2	2*32	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	904	P09	12
6W24	905	L05	1	2*33	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	905	P09	14
6W24	906	L05	3	2*34	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	906	P09	16
6W24	907	L05	2	2*35	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W28	907	P09	18
6W24	908							2W28	908		

6W25	00	MULTIPLY TO DIVIDE						2W29	00		
6W25	90	L06	1	2*36	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	90	P10	12
6W25	91	L06	3	2*37	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	91	P10	14
6W25	92	L06	2	2*38	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	92	P10	16
6W25	93	L07	1	2*39	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	93	P10	18
6W25	94	L07	3	2*40	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	94	M06	12
6W25	95	L07	2	2*41	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	95	M06	14
6W25	96	M01	1	2*42	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	96	M06	16
6W25	97	M01	3	2*43	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	97	M06	18
6W25	98	M01	2	2*44	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	98	M07	12
6W25	99	M02	1	2*45	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	99	M07	14
6W25	900	M02	3	2*46	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	900	M07	16
6W25	901	M02	2	2*47	MULTIPLY TO DIVIDE	(OPERAND)	X**K	2W29	901	M07	18
6W25	902			2*48	SIGN			2W29	902		
6W25	903	M03	1	2*0	MULTIPLY TO DIVIDE	(OPERAND)	X**J	2W29	903	Q06	12
6W25	904	M03	3	2*1	MULTIPLY TO DIVIDE	(OPERAND)	X**J	2W29	904	Q06	14
6W25	905	M03	2	2*2	MULTIPLY TO DIVIDE	(OPERAND)	X**J	2W29	905	Q06	16
6W25	906	M04	1	2*3	MULTIPLY TO DIVIDE	(OPERAND)	X**J	2W29	906	Q06	18
6W25	907	M04	3	2*4	MULTIPLY TO DIVIDE	(OPERAND)	X**J	2W29	907	Q07	12
6W25	908							2W29	908		

6W26	00	MULTIPLY TO DIVIDE					2W30	00
6W26	90	M04	2	2*5	MULTIPLY TO DIVIDE OPERAND X**J	2W30	90 Q07 14	
6W26	91	M05	1			2W30	91 Q07 16	
6W26	92	M05	3			2W30	92 Q07 18	
6W26	93	M05	2			2W30	93 Q08 12	
6W26	94	M06	1	2*9	MULTIPLY TO DIVIDE OPERAND X**J	2W30	94 Q08 14	
6W26	95	M06	3	2*10	MULTIPLY TO DIVIDE OPERAND X**J	2W30	95 Q08 16	
6W26	96	M06	2	2*11	MULTIPLY TO DIVIDE OPERAND X**J	2W30	96 Q08 18	
6W26	97	M07	1	2*12	MULTIPLY TO DIVIDE OPERAND X**J	2W30	97 Q09 12	
6W26	98	M07	3	2*13	MULTIPLY TO DIVIDE OPERAND X**J	2W30	98 Q09 14	
6W26	99	M07	2	2*14	MULTIPLY TO DIVIDE OPERAND X**J	2W30	99 Q09 16	
6W26	900	N01	1	2*15	MULTIPLY TO DIVIDE OPERAND X**J	2W30	900 Q09 18	
6W26	901	N01	3	2*16	MULTIPLY TO DIVIDE OPERAND X**J	2W30	901 Q10 12	
6W26	902	N01	2	2*17	MULTIPLY TO DIVIDE OPERAND X**J	2W30	902 Q10 14	
6W26	903	N02	1	2*18	MULTIPLY TO DIVIDE OPERAND X**J	2W30	903 Q10 16	
6W26	904	N02	3	2*19	MULTIPLY TO DIVIDE OPERAND X**J	2W30	904 Q10 18	
6W26	905	N02	2	2*20	MULTIPLY TO DIVIDE OPERAND X**J	2W30	905 N06 12	
6W26	906	N03	1	2*21	MULTIPLY TO DIVIDE OPERAND X**J	2W30	906 N06 14	
6W26	907	N03	3	2*22	MULTIPLY TO DIVIDE OPERAND X**J	2W30	907 N06 16	
6W26	908					2W30	908	

6W27	00	MULTIPLY TO DIVIDE					2W31	00
6W27	90	N03	2	2*23	MULTIPLY TO DIVIDE OPERAND X**J	2W31	90 N06 18	
6W27	91	N04	8	2*24	MULTIPLY TO DIVIDE OPERAND X**J	2W31	91 N07 12	
6W27	92	N04	6	2*25	MULTIPLY TO DIVIDE OPERAND X**J	2W31	92 N07 14	
6W27	93	N04	4	2*26	MULTIPLY TO DIVIDE OPERAND X**J	2W31	93 N07 16	
6W27	94	N04	27	2*27	MULTIPLY TO DIVIDE OPERAND X**J	2W31	94 N07 18	
6W27	95	N04	25	2*28	MULTIPLY TO DIVIDE OPERAND X**J	2W31	95 N08 12	
6W27	96	N04	23	2*29	MULTIPLY TO DIVIDE OPERAND X**J	2W31	96 N08 14	
6W27	97	N05	8	2*30	MULTIPLY TO DIVIDE OPERAND X**J	2W31	97 N08 16	
6W27	98	N05	6	2*31	MULTIPLY TO DIVIDE OPERAND X**J	2W31	98 N08 18	
6W27	99	N05	4	2*32	MULTIPLY TO DIVIDE OPERAND X**J	2W31	99 N09 12	
6W27	900	N05	27	2*33	MULTIPLY TO DIVIDE OPERAND X**J	2W31	900 N09 14	
6W27	901	N05	25	2*34	MULTIPLY TO DIVIDE OPERAND X**J	2W31	901 N09 16	
6W27	902	N05	23	2*35	MULTIPLY TO DIVIDE OPERAND X**J	2W31	902 N09 18	
6W27	903	N06	8	2*36	MULTIPLY TO DIVIDE OPERAND X**J	2W31	903 N10 12	
6W27	904	N06	6	2*37	MULTIPLY TO DIVIDE OPERAND X**J	2W31	904 N10 14	
6W27	905	N06	4	2*38	MULTIPLY TO DIVIDE OPERAND X**J	2W31	905 N10 16	
6W27	906	N06	27	2*39	MULTIPLY TO DIVIDE OPERAND X**J	2W31	906 N10 18	
6W27	907	N06	25	2*40	MULTIPLY TO DIVIDE OPERAND X**J	2W31	907 L06 12	
6W27	908					2W31	908	

6W28	00	MULTIPLY TO DIVIDE			2W32	00
6W28	90	N06	23	2*41 MULTIPLY TO DIVIDE X**J	2W32	90 L06 14
6W28	91	N07	8	2*42 MULTIPLY TO DIVIDE X**J	2W32	91 L06 16
6W28	92	N07	6	2*43 MULTIPLY TO DIVIDE X**J	2W32	92 L06 18
6W28	93	N07	4	2*44 MULTIPLY TO DIVIDE X**J	2W32	93 L07 12
6W28	94	N07	27	2*45 MULTIPLY TO DIVIDE X**J	2W32	94 L07 14
6W28	95	N07	25	2*46 MULTIPLY TO DIVIDE X**J	2W32	95 L07 16
6W28	96	N07	23	2*47 MULTIPLY TO DIVIDE X**J	2W32	96 L07 18
6W28	97			2*48 MULTIPLY TO DIVIDE X**J	2W32	97
6W28	98	I07	23	ERROR MULT. 1	2W32	98 J03 1
6W28	99	J07	23	ERROR MULT. 2	2W32	99 J03 23
6W28	900	I08	27	ERROR MULT. 1	2W32	900 J03 6
6W28	901	J08	27	ERROR MULT. 2	2W32	901 J03 28
6W28	902				2W32	902
6W28	903				2W32	903
6W28	904				2W32	904
6W28	905				2W32	905
6W28	906				2W32	906
6W28	907				2W32	907
6W28	908				2W32	908

7W05	00	LOWER REGISTER FROM READ DISTRIBUTOR				3W12	00
7W05	90	A41	2	2*8	MEMORY TO REGISTER	3W12	90 135 28
7W05	91	A41	26	2*9	MEMORY TO REGISTER	3W12	91 136 28
7W05	92	A42	2	2*10	MEMORY TO REGISTER	3W12	92 138 28
7W05	93	A42	26	2*11	MEMORY TO REGISTER	3W12	93 139 28
7W05	94	B37	2	2*12	MEMORY TO REGISTER	3W12	94 140 28
7W05	95	B37	26	2*13	MEMORY TO REGISTER	3W12	95 141 28
7W05	96	B38	2	2*14	MEMORY TO REGISTER	3W12	96 142 28
7W05	97					3W12	97
7W05	98					3W12	98
7W05	99					3W12	99
7W05	900	A37	2	2*0	MEMORY TO REGISTER	3W12	900 125 28
7W05	901	A37	26	2*1	MEMORY TO REGISTER	3W12	901 126 28
7W05	902	A38	2	2*2	MEMORY TO REGISTER	3W12	902 127 28
7W05	903	A38	26	2*3	MEMORY TO REGISTER	3W12	903 128 28
7W05	904	A39	2	2*4	MEMORY TO REGISTER	3W12	904 129 28
7W05	905	A39	26	2*5	MEMORY TO REGISTER	3W12	905 132 28
7W05	906	A40	2	2*6	MEMORY TO REGISTER	3W12	906 133 28
7W05	907	A40	26	2*7	MEMORY TO REGISTER	3W12	907 134 28
7W05	908					3W12	908

7W06	00	LOWER REGISTER FROM READ DISTRIBUTOR				4W18	00
7W06	90	B42	26	2*23	MEMORY TO REGISTER	4W18	90 111 28
7W06	91	C37	2	2*24	MEMORY TO REGISTER	4W18	91 112 28
7W06	92	C37	26	2*25	MEMORY TO REGISTER	4W18	92 114 28
7W06	93	C38	2	2*26	MEMORY TO REGISTER	4W18	93 115 28
7W06	94	C38	26	2*27	MEMORY TO REGISTER	4W18	94 116 28
7W06	95	C39	2	2*28	MEMORY TO REGISTER	4W18	95 117 28
7W06	96	C39	26	2*29	MEMORY TO REGISTER	4W18	96 118 28
7W06	97					4W18	97
7W06	98					4W18	98
7W06	99					4W18	99
7W06	900	B38	26	2*15	MEMORY TO REGISTER	4W18	900 101 28
7W06	901	B39	2	2*16	MEMORY TO REGISTER	4W18	901 102 28
7W06	902	B39	26	2*17	MEMORY TO REGISTER	4W18	902 103 28
7W06	903	B40	2	2*18	MEMORY TO REGISTER	4W18	903 104 28
7W06	904	B40	26	2*19	MEMORY TO REGISTER	4W18	904 105 28
7W06	905	B41	2	2*20	MEMORY TO REGISTER	4W18	905 108 28
7W06	906	B41	26	2*21	MEMORY TO REGISTER	4W18	906 109 28
7W06	907	B42	2	2*22	MEMORY TO REGISTER	4W18	907 110 28
7W06	908					4W18	908

7W07	00	LOWER REGISTER FROM READ DISTRIBUTOR				9W12	00
7W07	90					9W12	90
7W07	91					9W12	91
7W07	92					9W12	92
7W07	93					9W12	93
7W07	94					9W12	94
7W07	95					9W12	95
7W07	96					9W12	96
7W07	97					9W12	97
7W07	98					9W12	98
7W07	99					9W12	99
7W07	900	C40	2	2*30	MEMORY TO REGISTER	9W12	900 125 28
7W07	901	C40	26	2*31	MEMORY TO REGISTER	9W12	901 126 28
7W07	902	C41	2	2*32	MEMORY TO REGISTER	9W12	902 127 28
7W07	903	C41	26	2*33	MEMORY TO REGISTER	9W12	903 128 28
7W07	904	C42	2	2*34	MEMORY TO REGISTER	9W12	904 129 28
7W07	905	C42	26	2*35	MEMORY TO REGISTER	9W12	905 132 28
7W07	906					9W12	906
7W07	907					9W12	907
7W07	908					9W12	908

7W08	00	LOWER REGISTER TO MEMORY DISTRIBUTION				2W09	00
7W08	90	F28	21	DATA	2*8 REG. TO MEM.	2W09	90 A09 10
7W08	91	F29	3	DATA	2*9 REG. TO MEM.	2W09	91 A09 21
7W08	92	F29	25	DATA	2*10 REG. TO MEM.	2W09	92 A09 24
7W08	93	F29	21	DATA	2*11 REG. TO MEM.	2W09	93 A09 26
7W08	94	F30	3	DATA	2*12 REG. TO MEM.	2W09	94 A10 5
7W08	95	F30	25	DATA	2*13 REG. TO MEM.	2W09	95 A10 7
7W08	96	F30	21	DATA	2*14 REG. TO MEM.	2W09	96 A10 10
7W08	97	F31	3	DATA	2*15 REG. TO MEM.	2W09	97 A10 21
7W08	98	F31	25	DATA	2*16 REG. TO MEM.	2W09	98 A10 24
7W08	99	F31	21	DATA	2*17 REG. TO MEM.	2W09	99 A10 26
7W08	900	F26	3	DATA	2*0 REG. TO MEM.	2W09	900 A08 5
7W08	901	F26	25	DATA	2*1 REG. TO MEM.	2W09	901 A08 7
7W08	902	F26	21	DATA	2*2 REG. TO MEM.	2W09	902 A08 10
7W08	903	F27	3	DATA	2*3 REG. TO MEM.	2W09	903 A08 21
7W08	904	F27	25	DATA	2*4 REG. TO MEM.	2W09	904 A08 24
7W08	905	F27	21	DATA	2*5 REG. TO MEM.	2W09	905 A08 26
7W08	906	F28	3	DATA	2*6 REG. TO MEM.	2W09	906 A09 5
7W08	907	F28	25	DATA	2*7 REG. TO MEM.	2W09	907 A09 7
7W08	908					2W09	908

7W09	00	LOWER REGISTER TO MEMORY DISTRIBUTION				2W10	00
7W09	90	F34	21	DATA 2*26 REG. TO MEM.	2W10	90	A12 10
7W09	91	F35	3	DATA 2*27 REG. TO MEM.	2W10	91	A12 21
7W09	92	F35	25	DATA 2*28 REG. TO MEM.	2W10	92	A12 24
7W09	93	F35	21	DATA 2*29 REG. TO MEM.	2W10	93	A12 26
7W09	94	E26	3	DATA 2*30 REG. TO MEM.	2W10	94	A13 5
7W09	95	E26	25	DATA 2*31 REG. TO MEM.	2W10	95	A13 7
7W09	96	E26	21	DATA 2*32 REG. TO MEM.	2W10	96	A13 10
7W09	97	E27	3	DATA 2*33 REG. TO MEM.	2W10	97	A13 21
7W09	98	E27	25	DATA 2*34 REG. TO MEM.	2W10	98	A13 24
7W09	99	E27	21	DATA 2*35 REG. TO MEM.	2W10	99	A13 26
7W09	900	F32	3	DATA 2*18 REG. TO MEM.	2W10	900	A11 5
7W09	901	F32	25	DATA 2*19 REG. TO MEM.	2W10	901	A11 7
7W09	902	F32	21	DATA 2*20 REG. TO MEM.	2W10	902	A11 10
7W09	903	F33	3	DATA 2*21 REG. TO MEM.	2W10	903	A11 21
7W09	904	F33	25	DATA 2*22 REG. TO MEM.	2W10	904	A11 24
7W09	905	F33	21	DATA 2*23 REG. TO MEM.	2W10	905	A11 26
7W09	906	F34	3	DATA 2*24 REG. TO MEM.	2W10	906	A12 5
7W09	907	F34	25	DATA 2*25 REG. TO MEM.	2W10	907	A12 7
7W09	908				2W10	908	

7W10	00	REGISTER FROM MULTIPLY				6W10	00
7W10	90	D34	2	2*0 MULTIPLY TO REGISTER	6W10	90	E01 1
7W10	91	D34	26	2*1 MULTIPLY TO REGISTER	6W10	91	E01 5
7W10	92	D35	2	2*2 MULTIPLY TO REGISTER	6W10	92	E01 25
7W10	93	D35	26	2*3 MULTIPLY TO REGISTER	6W10	93	E01 27
7W10	94	D36	2	2*4 MULTIPLY TO REGISTER	6W10	94	E02 1
7W10	95	D36	26	2*5 MULTIPLY TO REGISTER	6W10	95	E02 5
7W10	96	D37	2	2*6 MULTIPLY TO REGISTER	6W10	96	E02 25
7W10	97	D37	26	2*7 MULTIPLY TO REGISTER	6W10	97	E02 27
7W10	98	D38	2	2*8 MULTIPLY TO REGISTER	6W10	98	E03 1
7W10	99	D38	26	2*9 MULTIPLY TO REGISTER	6W10	99	E03 5
7W10	900	D39	2	2*10 MULTIPLY TO REGISTER	6W10	900	E03 25
7W10	901	D39	26	2*11 MULTIPLY TO REGISTER	6W10	901	E03 27
7W10	902	D40	2	2*12 MULTIPLY TO REGISTER	6W10	902	E04 1
7W10	903	D40	26	2*13 MULTIPLY TO REGISTER	6W10	903	E04 5
7W10	904	D41	2	2*14 MULTIPLY TO REGISTER	6W10	904	E04 25
7W10	905	D41	26	2*15 MULTIPLY TO REGISTER	6W10	905	E04 27
7W10	906	D42	2	2*16 MULTIPLY TO REGISTER	6W10	906	E05 1
7W10	907	D42	26	2*17 MULTIPLY TO REGISTER	6W10	907	E05 5
7W10	908				6W10	908	

7W11	00	REGISTER FROM MULTIPLY				6W11	00
7W11	90	E34	2	2*18	MULTIPLY TO REGISTER	6W11	90 E05 25
7W11	91	E34	26	2*19	MULTIPLY TO REGISTER	6W11	91 E05 27
7W11	92	E35	2	2*20	MULTIPLY TO REGISTER	6W11	92 E06 1
7W11	93	E35	26	2*21	MULTIPLY TO REGISTER	6W11	93 E06 5
7W11	94	E36	2	2*22	MULTIPLY TO REGISTER	6W11	94 E06 25
7W11	95	E36	26	2*23	MULTIPLY TO REGISTER	6W11	95 E06 27
7W11	96	E37	2	2*24	MULTIPLY TO REGISTER	6W11	96 E07 1
7W11	97	E37	26	2*25	MULTIPLY TO REGISTER	6W11	97 E07 5
7W11	98	E38	2	2*26	MULTIPLY TO REGISTER	6W11	98 E07 25
7W11	99	E38	26	2*27	MULTIPLY TO REGISTER	6W11	99 E07 27
7W11	900	E39	2	2*28	MULTIPLY TO REGISTER	6W11	900 F01 1
7W11	901	E39	26	2*29	MULTIPLY TO REGISTER	6W11	901 F01 5
7W11	902	E40	2	2*30	MULTIPLY TO REGISTER	6W11	902 F01 25
7W11	903	E40	26	2*31	MULTIPLY TO REGISTER	6W11	903 F01 27
7W11	904	E41	2	2*32	MULTIPLY TO REGISTER	6W11	904 F02 1
7W11	905	E41	26	2*33	MULTIPLY TO REGISTER	6W11	905 F02 5
7W11	906	E42	2	2*34	MULTIPLY TO REGISTER	6W11	906 F02 25
7W11	907	E42	26	2*35	MULTIPLY TO REGISTER	6W11	907 F02 27
7W11	908					6W11	908

7W12	00	REGISTER TO MULTIPLY				6W12	00
7W12	90	G29	3	2*0	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	90 G01 12
7W12	91	G29	25	2*1	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	91 G01 14
7W12	92	G29	21	2*2	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	92 G01 16
7W12	93	G30	3	2*3	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	93 G01 18
7W12	94	G30	25	2*4	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	94 G02 12
7W12	95	G30	21	2*5	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	95 G02 14
7W12	96	G31	3	2*6	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	96 G02 16
7W12	97	G31	25	2*7	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	97 G02 18
7W12	98	G31	21	2*8	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	98 G03 12
7W12	99	G32	3	2*9	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	99 G03 14
7W12	900	G32	25	2*10	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	900 G03 16
7W12	901	G32	21	2*11	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	901 G03 18
7W12	902	G33	3	2*12	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	902 G04 12
7W12	903	G33	25	2*13	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	903 G04 14
7W12	904	G33	21	2*14	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	904 G04 16
7W12	905	G34	3	2*15	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	905 G04 18
7W12	906	G34	25	2*16	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	906 G05 12
7W12	907	G34	21	2*17	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	907 G05 14
7W12	908					6W12	908

7W13	00	REGISTER TO MULTIPLY				6W13	00		
7W13	90	J29	3	2*18	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	90	I05	16
7W13	91	J29	25	2*19	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	91	I05	18
7W13	92	J29	21	2*20	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	92	I06	12
7W13	93	J30	3	2*21	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	93	I06	14
7W13	94	J30	25	2*22	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	94	I06	16
7W13	95	J30	21	2*23	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	95	I06	18
7W13	96	J31	3	2*24	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	96	J01	12
7W13	97	J31	25	2*25	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	97	J01	14
7W13	98	J31	21	2*26	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	98	J01	16
7W13	99	J32	3	2*27	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	99	J01	18
7W13	900	J32	25	2*28	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	900	J02	12
7W13	901	J32	21	2*29	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	901	J02	14
7W13	902	J33	3	2*30	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	902	J02	16
7W13	903	J33	25	2*31	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	903	J02	18
7W13	904	J33	21	2*32	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	904	J03	12
7W13	905	J34	3	2*33	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	905	J03	14
7W13	906	J34	25	2*34	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	906	J03	16
7W13	907	J34	21	2*35	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	907	J03	18
7W13	908					6W13	908		

7W14	00	REGISTER TO MULTIPLY				6W14	00		
7W14	90	I29	3	2*0	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	90	I01	12
7W14	91	I29	25	2*1	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	91	I01	14
7W14	92	I29	21	2*2	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	92	I01	16
7W14	93	I30	3	2*3	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	93	I01	18
7W14	94	I30	25	2*4	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	94	I02	12
7W14	95	I30	21	2*5	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	95	I02	14
7W14	96	I31	3	2*6	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	96	I02	16
7W14	97	I31	25	2*7	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	97	I02	18
7W14	98	I31	21	2*8	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	98	I03	12
7W14	99	I32	3	2*9	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	99	I03	14
7W14	900	I32	25	2*10	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	900	I03	16
7W14	901	I32	21	2*11	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	901	I03	18
7W14	902	I33	3	2*12	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	902	I04	12
7W14	903	I33	25	2*13	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	903	I04	14
7W14	904	I33	21	2*14	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	904	I04	16
7W14	905	I34	3	2*15	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	905	I04	18
7W14	906	I34	25	2*16	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	906	I05	12
7W14	907	I34	21	2*17	REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	907	I05	14
7W14	908					6W14	908		

7W15	00	REGISTER TO MULTIPLY				6W15	00
7W15	90	H29	3	2*18 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	90	G05 16
7W15	91	H29	25	2*19 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	91	G05 18
7W15	92	H29	21	2*20 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	92	G06 12
7W15	93	H30	3	2*21 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	93	G06 14
7W15	94	H30	25	2*22 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	94	G06 16
7W15	95	H30	21	2*23 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	95	G06 18
7W15	96	H31	3	2*24 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	96	H01 12
7W15	97	H31	25	2*25 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	97	H01 14
7W15	98	H31	21	2*26 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	98	H01 16
7W15	99	H32	3	2*27 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	99	H01 18
7W15	900	H32	25	2*28 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	900	H02 12
7W15	901	H32	21	2*29 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	901	H02 14
7W15	902	H33	3	2*30 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	902	H02 16
7W15	903	H33	25	2*31 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	903	H02 18
7W15	904	H33	21	2*32 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	904	H03 12
7W15	905	H34	3	2*33 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	905	H03 14
7W15	906	H34	25	2*34 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	906	H03 16
7W15	907	H34	21	2*35 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W15	907	H03 18
7W15	908				6W15	908	

7W16	00	LOWER REGISTER FROM CONTROL				5W22	00	
7W16	90	I28	5	ADD TO X	2	5W22	90	H39 22
7W16	91	H28	17	INCREMENT TO X	GO	5W22	91	H40 5
7W16	92	H28	16	INCREMENT TO X	0	5W22	92	H40 1
7W16	93	H28	4	INCREMENT TO X	1	5W22	93	H40 26
7W16	94	H28	5	INCREMENT TO X	2	5W22	94	H40 22
7W16	95	K33	17	INCREMENT TO B	GO	5W22	95	H41 5
7W16	96	K33	16	INCREMENT TO B	0	5W22	96	H41 1
7W16	97	K33	4	INCREMENT TO B	1	5W22	97	H41 26
7W16	98	K33	5	INCREMENT TO B	2	5W22	98	H41 22
7W16	99	I27	7	XJ TO MULTIPLY (2) GO		5W22	99	J41 5
7W16	900					5W22	900	
7W16	901	I22	12	CLOCK		5W22	901	J39 4
7W16	902	G27	17	MULTIPLY TO X	GO	5W22	902	H38 5
7W16	903	G27	16	MULTIPLY TO X	0	5W22	903	H38 1
7W16	904	G27	4	MULTIPLY TO X	1	5W22	904	H38 26
7W16	905	G27	5	MULTIPLY TO X	2	5W22	905	H38 22
7W16	906	I28	17	ADD TO X	GO	5W22	906	H39 5
7W16	907	I28	16	ADD TO X	0	5W22	907	H39 1
7W16	908					5W22	908	

7W17	00	LOWER REGISTER FROM CONTROL				5W16	00		
7W17	90	G28	4	MEMORY TO D	0	5W16	90	I42	1
7W17	91	G28	5	MEMORY TO D	1	5W16	91	I42	26
7W17	92	G28	16	MEMORY TO D	2	5W16	92	I42	22
7W17	93	I26	21	K TO MULTIPLY (1)	GO	5W16	93	J42	5
7W17	94	I26	24	K TO MULTIPLY (1)	0	5W16	94	J42	1
7W17	95	I27	5	K TO MULTIPLY (1)	1	5W16	95	J42	26
7W17	96	I27	10	K TO MULTIPLY (1)	2	5W16	96	J42	22
7W17	97	I27	26	J TO MULTIPLY (2)	0	5W16	97	J41	1
7W17	98	I27	21	J TO MULTIPLY (2)	1	5W16	98	J41	26
7W17	99	I27	24	J TO MULTIPLY (2)	2	5W16	99	J41	22
7W17	900	H27	17	INCREMENT TO A	GO	5W16	900	H42	5
7W17	901	H27	16	INCREMENT TO A	0	5W16	901	H42	1
7W17	902	H27	4	INCREMENT TO A	1	5W16	902	H42	26
7W17	903	H27	5	INCREMENT TO A	2	5W16	903	H42	22
7W17	904	K34	17	SHIFT TO B	GO	5W16	904	I41	5
7W17	905	K34	16	SHIFT TO B	0	5W16	905	I41	1
7W17	906	K34	4	SHIFT TO B	1	5W16	906	I41	26
7W17	907	K34	5	SHIFT TO B	2	5W16	907	I41	22
7W17	908					5W16	908		

7W18	00	LOWER REGISTER FROM CONTROL				5W19	00		
7W18	90	J27	24	X TO INCREMENT	0	5W19	90	K40	1
7W18	91	J28	5	X TO INCREMENT	1	5W19	91	K40	26
7W18	92	J28	10	X TO INCREMENT	2	5W19	92	K40	22
7W18	93	M34	7	B TO ADD (1)	GO	5W19	93	M41	5
7W18	94	M34	26	B TO ADD (1)	0	5W19	94	M41	1
7W18	95	M34	21	B TO ADD (1)	1	5W19	95	M41	26
7W18	96	M34	24	B TO ADD (1)	2	5W19	96	M41	22
7W18	97					5W19	97		
7W18	98					5W19	98		
7W18	99					5W19	99		
7W18	900	J28	7	XJ TO ADD (1)	GO	5W19	900	K41	5
7W18	901	J28	26	XJ TO ADD (1)	0	5W19	901	K41	1
7W18	902	J28	21	XJ TO ADD (1)	1	5W19	902	K41	26
7W18	903	J28	24	XJ TO ADD (1)	2	5W19	903	K41	22
7W18	904	I26	5	XL TO ADD (2)	GO	5W19	904	K42	5
7W18	905	I26	10	XL TO ADD (2)	0	5W19	905	K42	1
7W18	906	I26	7	XL TO ADD (2)	1	5W19	906	K42	26
7W18	907	I26	26	XL TO ADD (2)	2	5W19	907	K42	22
7W18	908					5W19	908		

7W19	00	LOWER REGISTER FROM CONTROL				5W20	00
7W19	90	Q26	10	A TO INCREMENT	0	5W20	90 M42 1
7W19	91	Q26	7	A TO INCREMENT	1	5W20	91 M42 26
7W19	92	Q26	26	A TO INCREMENT	2	5W20	92 M42 22
7W19	93	I28	4	ADD TO X	1	5W20	93 H39 26
7W19	94	Q28	17	MEM. TO D	GO	5W20	94 I42 5
7W19	95	J27	21	X TO INCR.	GO	5W20	95 K40 5
7W19	96	Q26	5	A TO INCR.	GO	5W20	96 M42 5
7W19	97					5W20	97
7W19	98					5W20	98
7W19	99					5W20	99
7W19	900	L34	5	BJ TO INCREMENT	GO	5W20	900 L41 5
7W19	901	L34	10	BJ TO INCREMENT	0	5W20	901 L41 1
7W19	902	L34	7	BJ TO INCREMENT	1	5W20	902 L41 26
7W19	903	L34	26	BJ TO INCREMENT	2	5W20	903 L41 22
7W19	904	L34	21	BK TO INCREMENT	GO	5W20	904 L42 5
7W19	905	L34	24	BK TO INCREMENT	0	5W20	905 L42 1
7W19	906	M34	5	BK TO INCREMENT	1	5W20	906 L42 26
7W19	907	M34	10	BK TO INCREMENT	2	5W20	907 L42 22
7W19	908					5W20	908

7W20	00	LOWER REGISTER FROM CONTROL				5W25	00
7W20	90					5W25	90
7W20	91					5W25	91
7W20	92					5W25	92
7W20	93	G37	5	EXCHANGE		5W25	93 033 6
7W20	94					5W25	94
7W20	95					5W25	95
7W20	96					5W25	96
7W20	97					5W25	97
7W20	98	J26	24	D**5 TO X**5		5W25	98 034 4
7W20	99					5W25	99
7W20	900	J26	5	D**1 TO X**1		5W25	900 034 12
7W20	901	J26	7	D**2 TO X**2		5W25	901 034 10
7W20	902	J26	10	D**3 TO X**3		5W25	902 034 8
7W20	903	J26	21	D**4 TO X**4		5W25	903 034 6
7W20	904	H26	4	X TO MEMORY	GO	5W25	904 031 6
7W20	905	H26	6	X TO MEMORY	0	5W25	905 031 5
7W20	906	H26	7	X TO MEMORY	1	5W25	906 031 26
7W20	907	H26	27	X TO MEMORY	2	5W25	907 031 23
7W20	908					5W25	908

7W21	00	REGISTER FROM INCREMENT RESULT				5W08	00
7W21	90	F42	26	INCREMENT RESULT BIT 2*9	5W08	90	F41 27
7W21	91	G38	2	INCREMENT RESULT BIT 2*10	5W08	91	F42 4
7W21	92	G38	26	INCREMENT RESULT BIT 2*11	5W08	92	F42 6
7W21	93	G39	2	INCREMENT RESULT BIT 2*12	5W08	93	F42 8
7W21	94	G39	26	INCREMENT RESULT BIT 2*13	5W08	94	F42 10
7W21	95	G40	2	INCREMENT RESULT BIT 2*14	5W08	95	F42 12
7W21	96	G40	26	INCREMENT RESULT BIT 2*15	5W08	96	F42 19
7W21	97	G41	2	INCREMENT RESULT BIT 2*16	5W08	97	F42 21
7W21	98	G41	26	INCREMENT RESULT BIT 2*17	5W08	98	F42 23
7W21	99				5W08	99	
7W21	900	F38	2	INCREMENT RESULT BIT 2*0	5W08	900	F41 4
7W21	901	F38	26	INCREMENT RESULT BIT 2*1	5W08	901	F41 6
7W21	902	F39	2	INCREMENT RESULT BIT 2*2	5W08	902	F41 8
7W21	903	F39	26	INCREMENT RESULT BIT 2*3	5W08	903	F41 10
7W21	904	F40	2	INCREMENT RESULT BIT 2*4	5W08	904	F41 12
7W21	905	F40	26	INCREMENT RESULT BIT 2*5	5W08	905	F41 19
7W21	906	F41	2	INCREMENT RESULT BIT 2*6	5W08	906	F41 21
7W21	907	F41	26	INCREMENT RESULT BIT 2*7	5W08	907	F41 23
7W21	908	F42	2	INCREMENT RESULT BIT 2*8	5W08	908	F41 25

7W22	00	REGISTER FROM INCREMENT OPERAND				5W36	00
7W22	90	K30	3	INCREMENT OPERAND 1 BIT 2*9	5W36	90	026 7
7W22	91	K30	25	INCREMENT OPERAND 1 BIT 2*10	5W36	91	027 23
7W22	92	K30	21	INCREMENT OPERAND 1 BIT 2*11	5W36	92	027 7
7W22	93	K31	3	INCREMENT OPERAND 1 BIT 2*12	5W36	93	028 23
7W22	94	K31	25	INCREMENT OPERAND 1 BIT 2*13	5W36	94	028 7
7W22	95	K31	21	INCREMENT OPERAND 1 BIT 2*14	5W36	95	029 23
7W22	96	K32	3	INCREMENT OPERAND 1 BIT 2*15	5W36	96	029 7
7W22	97	K32	25	INCREMENT OPERAND 1 BIT 2*16	5W36	97	030 7
7W22	98	K32	21	INCREMENT OPERAND 1 BIT 2*17	5W36	98	030 24
7W22	99				5W36	99	
7W22	900	K27	3	INCREMENT OPERAND 1 BIT 2*0	5W36	900	022 23
7W22	901	K27	25	INCREMENT OPERAND 1 BIT 2*1	5W36	901	022 7
7W22	902	K27	21	INCREMENT OPERAND 1 BIT 2*2	5W36	902	023 23
7W22	903	K28	3	INCREMENT OPERAND 1 BIT 2*3	5W36	903	023 7
7W22	904	K28	25	INCREMENT OPERAND 1 BIT 2*4	5W36	904	024 23
7W22	905	K28	21	INCREMENT OPERAND 1 BIT 2*5	5W36	905	024 7
7W22	906	K29	3	INCREMENT OPERAND 1 BIT 2*6	5W36	906	025 23
7W22	907	K29	25	INCREMENT OPERAND 1 BIT 2*7	5W36	907	025 7
7W22	908	K29	21	INCREMENT OPERAND 1 BIT 2*8	5W36	908	026 23

7W23	00	REGISTER FROM INCREMENT OPERAND			5W37	00
7W23	90	L30	3	INCREMENT OPERAND K BIT 2*9	5W37	90 026 3
7W23	91	L30	25	INCREMENT OPERAND K BIT 2*10	5W37	91 027 27
7W23	92	L30	21	INCREMENT OPERAND K BIT 2*11	5W37	92 027 3
7W23	93	L31	3	INCREMENT OPERAND K BIT 2*12	5W37	93 028 27
7W23	94	L31	25	INCREMENT OPERAND K BIT 2*13	5W37	94 028 3
7W23	95	L31	21	INCREMENT OPERAND K BIT 2*14	5W37	95 029 27
7W23	96	L32	3	INCREMENT OPERAND K BIT 2*15	5W37	96 029 3
7W23	97	L32	25	INCREMENT OPERAND K BIT 2*16	5W37	97 030 3
7W23	98	L32	21	INCREMENT OPERAND K BIT 2*17	5W37	98 030 25
7W23	99				5W37	99
7W23	900	L27	3	INCREMENT OPERAND K BIT 2*0	5W37	900 022 27
7W23	901	L27	25	INCREMENT OPERAND K BIT 2*1	5W37	901 022 3
7W23	902	L27	21	INCREMENT OPERAND K BIT 2*2	5W37	902 023 27
7W23	903	L28	3	INCREMENT OPERAND K BIT 2*3	5W37	903 023 3
7W23	904	L28	25	INCREMENT OPERAND K BIT 2*4	5W37	904 024 27
7W23	905	L28	21	INCREMENT OPERAND K BIT 2*5	5W37	905 024 3
7W23	906	L29	3	INCREMENT OPERAND K BIT 2*6	5W37	906 025 27
7W23	907	L29	25	INCREMENT OPERAND K BIT 2*7	5W37	907 025 3
7W23	908	L29	21	INCREMENT OPERAND K BIT 2*8	5W37	908 026 27

7W24	00	REGISTER TO ADD			8W26	00
7W24	90	M39	3	2*0 REGISTER TO ADD, OPERAND 1(XK)	8W26	90 J01 11
7W24	91	M39	25	2*1 REGISTER TO ADD, OPERAND 1(XK)	8W26	91 J01 13
7W24	92	M39	21	2*2 REGISTER TO ADD, OPERAND 1(XK)	8W26	92 J02 11
7W24	93	M40	3	2*3 REGISTER TO ADD, OPERAND 1(XK)	8W26	93 J02 13
7W24	94	M40	25	2*4 REGISTER TO ADD, OPERAND 1(XK)	8W26	94 J03 11
7W24	95	M40	21	2*5 REGISTER TO ADD, OPERAND 1(XK)	8W26	95 J03 13
7W24	96	M41	3	2*6 REGISTER TO ADD, OPERAND 1(XK)	8W26	96 J04 8
7W24	97	M41	25	2*7 REGISTER TO ADD, OPERAND 1(XK)	8W26	97 J04 24
7W24	98	M41	21	2*8 REGISTER TO ADD, OPERAND 1(XK)	8W26	98 J05 8
7W24	99	M42	3	2*9 REGISTER TO ADD, OPERAND 1(XK)	8W26	99 J05 24
7W24	900	M42	25	2*10 REGISTER TO ADD, OPERAND 1(XK)	8W26	900 J06 8
7W24	901	M42	21	2*11 REGISTER TO ADD, OPERAND 1(XK)	8W26	901 J06 24
7W24	902	N35	3	2*12 REGISTER TO ADD, OPERAND 1(XK)	8W26	902 J07 6
7W24	903	N35	25	2*13 REGISTER TO ADD, OPERAND 1(XK)	8W26	903 J07 24
7W24	904	N35	21	2*14 REGISTER TO ADD, OPERAND 1(XK)	8W26	904 J08 6
7W24	905	N36	3	2*15 REGISTER TO ADD, OPERAND 1(XK)	8W26	905 J08 24
7W24	906	N36	25	2*16 REGISTER TO ADD, OPERAND 1(XK)	8W26	906 J09 6
7W24	907	N36	21	2*17 REGISTER TO ADD, OPERAND 1(XK)	8W26	907 J09 24
7W24	908				8W26	908

7W25	00	REGISTER TO ADD				8W27	00		
7W25	90	N37	3	2*18	REGISTER TO ADD, OPERAND 1(XK)	8W27	90	J10	6
7W25	91	N37	25	2*19	REGISTER TO ADD, OPERAND 1(XK)	8W27	91	J10	24
7W25	92	N37	21	2*20	REGISTER TO ADD, OPERAND 1(XK)	8W27	92	J11	6
7W25	93	N38	3	2*21	REGISTER TO ADD, OPERAND 1(XK)	8W27	93	J11	24
7W25	94	N38	25	2*22	REGISTER TO ADD, OPERAND 1(XK)	8W27	94	K01	6
7W25	95	N38	21	2*23	REGISTER TO ADD, OPERAND 1(XK)	8W27	95	K01	24
7W25	96	N39	3	2*24	REGISTER TO ADD, OPERAND 1(XK)	8W27	96	K02	6
7W25	97	N39	25	2*25	REGISTER TO ADD, OPERAND 1(XK)	8W27	97	K02	24
7W25	98	N39	21	2*26	REGISTER TO ADD, OPERAND 1(XK)	8W27	98	K03	6
7W25	99	N40	3	2*27	REGISTER TO ADD, OPERAND 1(XK)	8W27	99	K03	24
7W25	900	N40	25	2*28	REGISTER TO ADD, OPERAND 1(XK)	8W27	900	K04	6
7W25	901	N40	21	2*29	REGISTER TO ADD, OPERAND 1(XK)	8W27	901	K04	24
7W25	902	N41	3	2*30	REGISTER TO ADD, OPERAND 1(XK)	8W27	902	K05	6
7W25	903	N41	25	2*31	REGISTER TO ADD, OPERAND 1(XK)	8W27	903	K05	24
7W25	904	N41	21	2*32	REGISTER TO ADD, OPERAND 1(XK)	8W27	904	K06	6
7W25	905	N42	3	2*33	REGISTER TO ADD, OPERAND 1(XK)	8W27	905	K06	24
7W25	906	N42	25	2*34	REGISTER TO ADD, OPERAND 1(XK)	8W27	906	K07	6
7W25	907	N42	21	2*35	REGISTER TO ADD, OPERAND 1(XK)	8W27	907	K07	24
7W25	908					8W27	908		

7W26	00					8W24	00		
7W26	90	H37	2	2*0	ADD TO REGISTER	8W24	90	M01	1
7W26	91	H37	26	2*1	ADD TO REGISTER	8W24	91	M01	6
7W26	92	H38	2	2*2	ADD TO REGISTER	8W24	92	M01	23
7W26	93	H38	26	2*3	ADD TO REGISTER	8W24	93	M01	28
7W26	94	H39	2	2*4	ADD TO REGISTER	8W24	94	M02	1
7W26	95	H39	26	2*5	ADD TO REGISTER	8W24	95	M02	6
7W26	96	H40	2	2*6	ADD TO REGISTER	8W24	96	M02	23
7W26	97	H40	26	2*7	ADD TO REGISTER	8W24	97	M02	28
7W26	98	H41	2	2*8	ADD TO REGISTER	8W24	98	M03	1
7W26	99	H41	26	2*9	ADD TO REGISTER	8W24	99	M03	6
7W26	900	H42	2	2*10	ADD TO REGISTER	8W24	900	M03	23
7W26	901	H42	26	2*11	ADD TO REGISTER	8W24	901	M03	28
7W26	902	J37	2	2*12	ADD TO REGISTER	8W24	902	M04	1
7W26	903	J37	26	2*13	ADD TO REGISTER	8W24	903	M04	6
7W26	904	J38	2	2*14	ADD TO REGISTER	8W24	904	M04	23
7W26	905	J38	26	2*15	ADD TO REGISTER	8W24	905	M04	28
7W26	906	J39	2	2*16	ADD TO REGISTER	8W24	906	M05	1
7W26	907	J39	26	2*17	ADD TO REGISTER	8W24	907	M05	6
7W26	908					8W24	908		

7W27	00	REGISTER FROM ADD				8W25	00		
7W27	90	J40	2	2*18	ADD TO REGISTER	8W25	90	M05	23
7W27	91	J40	26	2*19	ADD TO REGISTER	8W25	91	M05	28
7W27	92	J41	2	2*20	ADD TO REGISTER	8W25	92	N01	1
7W27	93	J41	26	2*21	ADD TO REGISTER	8W25	93	N01	6
7W27	94	J42	2	2*22	ADD TO REGISTER	8W25	94	N01	23
7W27	95	J42	26	2*23	ADD TO REGISTER	8W25	95	N01	28
7W27	96	K37	2	2*24	ADD TO REGISTER	8W25	96	N02	1
7W27	97	K37	26	2*25	ADD TO REGISTER	8W25	97	N02	6
7W27	98	K38	2	2*26	ADD TO REGISTER	8W25	98	N02	23
7W27	99	K38	26	2*27	ADD TO REGISTER	8W25	99	N02	28
7W27	900	K39	2	2*28	ADD TO REGISTER	8W25	900	N03	1
7W27	901	K39	26	2*29	ADD TO REGISTER	8W25	901	N03	6
7W27	902	K40	2	2*30	ADD TO REGISTER	8W25	902	N03	23
7W27	903	K40	26	2*31	ADD TO REGISTER	8W25	903	N03	28
7W27	904	K41	2	2*32	ADD TO REGISTER	8W25	904	N04	1
7W27	905	K41	26	2*33	ADD TO REGISTER	8W25	905	N04	6
7W27	906	K42	2	2*34	ADD TO REGISTER	8W25	906	N04	23
7W27	907	K42	26	2*35	ADD TO REGISTER	8W25	907	N04	28
7W27	908					8W25	908		

7W28	00	REGISTER TO ADD				8W28	00		
7W28	90	L35	3	2*0	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	90	L09	14
7W28	91	L35	25	2*1	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	91	L09	18
7W28	92	L35	21	2*2	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	92	L10	14
7W28	93	L36	3	2*3	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	93	L10	18
7W28	94	L36	25	2*4	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	94	L11	14
7W28	95	L36	21	2*5	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	95	L11	18
7W28	96	L37	3	2*6	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	96	J04	7
7W28	97	L37	25	2*7	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	97	J04	21
7W28	98	L37	21	2*8	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	98	J05	7
7W28	99	L38	3	2*9	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	99	J05	21
7W28	900	L38	25	2*10	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	900	J06	7
7W28	901	L38	21	2*11	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	901	J06	21
7W28	902	L39	3	2*12	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	902	J07	7
7W28	903	L39	25	2*13	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	903	J07	21
7W28	904	L39	21	2*14	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	904	J08	7
7W28	905	L40	3	2*15	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	905	J08	21
7W28	906	L40	25	2*16	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	906	J09	7
7W28	907	L40	21	2*17	REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	907	I17	23
7W28	908					8W28	908		

7W29	00	REGISTER TO ADD				8W29	00
7W29	90	L41	3	2+18	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	90 J10 7
7W29	91	L41	25	2+19	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	91 J10 21
7W29	92	L41	21	2+20	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	92 J11 7
7W29	93	L42	3	2+21	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	93 J11 21
7W29	94	L42	25	2+22	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	94 K01 7
7W29	95	L42	21	2+23	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	95 K01 21
7W29	96	M35	3	2+24	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	96 K02 7
7W29	97	M35	25	2+25	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	97 K02 21
7W29	98	M35	21	2+26	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	98 K03 7
7W29	99	M36	3	2+27	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	99 K03 21
7W29	900	M36	25	2+28	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	900 K04 7
7W29	901	M36	21	2+29	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	901 K04 21
7W29	902	M37	3	2+30	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	902 K05 7
7W29	903	M37	25	2+31	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	903 K05 21
7W29	904	M37	21	2+32	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	904 K06 7
7W29	905	M38	3	2+33	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	905 K06 21
7W29	906	M38	25	2+34	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	906 K07 7
7W29	907	M38	21	2+35	REGISTER TO ADD, OPERAND 2(XJ+BJ)	8W29	907 K07 21
7W29	908					8W29	908

7W30	00	D TO X				8W23	00
7W30	90	L26	8	D+1	TO X*1 UPPER	8W23	90 G11 5
7W30	91	L26	6	D+2	TO X*2 UPPER	8W23	91 G11 7
7W30	92	L26	4	D+3	TO X*3 UPPER	8W23	92 G11 10
7W30	93	L26	27	D+4	TO X*4 UPPER	8W23	93 G11 21
7W30	94	L26	25	D+5	TO X*5 UPPER	8W23	94 G11 24
7W30	95					8W23	95
7W30	96					8W23	96
7W30	97					8W23	97
7W30	98					8W23	98
7W30	99					8W23	99
7W30	900					8W23	900
7W30	901					8W23	901
7W30	902					8W23	902
7W30	903					8W23	903
7W30	904					8W23	904
7W30	905					8W23	905
7W30	906					8W23	906
7W30	907					8W23	907
7W30	908					8W23	908

7W31	00	REGISTER FROM SHIFT				8W30	00
7W31	90	038	2	2*0	SHIFT TO REGISTER	8W30	90 H03 1
7W31	91	038	26	2*1	SHIFT TO REGISTER	8W30	91 H03 5
7W31	92	039	2	2*2	SHIFT TO REGISTER	8W30	92 H03 25
7W31	93	039	26	2*3	SHIFT TO REGISTER	8W30	93 H03 27
7W31	94	040	2	2*4	SHIFT TO REGISTER	8W30	94 H04 1
7W31	95	040	26	2*5	SHIFT TO REGISTER	8W30	95 H04 5
7W31	96	041	2	2*6	SHIFT TO REGISTER	8W30	96 H04 25
7W31	97	041	26	2*7	SHIFT TO REGISTER	8W30	97 H04 27
7W31	98	042	2	2*8	SHIFT TO REGISTER	8W30	98 H05 1
7W31	99	042	26	2*9	SHIFT TO REGISTER	8W30	99 H05 5
7W31	900	P38	2	2*10	SHIFT TO REGISTER	8W30	900 H17 1
7W31	901	P38	26	2*11	SHIFT TO REGISTER	8W30	901 H17 5
7W31	902	P39	2	2*12	SHIFT TO REGISTER	8W30	902 H17 25
7W31	903	P39	26	2*13	SHIFT TO REGISTER	8W30	903 H17 27
7W31	904	P40	2	2*14	SHIFT TO REGISTER	8W30	904 H18 1
7W31	905	P40	26	2*15	SHIFT TO REGISTER	8W30	905 H18 5
7W31	906	P41	2	2*16	SHIFT TO REGISTER	8W30	906 H18 25
7W31	907	P41	26	2*17	SHIFT TO REGISTER	8W30	907 H18 27
7W31	908					8W30	908

8W05	00	UPPER REGISTER FROM READ DISTRIBUTOR				9W21	00
8W05	90					9W21	90
8W05	91					9W21	91
8W05	92					9W21	92
8W05	93					9W21	93
8W05	94					9W21	94
8W05	95					9W21	95
8W05	96					9W21	96
8W05	97					9W21	97
8W05	98					9W21	98
8W05	99					9W21	99
8W05	900	A01	2	2*36	MEMORY TO REGISTER	9W21	900 133 28
8W05	901	A01	26	2*37	MEMORY TO REGISTER	9W21	901 134 28
8W05	902	A02	2	2*38	MEMORY TO REGISTER	9W21	902 135 28
8W05	903	A02	26	2*39	MEMORY TO REGISTER	9W21	903 136 28
8W05	904	A03	2	2*40	MEMORY TO REGISTER	9W21	904 138 28
8W05	905	A03	26	2*41	MEMORY TO REGISTER	9W21	905 139 28
8W05	906	A04	2	2*42	MEMORY TO REGISTER	9W21	906 140 28
8W05	907	A04	26	2*43	MEMORY TO REGISTER	9W21	907 141 28
8W05	908	A05	2	2*44	MEMORY TO REGISTER	9W21	908 142 28

8W06	00	UPPER REGISTER FROM READ DISTRIBUTOR				10W12	00
8W06	90	A05	26	2*45	MEMORY REGISTER	10W12	90 101 28
8W06	91	A06	2	2*46	MEMORY REGISTER	10W12	91 102 28
8W06	92	A06	26	2*47	MEMORY REGISTER	10W12	92 103 28
8W06	93	A07	2	2*48	MEMORY REGISTER	10W12	93 104 28
8W06	94	A07	26	2*49	MEMORY REGISTER	10W12	94 105 28
8W06	95	A08	2	2*50	MEMORY REGISTER	10W12	95 108 28
8W06	96	A08	26	2*51	MEMORY REGISTER	10W12	96 109 28
8W06	97	B01	2	2*52	MEMORY REGISTER	10W12	97 110 28
8W06	98	B01	26	2*53	MEMORY REGISTER	10W12	98 111 28
8W06	99	B02	2	2*54	MEMORY REGISTER	10W12	99 112 28
8W06	900	B02	26	2*55	MEMORY REGISTER	10W12	900 114 28
8W06	901	B03	2	2*56	MEMORY REGISTER	10W12	901 115 28
8W06	902	B03	26	2*57	MEMORY REGISTER	10W12	902 116 28
8W06	903	B04	2	2*58	MEMORY REGISTER	10W12	903 117 28
8W06	904	B04	26	2*59	MEMORY REGISTER	10W12	904 118 28
8W06	905					10W12	905
8W06	906					10W12	906
8W06	907					10W12	907
8W06	908					10W12	908

8W07	00	REGISTER TO MULTIPLY				6W07	00
8W07	90	C09	3	2+36	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	90 H04 12
8W07	91	C09	21	2+37	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	91 H04 14
8W07	92	C09	25	2+38	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	92 H04 16
8W07	93	C10	3	2+39	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	93 H04 18
8W07	94	C10	21	2+40	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	94 H05 12
8W07	95	C10	25	2+41	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	95 H05 14
8W07	96	C11	3	2+42	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	96 H05 16
8W07	97	C11	21	2+43	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	97 H05 18
8W07	98	C11	25	2+44	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	98 H06 12
8W07	99	C12	3	2+45	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	99 H06 14
8W07	900	C12	21	2+46	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	900 H06 16
8W07	901	C12	25	2+47	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	901 H06 18
8W07	902	D12	25	2+59	REGISTER TO MULTIPLY, OPERAND 1(XK)	6W07	902 F07 15
8W07	903					6W07	903
8W07	904					6W07	904
8W07	905					6W07	905
8W07	906					6W07	906
8W07	907					6W07	907
8W07	908					6W07	908

8W08	00	REGISTER FROM MULTIPLY				6W08	00
8W08	90	B05	2	2+36	MULTIPLY TO REGISTER	6W08	90 F03 1
8W08	91	B05	26	2+37	MULTIPLY TO REGISTER	6W08	91 F03 5
8W08	92	B06	2	2+38	MULTIPLY TO REGISTER	6W08	92 F03 25
8W08	93	B06	26	2+39	MULTIPLY TO REGISTER	6W08	93 F03 27
8W08	94	B07	2	2+40	MULTIPLY TO REGISTER	6W08	94 F04 1
8W08	95	B07	26	2+41	MULTIPLY TO REGISTER	6W08	95 F04 5
8W08	96	B08	2	2+42	MULTIPLY TO REGISTER	6W08	96 F04 25
8W08	97	B08	26	2+43	MULTIPLY TO REGISTER	6W08	97 F04 27
8W08	98	C01	2	2+44	MULTIPLY TO REGISTER	6W08	98 F05 1
8W08	99	C01	26	2+45	MULTIPLY TO REGISTER	6W08	99 F05 5
8W08	900	C02	2	2+46	MULTIPLY TO REGISTER	6W08	900 F05 25
8W08	901	C02	26	2+47	MULTIPLY TO REGISTER	6W08	901 F05 27
8W08	902					6W08	902
8W08	903					6W08	903
8W08	904					6W08	904
8W08	905					6W08	905
8W08	906					6W08	906
8W08	907					6W08	907
8W08	908					6W08	908

8W09	00	REGISTER TO MULTIPLY				6W09	00		
8W09	90	E09	3	2*36	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	90	J04	12
8W09	91	E09	21	2*37	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	91	J04	14
8W09	92	E09	25	2*38	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	92	J04	16
8W09	93	E10	3	2*39	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	93	J04	18
8W09	94	E10	21	2*40	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	94	J05	12
8W09	95	E10	25	2*41	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	95	J05	14
8W09	96	E11	3	2*42	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	96	J05	16
8W09	97	E11	21	2*43	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	97	J05	18
8W09	98	E11	25	2*44	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	98	J06	12
8W09	99	E12	3	2*45	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	99	J06	14
8W09	900	E12	21	2*46	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	900	J06	16
8W09	901	E12	25	2*47	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	901	J06	18
8W09	902	F12	25	2*59	REGISTER TO MULTIPLY, OPERAND (XJ)	6W09	902	F06	15
8W09	903					6W09	903		
8W09	904					6W09	904		
8W09	905					6W09	905		
8W09	906					6W09	906		
8W09	907					6W09	907		
8W09	908					6W09	908		

8W10	00	UPPER REGISTER TO MEMORY DISTRIBUTION				2W11	00		
8W10	90	A09	25	2*38	REGISTER TO DISTRIBUTOR	2W11	90	A14	10
8W10	91	A10	3	2*39	REGISTER TO DISTRIBUTOR	2W11	91	A14	21
8W10	92	A10	21	2*40	REGISTER TO DISTRIBUTOR	2W11	92	A14	24
8W10	93	A10	25	2*41	REGISTER TO DISTRIBUTOR	2W11	93	A14	26
8W10	94	A11	3	2*42	REGISTER TO DISTRIBUTOR	2W11	94	A15	5
8W10	95	A11	21	2*43	REGISTER TO DISTRIBUTOR	2W11	95	A15	7
8W10	96	A11	25	2*44	REGISTER TO DISTRIBUTOR	2W11	96	A15	10
8W10	97					2W11	97		
8W10	98					2W11	98		
8W10	99					2W11	99		
8W10	900					2W11	900		
8W10	901					2W11	901		
8W10	902					2W11	902		
8W10	903					2W11	903		
8W10	904					2W11	904		
8W10	905					2W11	905		
8W10	906	A09	3	2*36	REGISTER TO DISTRIBUTOR	2W11	906	A14	5
8W10	907	A09	21	2*37	REGISTER TO DISTRIBUTOR	2W11	907	A14	7
8W10	908					2W11	908		

8W11	00	UPPER REGISTER TO MEMORY DISTRIBUTION			2W12	00
8W11	90	B10	25	2*53 REGISTER TO DISTRIBUTOR	2W12	90 A16 26
8W11	91	B11	3	2*54 REGISTER TO DISTRIBUTOR	2W12	91 A17 5
8W11	92	B11	21	2*55 REGISTER TO DISTRIBUTOR	2W12	92 A17 7
8W11	93	B11	25	2*56 REGISTER TO DISTRIBUTOR	2W12	93 A17 10
8W11	94	B12	3	2*57 REGISTER TO DISTRIBUTOR	2W12	94 A17 21
8W11	95	B12	21	2*58 REGISTER TO DISTRIBUTOR	2W12	95 A17 24
8W11	96	B12	25	2*59 REGISTER TO DISTRIBUTOR	2W12	96 A17 26
8W11	97				2W12	97
8W11	98				2W12	98
8W11	99				2W12	99
8W11	900	A12	3	2*45 REGISTER TO DISTRIBUTOR	2W12	900 A15 21
8W11	901	A12	21	2*46 REGISTER TO DISTRIBUTOR	2W12	901 A15 24
8W11	902	A12	25	2*47 REGISTER TO DISTRIBUTOR	2W12	902 A15 26
8W11	903	B09	3	2*48 REGISTER TO DISTRIBUTOR	2W12	903 A16 5
8W11	904	B09	21	2*49 REGISTER TO DISTRIBUTOR	2W12	904 A16 7
8W11	905	B09	25	2*50 REGISTER TO DISTRIBUTOR	2W12	905 A16 10
8W11	906	B10	3	2*51 REGISTER TO DISTRIBUTOR	2W12	906 A16 21
8W11	907	B10	21	2*52 REGISTER TO DISTRIBUTOR	2W12	907 A16 24
8W11	908				2W12	908

8W12	00	LONG ADD, ADD AND SHIFT FROM CONTROL			5W12	00
8W12	90	H01	5	LONG ADD IN	5W12	90 E40 27
8W12	91	H01	10	ADD IN	5W12	91 E40 25
8W12	92	I19	9	MASK	5W12	92 E39 12
8W12	93	H01	21	ROUND ADD	5W12	93 E39 21
8W12	94	H01	24	ADD	5W12	94 E39 19
8W12	95	G16	5	PACK	5W12	95 E39 8
8W12	96	G16	10	UN-PACK	5W12	96 E39 10
8W12	97	G16	21	SHIFT (JK)	5W12	97 E38 19
8W12	98	G16	26	SHIFT NOMINAL	5W12	98 E38 23
8W12	99	G16	24	SHIFT LEFT	5W12	99 E38 21
8W12	900	H01	7	ROUND SHIFT	5W12	900 E39 6
8W12	901	F01	24	TRANSMIT LONG ADD	5W12	901 J40 21
8W12	902	G01	24	TRANSMIT SHIFT	5W12	902 J40 12
8W12	903	G02	24	TRANSMIT ADD	5W12	903 J40 19
8W12	904	H06	4	REQUEST RELEASE SHIFT	5W12	904 J22 19
8W12	905	I19	5	LONG ADD - PLUS	5W12	905 E39 27
8W12	906	H06	8	REQUEST RELEASE LONG ADD	5W12	906 K22 19
8W12	907	H01	26	SHIFT IN	5W12	907 E40 23
8W12	908	G16	7	NORM. (SHIFT)	5W12	908 E39 4

8W13	00	UPPER REGISTER FROM CONTROL			5W11	00
8W13	90	E06	26	INCR SIGN	5W11	90 F42 25
8W13	91				5W11	91
8W13	92	G01	26	ADD MINUS	5W11	92 E39 23
8W13	93				5W11	93
8W13	94				5W11	94
8W13	95				5W11	95
8W13	96				5W11	96
8W13	97				5W11	97
8W13	98				5W11	98
8W13	99				5W11	99
8W13	900	H06	12	LONG ADD INDEFINITE	5W11	900 042 24
8W13	901	H06	10	LONG ADD INFINITE	5W11	901 042 21
8W13	902				5W11	902
8W13	903				5W11	903
8W13	904				5W11	904
8W13	905				5W11	905
8W13	906				5W11	906
8W13	907				5W11	907
8W13	908				5W11	908

8W18	00	REGISTER TO DIVIDE			2W22	00
8W18	90	D09	3	2*48 X**K (EXPONENT)	2W22	90 M08 12
8W18	91	D09	21	2*49 X**K (EXPONENT)	2W22	91 M08 14
8W18	92	D09	25	2*50 X**K (EXPONENT)	2W22	92 M08 16
8W18	93	D10	3	2*51 X**K (EXPONENT)	2W22	93 M08 18
8W18	94	D10	21	2*52 X**K (EXPONENT)	2W22	94 M09 12
8W18	95	D10	25	2*53 X**K (EXPONENT)	2W22	95 M09 14
8W18	96	D11	3	2*54 X**K (EXPONENT)	2W22	96 M09 16
8W18	97	D11	21	2*55 X**K (EXPONENT)	2W22	97 M09 18
8W18	98	D11	25	2*56 X**K (EXPONENT)	2W22	98 M10 12
8W18	99	D12	3	2*57 X**K (EXPONENT)	2W22	99 M10 14
8W18	900	D12	21	2*58 X**K (EXPONENT)	2W22	900 M10 16
8W18	901	D12	23	2*59 X**K (EXPONENT)	2W22	901 M10 18
8W18	902	C03	2	2*48 EXPONENT RESULT-REG.	2W22	902 K13 1
8W18	903	C03	26	2*49 EXPONENT RESULT-REG.	2W22	903 K13 6
8W18	904	C04	2	2*50 EXPONENT RESULT-REG.	2W22	904 K13 23
8W18	905	C04	26	2*51 EXPONENT RESULT-REG.	2W22	905 K13 28
8W18	906	C05	2	2*52 EXPONENT RESULT-REG.	2W22	906 K14 1
8W18	907	C05	26	2*53 EXPONENT RESULT-REG.	2W22	907 K14 6
8W18	908	H06	19	ERROR (EXP = 1777 FROM CHASSIS 7)	2W22	908 E02 5

8W19	00	REGISTER TO DIVIDE				2W23	00
8W19	90	F09	3	2*48 X**J (EXPONENT)	2W23	90	L08 12
8W19	91	F09	21	2*49 X**J (EXPONENT)	2W23	91	L08 14
8W19	92	F09	25	2*50 X**J (EXPONENT)	2W23	92	L08 16
8W19	93	F10	3	2*51 X**J (EXPONENT)	2W23	93	L08 18
8W19	94	F10	21	2*52 X**J (EXPONENT)	2W23	94	L09 12
8W19	95	F10	25	2*53 X**J (EXPONENT)	2W23	95	L09 14
8W19	96	F11	3	2*54 X**J (EXPONENT)	2W23	96	L09 16
8W19	97	F11	21	2*55 X**J (EXPONENT)	2W23	97	L09 18
8W19	98	F11	25	2*56 X**J (EXPONENT)	2W23	98	L10 12
8W19	99	F12	3	2*57 X**J (EXPONENT)	2W23	99	L10 14
8W19	900	F12	21	2*58 X**J (EXPONENT)	2W23	900	L10 16
8W19	901	F12	23	2*59 X**J (EXPONENT)	2W23	901	L10 18
8W19	902	C06	2	2*54 EXPONENT RESULT-REG	2W23	902	K14 23
8W19	903	C06	26	2*55 EXPONENT RESULT-REG	2W23	903	K14 28
8W19	904	C07	2	2*56 EXPONENT RESULT-REG	2W23	904	K15 1
8W19	905	C07	26	2*57 EXPONENT RESULT-REG	2W23	905	K15 6
8W19	906	C08	2	2*58 EXPONENT RESULT-REG	2W23	906	K15 23
8W19	907	C08	26	2*59 EXPONENT RESULT-REG	2W23	907	K15 28
8W19	908	H06	21	ERROR (EXP = 3777 FROM CHASSIS 8)	2W23	908	E02 7

8W20	00	UPPER REGISTER FROM CONTROL				5W15	00	
8W20	90	G05	17	MEMORY TO D	GO	5W15	90	I42 7
8W20	91	G05	5	MEMORY TO D	0	5W15	91	I42 3
8W20	92	G05	4	MEMORY TO D	1	5W15	92	I42 28
8W20	93	G05	16	MEMORY TO D	2	5W15	93	I42 24
8W20	94	F02	21	K TO MULTIPLY	GO	5W15	94	J42 7
8W20	95	F02	5	K TO MULTIPLY	0	5W15	95	J42 3
8W20	96	F02	7	K TO MULTIPLY	1	5W15	96	J42 28
8W20	97	F02	10	K TO MULTIPLY	2	5W15	97	J42 24
8W20	98	G07	16	ADD TO X	2	5W15	98	H39 24
8W20	99					5W15	99	
8W20	900	I21	12	CLOCK		5W15	900	J39 25
8W20	901	G06	17	MULTIPLY TO X	GO	5W15	901	H38 7
8W20	902	G06	5	MULTIPLY TO X	0	5W15	902	H38 3
8W20	903	G06	4	MULTIPLY TO X	1	5W15	903	H38 28
8W20	904	G06	16	MULTIPLY TO X	2	5W15	904	H38 24
8W20	905	G07	17	ADD TO X	GO	5W15	905	H39 7
8W20	906	G07	5	ADD TO X	0	5W15	906	H39 3
8W20	907	G07	4	ADD TO X	1	5W15	907	H39 28
8W20	908					5W15	908	

8W21	00	UPPER REGISTER FROM CONTROL				5W17	00		
8W21	90	F01	5	X TO ADD	0	5W17	90	K42	3
8W21	91	F01	7	X TO ADD	1	5W17	91	K42	28
8W21	92	F01	10	X TO ADD	2	5W17	92	K42	24
8W21	93	F01	21	X TO ADD	00	5W17	93	K42	7
8W21	94	G08	17	INCR TO X	GO	5W17	94	H40	7
8W21	95	G08	5	INCR TO X	0	5W17	95	H40	3
8W21	96	G08	4	INCR TO X	1	5W17	96	H40	28
8W21	97	G08	16	INCR TO X	2	5W17	97	H40	24
8W21	98	H06	23	LONG ADD ZERO	CH 5	5W17	98	O42	10
8W21	99	H06	25	LONG ADD SIGN	CH 5	5W17	99	O42	7
8W21	900	G01	21	J TO MULTIPLY	GO	5W17	900	J41	7
8W21	901	G01	5	J TO MULTIPLY	0	5W17	901	J41	3
8W21	902	G01	7	J TO MULTIPLY	1	5W17	902	J41	28
8W21	903	G01	10	J TO MULTIPLY	2	5W17	903	J41	24
8W21	904	E01	21	X TO ADD	00	5W17	904	K41	7
8W21	905	E01	5	X TO ADD	0	5W17	905	K41	3
8W21	906	E01	7	X TO ADD	1	5W17	906	K41	28
8W21	907	E01	10	X TO ADD	2	5W17	907	K41	24
8W21	908					5W17	908		

8W22	00	UPPER REGISTER FROM CONTROL				5W26	00		
8W22	90			D**4 TO X**4		5W26	90		
8W22	91	E01	24	EXCHANGE		5W26	91	033	8
8W22	92	H02	4	ENTER (JK)	2*0	5W26	92	035	8
8W22	93	H02	6	ENTER (JK)	2*1	5W26	93	035	6
8W22	94	H02	7	ENTER (JK)	2*2	5W26	94	035	4
8W22	95	H02	22	ENTER (JK)	2*3	5W26	95	035	27
8W22	96	H02	25	ENTER (JK)	2*4	5W26	96	035	25
8W22	97	H02	27	ENTER (JK)	2*5	5W26	97	035	23
8W22	98	H06	6	REQUEST RELEASE	ADD	5W26	98	K23	19
8W22	99	H06	27	LONG ADD SER	CONTROL	5W26	99	O42	5
8W22	900			D TO X**0					
8W22	901			D TO X**1					
8W22	902			D TO X**2					
8W22	903			D TO X**3					
8W22	904	G02	21	X TO MEMORY	GO	5W26	904	031	8
8W22	905	G02	5	X TO MEMORY	0	5W26	905	031	3
8W22	906	G02	7	X TO MEMORY	1	5W26	906	031	28
8W22	907	G02	10	X TO MEMORY	2	5W26	907	031	25
8W22	908					5W26	908		

8W23	00	D TO X			7W30	00		
8W23	90	G11	5	D*1 TO X*1 UPPER	7W30	90	L26	8
8W23	91	G11	7	D*2 TO X*2 UPPER	7W30	91	L26	6
8W23	92	G11	10	D*3 TO X*3 UPPER	7W30	92	L26	4
8W23	93	G11	21	D*4 TO X*4 UPPER	7W30	93	L26	27
8W23	94	G11	24	D*5 TO X*5 UPPER	7W30	94	L26	25
8W23	95				7W30	95		
8W23	96				7W30	96		
8W23	97				7W30	97		
8W23	98				7W30	98		
8W23	99				7W30	99		
8W23	900				7W30	900		
8W23	901				7W30	901		
8W23	902				7W30	902		
8W23	903				7W30	903		
8W23	904				7W30	904		
8W23	905				7W30	905		
8W23	906				7W30	906		
8W23	907				7W30	907		
8W23	908				7W30	908		

8W24	00	ADD TO REGISTER			7W26	00		
8W24	90	M01	1	2*0 ADD TO REGISTER	7W26	90	H37	2
8W24	91	M01	6	2*1 ADD TO REGISTER	7W26	91	H37	26
8W24	92	M01	23	2*2 ADD TO REGISTER	7W26	92	H38	2
8W24	93	M01	28	2*3 ADD TO REGISTER	7W26	93	H38	26
8W24	94	M02	1	2*4 ADD TO REGISTER	7W26	94	H39	2
8W24	95	M02	6	2*5 ADD TO REGISTER	7W26	95	H39	26
8W24	96	M02	23	2*6 ADD TO REGISTER	7W26	96	H40	2
8W24	97	M02	28	2*7 ADD TO REGISTER	7W26	97	H40	26
8W24	98	M03	1	2*8 ADD TO REGISTER	7W26	98	H41	2
8W24	99	M03	6	2*9 ADD TO REGISTER	7W26	99	H41	26
8W24	900	M03	23	2*10 ADD TO REGISTER	7W26	900	H42	2
8W24	901	M03	28	2*11 ADD TO REGISTER	7W26	901	H42	26
8W24	902	M04	1	2*12 ADD TO REGISTER	7W26	902	J37	2
8W24	903	M04	6	2*13 ADD TO REGISTER	7W26	903	J37	26
8W24	904	M04	23	2*14 ADD TO REGISTER	7W26	904	J38	2
8W24	905	M04	28	2*15 ADD TO REGISTER	7W26	905	J38	26
8W24	906	M05	1	2*16 ADD TO REGISTER	7W26	906	J39	2
8W24	907	M05	6	2*17 ADD TO REGISTER	7W26	907	J39	26
8W24	908				7W26	908		

8W25	00	ADD TO REGISTER				7W27	00		
8W25	90	M05	23	2*18	ADD TO REGISTER	7W27	90	J40	2
8W25	91	M05	28	2*19	ADD TO REGISTER	7W27	91	J40	26
8W25	92	N01	1	2*20	ADD TO REGISTER	7W27	92	J41	2
8W25	93	N01	6	2*21	ADD TO REGISTER	7W27	93	J41	26
8W25	94	N01	23	2*22	ADD TO REGISTER	7W27	94	J42	2
8W25	95	N01	28	2*23	ADD TO REGISTER	7W27	95	J42	26
8W25	96	N02	1	2*24	ADD TO REGISTER	7W27	96	K37	2
8W25	97	N02	6	2*25	ADD TO REGISTER	7W27	97	K37	26
8W25	98	N02	23	2*26	ADD TO REGISTER	7W27	98	K38	2
8W25	99	N02	28	2*27	ADD TO REGISTER	7W27	99	K38	26
8W25	900	N03	1	2*28	ADD TO REGISTER	7W27	900	K39	2
8W25	901	N03	6	2*29	ADD TO REGISTER	7W27	901	K39	26
8W25	902	N03	23	2*30	ADD TO REGISTER	7W27	902	K40	2
8W25	903	N03	28	2*31	ADD TO REGISTER	7W27	903	K40	26
8W25	904	N04	1	2*32	ADD TO REGISTER	7W27	904	K41	2
8W25	905	N04	6	2*33	ADD TO REGISTER	7W27	905	K41	26
8W25	906	N04	23	2*34	ADD TO REGISTER	7W27	906	K42	2
8W25	907	N04	28	2*35	ADD TO REGISTER	7W27	907	K42	26
8W25	908					7W27	908		

8W26	00	ADD FROM REGISTER				7W24	00		
8W26	90	J01	11	2*0	REGISTER TO ADD, OPERAND 1(XK)	7W24	90	M39	3
8W26	91	J01	13	2*1	REGISTER TO ADD, OPERAND 1(XK)	7W24	91	M39	25
8W26	92	J02	11	2*2	REGISTER TO ADD, OPERAND 1(XK)	7W24	92	M39	21
8W26	93	J02	13	2*3	REGISTER TO ADD, OPERAND 1(XK)	7W24	93	M40	3
8W26	94	J03	11	2*4	REGISTER TO ADD, OPERAND 1(XK)	7W24	94	M40	25
8W26	95	J03	13	2*5	REGISTER TO ADD, OPERAND 1(XK)	7W24	95	M40	21
8W26	96	J04	8	2*6	REGISTER TO ADD, OPERAND 1(XK)	7W24	96	M41	3
8W26	97	J04	24	2*7	REGISTER TO ADD, OPERAND 1(XK)	7W24	97	M41	25
8W26	98	J05	8	2*8	REGISTER TO ADD, OPERAND 1(XK)	7W24	98	M41	21
8W26	99	J05	24	2*9	REGISTER TO ADD, OPERAND 1(XK)	7W24	99	M42	3
8W26	900	J06	8	2*10	REGISTER TO ADD, OPERAND 1(XK)	7W24	900	M42	25
8W26	901	J06	24	2*11	REGISTER TO ADD, OPERAND 1(XK)	7W24	901	M42	21
8W26	902	J07	6	2*12	REGISTER TO ADD, OPERAND 1(XK)	7W24	902	N35	3
8W26	903	J07	24	2*13	REGISTER TO ADD, OPERAND 1(XK)	7W24	903	N35	25
8W26	904	J08	6	2*14	REGISTER TO ADD, OPERAND 1(XK)	7W24	904	N35	21
8W26	905	J08	24	2*15	REGISTER TO ADD, OPERAND 1(XK)	7W24	905	N36	3
8W26	906	J09	6	2*16	REGISTER TO ADD, OPERAND 1(XK)	7W24	906	N36	25
8W26	907	J09	24	2*17	REGISTER TO ADD, OPERAND 1(XK)	7W24	907	N36	21
8W26	908					7W24	908		

8W27	00	ADD FROM REGISTER				7W25	00
8W27	90	J10	6	2*18	REGISTER TO ADD, OPERAND 1(XK)	7W25	90 N37 3
8W27	91	J10	24	2*19	REGISTER TO ADD, OPERAND 1(XK)	7W25	91 N37 25
8W27	92	J11	6	2*20	REGISTER TO ADD, OPERAND 1(XK)	7W25	92 N37 21
8W27	93	J11	24	2*21	REGISTER TO ADD, OPERAND 1(XK)	7W25	93 N38 3
8W27	94	K01	6	2*22	REGISTER TO ADD, OPERAND 1(XK)	7W25	94 N38 25
8W27	95	K01	24	2*23	REGISTER TO ADD, OPERAND 1(XK)	7W25	95 N38 21
8W27	96	K02	6	2*24	REGISTER TO ADD, OPERAND 1(XK)	7W25	96 N39 3
8W27	97	K02	24	2*25	REGISTER TO ADD, OPERAND 1(XK)	7W25	97 N39 25
8W27	98	K03	6	2*26	REGISTER TO ADD, OPERAND 1(XK)	7W25	98 N39 21
8W27	99	K03	24	2*27	REGISTER TO ADD, OPERAND 1(XK)	7W25	99 N40 3
8W27	900	K04	6	2*28	REGISTER TO ADD, OPERAND 1(XK)	7W25	900 N40 25
8W27	901	K04	24	2*29	REGISTER TO ADD, OPERAND 1(XK)	7W25	901 N40 21
8W27	902	K05	6	2*30	REGISTER TO ADD, OPERAND 1(XK)	7W25	902 N41 3
8W27	903	K05	24	2*31	REGISTER TO ADD, OPERAND 1(XK)	7W25	903 N41 25
8W27	904	K06	6	2*32	REGISTER TO ADD, OPERAND 1(XK)	7W25	904 N41 21
8W27	905	K06	24	2*33	REGISTER TO ADD, OPERAND 1(XK)	7W25	905 N42 3
8W27	906	K07	6	2*34	REGISTER TO ADD, OPERAND 1(XK)	7W25	906 N42 25
8W27	907	K07	24	2*35	REGISTER TO ADD, OPERAND 1(XK)	7W25	907 N42 21
8W27	908					7W25	908

8W28	00	ADD FROM REGISTER				7W28	00
8W28	90	L09	14	2*0	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	90 L35 3
8W28	91	L09	18	2*1	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	91 L35 25
8W28	92	L10	14	2*2	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	92 L35 21
8W28	93	L10	18	2*3	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	93 L36 3
8W28	94	L11	14	2*4	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	94 L36 25
8W28	95	L11	18	2*5	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	95 L36 21
8W28	96	J04	7	2*6	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	96 L37 3
8W28	97	J04	21	2*7	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	97 L37 25
8W28	98	J05	7	2*8	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	98 L37 21
8W28	99	J05	21	2*9	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	99 L38 3
8W28	900	J06	7	2*10	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	900 L38 25
8W28	901	J06	21	2*11	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	901 L38 21
8W28	902	J07	7	2*12	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	902 L39 3
8W28	903	J07	21	2*13	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	903 L39 25
8W28	904	J08	7	2*14	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	904 L39 21
8W28	905	J08	21	2*15	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	905 L40 3
8W28	906	J09	7	2*16	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	906 L40 25
8W28	907	I17	23	2*17	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	907 L40 21
8W28	908					7W28	908

8W29	00	ADD FROM REGISTER						7W29	00
8W29	90	J10	7	2*18	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	90	L41 3
8W29	91	J10	21	2*19	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	91	L41 25
8W29	92	J11	7	2*20	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	92	L41 21
8W29	93	J11	21	2*21	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	93	L42 3
8W29	94	K01	7	2*22	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	94	L42 25
8W29	95	K01	21	2*23	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	95	L42 21
8W29	96	K02	7	2*24	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	96	M35 3
8W29	97	K02	21	2*25	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	97	M35 25
8W29	98	K03	7	2*26	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	98	M35 21
8W29	99	K03	21	2*27	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	99	M36 3
8W29	900	K04	7	2*28	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	900	M36 25
8W29	901	K04	21	2*29	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	901	M36 21
8W29	902	K05	7	2*30	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	902	M37 3
8W29	903	K05	21	2*31	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	903	M37 25
8W29	904	K06	7	2*32	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	904	M37 21
8W29	905	K06	21	2*33	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	905	M38 3
8W29	906	K07	7	2*34	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	906	M38 25
8W29	907	K07	21	2*35	REGISTER TO ADD,	OPERAND 2(XJ+BJ)	7W29	907	M38 21
8W29	908						7W29	908	

8W30	00	SHIFT TO REGISTER						7W31	00
8W30	90	H03	1	2*0	SHIFT TO REGISTER		7W31	90	038 2
8W30	91	H03	5	2*1	SHIFT TO REGISTER		7W31	91	038 26
8W30	92	H03	25	2*2	SHIFT TO REGISTER		7W31	92	039 2
8W30	93	H03	27	2*3	SHIFT TO REGISTER		7W31	93	039 26
8W30	94	H04	1	2*4	SHIFT TO REGISTER		7W31	94	040 2
8W30	95	H04	5	2*5	SHIFT TO REGISTER		7W31	95	040 26
8W30	96	H04	25	2*6	SHIFT TO REGISTER		7W31	96	041 2
8W30	97	H04	27	2*7	SHIFT TO REGISTER		7W31	97	041 26
8W30	98	H05	1	2*8	SHIFT TO REGISTER		7W31	98	042 2
8W30	99	H05	5	2*9	SHIFT TO REGISTER		7W31	99	042 26
8W30	900	H17	1	2*10	SHIFT TO REGISTER		7W31	900	P38 2
8W30	901	H17	5	2*11	SHIFT TO REGISTER		7W31	901	P38 26
8W30	902	H17	25	2*12	SHIFT TO REGISTER		7W31	902	P39 2
8W30	903	H17	27	2*13	SHIFT TO REGISTER		7W31	903	P39 26
8W30	904	H18	1	2*14	SHIFT TO REGISTER		7W31	904	P40 2
8W30	905	H18	5	2*15	SHIFT TO REGISTER		7W31	905	P40 26
8W30	906	H18	25	2*16	SHIFT TO REGISTER		7W31	906	P41 2
8W30	907	H18	27	2*17	SHIFT TO REGISTER		7W31	907	P41 26
8W30	908						7W31	908	



9W03	00	MEMORY FROM WRITE DISTRIBUTOR				13W14	00
9W03	90	H21	24	2*8	FAN OUT TO MEMORY	13W14	90 133 28
9W03	91	H22	2	2*9	FAN OUT TO MEMORY	13W14	91 134 24
9W03	92	H22	19	2*10	FAN OUT TO MEMORY	13W14	92 134 7
9W03	93	H22	24	2*11	FAN OUT TO MEMORY	13W14	93 135 28
9W03	94	H24	2	2*12	FAN OUT TO MEMORY	13W14	94 136 24
9W03	95	H24	19	2*13	FAN OUT TO MEMORY	13W14	95 136 7
9W03	96	H24	24	2*14	FAN OUT TO MEMORY	13W14	96 137 28
9W03	97					13W14	97
9W03	98					13W14	98
9W03	99					13W14	99
9W03	900	H19	2	2*0	FAN OUT TO MEMORY	13W14	900 128 24
9W03	901	H19	19	2*1	FAN OUT TO MEMORY	13W14	901 128 7
9W03	902	H19	24	2*2	FAN OUT TO MEMORY	13W14	902 129 28
9W03	903	H20	2	2*3	FAN OUT TO MEMORY	13W14	903 130 24
9W03	904	H20	19	2*4	FAN OUT TO MEMORY	13W14	904 130 7
9W03	905	H20	24	2*5	FAN OUT TO MEMORY	13W14	905 131 28
9W03	906	H21	2	2*6	FAN OUT TO MEMORY	13W14	906 132 24
9W03	907	H21	19	2*7	FAN OUT TO MEMORY	13W14	907 132 7
9W03	908					13W14	908

9W04	00	MEMORY FROM WRITE DISTRIBUTOR				14W14	00
9W04	90	H27	24	2*23	FAN OUT TO MEMORY	14W14	90 111 13
9W04	91	H29	2	2*24	FAN OUT TO MEMORY	14W14	91 112 3
9W04	92	H29	19	2*25	FAN OUT TO MEMORY	14W14	92 113 18
9W04	93	H29	24	2*26	FAN OUT TO MEMORY	14W14	93 113 13
9W04	94	H30	2	2*27	FAN OUT TO MEMORY	14W14	94 114 3
9W04	95	H30	19	2*28	FAN OUT TO MEMORY	14W14	95 115 18
9W04	96	H30	24	2*29	FAN OUT TO MEMORY	14W14	96 115 13
9W04	97					14W14	97
9W04	98					14W14	98
9W04	99					14W14	99
9W04	900	H25	2	2*15	FAN OUT TO MEMORY	14W14	900 106 3
9W04	901	H25	19	2*16	FAN OUT TO MEMORY	14W14	901 107 18
9W04	902	H25	24	2*17	FAN OUT TO MEMORY	14W14	902 107 13
9W04	903	H26	2	2*18	FAN OUT TO MEMORY	14W14	903 108 3
9W04	904	H26	19	2*19	FAN OUT TO MEMORY	14W14	904 109 18
9W04	905	H26	24	2*20	FAN OUT TO MEMORY	14W14	905 109 13
9W04	906	H27	2	2*21	FAN OUT TO MEMORY	14W14	906 110 3
9W04	907	H27	19	2*22	FAN OUT TO MEMORY	14W14	907 111 18
9W04	908					14W14	908

9W05	00	MEMORY FROM WRITE DISTRIBUTOR				15W14	00
9W05	90	H34	24	2*38	FAN OUT TO MEMORY	15W14	90 133 13
9W05	91	H35	2	2*39	FAN OUT TO MEMORY	15W14	91 134 3
9W05	92	H35	19	2*40	FAN OUT TO MEMORY	15W14	92 135 18
9W05	93	H35	24	2*41	FAN OUT TO MEMORY	15W14	93 135 13
9W05	94	H36	2	2*42	FAN OUT TO MEMORY	15W14	94 136 3
9W05	95	H36	19	2*43	FAN OUT TO MEMORY	15W14	95 137 18
9W05	96	H36	24	2*44	FAN OUT TO MEMORY	15W14	96 137 13
9W05	97					15W14	97
9W05	98					15W14	98
9W05	99					15W14	99
9W05	900	H31	2	2*30	FAN OUT TO MEMORY	15W14	900 128 3
9W05	901	H31	19	2*31	FAN OUT TO MEMORY	15W14	901 129 18
9W05	902	H31	24	2*32	FAN OUT TO MEMORY	15W14	902 129 13
9W05	903	H32	2	2*33	FAN OUT TO MEMORY	15W14	903 130 3
9W05	904	H32	19	2*34	FAN OUT TO MEMORY	15W14	904 131 18
9W05	905	H32	24	2*35	FAN OUT TO MEMORY	15W14	905 131 13
9W05	906	H34	2	2*36	FAN OUT TO MEMORY	15W14	906 132 3
9W05	907	H34	19	2*37	FAN OUT TO MEMORY	15W14	907 133 18
9W05	908					15W14	908

9W06	00	MEMORY FROM WRITE DISTRIBUTOR				16W14	00
9W06	90	H40	24	2*53	FAN OUT TO MEMORY	16W14	90 111 13
9W06	91	H41	2	2*54	FAN OUT TO MEMORY	16W14	91 112 3
9W06	92	H41	19	2*55	FAN OUT TO MEMORY	16W14	92 113 18
9W06	93	H41	24	2*56	FAN OUT TO MEMORY	16W14	93 113 13
9W06	94	H42	2	2*57	FAN OUT TO MEMORY	16W14	94 114 3
9W06	95	H42	19	2*58	FAN OUT TO MEMORY	16W14	95 115 18
9W06	96	H42	24	2*59	FAN OUT TO MEMORY	16W14	96 115 13
9W06	97					16W14	97
9W06	98					16W14	98
9W06	99					16W14	99
9W06	900	H37	2	2*45	FAN OUT TO MEMORY	16W14	900 106 3
9W06	901	H37	19	2*46	FAN OUT TO MEMORY	16W14	901 107 18
9W06	902	H37	24	2*47	FAN OUT TO MEMORY	16W14	902 107 13
9W06	903	H39	2	2*48	FAN OUT TO MEMORY	16W14	903 108 3
9W06	904	H39	19	2*49	FAN OUT TO MEMORY	16W14	904 109 18
9W06	905	H39	24	2*50	FAN OUT TO MEMORY	16W14	905 109 13
9W06	906	H40	2	2*51	FAN OUT TO MEMORY	16W14	906 110 3
9W06	907	H40	19	2*52	FAN OUT TO MEMORY	16W14	907 111 18
9W06	908					16W14	908

9W07	00	MEMORY TO READ DISTRIBUTOR				3W07	00
9W07	90	H21	28	2*8	3W07	90	135 7
9W07	91	H22	1	2*9	3W07	91	136 7
9W07	92	H22	8	2*10	3W07	92	138 7
9W07	93	H22	28	2*11	3W07	93	139 7
9W07	94	H24	1	2*12	3W07	94	140 7
9W07	95	H24	8	2*13	3W07	95	141 7
9W07	96	H24	28	2*14	3W07	96	142 7
9W07	97				3W07	97	
9W07	98				3W07	98	
9W07	99	G39	28	ACCEPT	3W07	99	123 7
9W07	900	H19	1	2*0	3W07	900	125 7
9W07	901	H19	8	2*1	3W07	901	126 7
9W07	902	H19	28	2*2	3W07	902	127 7
9W07	903	H20	1	2*3	3W07	903	128 7
9W07	904	H20	8	2*4	3W07	904	129 7
9W07	905	H20	28	2*5	3W07	905	132 7
9W07	906	H21	1	2*6	3W07	906	133 7
9W07	907	H21	8	2*7	3W07	907	134 7
9W07	908				3W07	908	

9W08	00	MEMORY TO READ DISTRIBUTOR				4W12	00
9W08	90	H27	28	2*23	4W12	90	111 7
9W08	91	H29	1	2*24	4W12	91	112 7
9W08	92	H29	8	2*25	4W12	92	114 7
9W08	93	H29	28	2*26	4W12	93	115 7
9W08	94	H30	1	2*27	4W12	94	116 7
9W08	95	H30	8	2*28	4W12	95	117 7
9W08	96	H30	28	2*29	4W12	96	118 7
9W08	97				4W12	97	
9W08	98				4W12	98	
9W08	99				4W12	99	
9W08	900	H25	1	2*15	4W12	900	101 7
9W08	901	H25	8	2*16	4W12	901	102 7
9W08	902	H25	28	2*17	4W12	902	103 7
9W08	903	H26	1	2*18	4W12	903	104 7
9W08	904	H26	8	2*19	4W12	904	105 7
9W08	905	H26	28	2*20	4W12	905	108 7
9W08	906	H27	1	2*21	4W12	906	109 7
9W08	907	H27	8	2*22	4W12	907	110 7
9W08	908				4W12	908	

9W09	00	MEMORY TO READ DISTRIBUTOR				10W15	00
9W09	90	H40	28	2*53	10W15	90	I11 7
9W09	91	H41	1	2*54	10W15	91	I12 7
9W09	92	H41	8	2*55	10W15	92	I14 7
9W09	93	H41	28	2*56	10W15	93	I15 7
9W09	94	H42	1	2*57	10W15	94	I16 7
9W09	95	H42	8	2*58	10W15	95	I17 7
9W09	96	H42	28	2*59	10W15	96	I18 7
9W09	97				10W15	97	
9W09	98				10W15	98	
9W09	99				10W15	99	
9W09	900	H37	1	2*45	10W15	900	I01 7
9W09	901	H37	8	2*46	10W15	901	I02 7
9W09	902	H37	28	2*47	10W15	902	I03 7
9W09	903	H39	1	2*48	10W15	903	I04 7
9W09	904	H39	8	2*49	10W15	904	I05 7
9W09	905	H39	28	2*50	10W15	905	I08 7
9W09	906	H40	1	2*51	10W15	906	I09 7
9W09	907	H40	8	2*52	10W15	907	I10 7
9W09	908				10W15	908	

9W10	00	READ DISTRIBUTOR TO CONTROL				5W04	00	
9W10	90	I35	26	2*38	BUFFER TO CONTROL	5W04	90	B42 27
9W10	91	I36	26	2*39	BUFFER TO CONTROL	5W04	91	C41 4
9W10	92	I38	26	2*40	BUFFER TO CONTROL	5W04	92	C41 6
9W10	93	I39	26	2*41	BUFFER TO CONTROL	5W04	93	C41 7
9W10	94	I40	26	2*42	BUFFER TO CONTROL	5W04	94	C41 22
9W10	95	I41	26	2*43	BUFFER TO CONTROL	5W04	95	C41 25
9W10	96	I42	26	2*44	BUFFER TO CONTROL	5W04	96	C41 27
9W10	97					5W04	97	
9W10	98					5W04	98	
9W10	99					5W04	99	
9W10	900	I25	26	2*30	BUFFER TO CONTROL	5W04	900	B41 22
9W10	901	I26	26	2*31	BUFFER TO CONTROL	5W04	901	B41 25
9W10	902	I27	26	2*32	BUFFER TO CONTROL	5W04	902	B41 27
9W10	903	I28	26	2*33	BUFFER TO CONTROL	5W04	903	B42 4
9W10	904	I29	26	2*34	BUFFER TO CONTROL	5W04	904	B42 6
9W10	905	I32	26	2*35	BUFFER TO CONTROL	5W04	905	B42 7
9W10	906	I33	26	2*36	BUFFER TO CONTROL	5W04	906	B42 22
9W10	907	I34	26	2*37	BUFFER TO CONTROL	5W04	907	B42 25
9W10	908					5W04	908	

9W11	00	CENTRAL MEMORY TO PERIPHERAL				1W06	00
9W11	90	135	1	2*38	CENTRAL TO PERIPHERAL DATA	1W06	90 G39 10
9W11	91	136	1	2*39	CENTRAL TO PERIPHERAL DATA	1W06	91 G39 21
9W11	92	138	1	2*40	CENTRAL TO PERIPHERAL DATA	1W06	92 G39 24
9W11	93	139	1	2*41	CENTRAL TO PERIPHERAL DATA	1W06	93 G39 26
9W11	94	140	1	2*42	CENTRAL TO PERIPHERAL DATA	1W06	94 G40 5
9W11	95	141	1	2*43	CENTRAL TO PERIPHERAL DATA	1W06	95 G40 7
9W11	96	142	1	2*44	CENTRAL TO PERIPHERAL DATA	1W06	96 G40 10
9W11	97	C14	14	MEMORY MARGIN		1W06	97 H29 18
9W11	98					1W06	98
9W11	99					1W06	99
9W11	900	125	1	2*30	CENTRAL TO PERIPHERAL DATA	1W06	900 G38 5
9W11	901	126	1	2*31	CENTRAL TO PERIPHERAL DATA	1W06	901 G38 7
9W11	902	127	1	2*32	CENTRAL TO PERIPHERAL DATA	1W06	902 G38 10
9W11	903	128	1	2*33	CENTRAL TO PERIPHERAL DATA	1W06	903 G38 21
9W11	904	129	1	2*34	CENTRAL TO PERIPHERAL DATA	1W06	904 G38 24
9W11	905	132	1	2*35	CENTRAL TO PERIPHERAL DATA	1W06	905 G38 26
9W11	906	133	1	2*36	CENTRAL TO PERIPHERAL DATA	1W06	906 G39 5
9W11	907	134	1	2*37	CENTRAL TO PERIPHERAL DATA	1W06	907 G39 7
9W11	908					1W06	908

9W12	00	READ DISTRIBUTOR TO LOWER REGISTER				7W07	00
9W12	90					7W07	90
9W12	91					7W07	91
9W12	92					7W07	92
9W12	93					7W07	93
9W12	94					7W07	94
9W12	95					7W07	95
9W12	96					7W07	96
9W12	97					7W07	97
9W12	98					7W07	98
9W12	99					7W07	99
9W12	900	125	28	2*30	MEMORY TO REGISTER	7W07	900 C40 2
9W12	901	126	28	2*31	MEMORY TO REGISTER	7W07	901 C40 26
9W12	902	127	28	2*32	MEMORY TO REGISTER	7W07	902 C41 2
9W12	903	128	28	2*33	MEMORY TO REGISTER	7W07	903 C41 26
9W12	904	129	28	2*34	MEMORY TO REGISTER	7W07	904 C42 2
9W12	905	132	28	2*35	MEMORY TO REGISTER	7W07	905 C42 26
9W12	906					7W07	906
9W12	907					7W07	907
9W12	908					7W07	908

9W13	00	READ DISTRIBUTOR FROM MEMORY				3W02	00			
9W13	90	I35	5	2*38	3W02	90	H34	28		
9W13	91	I36	5	2*39	3W02	91	H35	1		
9W13	92	I38	5	2*40	3W02	92	H35	8		
9W13	93	I39	5	2*41	3W02	93	H35	28		
9W13	94	I40	5	2*42	3W02	94	H36	1		
9W13	95	I41	5	2*43	3W02	95	H36	8		
9W13	96	I42	5	2*44	3W02	96	H36	28		
9W13	97				3W02	97				
9W13	98				3W02	98				
9W13	99				3W02	99				
9W13	900	I25	5	2*30	3W02	900	H31	1		
9W13	901	I26	5	2*31	3W02	901	H31	8		
9W13	902	I27	5	2*32	3W02	902	H31	28		
9W13	903	I28	5	2*33	3W02	903	H32	1		
9W13	904	I29	5	2*34	3W02	904	H32	8		
9W13	905	I32	5	2*35	3W02	905	H32	28		
9W13	906	I33	5	2*36	3W02	906	H34	1		
9W13	907	I34	5	2*37	3W02	907	H34	8		
9W13	908				3W02	908				

9W14	00	READ DISTRIBUTOR FROM MEMORY				4W08	00			
9W14	90	I35	3	2*38	4W08	90	H16	28		
9W14	91	I36	3	2*39	4W08	91	H17	1		
9W14	92	I38	3	2*40	4W08	92	H17	8		
9W14	93	I39	3	2*41	4W08	93	H17	28		
9W14	94	I40	3	2*42	4W08	94	H18	1		
9W14	95	I41	3	2*43	4W08	95	H18	8		
9W14	96	I42	3	2*44	4W08	96	H18	28		
9W14	97	G38	21	GO	4W08	97	I21	20		
9W14	98	G38	20	WRIT	4W08	98	I24	5		
9W14	99	I24	12	READ PERIPHERAL	4W08	99	I21	9		
9W14	900	I25	3	2*30	4W08	900	H13	1		
9W14	901	I26	3	2*31	4W08	901	H13	8		
9W14	902	I27	3	2*32	4W08	902	H13	28		
9W14	903	I28	3	2*33	4W08	903	H14	1		
9W14	904	I29	3	2*34	4W08	904	H14	8		
9W14	905	I32	3	2*35	4W08	905	H14	28		
9W14	906	I33	3	2*36	4W08	906	H16	1		
9W14	907	I34	3	2*37	4W08	907	H16	8		
9W14	908				4W08	908				

9W15	00	READ DISTRIBUTOR FROM MEMORY				10W09	00
9W15	90	135	9	2*38	10W09	90	H16 28
9W15	91	136	9	2*39	10W09	91	H17 1
9W15	92	138	9	2*40	10W09	92	H17 8
9W15	93	139	9	2*41	10W09	93	H17 28
9W15	94	140	9	2*42	10W09	94	H18 1
9W15	95	141	9	2*43	10W09	95	H18 8
9W15	96	142	9	2*44	10W09	96	H18 28
9W15	97				10W09	97	
9W15	98				10W09	98	
9W15	99				10W09	99	
9W15	900	125	9	2*30	10W09	900	H13 1
9W15	901	126	9	2*31	10W09	901	H13 8
9W15	902	127	9	2*32	10W09	902	H13 28
9W15	903	128	9	2*33	10W09	903	H14 1
9W15	904	129	9	2*34	10W09	904	H14 8
9W15	905	132	9	2*35	10W09	905	H14 28
9W15	906	133	9	2*36	10W09	906	H16 1
9W15	907	134	9	2*37	10W09	907	H16 8
9W15	908				10W09	908	

9W16	00	READ DISTRIBUTOR FROM MEMORY				13W08	00
9W16	90	135	11	2*38	13W08	90	H34 28
9W16	91	136	11	2*39	13W08	91	H35 1
9W16	92	138	11	2*40	13W08	92	H35 8
9W16	93	139	11	2*41	13W08	93	H35 28
9W16	94	140	11	2*42	13W08	94	H36 1
9W16	95	141	11	2*43	13W08	95	H36 8
9W16	96	142	11	2*44	13W08	96	H36 28
9W16	97				13W08	97	
9W16	98				13W08	98	
9W16	99				13W08	99	
9W16	900	125	11	2*30	13W08	900	H31 1
9W16	901	126	11	2*31	13W08	901	H31 8
9W16	902	127	11	2*32	13W08	902	H31 28
9W16	903	128	11	2*33	13W08	903	H32 1
9W16	904	129	11	2*34	13W08	904	H32 8
9W16	905	132	11	2*35	13W08	905	H32 28
9W16	906	133	11	2*36	13W08	906	H34 1
9W16	907	134	11	2*37	13W08	907	H34 8
9W16	908				13W08	908	

9W17	00	READ DISTRIBUTOR FROM MEMORY				14W08	00
9W17	90	I35	19	2*38	14W08	90	H16 28
9W17	91	I36	19	2*39	14W08	91	H17 1
9W17	92	I38	19	2*40	14W08	92	H17 8
9W17	93	I39	19	2*41	14W08	93	H17 28
9W17	94	I40	19	2*42	14W08	94	H18 1
9W17	95	I41	19	2*43	14W08	95	H18 8
9W17	96	I42	19	2*44	14W08	96	H18 28
9W17	97				14W08	97	
9W17	98				14W08	98	
9W17	99				14W08	99	
9W17	900	I25	19	2*30	14W08	900	H13 1
9W17	901	I26	19	2*31	14W08	901	H13 8
9W17	902	I27	19	2*32	14W08	902	H13 28
9W17	903	I28	19	2*33	14W08	903	H14 1
9W17	904	I29	19	2*34	14W08	904	H14 8
9W17	905	I32	19	2*35	14W08	905	H14 28
9W17	906	I33	19	2*36	14W08	906	H16 1
9W17	907	I34	19	2*37	14W08	907	H16 8
9W17	908				14W08	908	

9W18	00	READ DISTRIBUTOR FROM MEMORY				15W08	00
9W18	90	I35	21	2*38	15W08	90	H34 28
9W18	91	I36	21	2*39	15W08	91	H35 1
9W18	92	I38	21	2*40	15W08	92	H35 8
9W18	93	I39	21	2*41	15W08	93	H35 28
9W18	94	I40	21	2*42	15W08	94	H36 1
9W18	95	I41	21	2*43	15W08	95	H36 8
9W18	96	I42	21	2*44	15W08	96	H36 28
9W18	97				15W08	97	
9W18	98				15W08	98	
9W18	99				15W08	99	
9W18	900	I25	21	2*30	15W08	900	H31 1
9W18	901	I26	21	2*31	15W08	901	H31 8
9W18	902	I27	21	2*32	15W08	902	H31 28
9W18	903	I28	21	2*33	15W08	903	H32 1
9W18	904	I29	21	2*34	15W08	904	H32 8
9W18	905	I32	21	2*35	15W08	905	H32 28
9W18	906	I33	21	2*36	15W08	906	H34 1
9W18	907	I34	21	2*37	15W08	907	H34 8
9W18	908				15W08	908	

9W19	00	READ DISTRIBUTOR FROM MEMORY				16W08	00
9W19	90	I35	23	2*38	16W08	90	H16 28
9W19	91	I36	23	2*39	16W08	91	H17 1
9W19	92	I38	23	2*40	16W08	92	H17 8
9W19	93	I39	23	2*41	16W08	93	H17 28
9W19	94	I40	23	2*42	16W08	94	H18 1
9W19	95	I41	23	2*43	16W08	95	H18 8
9W19	96	I42	23	2*44	16W08	96	H18 28
9W19	97				16W08	97	
9W19	98				16W08	98	
9W19	99				16W08	99	
9W19	900	I25	23	2*30	16W08	900	H13 1
9W19	901	I26	23	2*31	16W08	901	H13 8
9W19	902	I27	23	2*32	16W08	902	H13 28
9W19	903	I28	23	2*33	16W08	903	H14 1
9W19	904	I29	23	2*34	16W08	904	H14 8
9W19	905	I32	23	2*35	16W08	905	H14 28
9W19	906	I33	23	2*36	16W08	906	H16 1
9W19	907	I34	23	2*37	16W08	907	H16 8
9W19	908				16W08	908	

9W20	00	MEMORY ADDRESS FROM CONTROL				5W30	00
9W20	90	G40	18	2*8 ADDRESS TO MEMORY	5W30	90	Q36 22
9W20	91	G41	11	2*9 ADDRESS TO MEMORY	5W30	91	Q37 11
9W20	92	G41	14	2*10 ADDRESS TO MEMORY	5W30	92	Q37 26
9W20	93	G41	17	2*11 ADDRESS TO MEMORY	5W30	93	Q37 22
9W20	94	G41	18	2*12 ADDRESS TO MEMORY	5W30	94	Q38 11
9W20	95	G42	11	2*13 ADDRESS TO MEMORY	5W30	95	Q38 26
9W20	96	G42	14	2*14 ADDRESS TO MEMORY	5W30	96	Q38 22
9W20	97	G42	17	2*15 ADDRESS TO MEMORY	5W30	97	Q39 11
9W20	98	G42	18	2*16 ADDRESS TO MEMORY	5W30	98	Q39 26
9W20	99				5W30	99	
9W20	900	G35	12	CLOCK	5W30	900	M32 8
9W20	901	G39	18	2*0 BANK SELECT	5W30	901	Q34 11
9W20	902	G39	22	2*1 BANK SELECT	5W30	902	Q34 26
9W20	903	G38	9	2*2 CHASSIS 9 SELECT (010)	5W30	903	Q34 22
9W20	904	G38	5	2*3 CHASSIS 9 SELECT (010)	5W30	904	Q35 11
9W20	905	G38	14	2*4 CHASSIS 9 SELECT (010)	5W30	905	Q35 26
9W20	906	G40	11	2*5 ADDRESS TO MEMORY	5W30	906	Q35 22
9W20	907	G40	14	2*6 ADDRESS TO MEMORY	5W30	907	Q36 11
9W20	908	G40	17	2*7 ADDRESS TO MEMORY	5W30	908	Q36 26

9W21	00	READ DISTRIBUTOR TO UPPER REGISTER				8W05	00		
9W21	90				8W05	90			
9W21	91				8W05	91			
9W21	92				8W05	92			
9W21	93				8W05	93			
9W21	94				8W05	94			
9W21	95				8W05	95			
9W21	96				8W05	96			
9W21	97				8W05	97			
9W21	98				8W05	98			
9W21	99				8W05	99			
9W21	900	I33	28	2*36 MEMORY TO REGISTER	8W05	900	A01	2	
9W21	901	I34	28	2*37 MEMORY TO REGISTER	8W05	901	A01	26	
9W21	902	I35	28	2*38 MEMORY TO REGISTER	8W05	902	A02	2	
9W21	903	I36	28		8W05	903	A02	26	
9W21	904	I38	28	2*40 MEMORY TO REGISTER	8W05	904	A03	2	
9W21	905	I39	28	2*41 MEMORY TO REGISTER	8W05	905	A03	26	
9W21	906	I40	28	2*42 MEMORY TO REGISTER	8W05	906	A04	2	
9W21	907	I41	28	2*43 MEMORY TO REGISTER	8W05	907	A04	26	
9W21	908	I42	28	2*44 MEMORY TO REGISTER	8W05	908	A05	2	

9W34 00 626 SYNC TO CHANNEL (INPUT)

9W34	90	J35	7	2*0 INPUT DATA
9W34	91	J35	5	2*1 INPUT DATA
9W34	92	J35	3	2*2 INPUT DATA
9W34	93	J35	28	2*3 INPUT DATA
9W34	94	J35	26	2*4 INPUT DATA
9W34	95	J35	24	2*5 INPUT DATA
9W34	96	J37	7	2*6 INPUT DATA
9W34	97	J37	5	2*7 INPUT DATA
9W34	98	J37	3	2*8 INPUT DATA
9W34	99	J37	28	2*9 INPUT DATA
9W34	900	J37	26	2*10 INPUT DATA
9W34	901	J37	24	2*11 INPUT DATA
9W34	902	J33	28	ACTIVE
9W34	903	J33	3	INACTIVE
9W34	904	J33	7	FULL
9W34	905	J33	5	EMPTY
9W34	906	J31	13	
9W34	907	J31	3	CLOCK (1US)
9W34	908			

9W35 00 626 SYNC TO CHANNEL (OUTPUT)

9W35	90	J40	3	2*0	OUTPUT DATA
9W35	91	J40	13	2*1	OUTPUT DATA
9W35	92	J40	16	2*2	OUTPUT DATA
9W35	93	J40	26	2*3	OUTPUT DATA
9W35	94	J41	3	2*4	OUTPUT DATA
9W35	95	J41	13	2*5	OUTPUT DATA
9W35	96	J41	16	2*6	OUTPUT DATA
9W35	97	J41	26	2*7	OUTPUT DATA
9W35	98	J42	3	2*8	OUTPUT DATA
9W35	99	J42	13	2*9	OUTPUT DATA
9W35	900	J42	16	2*10	OUTPUT DATA
9W35	901	J42	26	2*11	OUTPUT DATA
9W35	902	J38	13		ACTIVE
9W35	903	J38	16		INACTIVE
9W35	904	J38	26		FULL
9W35	905	J39	3		EMPTY
9W35	906	J38	3		FUNCTION
9W35	907	J39	13		MASTER CLEAR
9W35	908				

9W36 00 626 SYNC PASSON (INPUT)

9W36	90	J35	23	2*0	INPUT DATA
9W36	91	J35	22	2*1	INPUT DATA
9W36	92	J35	21	2*2	INPUT DATA
9W36	93	J35	10	2*3	INPUT DATA
9W36	94	J35	9	2*4	INPUT DATA
9W36	95	J35	8	2*5	INPUT DATA
9W36	96	J37	23	2*6	INPUT DATA
9W36	97	J37	22	2*7	INPUT DATA
9W36	98	J37	21	2*8	INPUT DATA
9W36	99	J37	10	2*9	INPUT DATA
9W36	900	J37	9	2*10	INPUT DATA
9W36	901	J37	8	2*11	INPUT DATA
9W36	902	J33	10		ACTIVE
9W36	903	J33	21		INACTIVE
9W36	904	J33	23		FULL
9W36	905	J33	22		EMPTY
9W36	906	J31	15		
9W36	907	J31	1		CLOCK (1 M SEC)
9W36	908				

9W37 00 626 SYNC PASSON (OUTPUT)

9W37	90	J40	1	2*0	OUTPUT DATA
9W37	91	J40	15	2*1	OUTPUT DATA
9W37	92	J40	14	2*2	OUTPUT DATA
9W37	93	J40	28	2*3	OUTPUT DATA
9W37	94	J41	1	2*4	OUTPUT DATA
9W37	95	J41	15	2*5	OUTPUT DATA
9W37	96	J41	14	2*6	OUTPUT DATA
9W37	97	J41	28	2*7	OUTPUT DATA
9W37	98	J42	1	2*8	OUTPUT DATA
9W37	99	J42	15	2*9	OUTPUT DATA
9W37	900	J42	14	2*10	OUTPUT DATA
9W37	901	J42	28	2*11	OUTPUT DATA
9W37	902	J38	15		ACTIVE
9W37	903	J38	14		INACTIVE
9W37	904	J38	28		FULL
9W37	905	J39	1		EMPTY
9W37	906	J38	1		FUNCTION
9W37	907	J39	15		MASTER CLEAR
9W37	908				

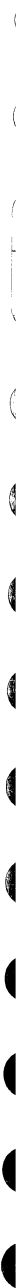
9W39 00 MEMORY TO READ DISTRIBUTOR

9W39	90	H34	28	2*38	
9W39	91	H35	1	2*39	
9W39	92	H35	8	2*40	
9W39	93	H35	28	2*41	
9W39	94	H36	1	2*42	
9W39	95	H36	8	2*43	
9W39	96	H36	28	2*44	
9W39	97				
9W39	98				
9W39	99				
9W39	900	H31	1	2*30	
9W39	901	H31	8	2*31	
9W39	902	H31	28	2*32	
9W39	903	H32	1	2*33	
9W39	904	H32	8	2*34	
9W39	905	H32	28	2*35	
9W39	906	H34	1	2*36	
9W39	907	H34	8	2*37	
9W39	908				

9W39 00

9W39	90	135	7	
9W39	91	136	7	
9W39	92	138	7	
9W39	93	139	7	
9W39	94	140	7	
9W39	95	141	7	
9W39	96	142	7	
9W39	97			
9W39	98			
9W39	99			
9W39	900	125	7	
9W39	901	126	7	
9W39	902	127	7	
9W39	903	128	7	
9W39	904	129	7	
9W39	905	132	7	
9W39	906	133	7	
9W39	907	134	7	
9W39	908			

9W39 00 READ DISTRIBUTOR FROM MEMORY				9W39 00			
9W39	90	135	7	2*38	9W39	90	H34 28
9W39	91	136	7	2*39	9W39	91	H35 1
9W39	92	138	7	2*40	9W39	92	H35 8
9W39	93	139	7	2*41	9W39	93	H35 28
9W39	94	140	7	2*42	9W39	94	H36 1
9W39	95	141	7	2*43	9W39	95	H36 8
9W39	96	142	7	2*44	9W39	96	H36 28
9W39	97				9W39	97	
9W39	98				9W39	98	
9W39	99				9W39	99	
9W39	900	125	7	2*30	9W39	900	H31 1
9W39	901	126	7	2*31	9W39	901	H31 8
9W39	902	127	7	2*32	9W39	902	H31 28
9W39	903	128	7	2*33	9W39	903	H32 1
9W39	904	129	7	2*34	9W39	904	H32 8
9W39	905	132	7	2*35	9W39	905	H32 28
9W39	906	133	7	2*36	9W39	906	H34 1
9W39	907	134	7	2*37	9W39	907	H34 8
9W39	908				9W39	908	



10W03	00	MEMORY FROM WRITE DISTRIBUTOR				13W15	00
10W03	90	H03	24	2*8	FAN OUT TO MEMORY	13W15	90 133 13
10W03	91	H04	2	2*9	FAN OUT TO MEMORY	13W15	91 134 3
10W03	92	H04	19	2*10	FAN OUT TO MEMORY	13W15	92 135 18
10W03	93	H04	24	2*11	FAN OUT TO MEMORY	13W15	93 135 13
10W03	94	H06	2	2*12	FAN OUT TO MEMORY	13W15	94 136 3
10W03	95	H06	19	2*13	FAN OUT TO MEMORY	13W15	95 137 18
10W03	96	H06	24	2*14	FAN OUT TO MEMORY	13W15	96 137 13
10W03	97					13W15	97
10W03	98					13W15	98
10W03	99					13W15	99
10W03	900	H01	2	2*0	FAN OUT TO MEMORY	13W15	900 128 3
10W03	901	H01	19	2*1	FAN OUT TO MEMORY	13W15	901 129 18
10W03	902	H01	24	2*2	FAN OUT TO MEMORY	13W15	902 129 13
10W03	903	H02	2	2*3	FAN OUT TO MEMORY	13W15	903 130 3
10W03	904	H02	19	2*4	FAN OUT TO MEMORY	13W15	904 131 18
10W03	905	H02	24	2*5	FAN OUT TO MEMORY	13W15	905 131 13
10W03	906	H03	2	2*6	FAN OUT TO MEMORY	13W15	906 132 3
10W03	907	H03	19	2*7	FAN OUT TO MEMORY	13W15	907 133 18
10W03	908					13W15	908

10W04	00	MEMORY FROM WRITE DISTRIBUTOR				14W15	00
10W04	90	H09	24	2*23	FAN OUT TO MEMORY	14W15	90 111 11
10W04	91	H11	2	2*24	FAN OUT TO MEMORY	14W15	91 112 5
10W04	92	H11	19	2*25	FAN OUT TO MEMORY	14W15	92 113 20
10W04	93	H11	24	2*26	FAN OUT TO MEMORY	14W15	93 113 11
10W04	94	H12	2	2*27	FAN OUT TO MEMORY	14W15	94 114 5
10W04	95	H12	19	2*28	FAN OUT TO MEMORY	14W15	95 115 20
10W04	96	H12	24	2*29	FAN OUT TO MEMORY	14W15	96 115 11
10W04	97					14W15	97
10W04	98					14W15	98
10W04	99					14W15	99
10W04	900	H07	2	2*15	FAN OUT TO MEMORY	14W15	900 106 5
10W04	901	H07	19	2*16	FAN OUT TO MEMORY	14W15	901 107 20
10W04	902	H07	24	2*17	FAN OUT TO MEMORY	14W15	902 107 11
10W04	903	H08	2	2*18	FAN OUT TO MEMORY	14W15	903 108 5
10W04	904	H08	19	2*19	FAN OUT TO MEMORY	14W15	904 109 20
10W04	905	H08	24	2*20	FAN OUT TO MEMORY	14W15	905 109 11
10W04	906	H09	2	2*21	FAN OUT TO MEMORY	14W15	906 110 5
10W04	907	H09	19	2*22	FAN OUT TO MEMORY	14W15	907 111 20
10W04	908					14W15	908

10W05	00	MEMORY FROM WRITE DISTRIBUTOR				15W15	00
10W05	90	H16	24	2*38	FAN OUT TO MEMORY	15W15	90 133 11
10W05	91	H17	2	2*39	FAN OUT TO MEMORY	15W15	91 134 5
10W05	92	H17	19	2*40	FAN OUT TO MEMORY	15W15	92 135 20
10W05	93	H17	24	2*41	FAN OUT TO MEMORY	15W15	93 135 11
10W05	94	H18	2	2*42	FAN OUT TO MEMORY	15W15	94 136 5
10W05	95	H18	19	2*43	FAN OUT TO MEMORY	15W15	95 137 20
10W05	96	H18	24	2*44	FAN OUT TO MEMORY	15W15	96 137 11
10W05	97					15W15	97
10W05	98					15W15	98
10W05	99					15W15	99
10W05	900	H13	2	2*30	FAN OUT TO MEMORY	15W15	900 128 5
10W05	901	H13	19	2*31	FAN OUT TO MEMORY	15W15	901 129 20
10W05	902	H13	24	2*32	FAN OUT TO MEMORY	15W15	902 129 11
10W05	903	H14	2	2*33	FAN OUT TO MEMORY	15W15	903 130 5
10W05	904	H14	19	2*34	FAN OUT TO MEMORY	15W15	904 131 20
10W05	905	H14	24	2*35	FAN OUT TO MEMORY	15W15	905 131 11
10W05	906	H16	2	2*36	FAN OUT TO MEMORY	15W15	906 132 5
10W05	907	H16	19	2*37	FAN OUT TO MEMORY	15W15	907 133 20
10W05	908					15W15	908

10W06	00	MEMORY FROM WRITE DISTRIBUTOR				16W15	00
10W06	90	H22	24	2*53	FAN OUT TO MEMORY	16W15	90 111 11
10W06	91	H23	2	2*54	FAN OUT TO MEMORY	16W15	91 112 5
10W06	92	H23	19	2*55	FAN OUT TO MEMORY	16W15	92 113 20
10W06	93	H23	24	2*56	FAN OUT TO MEMORY	16W15	93 113 11
10W06	94	H24	2	2*57	FAN OUT TO MEMORY	16W15	94 114 5
10W06	95	H24	19	2*58	FAN OUT TO MEMORY	16W15	95 115 20
10W06	96	H24	24	2*59	FAN OUT TO MEMORY	16W15	96 115 11
10W06	97					16W15	97
10W06	98					16W15	98
10W06	99					16W15	99
10W06	900	H19	2	2*45	FAN OUT TO MEMORY	16W15	900 106 5
10W06	901	H19	19	2*46	FAN OUT TO MEMORY	16W15	901 107 20
10W06	902	H19	24	2*47	FAN OUT TO MEMORY	16W15	902 107 11
10W06	903	H21	2	2*48	FAN OUT TO MEMORY	16W15	903 108 5
10W06	904	H21	19	2*49	FAN OUT TO MEMORY	16W15	904 109 20
10W06	905	H21	24	2*50	FAN OUT TO MEMORY	16W15	905 109 11
10W06	906	H22	2	2*51	FAN OUT TO MEMORY	16W15	906 110 5
10W06	907	H22	19	2*52	FAN OUT TO MEMORY	16W15	907 111 20
10W06	908					16W15	908

10W07	00	MEMORY TO READ DISTRIBUTOR				3W08	00		
10W07	90	H03	28	2*8	3W08	90	I35	9	
10W07	91	H04	1	2*9	3W08	91	I36	9	
10W07	92	H04	8	2*10	3W08	92	I38	9	
10W07	93	H04	28	2*11	3W08	93	I39	9	
10W07	94	H06	1	2*12	3W08	94	I40	9	
10W07	95	H06	8	2*13	3W08	95	I41	9	
10W07	96	H06	28	2*14	3W08	96	I42	9	
10W07	97				3W08	97			
10W07	98				3W08	98			
10W07	99				3W08	99			
10W07	900	H01	1	2*0	3W08	900	I25	9	
10W07	901	H01	8	2*1	3W08	901	I26	9	
10W07	902	H01	28	2*2	3W08	902	I27	9	
10W07	903	H02	1	2*3	3W08	903	I28	9	
10W07	904	H02	8	2*4	3W08	904	I29	9	
10W07	905	H02	28	2*5	3W08	905	I32	9	
10W07	906	H03	1	2*6	3W08	906	I33	9	
10W07	907	H03	8	2*7	3W08	907	I34	9	
10W07	908				3W08	908			

10W08	00	MEMORY TO READ DISTRIBUTOR				4W13	00		
10W08	90	H09	28	2*23	4W13	90	I11	9	
10W08	91	H11	1	2*24	4W13	91	I12	9	
10W08	92	H11	8	2*25	4W13	92	I14	9	
10W08	93	H11	28	2*26	4W13	93	I15	9	
10W08	94	H12	1	2*27	4W13	94	I16	9	
10W08	95	H12	8	2*28	4W13	95	I17	9	
10W08	96	H12	28	2*29	4W13	96	I18	9	
10W08	97				4W13	97			
10W08	98				4W13	98			
10W08	99				4W13	99			
10W08	900	H07	1	2*15	4W13	900	I01	9	
10W08	901	H07	8	2*16	4W13	901	I02	9	
10W08	902	H07	28	2*17	4W13	902	I03	9	
10W08	903	H08	1	2*18	4W13	903	I04	9	
10W08	904	H08	8	2*19	4W13	904	I05	9	
10W08	905	H08	28	2*20	4W13	905	I08	9	
10W08	906	H09	1	2*21	4W13	906	I09	9	
10W08	907	H09	8	2*22	4W13	907	I10	9	
10W08	908				4W13	908			

10W09	00	MEMORY TO READ DISTRIBUTOR				9W15	00
10W09	90	H16	28	2+38	9W15	90	I35 9
10W09	91	H17	1	2+39	9W15	91	I36 9
10W09	92	H17	8	2+40	9W15	92	I38 9
10W09	93	H17	28	2+41	9W15	93	I39 9
10W09	94	H18	1	2+42	9W15	94	I40 9
10W09	95	H18	8	2+43	9W15	95	I41 9
10W09	96	H18	28	2+44	9W15	96	I42 9
10W09	97				9W15	97	
10W09	98				9W15	98	
10W09	99				9W15	99	
10W09	900	H13	1	2+30	9W15	900	I25 9
10W09	901	H13	8	2+31	9W15	901	I26 9
10W09	902	H13	28	2+32	9W15	902	I27 9
10W09	903	H14	1	2+33	9W15	903	I28 9
10W09	904	H14	8	2+34	9W15	904	I29 9
10W09	905	H14	28	2+35	9W15	905	I32 9
10W09	906	H16	1	2+36	9W15	906	I33 9
10W09	907	H16	8	2+37	9W15	907	I34 9
10W09	908				9W15	908	

10W10	00	READ DISTRIBUTOR TO CONTROL				5W03	00
10W10	90	I11	26	2+53	5W03	90	A42 7
10W10	91	I12	26	2+54	5W03	91	A42 22
10W10	92	I14	26	2+55	5W03	92	A42 25
10W10	93	I15	26	2+56	5W03	93	A42 27
10W10	94	I16	26	2+57	5W03	94	B41 4
10W10	95	I17	26	2+58	5W03	95	B41 6
10W10	96	I18	26	2+59	5W03	96	B41 7
10W10	97				5W03	97	
10W10	98				5W03	98	
10W10	99				5W03	99	
10W10	900	I01	26	2+45	5W03	900	A41 4
10W10	901	I02	26	2+46	5W03	901	A41 6
10W10	902	I03	26	2+47	5W03	902	A41 7
10W10	903	I04	26	2+48	5W03	903	A41 22
10W10	904	I05	26	2+49	5W03	904	A41 25
10W10	905	I08	26	2+50	5W03	905	A41 27
10W10	906	I09	26	2+51	5W03	906	A42 4
10W10	907	I10	26	2+52	5W03	907	A42 6
10W10	908				5W03	908	

10W11	00	CENTRAL MEMORY TO PERIPHERAL				1W07	00
10W11	90	I11	1	2*53	CENTRAL TO PERIPHERAL DATA	1W07	90 G41 26
10W11	91	I12	1	2*54	CENTRAL TO PERIPHERAL DATA	1W07	91 G42 5
10W11	92	I14	1	2*55	CENTRAL TO PERIPHERAL DATA	1W07	92 G42 7
10W11	93	I15	1	2*56	CENTRAL TO PERIPHERAL DATA	1W07	93 G42 10
10W11	94	I16	1	2*57	CENTRAL TO PERIPHERAL DATA	1W07	94 G42 21
10W11	95	I17	1	2*58	CENTRAL TO PERIPHERAL DATA	1W07	95 G42 24
10W11	96	I18	1	2*59	CENTRAL TO PERIPHERAL DATA	1W07	96 G42 26
10W11	97	C14	14		MEMORY MARGIN	1W07	97 H29 16
10W11	98					1W07	98
10W11	99					1W07	99
10W11	900	I01	1	2*45	CENTRAL TO PERIPHERAL DATA	1W07	900 G40 21
10W11	901	I02	1	2*46	CENTRAL TO PERIPHERAL DATA	1W07	901 G40 24
10W11	902	I03	1	2*47	CENTRAL TO PERIPHERAL DATA	1W07	902 G40 26
10W11	903	I04	1	2*48	CENTRAL TO PERIPHERAL DATA	1W07	903 G41 5
10W11	904	I05	1	2*49	CENTRAL TO PERIPHERAL DATA	1W07	904 G41 7
10W11	905	I08	1	2*50	CENTRAL TO PERIPHERAL DATA	1W07	905 G41 10
10W11	906	I09	1	2*51	CENTRAL TO PERIPHERAL DATA	1W07	906 G41 21
10W11	907	I10	1	2*52	CENTRAL TO PERIPHERAL DATA	1W07	907 G41 24
10W11	908					1W07	908

10W12	00	READ DISTRIBUTOR TO UPPER REGISTER				8W06	00
10W12	90	I01	28	2*45	MEMORY REGISTER	8W06	90 A05 26
10W12	91	I02	28	2*46	MEMORY REGISTER	8W06	91 A06 2
10W12	92	I03	28	2*47	MEMORY REGISTER	8W06	92 A06 26
10W12	93	I04	28	2*48	MEMORY REGISTER	8W06	93 A07 2
10W12	94	I05	28	2*49	MEMORY REGISTER	8W06	94 A07 26
10W12	95	I08	28	2*50	MEMORY REGISTER	8W06	95 A08 2
10W12	96	I09	28	2*51	MEMORY REGISTER	8W06	96 A08 26
10W12	97	I10	28	2*52	MEMORY REGISTER	8W06	97 B01 2
10W12	98	I11	28	2*53	MEMORY REGISTER	8W06	98 B01 26
10W12	99	I12	28	2*54	MEMORY REGISTER	8W06	99 B02 2
10W12	900	I14	28	2*55	MEMORY REGISTER	8W06	900 B02 26
10W12	901	I15	28	2*56	MEMORY REGISTER	8W06	901 B03 2
10W12	902	I16	28	2*57	MEMORY REGISTER	8W06	902 B03 26
10W12	903	I17	28	2*58	MEMORY REGISTER	8W06	903 B04 2
10W12	904	I18	28	2*59	MEMORY REGISTER	8W06	904 B04 26
10W12	905					8W06	905
10W12	906					8W06	906
10W12	907					8W06	907
10W12	908					8W06	908

10W13	00	READ DISTRIBUTOR FROM MEMORY				3W04	00
10W13	90	I11	5	2+53	3W04	90	H40 28
10W13	91	I12	5	2+54	3W04	91	H41 1
10W13	92	I14	5	2+55	3W04	92	H41 8
10W13	93	I15	5	2+56	3W04	93	H41 28
10W13	94	I16	5	2+57	3W04	94	H42 1
10W13	95	I17	5	2+58	3W04	95	H42 8
10W13	96	I18	5	2+59	3W04	96	H42 28
10W13	97	G05	28	ACCEPT FROM CHAS. 10	3W04	97	I23 9
10W13	98				3W04	98	
10W13	99				3W04	99	
10W13	900	I01	5	2+45	3W04	900	H37 1
10W13	901	I02	5	2+46	3W04	901	H37 8
10W13	902	I03	5	2+47	3W04	902	H37 28
10W13	903	I04	5	2+48	3W04	903	H39 1
10W13	904	I05	5	2+49	3W04	904	H39 8
10W13	905	I08	5	2+50	3W04	905	H39 28
10W13	906	I09	5	2+51	3W04	906	H40 1
10W13	907	I10	5	2+52	3W04	907	H40 8
10W13	908				3W04	908	

10W14	00	READ DISTRIBUTOR FROM MEMORY				4W11	00
10W14	90	I11	3	2+53	4W11	90	H22 28
10W14	91	I12	3	2+54	4W11	91	H23 1
10W14	92	I14	3	2+55	4W11	92	H23 8
10W14	93	I15	3	2+56	4W11	93	H23 28
10W14	94	I16	3	2+57	4W11	94	H24 1
10W14	95	I17	3	2+58	4W11	95	H24 8
10W14	96	I18	3	2+59	4W11	96	H24 28
10W14	97	G04	21	00	4W11	97	I21 18
10W14	98	G04	20	WRITE	4W11	98	I24 3
10W14	99	I19	12	READ PERIPHERAL	4W11	99	I21 7
10W14	900	I01	3	2+45	4W11	900	H19 1
10W14	901	I02	3	2+46	4W11	901	H19 8
10W14	902	I03	3	2+47	4W11	902	H19 28
10W14	903	I04	3	2+48	4W11	903	H21 1
10W14	904	I05	3	2+49	4W11	904	H21 8
10W14	905	I08	3	2+50	4W11	905	H21 28
10W14	906	I09	3	2+51	4W11	906	H22 1
10W14	907	I10	3	2+52	4W11	907	H22 8
10W14	908				4W11	908	

10W15	00	READ DISTRIBUTOR FROM MEMORY				9W09	00		
10W15	90	I11	7	2*53	9W09	90	H40	28	
10W15	91	I12	7	2*54	9W09	91	H41	1	
10W15	92	I14	7	2*55	9W09	92	H41	8	
10W15	93	I15	7	2*56	9W09	93	H41	28	
10W15	94	I16	7	2*57	9W09	94	H42	1	
10W15	95	I17	7	2*58	9W09	95	H42	8	
10W15	96	I18	7	2*59	9W09	96	H42	28	
10W15	97				9W09	97			
10W15	98				9W09	98			
10W15	99				9W09	99			
10W15	900	I01	7	2*45	9W09	900	H37	1	
10W15	901	I02	7	2*46	9W09	901	H37	8	
10W15	902	I03	7	2*47	9W09	902	H37	28	
10W15	903	I04	7	2*48	9W09	903	H39	1	
10W15	904	I05	7	2*49	9W09	904	H39	8	
10W15	905	I08	7	2*50	9W09	905	H39	28	
10W15	906	I09	7	2*51	9W09	906	H40	1	
10W15	907	I10	7	2*52	9W09	907	H40	8	
10W15	908				9W09	908			

10W16	00	READ DISTRIBUTOR FROM MEMORY				13W09	00		
10W16	90	I11	11	2*53	13W09	90	H40	28	
10W16	91	I12	11	2*54	13W09	91	H41	1	
10W16	92	I14	11	2*55	13W09	92	H41	8	
10W16	93	I15	11	2*56	13W09	93	H41	28	
10W16	94	I16	11	2*57	13W09	94	H42	1	
10W16	95	I17	11	2*58	13W09	95	H42	8	
10W16	96	I18	11	2*59	13W09	96	H42	28	
10W16	97				13W09	97			
10W16	98				13W09	98			
10W16	99				13W09	99			
10W16	900	I01	11	2*45	13W09	900	H37	1	
10W16	901	I02	11	2*46	13W09	901	H37	8	
10W16	902	I03	11	2*47	13W09	902	H37	28	
10W16	903	I04	11	2*48	13W09	903	H39	1	
10W16	904	I05	11	2*49	13W09	904	H39	8	
10W16	905	I08	11	2*50	13W09	905	H39	28	
10W16	906	I09	11	2*51	13W09	906	H40	1	
10W16	907	I10	11	2*52	13W09	907	H40	8	
10W16	908				13W09	908			

10W17 00 READ DISTRIBUTOR FROM MEMORY

10W17 90 I11 19 2+53
 10W17 91 I12 19 2+54
 10W17 92 I14 19 2+55
 10W17 93 I15 19 2+56
 10W17 94 I16 19 2+57
 10W17 95 I17 19 2+58
 10W17 96 I18 19 2+59
 10W17 97
 10W17 98
 10W17 99
 10W17 900 I01 19 2+45
 10W17 901 I02 19 2+46
 10W17 902 I03 19 2+47
 10W17 903 I04 19 2+48
 10W17 904 I05 19 2+49
 10W17 905 I08 19 2+50
 10W17 906 I09 19 2+51
 10W17 907 I10 19 2+52
 10W17 908

14W09 00

14W09 90 H22 28
 14W09 91 H23 1
 14W09 92 H23 8
 14W09 93 H23 28
 14W09 94 H24 1
 14W09 95 H24 8
 14W09 96 H24 28
 14W09 97
 14W09 98
 14W09 99
 14W09 900 H19 1
 14W09 901 H19 8
 14W09 902 H19 28
 14W09 903 H21 1
 14W09 904 H21 8
 14W09 905 H21 28
 14W09 906 H22 1
 14W09 907 H22 8
 14W09 908

10W18 00 READ DISTRIBUTOR FROM MEMORY

10W18 90 I11 21 2+53
 10W18 91 I12 21 2+54
 10W18 92 I14 21 2+55
 10W18 93 I15 21 2+56
 10W18 94 I16 21 2+57
 10W18 95 I17 21 2+58
 10W18 96 I18 21 2+59
 10W18 97
 10W18 98
 10W18 99
 10W18 900 I01 21 2+45
 10W18 901 I02 21 2+46
 10W18 902 I03 21 2+47
 10W18 903 I04 21 2+48
 10W18 904 I05 21 2+49
 10W18 905 I08 21 2+50
 10W18 906 I09 21 2+51
 10W18 907 I10 21 2+52
 10W18 908

15W09 00

15W09 90 H40 28
 15W09 91 H41 1
 15W09 92 H41 8
 15W09 93 H41 28
 15W09 94 H42 1
 15W09 95 H42 8
 15W09 96 H42 28
 15W09 97
 15W09 98
 15W09 99
 15W09 900 H37 1
 15W09 901 H37 8
 15W09 902 H37 28
 15W09 903 H39 1
 15W09 904 H39 8
 15W09 905 H39 28
 15W09 906 H40 1
 15W09 907 H40 8
 15W09 908

10W19	00	READ DISTRIBUTOR FROM MEMORY			16W09	00
10W19	90	I11	23	2*53	16W09	90 H22 28
10W19	91	I12	23	2*54	16W09	91 H23 1
10W19	92	I14	23	2*55	16W09	92 H23 8
10W19	93	I15	23	2*56	16W09	93 H23 28
10W19	94	I16	23	2*57	16W09	94 H24 1
10W19	95	I17	23	2*58	16W09	95 H24 8
10W19	96	I18	23	2*59	16W09	96 H24 28
10W19	97				16W09	97
10W19	98				16W09	98
10W19	99				16W09	99
10W19	900	I01	23	2*45	16W09	900 H19 1
10W19	901	I02	23	2*46	16W09	901 H19 8
10W19	902	I03	23	2*47	16W09	902 H19 28
10W19	903	I04	23	2*48	16W09	903 H21 1
10W19	904	I05	23	2*49	16W09	904 H21 8
10W19	905	I08	23	2*50	16W09	905 H21 28
10W19	906	I09	23	2*51	16W09	906 H22 1
10W19	907	I10	23	2*52	16W09	907 H22 8
10W19	908				16W09	908

10W20	00	MEMORY ADDRESS FROM CONTROL			5W31	00
10W20	90	G01	18	2*8 ADDRESS TO MEMORY	5W31	90 Q36 24
10W20	91	G02	11	2*9 ADDRESS TO MEMORY	5W31	91 Q37 13
10W20	92	G02	14	2*10 ADDRESS TO MEMORY	5W31	92 Q37 28
10W20	93	G02	17	2*11 ADDRESS TO MEMORY	5W31	93 Q37 24
10W20	94	G02	18	2*12 ADDRESS TO MEMORY	5W31	94 Q38 13
10W20	95	G03	11	2*13 ADDRESS TO MEMORY	5W31	95 Q38 28
10W20	96	G03	14	2*14 ADDRESS TO MEMORY	5W31	96 Q38 24
10W20	97	G03	17	2*15 ADDRESS TO MEMORY	5W31	97 Q39 13
10W20	98	G03	18	2*16 ADDRESS TO MEMORY	5W31	98 Q39 28
10W20	99				5W31	99
10W20	900	G13	12	CLOCK	5W31	900 M32 6
10W20	901	G05	18	2*0 BANK SELECT	5W31	901 Q34 13
10W20	902	G05	22	2*1 BANK SELECT	5W31	902 Q34 28
10W20	903	G04	8	2*2 CHASSIS 10 SELECT (011)	5W31	903 Q34 24
10W20	904	G04	5	2*3 CHASSIS 10 SELECT (011)	5W31	904 Q35 13
10W20	905	G04	14	2*4 CHASSIS 10 SELECT (011)	5W31	905 Q35 28
10W20	906	G01	11	2*5 ADDRESS TO MEMORY	5W31	906 Q35 24
10W20	907	G01	14	2*6 ADDRESS TO MEMORY	5W31	907 Q36 13
10W20	908	G01	17	2*7 ADDRESS TO MEMORY	5W31	908 Q36 28

10W23 00 DISK SYNC TO DISK FILE (INPUT)

10W23 90 J01 12 2*0 READ DATA
10W23 91 J01 5 2*1 READ DATA
10W23 92 J01 26 2*2 READ DATA
10W23 93 J01 19 2*3 READ DATA
10W23 94 J02 12 2*4 READ DATA
10W23 95 J02 5 2*5 READ DATA
10W23 96 J02 26 2*6 READ DATA
10W23 97 J02 19 2*7 READ DATA
10W23 98 J03 12 2*8 READ DATA
10W23 99 J03 5 2*9 READ DATA
10W23 900 J03 26 2*10
10W23 901 J03 19 2*11
10W23 902 J04 12 REV MARK
10W23 903 J04 5 CLOCK
10W23 904 SECTOR MARK
10W23 905
10W23 906
10W23 907
10W23 908

10W24 00 DISK SYNC TO DISK FILE (OUTPUT)

10W24 90 J06 8 2*0 WRITE DATA
10W24 92 J06 4 2*2 WRITE DATA
10W24 91 J06 6 2*1 WRITE DATA
10W24 93 J06 27 2*3 WRITE DATA
10W24 94 J06 25 2*4 WRITE DATA
10W24 95 J06 23 2*5 WRITE DATA
10W24 96 J05 8 2*6 WRITE DATA
10W24 97 J05 6 2*7 WRITE DATA
10W24 98 J05 4 2*8 WRITE DATA
10W24 99 J05 27 2*9 WRITE DATA
10W24 900 J05 25 2*10
10W24 901 J05 23 2*11
10W24 902 J07 8 POS CONTROL
10W24 903 J07 6 GROUP
10W24 904 J07 4 WRITE
10W24 905 J07 27 CLOCK
10W24 906
10W24 907
10W24 908

10W25 00 DISK SYNC PASSON (OUTPUT)

10W25	90	K02	8	2*0	OUTPUT DATA
10W25	91	K02	6	2*1	OUTPUT DATA
10W25	92	K02	4	2*2	OUTPUT DATA
10W25	93	K02	27	2*3	OUTPUT DATA
10W25	94	K02	25	2*4	OUTPUT DATA
10W25	95	K02	23	2*5	OUTPUT DATA
10W25	96	K03	8	2*6	OUTPUT DATA
10W25	97	K03	6	2*7	OUTPUT DATA
10W25	98	K03	4	2*8	OUTPUT DATA
10W25	99	K03	27	2*9	OUTPUT DATA
10W25	900	K03	25	2*10	OUTPUT DATA
10W25	901	K03	23	2*11	OUTPUT DATA
10W25	902	K01	23		ACTIVE
10W25	903	K01	8		INACTIVE
10W25	904	K01	6		FULL
10W25	905	K01	27		EMPTY
10W25	906	K01	4		FUNCTION
10W25	907	K01	25		MASTER CLEAR
10W25	908				

10W26 00 DISK SYNC PASSON (INPUT)

10W26	90	L09	5	2*0	INPUT DATA
10W26	91	L09	7	2*1	INPUT DATA
10W26	92	L09	10	2*2	INPUT DATA
10W26	93	L09	21	2*3	INPUT DATA
10W26	94	L09	24	2*4	INPUT DATA
10W26	95	L09	26	2*5	INPUT DATA
10W26	96	L08	5	2*6	INPUT DATA
10W26	97	L08	7	2*7	INPUT DATA
10W26	98	L08	10	2*8	INPUT DATA
10W26	99	L08	21	2*9	INPUT DATA
10W26	900	L08	24	2*10	INPUT DATA
10W26	901	L08	26	2*11	INPUT DATA
10W26	902				ACTIVE
10W26	903	L07	5		INACTIVE
10W26	904	L07	7		FULL
10W26	905				EMPTY
10W26	906	L26	1		
10W26	907	L26	15		
10W26	908				

10W27 00 DISK SYNC TO CHANNEL (INPUT)

10W27	90	K05	8	2*0	INPUT DATA
10W27	91	K05	6	2*1	INPUT DATA
10W27	92	K05	4	2*2	INPUT DATA
10W27	93	K05	27	2*3	INPUT DATA
10W27	94	K05	25	2*4	INPUT DATA
10W27	95	K05	23	2*5	INPUT DATA
10W27	96	K06	8	2*6	INPUT DATA
10W27	97	K06	6	2*7	INPUT DATA
10W27	98	K06	4	2*8	INPUT DATA
10W27	99	K06	27	2*9	INPUT DATA
10W27	900	K06	25	2*10	INPUT DATA
10W27	901	K06	23	2*11	INPUT DATA
10W27	902	K04	4		ACTIVE
10W27	903	K04	8		INACTIVE
10W27	904	K04	6		FULL
10W27	905	K04	27		EMPTY
10W27	906	L26	3		
10W27	907	L26	13		
10W27	908				

10W28 00 DISK SYNC TO CHANNEL (OUTPUT)

10W28	90	L01	5	2*0	OUTPUT DATA
10W28	91	L01	7	2*1	OUTPUT DATA
10W28	92	L01	10	2*2	OUTPUT DATA
10W28	93	L01	21	2*3	OUTPUT DATA
10W28	94	L01	24	2*4	OUTPUT DATA
10W28	95	L01	26	2*5	OUTPUT DATA
10W28	96	L02	5	2*6	OUTPUT DATA
10W28	97	L02	7	2*7	OUTPUT DATA
10W28	98	L02	10	2*8	OUTPUT DATA
10W28	99	L02	21	2*9	OUTPUT DATA
10W28	900	L02	24	2*10	OUTPUT DATA
10W28	901	L02	26	2*11	OUTPUT DATA
10W28	902	L03	7		ACTIVE
10W28	903	L03	21		INACTIVE
10W28	904	L03	10		FULL
10W28	905	L03	26		EMPTY
10W28	906	L03	5		FUNCTION
10W28	907	L03	24		MASTER CLEAR
10W28	908				

10W39	00	MEMORY TO READ DISTRIBUTOR				10W39	00
10W39	90	H22	28	2*53	10W39	90	I11 9
10W39	91	H23	1	2*54	10W39	91	I12 9
10W39	92	H23	8	2*55	10W39	92	I14 9
10W39	93	H23	28	2*56	10W39	93	I15 9
10W39	94	H24	1	2*57	10W39	94	I16 9
10W39	95	H24	8	2*58	10W39	95	I17 9
10W39	96	H24	28	2*59	10W39	96	I18 9
10W39	97				10W39	97	
10W39	98				10W39	98	
10W39	99				10W39	99	
10W39	900	H19	1	2*45	10W39	900	I01 9
10W39	901	H19	8	2*46	10W39	901	I02 9
10W39	902	H19	28	2*47	10W39	902	I03 9
10W39	903	H21	1	2*48	10W39	903	I04 9
10W39	904	H21	8	2*49	10W39	904	I05 9
10W39	905	H21	28	2*50	10W39	905	I08 9
10W39	906	H22	1	2*51	10W39	906	I09 9
10W39	907	H22	8	2*52	10W39	907	I10 9
10W39	908				10W39	908	

10W39	00	READ DISTRIBUTOR FROM MEMORY				10W39	00
10W39	90	I11	9	2*53	10W39	90	H22 28
10W39	91	I12	9	2*54	10W39	91	H23 1
10W39	92	I14	9	2*55	10W39	92	H23 8
10W39	93	I15	9	2*56	10W39	93	H23 28
10W39	94	I16	9	2*57	10W39	94	H24 1
10W39	95	I17	9	2*58	10W39	95	H24 8
10W39	96	I18	9	2*59	10W39	96	H24 28
10W39	97				10W39	97	
10W39	98				10W39	98	
10W39	99				10W39	99	
10W39	900	I01	9	2*45	10W39	900	H19 1
10W39	901	I02	9	2*46	10W39	901	H19 8
10W39	902	I03	9	2*47	10W39	902	H19 28
10W39	903	I04	9	2*48	10W39	903	H21 1
10W39	904	I05	9	2*49	10W39	904	H21 8
10W39	905	I08	9	2*50	10W39	905	H21 28
10W39	906	I09	9	2*51	10W39	906	H22 1
10W39	907	I10	9	2*52	10W39	907	H22 8
10W39	908				10W39	908	



12W01 00 DISPLAY SYNC 1 TO CHANNEL (OUTPUT)

12W01	90	A01	3	2*0	OUTPUT DATA
12W01	91	A01	13	2*1	OUTPUT DATA
12W01	92	A01	16	2*2	OUTPUT DATA
12W01	93	A01	26	2*3	OUTPUT DATA
12W01	94	A02	3	2*4	OUTPUT DATA
12W01	95	A02	13	2*5	OUTPUT DATA
12W01	96	A02	16	2*6	OUTPUT DATA
12W01	97	A02	26	2*7	OUTPUT DATA
12W01	98	A03	3	2*8	OUTPUT DATA
12W01	99	A03	13	2*9	OUTPUT DATA
12W01	900	A03	16	2*10	OUTPUT DATA
12W01	901	A03	26	2*11	OUTPUT DATA
12W01	902	B01	3		ACTIVE
12W01	903	B01	13		INACTIVE
12W01	904	B01	16		FULL
12W01	905	B01	26		EMPTY
12W01	906	B02	3		FUNCTION
12W01	907	B02	13		MASTER CLEAR
12W01	908				

12W02 00 DISPLAY SYNC 1 TO CHANNEL (INPUT)

12W02	90	E02	7	2*0	INPUT DATA
12W02	91	E02	5	2*1	INPUT DATA
12W02	92	E02	3	2*2	INPUT DATA
12W02	93	E02	28	2*3	INPUT DATA
12W02	94	E02	26	2*4	INPUT DATA
12W02	95	E02	24	2*5	INPUT DATA
12W02	96	E01	7	2*6	INPUT DATA
12W02	97	E01	5	2*7	INPUT DATA
12W02	98	E01	3	2*8	INPUT DATA
12W02	99	E01	28	2*9	INPUT DATA
12W02	900	E01	26	2*10	INPUT DATA
12W02	901	E01	24	2*11	INPUT DATA
12W02	902	E03	7		ACTIVE
12W02	903	E03	5		INACTIVE
12W02	904	E03	3		FULL
12W02	905	E03	28		EMPTY
12W02	906	D01	12		CLOCK (100NSEC)
12W02	907	D03	12		RESYNC (1US)
12W02	908				

12W03 00 DISPLAY SYNC 1 PASSON (OUTPUT)

12W03 90 A01 1 2*0 OUTPUT DATA
12W03 91 A01 15 2*1 OUTPUT DATA
12W03 92 A01 14 2*2 OUTPUT DATA
12W03 93 A01 28 2*3 OUTPUT DATA
12W03 94 A02 1 2*4 OUTPUT DATA
12W03 95 A02 15 2*5 OUTPUT DATA
12W03 96 A02 14 2*6 OUTPUT DATA
12W03 97 A02 28 2*7 OUTPUT DATA
12W03 98 A03 1 2*8 OUTPUT DATA
12W03 99 A03 15 2*9 OUTPUT DATA
12W03 900 A03 14 2*10 OUTPUT DATA
12W03 901 A03 28 2*11 OUTPUT DATA
12W03 902 B01 1 ACTIVE
12W03 903 B01 15 INACTIVE
12W03 904 B01 14 FULL
12W03 905 B01 28 EMPTY
12W03 906 B02 1 FUNCTION
12W03 907 B02 15 MASTER CLEAR
12W03 908

12W04 00 DISPLAY SYNC 1 PASSON (INPUT)

12W04 90 E02 23 2*0 INPUT DATA
12W04 91 E02 22 2*1 INPUT DATA
12W04 92 E02 21 2*2 INPUT DATA
12W04 93 E02 10 2*3 INPUT DATA
12W04 94 E02 9 2*4 INPUT DATA
12W04 95 E02 8 2*5 INPUT DATA
12W04 96 E01 23 2*6 INPUT DATA
12W04 97 E01 22 2*7 INPUT DATA
12W04 98 E01 21 2*8 INPUT DATA
12W04 99 E01 10 2*9 INPUT DATA
12W04 900 E01 9 2*10 INPUT DATA
12W04 901 E01 8 2*11 INPUT DATA
12W04 902 E03 23 ACTIVE
12W04 903 E03 22 INACTIVE
12W04 904 E03 21 FULL
12W04 905 E03 10 EMPTY
12W04 906 D07 7
12W04 907 D07 5
12W04 908

12W05 00 DISPLAY SYNC 1 TO CONSOLE 0 (OUTPUT)

12W05	90	F16	1	X*0
12W05	91	F16	2	X*1
12W05	92	F16	14	X*2
12W05	93	F19	10	X*3
12W05	94	F19	11	X*4
12W05	95	F19	8	X*5
12W05	96	F19	19	X*6
12W05	97	F19	18	X*7
12W05	98	F19	21	X*8
12W05	99	F16	19	Y*0
12W05	900	F16	27	Y*1
12W05	901	F16	28	Y*2
12W05	902	F22	1	Y*3
12W05	903	F22	2	Y*4
12W05	904	F22	14	Y*5
12W05	905	F22	19	Y*6
12W05	906	F22	27	Y*7
12W05	907	F22	28	Y*8
12W05	908	F07	25	FOCUS AND ASTIGMATISM

12W06 00 DISPLAY SYNC 1 TO CONSOLE 0 (INPUT)

12W06	90	F05	1	KEYBOARD BIT 0
12W06	91	F05	4	KEYBOARD BIT 1
12W06	92	F05	5	KEYBOARD BIT 2
12W06	93	F05	24	KEYBOARD BIT 3
12W06	94	F05	25	KEYBOARD BIT 4
12W06	95	F05	28	KEYBOARD BIT 5
12W06	96	F05	19	KEY DOWN
12W06	97	F05	12	KEY UP
12W06	98	E35	14	UNBLANK LEFT CONSOLE 0
12W06	99	E35	20	UNBLANK RIGHT CONSOLE 0
12W06	900	F13	10	SMALL SIZE
12W06	901	F13	11	MED. SIZE
12W06	902			
12W06	903	F27	16	VERT. PUSH ANALOG
12W06	904	F27	15	VERT. PULL ANALOG
12W06	905	F27	14	HORIZ. PUSH ANALOG
12W06	906	F27	13	HORIZ. PULL ANALOG
12W06	907			
12W06	908			

12W07 00 DISPLAY SYNC 1 TO CONSOLE 1 (OUTPUT)

12W07	90	F16	3	X*0
12W07	91	F16	6	X*1
12W07	92	F16	10	X*2
12W07	93	F19	4	X*3
12W07	94	F19	13	X*4
12W07	95	F19	7	X*5
12W07	96	F19	17	X*6
12W07	97	F19	20	X*7
12W07	98	F19	15	X*8
12W07	99	F16	17	Y*0
12W07	900	F16	25	Y*1
12W07	901	F16	26	Y*2
12W07	902	F22	3	Y*3
12W07	903	F22	6	Y*4
12W07	904	F22	10	Y*5
12W07	905	F22	17	Y*6
12W07	906	F22	25	Y*7
12W07	907	F22	26	Y*8
12W07	908	F07	27	FOCUS AND ASTIGMATISM

12W08 00 DISPLAY SYNC 1 TO CONSOLE 1 (INPUT)

12W08	90	F06	1	KEYBOARD INPUT BIT 0
12W08	91	F06	4	KEYBOARD INPUT BIT 1
12W08	92	F06	5	KEYBOARD INPUT BIT 2
12W08	93	F06	24	KEYBOARD INPUT BIT 3
12W08	94	F06	25	KEYBOARD INPUT BIT 4
12W08	95	F06	28	KEYBOARD INPUT BIT 5
12W08	96	F06	19	KEY DOWN
12W08	97	F06	12	KEY UP
12W08	98	E34	14	UNBLANK LEFT
12W08	99	E34	20	UNBLANK RIGHT
12W08	900	F13	12	SMALL SIZE
12W08	901	F13	13	MED. SIZE
12W08	902			
12W08	903	F26	16	VERT. PUSH ANALOG
12W08	904	F26	15	VERT. PULL ANALOG
12W08	905	F26	14	HORIZ. PUSH ANALOG
12W08	906	F26	13	HORIZ. PULL ANALOG
12W08	907			
12W08	908			

12W14 00 DISPLAY SYNC 2 TO CHANNEL (OUTPUT)

12W14	90	G01	3	2*0	OUTPUT DATA
12W14	91	G01	13	2*1	OUTPUT DATA
12W14	92	G01	16	2*2	OUTPUT DATA
12W14	93	G01	26	2*3	OUTPUT DATA
12W14	94	G02	3	2*4	OUTPUT DATA
12W14	95	G02	13	2*5	OUTPUT DATA
12W14	96	G02	16	2*6	OUTPUT DATA
12W14	97	G02	26	2*7	OUTPUT DATA
12W14	98	G03	3	2*8	OUTPUT DATA
12W14	99	G03	13	2*9	OUTPUT DATA
12W14	900	G03	16	2*10	OUTPUT DATA
12W14	901	G03	26	2*11	OUTPUT DATA
12W14	902	H01	3		ACTIVE
12W14	903	H01	13		INACTIVE
12W14	904	H01	16		FULL
12W14	905	H01	26		EMPTY
12W14	906	H02	3		FUNCTION
12W14	907	H02	13		MASTER CLEAR
12W14	908				

12W15 00 DISPLAY SYNC 2 TO CHANNEL (INPUT)

12W15	90	K02	7	2*0	INPUT DATA
12W15	91	K02	5	2*1	INPUT DATA
12W15	92	K02	3	2*2	INPUT DATA
12W15	93	K02	28	2*3	INPUT DATA
12W15	94	K02	26	2*4	INPUT DATA
12W15	95	K02	24	2*5	INPUT DATA
12W15	96	K01	7	2*6	INPUT DATA
12W15	97	K01	5	2*7	INPUT DATA
12W15	98	K01	3	2*8	INPUT DATA
12W15	99	K01	28	2*9	INPUT DATA
12W15	900	K01	26	2*10	INPUT DATA
12W15	901	K01	24	2*11	INPUT DATA
12W15	902	K03	7		ACTIVE
12W15	903	K03	5		INACTIVE
12W15	904	K03	3		FULL
12W15	905	K03	28		EMPTY
12W15	906	J01	12		CLOCK (100US)
12W15	907	J03	12		RESYNC (1US)
12W15	908				

12W16 00 DISPLAY SYNC 2 PASSON (OUTPUT)

12W16	90	G01	1	2*0	OUTPUT DATA
12W16	91	G01	15	2*1	OUTPUT DATA
12W16	92	G01	14	2*2	OUTPUT DATA
12W16	93	G01	28	2*3	OUTPUT DATA
12W16	94	G02	1	2*4	OUTPUT DATA
12W16	95	G02	15	2*5	OUTPUT DATA
12W16	96	G02	14	2*6	OUTPUT DATA
12W16	97	G02	28	2*7	OUTPUT DATA
12W16	98	G03	1	2*8	OUTPUT DATA
12W16	99	G03	15	2*9	OUTPUT DATA
12W16	900	G03	14	2*10	OUTPUT DATA
12W16	901	G03	28	2*11	OUTPUT DATA
12W16	902	H01	1		ACTIVE
12W16	903	H01	15		INACTIVE
12W16	904	H01	14		FULL
12W16	905	H01	28		EMPTY
12W16	906	H02	1		FUNCTION
12W16	907	H02	15		MASTER CLEAR
12W16	908				

12W17 00 DISPLAY SYNC 2 PASSON (INPUT)

12W17	90	K02	23	2*0	INPUT DATA
12W17	91	K02	22	2*1	INPUT DATA
12W17	92	K02	21	2*2	INPUT DATA
12W17	93	K02	10	2*3	INPUT DATA
12W17	94	K02	9	2*4	INPUT DATA
12W17	95	K02	8	2*5	INPUT DATA
12W17	96	K01	23	2*6	INPUT DATA
12W17	97	K01	22	2*7	INPUT DATA
12W17	98	K01	21	2*8	INPUT DATA
12W17	99	K01	10	2*9	INPUT DATA
12W17	900	K01	9	2*10	INPUT DATA
12W17	901	K01	8	2*11	INPUT DATA
12W17	902	K03	23		ACTIVE
12W17	903	K03	22		INACTIVE
12W17	904	K03	21		FULL
12W17	905	K03	10		EMPTY
12W17	906	J07	7		
12W17	907	J07	5		
12W17	908				

12W18 00 DISPLAY SYNC 2 TO CONSOLE 0 (OUTPUT)

12W18	90	L16	1	X*0
12W18	91	L16	2	X*1
12W18	92	L16	14	X*2
12W18	93	L19	10	X*3
12W18	94	L19	11	X*4
12W18	95	L19	8	X*5
12W18	96	L19	19	X*6
12W18	97	L19	18	X*7
12W18	98	L19	21	X*8
12W18	99	L16	19	Y*0
12W18	900	L16	27	Y*1
12W18	901	L16	28	Y*2
12W18	902	L22	1	Y*3
12W18	903	L22	2	Y*4
12W18	904	L22	14	Y*5
12W18	905	L22	19	Y*6
12W18	906	L22	27	Y*7
12W18	907	L22	28	Y*8
12W18	908	L07	25	FOCUS AND ASTIGMATISM

12W19 00 DISPLAY SYNC 2 TO CONSOLE 0 (INPUT)

12W19	90	L05	1	KEYBOARD BIT 0
12W19	91	L05	4	KEYBOARD BIT 1
12W19	92	L05	5	KEYBOARD BIT 2
12W19	93	L05	24	KEYBOARD BIT 3
12W19	94	L05	25	KEYBOARD BIT 4
12W19	95	L05	28	KEYBOARD BIT 5
12W19	96	L05	19	KEY DOWN
12W19	97	L05	12	KEY UP
12W19	98	K35	14	UNBLANK LEFT
12W19	99	K35	20	UNBLANK RIGHT
12W19	900	L13	10	SMALL SIZE
12W19	901	L13	11	MED. SIZE
12W19	902			
12W19	903	L27	16	VERT. PUSH ANALOG
12W19	904	L27	15	VERT. PULL ANALOG
12W19	905	L27	14	HORIZ. PUSH ANALOG
12W19	906	L27	13	HORIZ. PULL ANALOG
12W19	907			
12W19	908			

12W20 00 DISPLAY SYNC 2 TO CONSOLE 1 (OUTPUT)

12W20	90	L16	3	X*0
12W20	91	L16	6	X*1
12W20	92	L16	10	X*2
12W20	93	L19	4	X*3
12W20	94	L19	13	X*4
12W20	95	L19	7	X*5
12W20	96	L19	17	X*6
12W20	97	L19	20	X*7
12W20	98	L19	15	X*8
12W20	99	L16	17	Y*0
12W20	900	L16	25	Y*1
12W20	901	L16	26	Y*2
12W20	902	L22	3	Y*3
12W20	903	L22	6	Y*4
12W20	904	L22	10	Y*5
12W20	905	L22	17	Y*6
12W20	906	L22	25	Y*7
12W20	907	L22	26	Y*8
12W20	908	L07	27	FOCUS AND ASTIGMATISM

12W21 00 DISPLAY SYNC 2 TO CONSOLE 1 (INPUT)

12W21	90	L06	1	KEYBOARD BIT 0
12W21	91	L06	4	KEYBOARD BIT 1
12W21	92	L06	5	KEYBOARD BIT 2
12W21	93	L06	24	KEYBOARD BIT 3
12W21	94	L06	25	KEYBOARD BIT 4
12W21	95	L06	28	KEYBOARD BIT 5
12W21	96	L06	19	KEY DOWN
12W21	97	L06	12	KEY UP
12W21	98	K34	14	UNBLANK LEFT
12W21	99	K34	20	UNBLANK RIGHT
12W21	900	L13	12	SMALL SIZE
12W21	901	L13	13	MED. SIZE
12W21	902			
12W21	903	L26	16	VERT. PUSH ANALOG
12W21	904	L26	15	VERT. PULL ANALOG
12W21	905	L26	14	HORIZ. PUSH ANALOG
12W21	906	L26	13	HORIZ. PULL ANALOG
12W21	907			
12W21	908			

13W03	00	MEMORY FROM WRITE DISTRIBUTOR				14W11	00
13W03	90	H27	24	2*23	FAN OUT TO MEMORY	14W11	90 I11 3
13W03	91	H29	2	2*24	FAN OUT TO MEMORY	14W11	91 I12 18
13W03	92	H29	19	2*25	FAN OUT TO MEMORY	14W11	92 I12 13
13W03	93	H29	24	2*26	FAN OUT TO MEMORY	14W11	93 I13 3
13W03	94	H30	2	2*27	FAN OUT TO MEMORY	14W11	94 I14 18
13W03	95	H30	19	2*28	FAN OUT TO MEMORY	14W11	95 I14 13
13W03	96	H30	24	2*29	FAN OUT TO MEMORY	14W11	96 I15 3
13W03	97					14W11	97
13W03	98					14W11	98
13W03	99					14W11	99
13W03	900	H25	2	2*15	FAN OUT TO MEMORY	14W11	900 I06 18
13W03	901	H25	19	2*16	FAN OUT TO MEMORY	14W11	901 I06 13
13W03	902	H25	24	2*17	FAN OUT TO MEMORY	14W11	902 I07 3
13W03	903	H26	2	2*18	FAN OUT TO MEMORY	14W11	903 I08 18
13W03	904	H26	19	2*19	FAN OUT TO MEMORY	14W11	904 I08 13
13W03	905	H26	24	2*20	FAN OUT TO MEMORY	14W11	905 I09 3
13W03	906	H27	2	2*21	FAN OUT TO MEMORY	14W11	906 I10 18
13W03	907	H27	19	2*22	FAN OUT TO MEMORY	14W11	907 I10 13
13W03	908					14W11	908

13W04	00	MEMORY FROM WRITE DISTRIBUTOR				15W11	00
13W04	90	H34	24	2*38	FAN OUT TO MEMORY	15W11	90 I33 3
13W04	91	H35	2	2*39	FAN OUT TO MEMORY	15W11	91 I34 18
13W04	92	H35	19	2*40	FAN OUT TO MEMORY	15W11	92 I34 13
13W04	93	H35	24	2*41	FAN OUT TO MEMORY	15W11	93 I35 3
13W04	94	H36	2	2*42	FAN OUT TO MEMORY	15W11	94 I36 18
13W04	95	H36	19	2*43	FAN OUT TO MEMORY	15W11	95 I36 13
13W04	96	H36	24	2*44	FAN OUT TO MEMORY	15W11	96 I37 3
13W04	97					15W11	97
13W04	98					15W11	98
13W04	99					15W11	99
13W04	900	H31	2	2*30	FAN OUT TO MEMORY	15W11	900 I28 18
13W04	901	H31	19	2*31	FAN OUT TO MEMORY	15W11	901 I28 13
13W04	902	H31	24	2*32	FAN OUT TO MEMORY	15W11	902 I29 3
13W04	903	H32	2	2*33	FAN OUT TO MEMORY	15W11	903 I30 18
13W04	904	H32	19	2*34	FAN OUT TO MEMORY	15W11	904 I30 13
13W04	905	H32	24	2*35	FAN OUT TO MEMORY	15W11	905 I31 3
13W04	906	H34	2	2*36	FAN OUT TO MEMORY	15W11	906 I32 18
13W04	907	H34	19	2*37	FAN OUT TO MEMORY	15W11	907 I32 13
13W04	908					15W11	908

13W05	00	MEMORY FROM WRITE DISTRIBUTOR				16W11	00
13W05	90	H40	24	2*53	FAN OUT TO MEMORY	16W11	90 I11 3
13W05	91	H41	2	2*54	FAN OUT TO MEMORY	16W11	91 I12 18
13W05	92	H41	19	2*55	FAN OUT TO MEMORY	16W11	92 I12 13
13W05	93	H41	24	2*56	FAN OUT TO MEMORY	16W11	93 I13 3
13W05	94	H42	2	2*57	FAN OUT TO MEMORY	16W11	94 I14 18
13W05	95	H42	19	2*58	FAN OUT TO MEMORY	16W11	95 I14 13
13W05	96	H42	24	2*59	FAN OUT TO MEMORY	16W11	96 I15 3
13W05	97					16W11	97
13W05	98					16W11	98
13W05	99					16W11	99
13W05	900	H37	2	2*45	FAN OUT TO MEMORY	16W11	900 I06 18
13W05	901	H37	19	2*46	FAN OUT TO MEMORY	16W11	901 I06 13
13W05	902	H37	24	2*47	FAN OUT TO MEMORY	16W11	902 I07 3
13W05	903	H39	2	2*48	FAN OUT TO MEMORY	16W11	903 I08 18
13W05	904	H39	19	2*49	FAN OUT TO MEMORY	16W11	904 I08 13
13W05	905	H39	24	2*50	FAN OUT TO MEMORY	16W11	905 I09 3
13W05	906	H40	2	2*51	FAN OUT TO MEMORY	16W11	906 I10 18
13W05	907	H40	19	2*52	FAN OUT TO MEMORY	16W11	907 I10 13
13W05	908					16W11	908

13W06	00	MEMORY TO READ DISTRIBUTOR				3W09	00
13W06	90	H21	28	2*8		3W09	90 I35 11
13W06	91	H22	1	2*9		3W09	91 I36 11
13W06	92	H22	8	2*10		3W09	92 I38 11
13W06	93	H22	28	2*11		3W09	93 I39 11
13W06	94	H24	1	2*12		3W09	94 I40 11
13W06	95	H24	8	2*13		3W09	95 I41 11
13W06	96	H24	28	2*14		3W09	96 I42 11
13W06	97					3W09	97
13W06	98					3W09	98
13W06	99					3W09	99
13W06	900	H19	1	2*0		3W09	900 I25 11
13W06	901	H19	8	2*1		3W09	901 I26 11
13W06	902	H19	28	2*2		3W09	902 I27 11
13W06	903	H20	1	2*3		3W09	903 I28 11
13W06	904	H20	8	2*4		3W09	904 I29 11
13W06	905	H20	28	2*5		3W09	905 I32 11
13W06	906	H21	1	2*6		3W09	906 I33 11
13W06	907	H21	8	2*7		3W09	907 I34 11
13W06	908					3W09	908

13W07	00	MEMORY TO READ DISTRIBUTOR			4W15	00
13W07	90	H27	28	2*23	4W15	90 I11 11
13W07	91	H29	1	2*24	4W15	91 I12 11
13W07	92	H29	8	2*25	4W15	92 I14 11
13W07	93	H29	28	2*26	4W15	93 I15 11
13W07	94	H30	1	2*27	4W15	94 I16 11
13W07	95	H30	8	2*28	4W15	95 I17 11
13W07	96	H30	28	2*29	4W15	96 I18 11
13W07	97	G38	21	MEMORY GO	4W15	97 I21 28
13W07	98	G38	20	MEMORY WRITE	4W15	98 I24 18
13W07	99				4W15	99
13W07	900	H25	1	2*15	4W15	900 I01 11
13W07	901	H25	8	2*16	4W15	901 I02 11
13W07	902	H25	28	2*17	4W15	902 I03 11
13W07	903	H26	1	2*18	4W15	903 I04 11
13W07	904	H26	8	2*19	4W15	904 I05 11
13W07	905	H26	28	2*20	4W15	905 I08 11
13W07	906	H27	1	2*21	4W15	906 I09 11
13W07	907	H27	8	2*22	4W15	907 I10 11
13W07	908				4W15	908

13W08	00	MEMORY TO READ DISTRIBUTOR			9W16	00
13W08	90	H34	28	2*38	9W16	90 I35 11
13W08	91	H35	1	2*39	9W16	91 I36 11
13W08	92	H35	8	2*40	9W16	92 I38 11
13W08	93	H35	28	2*41	9W16	93 I39 11
13W08	94	H36	1	2*42	9W16	94 I40 11
13W08	95	H36	8	2*43	9W16	95 I41 11
13W08	96	H36	28	2*44	9W16	96 I42 11
13W08	97				9W16	97
13W08	98				9W16	98
13W08	99				9W16	99
13W08	900	H31	1	2*30	9W16	900 I25 11
13W08	901	H31	8	2*31	9W16	901 I26 11
13W08	902	H31	28	2*32	9W16	902 I27 11
13W08	903	H32	1	2*33	9W16	903 I28 11
13W08	904	H32	8	2*34	9W16	904 I29 11
13W08	905	H32	28	2*35	9W16	905 I32 11
13W08	906	H34	1	2*36	9W16	906 I33 11
13W08	907	H34	8	2*37	9W16	907 I34 11
13W08	908				9W16	908

13W09	00	MEMORY TO READ DISTRIBUTOR				10W16	00
13W09	90	H40	28	2*53	10W16	90	I11 11
13W09	91	H41	1	2*54	10W16	91	I12 11
13W09	92	H41	8	2*55	10W16	92	I14 11
13W09	93	H41	28	2*56	10W16	93	I15 11
13W09	94	H42	1	2*57	10W16	94	I16 11
13W09	95	H42	8	2*58	10W16	95	I17 11
13W09	96	H42	28	2*59	10W16	96	I18 11
13W09	97				10W16	97	
13W09	98				10W16	98	
13W09	99				10W16	99	
13W09	900	H37	1	2*45	10W16	900	I01 11
13W09	901	H37	8	2*46	10W16	901	I02 11
13W09	902	H37	28	2*47	10W16	902	I03 11
13W09	903	H39	1	2*48	10W16	903	I04 11
13W09	904	H39	8	2*49	10W16	904	I05 11
13W09	905	H39	28	2*50	10W16	905	I08 11
13W09	906	H40	1	2*51	10W16	906	I09 11
13W09	907	H40	8	2*52	10W16	907	I10 11
13W09	908				10W16	908	

13W10	00	DISTRIBUTOR FROM WRITE BUFFER				2W05	00
13W10	90	I39	4	2*0 DATA TO MEMORY FAN OUT	2W05	90	B12 8
13W10	91	I39	6	2*1 DATA TO MEMORY FAN OUT	2W05	91	B12 6
13W10	92	I39	7	2*2 DATA TO MEMORY FAN OUT	2W05	92	B12 4
13W10	93	I39	27	2*3 DATA TO MEMORY FAN OUT	2W05	93	B12 27
13W10	94	I39	25	2*4 DATA TO MEMORY FAN OUT	2W05	94	B12 25
13W10	95	I39	22	2*5 DATA TO MEMORY FAN OUT	2W05	95	B12 23
13W10	96	I40	4	2*6 DATA TO MEMORY FAN OUT	2W05	96	B13 8
13W10	97	I40	6	2*7 DATA TO MEMORY FAN OUT	2W05	97	B13 6
13W10	98	I40	7	2*8 DATA TO MEMORY FAN OUT	2W05	98	B13 4
13W10	99	I40	27	2*9 DATA TO MEMORY FAN OUT	2W05	99	B13 27
13W10	900	I40	25	2*10 DATA TO MEMORY FAN OUT	2W05	900	B13 25
13W10	901	I40	22	2*11 DATA TO MEMORY FAN OUT	2W05	901	B13 23
13W10	902	I41	4	2*12 DATA TO MEMORY FAN OUT	2W05	902	B14 8
13W10	903	I41	6	2*13 DATA TO MEMORY FAN OUT	2W05	903	B14 6
13W10	904	I41	7	2*14 DATA TO MEMORY FAN OUT	2W05	904	B14 4
13W10	905	C14	14	MEMORY MARGIN	2W05	905	B22 22
13W10	906				2W05	906	
13W10	907				2W05	907	
13W10	908				2W05	908	

13W11	00	WRITE DISTRIBUTOR TO MEMORY				14W03	00
13W11	90	I33	11	2*8	FAN OUT TO MEMORY	14W03	90 H03 24
13W11	91	I34	5	2*9	FAN OUT TO MEMORY	14W03	91 H04 2
13W11	92	I35	20	2*10	FAN OUT TO MEMORY	14W03	92 H04 19
13W11	93	I35	11	2*11	FAN OUT TO MEMORY	14W03	93 H04 24
13W11	94	I36	5	2*12	FAN OUT TO MEMORY	14W03	94 H06 2
13W11	95	I37	20	2*13	FAN OUT TO MEMORY	14W03	95 H06 19
13W11	96	I37	11	2*14	FAN OUT TO MEMORY	14W03	96 H06 24
13W11	97					14W03	97
13W11	98					14W03	98
13W11	99					14W03	99
13W11	900	I28	5	2*0	FAN OUT TO MEMORY	14W03	900 H01 2
13W11	901	I29	20	2*1	FAN OUT TO MEMORY	14W03	901 H01 19
13W11	902	I29	11	2*2	FAN OUT TO MEMORY	14W03	902 H01 24
13W11	903	I30	5	2*3	FAN OUT TO MEMORY	14W03	903 H02 2
13W11	904	I31	20	2*4	FAN OUT TO MEMORY	14W03	904 H02 19
13W11	905	I31	11	2*5	FAN OUT TO MEMORY	14W03	905 H02 24
13W11	906	I32	5	2*6	FAN OUT TO MEMORY	14W03	906 H03 2
13W11	907	I33	20	2*7	FAN OUT TO MEMORY	14W03	907 H03 19
13W11	908					14W03	908

13W12	00	WRITE DISTRIBUTOR TO MEMORY				4W03	00
13W12	90	I33	5	2*8	FAN OUT TO MEMORY	4W03	90 H03 24
13W12	91	I34	20	2*9	FAN OUT TO MEMORY	4W03	91 H04 2
13W12	92	I34	11	2*10	FAN OUT TO MEMORY	4W03	92 H04 19
13W12	93	I35	5	2*11	FAN OUT TO MEMORY	4W03	93 H04 24
13W12	94	I36	20	2*12	FAN OUT TO MEMORY	4W03	94 H06 2
13W12	95	I36	11	2*13	FAN OUT TO MEMORY	4W03	95 H06 19
13W12	96	I37	5	2*14	FAN OUT TO MEMORY	4W03	96 H06 24
13W12	97					4W03	97
13W12	98					4W03	98
13W12	99					4W03	99
13W12	900	I28	20	2*0	FAN OUT TO MEMORY	4W03	900 H01 2
13W12	901	I28	11	2*1	FAN OUT TO MEMORY	4W03	901 H01 19
13W12	902	I29	5	2*2	FAN OUT TO MEMORY	4W03	902 H01 24
13W12	903	I30	20	2*3	FAN OUT TO MEMORY	4W03	903 H02 2
13W12	904	I30	11	2*4	FAN OUT TO MEMORY	4W03	904 H02 19
13W12	905	I31	5	2*5	FAN OUT TO MEMORY	4W03	905 H02 24
13W12	906	I32	20	2*6	FAN OUT TO MEMORY	4W03	906 H03 2
13W12	907	I32	11	2*7	FAN OUT TO MEMORY	4W03	907 H03 19
13W12	908					4W03	908

13W13	00	WRITE DISTRIBUTOR TO MEMORY				3W15	00
13W13	90	I33	3	2*8	FAN OUT TO MEMORY	3W15	90 H21 24
13W13	91	I34	18	2*9	FAN OUT TO MEMORY	3W15	91 H22 2
13W13	92	I34	13	2*10	FAN OUT TO MEMORY	3W15	92 H22 19
13W13	93	I35	3	2*11	FAN OUT TO MEMORY	3W15	93 H22 24
13W13	94	I36	18	2*12	FAN OUT TO MEMORY	3W15	94 H24 2
13W13	95	I36	13	2*13	FAN OUT TO MEMORY	3W15	95 H24 19
13W13	96	I37	3	2*14	FAN OUT TO MEMORY	3W15	96 H24 24
13W13	97	G39	28			3W15	97 I23 19
13W13	98					3W15	98
13W13	99					3W15	99
13W13	900	I28	18	2*0	FAN OUT TO MEMORY	3W15	900 H19 2
13W13	901	I28	13	2*1	FAN OUT TO MEMORY	3W15	901 H19 19
13W13	902	I29	3	2*2	FAN OUT TO MEMORY	3W15	902 H19 24
13W13	903	I30	18	2*3	FAN OUT TO MEMORY	3W15	903 H20 2
13W13	904	I30	13	2*4	FAN OUT TO MEMORY	3W15	904 H20 19
13W13	905	I31	3	2*5	FAN OUT TO MEMORY	3W15	905 H20 24
13W13	906	I32	18	2*6	FAN OUT TO MEMORY	3W15	906 H21 2
13W13	907	I32	13	2*7	FAN OUT TO MEMORY	3W15	907 H21 19
13W13	908					3W15	908

13W14	00	WRITE DISTRIBUTOR TO MEMORY				9W03	00
13W14	90	I33	28	2*8	FAN OUT TO MEMORY	9W03	90 H21 24
13W14	91	I34	24	2*9	FAN OUT TO MEMORY	9W03	91 H22 2
13W14	92	I34	7	2*10	FAN OUT TO MEMORY	9W03	92 H22 19
13W14	93	I35	28	2*11	FAN OUT TO MEMORY	9W03	93 H22 24
13W14	94	I36	24	2*12	FAN OUT TO MEMORY	9W03	94 H24 2
13W14	95	I36	7	2*13	FAN OUT TO MEMORY	9W03	95 H24 19
13W14	96	I37	28	2*14	FAN OUT TO MEMORY	9W03	96 H24 24
13W14	97					9W03	97
13W14	98					9W03	98
13W14	99					9W03	99
13W14	900	I28	24	2*0	FAN OUT TO MEMORY	9W03	900 H19 2
13W14	901	I28	7	2*1	FAN OUT TO MEMORY	9W03	901 H19 19
13W14	902	I29	28	2*2	FAN OUT TO MEMORY	9W03	902 H19 24
13W14	903	I30	24	2*3	FAN OUT TO MEMORY	9W03	903 H20 2
13W14	904	I30	7	2*4	FAN OUT TO MEMORY	9W03	904 H20 19
13W14	905	I31	28	2*5	FAN OUT TO MEMORY	9W03	905 H20 24
13W14	906	I32	24	2*6	FAN OUT TO MEMORY	9W03	906 H21 2
13W14	907	I32	7	2*7	FAN OUT TO MEMORY	9W03	907 H21 19
13W14	908					9W03	908

13W15	00	WRITE DISTRIBUTOR TO MEMORY				10W03	00
13W15	90	I33	13	2*8	FAN OUT TO MEMORY	10W03	90 H03 24
13W15	91	I34	3	2*9	FAN OUT TO MEMORY	10W03	91 H04 2
13W15	92	I35	18	2*10	FAN OUT TO MEMORY	10W03	92 H04 19
13W15	93	I35	13	2*11	FAN OUT TO MEMORY	10W03	93 H04 24
13W15	94	I36	3	2*12	FAN OUT TO MEMORY	10W03	94 H06 2
13W15	95	I37	18	2*13	FAN OUT TO MEMORY	10W03	95 H06 19
13W15	96	I37	13	2*14	FAN OUT TO MEMORY	10W03	96 H06 24
13W15	97					10W03	97
13W15	98					10W03	98
13W15	99					10W03	99
13W15	900	I28	3	2*0	FAN OUT TO MEMORY	10W03	900 H01 2
13W15	901	I29	18	2*1	FAN OUT TO MEMORY	10W03	901 H01 19
13W15	902	I29	13	2*2	FAN OUT TO MEMORY	10W03	902 H01 24
13W15	903	I30	3	2*3	FAN OUT TO MEMORY	10W03	903 H02 2
13W15	904	I31	18	2*4	FAN OUT TO MEMORY	10W03	904 H02 19
13W15	905	I31	13	2*5	FAN OUT TO MEMORY	10W03	905 H02 24
13W15	906	I32	3	2*6	FAN OUT TO MEMORY	10W03	906 H03 2
13W15	907	I33	18	2*7	FAN OUT TO MEMORY	10W03	907 H03 19
13W15	908					10W03	908

13W16	00	WRITE DISTRIBUTOR TO MEMORY				15W03	00
13W16	90	I33	9	2*8	FAN OUT TO MEMORY	15W03	90 H21 24
13W16	91	I34	26	2*9	FAN OUT TO MEMORY	15W03	91 H22 2
13W16	92	I35	22	2*10	FAN OUT TO MEMORY	15W03	92 H22 19
13W16	93	I35	9	2*11	FAN OUT TO MEMORY	15W03	93 H22 24
13W16	94	I36	26	2*12	FAN OUT TO MEMORY	15W03	94 H24 2
13W16	95	I37	22	2*13	FAN OUT TO MEMORY	15W03	95 H24 19
13W16	96	I37	9	2*14	FAN OUT TO MEMORY	15W03	96 H24 24
13W16	97					15W03	97
13W16	98					15W03	98
13W16	99					15W03	99
13W16	900	I28	26	2*0	FAN OUT TO MEMORY	15W03	900 H19 2
13W16	901	I29	22	2*1	FAN OUT TO MEMORY	15W03	901 H19 19
13W16	902	I29	9	2*2	FAN OUT TO MEMORY	15W03	902 H19 24
13W16	903	I30	26	2*3	FAN OUT TO MEMORY	15W03	903 H20 2
13W16	904	I31	22	2*4	FAN OUT TO MEMORY	15W03	904 H20 19
13W16	905	I31	9	2*5	FAN OUT TO MEMORY	15W03	905 H20 24
13W16	906	I32	26	2*6	FAN OUT TO MEMORY	15W03	906 H21 2
13W16	907	I33	22	2*7	FAN OUT TO MEMORY	15W03	907 H21 19
13W16	908					15W03	908

13W17	00	WRITE DISTRIBUTOR TO MEMORY				16W03	00
13W17	90	I33	7	2*8	FAN OUT TO MEMORY	16W03	90 H03 24
13W17	91	I34	28	2*9	FAN OUT TO MEMORY	16W03	91 H04 2
13W17	92	I35	24	2*10	FAN OUT TO MEMORY	16W03	92 H04 19
13W17	93	I35	7	2*11	FAN OUT TO MEMORY	16W03	93 H04 24
13W17	94	I36	28	2*12	FAN OUT TO MEMORY	16W03	94 H06 2
13W17	95	I37	24	2*13	FAN OUT TO MEMORY	16W03	95 H06 19
13W17	96	I37	7	2*14	FAN OUT TO MEMORY	16W03	96 H06 24
13W17	97					16W03	97
13W17	98					16W03	98
13W17	99					16W03	99
13W17	900	I28	28	2*0	FAN OUT TO MEMORY	16W03	900 H01 2
13W17	901	I29	24	2*1	FAN OUT TO MEMORY	16W03	901 H01 19
13W17	902	I29	7	2*2	FAN OUT TO MEMORY	16W03	902 H01 24
13W17	903	I30	28	2*3	FAN OUT TO MEMORY	16W03	903 H02 2
13W17	904	I31	24	2*4	FAN OUT TO MEMORY	16W03	904 H02 19
13W17	905	I31	7	2*5	FAN OUT TO MEMORY	16W03	905 H02 24
13W17	906	I32	28	2*6	FAN OUT TO MEMORY	16W03	906 H03 2
13W17	907	I33	24	2*7	FAN OUT TO MEMORY	16W03	907 H03 19
13W17	908					16W03	908

13W18	00	MEMORY ADDRESS FROM CONTROL				5W32	00
13W18	90	G40	18	2*8	ADDRESS TO MEMORY	5W32	90 Q42 18
13W18	91	G41	11	2*9	ADDRESS TO MEMORY	5W32	91 R40 7
13W18	92	G41	14	2*10	ADDRESS TO MEMORY	5W32	92 R40 3
13W18	93	G41	17	2*11	ADDRESS TO MEMORY	5W32	93 R40 18
13W18	94	G41	18	2*12	ADDRESS TO MEMORY	5W32	94 R41 7
13W18	95	G42	11	2*13	ADDRESS TO MEMORY	5W32	95 R41 3
13W18	96	G42	14	2*14	ADDRESS TO MEMORY	5W32	96 R41 18
13W18	97	G42	17	2*15	ADDRESS TO MEMORY	5W32	97 R42 7
13W18	98	G42	18	2*16	ADDRESS TO MEMORY	5W32	98 R42 3
13W18	99					5W32	99
13W18	900	G35	12		CLOCK	5W32	900 M32 4
13W18	901	G39	18	2*0	BANK SELECT	5W32	901 Q40 7
13W18	902	G39	22	2*1	BANK SELECT	5W32	902 Q40 3
13W18	903	G38	9	2*2	CHASSIS 13 SELECT (100)	5W32	903 Q40 18
13W18	904	G38	4	2*3	CHASSIS 13 SELECT (100)	5W32	904 Q41 7
13W18	905	G38	13	2*4	CHASSIS 13 SELECT (160)	5W32	905 Q41 3
13W18	906	G40	11	2*5	ADDRESS TO MEMORY	5W32	906 Q41 18
13W18	907	G40	14	2*6	ADDRESS TO MEMORY	5W32	907 Q42 7
13W18	908	G40	17	2*7	ADDRESS TO MEMORY	5W32	908 Q42 3

13W34	00	626 SYNC TO CHANNEL (INPUT)			
13W34	90	J35	7	2*0	INPUT DATA
13W34	91	J35	5	2*1	INPUT DATA
13W34	92	J35	3	2*2	INPUT DATA
13W34	93	J35	28	2*3	INPUT DATA
13W34	94	J35	26	2*4	INPUT DATA
13W34	95	J35	24	2*5	INPUT DATA
13W34	96	J37	7	2*6	INPUT DATA
13W34	97	J37	5	2*7	INPUT DATA
13W34	98	J37	3	2*8	INPUT DATA
13W34	99	J37	28	2*9	INPUT DATA
13W34	900	J37	26	2*10	INPUT DATA
13W34	901	J37	24	2*11	INPUT DATA
13W34	902	J33	28		ACTIVE
13W34	903	J33	3		INACTIVE
13W34	904	J33	7		FULL
13W34	905	J33	5		EMPTY
13W34	906	J31	13		
13W34	907	J33	9		CLOCK (1 MS)
13W34	908				

13W35	00	626 SYNC TO CHANNEL (OUTPUT)			
13W35	90	J40	3	2*0	OUTPUT DATA
13W35	91	J40	13	2*1	OUTPUT DATA
13W35	92	J40	16	2*2	OUTPUT DATA
13W35	93	J40	26	2*3	OUTPUT DATA
13W35	94	J41	3	2*4	OUTPUT DATA
13W35	95	J41	13	2*5	OUTPUT DATA
13W35	96	J41	16	2*6	OUTPUT DATA
13W35	97	J41	26	2*7	OUTPUT DATA
13W35	98	J42	3	2*8	OUTPUT DATA
13W35	99	J42	13	2*9	OUTPUT DATA
13W35	900	J42	16	2*10	OUTPUT DATA
13W35	901	J42	26	2*11	OUTPUT DATA
13W35	902	J38	13		ACTIVE
13W35	903	J38	16		INACTIVE
13W35	904	J38	26		FULL
13W35	905	J39	3		EMPTY
13W35	906	J38	3		FUNCTION
13W35	907	J39	13		MASTER CLEAR
13W35	908				

13W36 00 626 SYNC PASSON (INPUT)

13W36 90 J35 23 2*0 INPUT DATA
13W36 91 J35 22 2*1 INPUT DATA
13W36 92 J35 21 2*2 INPUT DATA
13W36 93 J35 10 2*3 INPUT DATA
13W36 94 J35 9 2*4 INPUT DATA
13W36 95 J35 8 2*5 INPUT DATA
13W36 96 J37 23 2*6 INPUT DATA
13W36 97 J37 22 2*7 INPUT DATA
13W36 98 J37 21 2*8 INPUT DATA
13W36 99 J37 10 2*9 INPUT DATA
13W36 900 J37 9 2*10 INPUT DATA
13W36 901 J37 8 2*11 INPUT DATA
13W36 902 J33 10 ACTIVE
13W36 903 J33 21 INACTIVE
13W36 904 J33 23 FULL
13W36 905 J33 22 EMPTY
13W36 906 J31 15
13W36 907 J31 1 CLOCK (1 M SEC)
13W36 908

13W37 00 626 SYNC PASSON (OUTPUT)

13W37 90 J40 1 2*0 OUTPUT DATA
13W37 91 J40 15 2*1 OUTPUT DATA
13W37 92 J40 14 2*2 OUTPUT DATA
13W37 93 J40 28 2*3 OUTPUT DATA
13W37 94 J41 1 2*4 OUTPUT DATA
13W37 95 J41 15 2*5 OUTPUT DATA
13W37 96 J41 14 2*6 OUTPUT DATA
13W37 97 J41 28 2*7 OUTPUT DATA
13W37 98 J42 1 2*8 OUTPUT DATA
13W37 99 J42 15 2*9 OUTPUT DATA
13W37 900 J42 14 2*10 OUTPUT DATA
13W37 901 J42 28 2*11 OUTPUT DATA
13W37 902 J38 15 ACTIVE
13W37 903 J38 14 INACTIVE
13W37 904 J38 28 FULL
13W37 905 J39 1 EMPTY
13W37 906 J38 1 FUNCTION
13W37 907 J39 15 MASTER CLEAR
13W37 908

13W39	00	WRITE DISTRIBUTOR TO MEMORY				13W39	00		
13W39	90	I33	26	2*8	FAN OUT TO MEMORY	13W39	90	H21	24
13W39	91	I34	22	2*9	FAN OUT TO MEMORY	13W39	91	H22	2
13W39	92	I34	9	2*10	FAN OUT TO MEMORY	13W39	92	H22	19
13W39	93	I35	26	2*11	FAN OUT TO MEMORY	13W39	93	H22	24
13W39	94	I36	22	2*12	FAN OUT TO MEMORY	13W39	94	H24	2
13W39	95	I36	9	2*13	FAN OUT TO MEMORY	13W39	95	H24	19
13W39	96	I37	26	2*14	FAN OUT TO MEMORY	13W39	96	H24	24
13W39	97					13W39	97		
13W39	98					13W39	98		
13W39	99					13W39	99		
13W39	900	I28	22	2*0	FAN OUT TO MEMORY	13W39	900	H19	2
13W39	901	I28	9	2*1	FAN OUT TO MEMORY	13W39	901	H19	19
13W39	902	I29	26	2*2	FAN OUT TO MEMORY	13W39	902	H19	24
13W39	903	I30	22	2*3	FAN OUT TO MEMORY	13W39	903	H20	2
13W39	904	I30	9	2*4	FAN OUT TO MEMORY	13W39	904	H20	19
13W39	905	I31	26	2*5	FAN OUT TO MEMORY	13W39	905	H20	24
13W39	906	I32	22	2*6	FAN OUT TO MEMORY	13W39	906	H21	2
13W39	907	I32	9	2*7	FAN OUT TO MEMORY	13W39	907	H21	19
13W39	908					13W39	908		

13W39	00	MEMORY FROM WRITE DISTRIBUTOR				13W39	00		
13W39	90	H21	24	2*8	FAN OUT TO MEMORY	13W39	90	I33	26
13W39	91	H22	2	2*9	FAN OUT TO MEMORY	13W39	91	I34	22
13W39	92	H22	19	2*10	FAN OUT TO MEMORY	13W39	92	I34	9
13W39	93	H22	24	2*11	FAN OUT TO MEMORY	13W39	93	I35	26
13W39	94	H24	2	2*12	FAN OUT TO MEMORY	13W39	94	I36	22
13W39	95	H24	19	2*13	FAN OUT TO MEMORY	13W39	95	I36	9
13W39	96	H24	24	2*14	FAN OUT TO MEMORY	13W39	96	I37	26
13W39	97					13W39	97		
13W39	98					13W39	98		
13W39	99					13W39	99		
13W39	900	H19	2	2*0	FAN OUT TO MEMORY	13W39	900	I28	22
13W39	901	H19	19	2*1	FAN OUT TO MEMORY	13W39	901	I28	9
13W39	902	H19	24	2*2	FAN OUT TO MEMORY	13W39	902	I29	26
13W39	903	H20	2	2*3	FAN OUT TO MEMORY	13W39	903	I30	22
13W39	904	H20	19	2*4	FAN OUT TO MEMORY	13W39	904	I30	9
13W39	905	H20	24	2*5	FAN OUT TO MEMORY	13W39	905	I31	26
13W39	906	H21	2	2*6	FAN OUT TO MEMORY	13W39	906	I32	22
13W39	907	H21	19	2*7	FAN OUT TO MEMORY	13W39	907	I32	9
13W39	908					13W39	908		



14W03	00	MEMORY FROM WRITE DISTRIBUTOR				13W11	00
14W03	90	H03	24	2+8	FAN OUT TO MEMORY	13W11	90 133 11
14W03	91	H04	2	2+9	FAN OUT TO MEMORY	13W11	91 134 5
14W03	92	H04	19	2+10	FAN OUT TO MEMORY	13W11	92 135 20
14W03	93	H04	24	2+11	FAN OUT TO MEMORY	13W11	93 135 11
14W03	94	H06	2	2+12	FAN OUT TO MEMORY	13W11	94 136 5
14W03	95	H06	19	2+13	FAN OUT TO MEMORY	13W11	95 137 20
14W03	96	H06	24	2+14	FAN OUT TO MEMORY	13W11	96 137 11
14W03	97					13W11	97
14W03	98					13W11	98
14W03	99					13W11	99
14W03	900	H01	2	2+0	FAN OUT TO MEMORY	13W11	900 128 5
14W03	901	H01	19	2+1	FAN OUT TO MEMORY	13W11	901 129 20
14W03	902	H01	24	2+2	FAN OUT TO MEMORY	13W11	902 129 11
14W03	903	H02	2	2+3	FAN OUT TO MEMORY	13W11	903 130 5
14W03	904	H02	19	2+4	FAN OUT TO MEMORY	13W11	904 131 20
14W03	905	H02	24	2+5	FAN OUT TO MEMORY	13W11	905 131 11
14W03	906	H03	2	2+6	FAN OUT TO MEMORY	13W11	906 132 5
14W03	907	H03	19	2+7	FAN OUT TO MEMORY	13W11	907 133 20
14W03	908					13W11	908

14W04	00	MEMORY FROM WRITE DISTRIBUTOR				15W16	00
14W04	90	H17	2	2+39	FAN OUT TO MEMORY	15W16	90 134 26
14W04	91	H17	19	2+40	FAN OUT TO MEMORY	15W16	91 135 22
14W04	92	H17	24	2+41	FAN OUT TO MEMORY	15W16	92 135 9
14W04	93	H18	2	2+42	FAN OUT TO MEMORY	15W16	93 136 26
14W04	94	H18	19	2+43	FAN OUT TO MEMORY	15W16	94 137 22
14W04	95	H18	24	2+44	FAN OUT TO MEMORY	15W16	95 137 9
14W04	96					15W16	96
14W04	97					15W16	97
14W04	98					15W16	98
14W04	99					15W16	99
14W04	900	H13	2	2+30	FAN OUT TO MEMORY	15W16	900 128 26
14W04	901	H13	19	2+31	FAN OUT TO MEMORY	15W16	901 129 22
14W04	902	H13	24	2+32	FAN OUT TO MEMORY	15W16	902 129 9
14W04	903	H14	2	2+33	FAN OUT TO MEMORY	15W16	903 130 26
14W04	904	H14	19	2+34	FAN OUT TO MEMORY	15W16	904 131 22
14W04	905	H14	24	2+35	FAN OUT TO MEMORY	15W16	905 131 9
14W04	906	H16	2	2+36	FAN OUT TO MEMORY	15W16	906 132 26
14W04	907	H16	19	2+37	FAN OUT TO MEMORY	15W16	907 133 22
14W04	908	H16	24	2+38	FAN OUT TO MEMORY	15W16	908 133 9

14W05	00	MEMORY FROM WRITE DISTRIBUTOR				16W16	00		
14W05	90	H23	2	2*54	FAN OUT TO MEMORY	16W16	90	I12	26
14W05	91	H23	19	2*55	FAN OUT TO MEMORY	16W16	91	I13	22
14W05	92	H23	24	2*56	FAN OUT TO MEMORY	16W16	92	I13	9
14W05	93	H24	2	2*57	FAN OUT TO MEMORY	16W16	93	I14	26
14W05	94	H24	19	2*58	FAN OUT TO MEMORY	16W16	94	I15	22
14W05	95	H24	24	2*59	FAN OUT TO MEMORY	16W16	95	I15	9
14W05	96					16W16	96		
14W05	97					16W16	97		
14W05	98					16W16	98		
14W05	99					16W16	99		
14W05	900	H19	2	2*45	FAN OUT TO MEMORY	16W16	900	I06	26
14W05	901	H19	19	2*46	FAN OUT TO MEMORY	16W16	901	I07	22
14W05	902	H19	24	2*47	FAN OUT TO MEMORY	16W16	902	I07	9
14W05	903	H21	2	2*48	FAN OUT TO MEMORY	16W16	903	I08	26
14W05	904	H21	19	2*49	FAN OUT TO MEMORY	16W16	904	I09	22
14W05	905	H21	24	2*50	FAN OUT TO MEMORY	16W16	905	I09	9
14W05	906	H22	2	2*51	FAN OUT TO MEMORY	16W16	906	I10	26
14W05	907	H22	19	2*52	FAN OUT TO MEMORY	16W16	907	I11	22
14W05	908	H22	24	2*53	FAN OUT TO MEMORY	16W16	908	I11	9

14W06	00	MEMORY TO READ DISTRIBUTOR				3W10	00		
14W06	90	H03	28	2*8		3W10	90	I35	19
14W06	91	H04	1	2*9		3W10	91	I36	19
14W06	92	H04	8	2*10		3W10	92	I38	19
14W06	93	H04	28	2*11		3W10	93	I39	19
14W06	94	H06	1	2*12		3W10	94	I40	19
14W06	95	H06	8	2*13		3W10	95	I41	19
14W06	96	H06	28	2*14		3W10	96	I42	19
14W06	97					3W10	97		
14W06	98					3W10	98		
14W06	99					3W10	99		
14W06	900	H01	1	2*0		3W10	900	I25	19
14W06	901	H01	8	2*1		3W10	901	I26	19
14W06	902	H01	28	2*2		3W10	902	I27	19
14W06	903	H02	1	2*3		3W10	903	I28	19
14W06	904	H02	8	2*4		3W10	904	I29	19
14W06	905	H02	28	2*5		3W10	905	I32	19
14W06	906	H03	1	2*6		3W10	906	I33	19
14W06	907	H03	8	2*7		3W10	907	I34	19
14W06	908					3W10	908		

14W07	00	MEMORY TO READ DISTRIBUTOR				4W16	00
14W07	90	H09	28	2*23	4W16	90	I11 19
14W07	91	H11	1	2*24	4W16	91	I12 19
14W07	92	H11	8	2*25	4W16	92	I14 19
14W07	93	H11	28	2*26	4W16	93	I15 19
14W07	94	H12	1	2*27	4W16	94	I16 19
14W07	95	H12	8	2*28	4W16	95	I17 19
14W07	96	H12	28	2*29	4W16	96	I18 19
14W07	97	G04	21	GO MEMORY	4W16	97	I21 26
14W07	98	G04	20	WRITE MEMORY	4W16	98	I24 20
14W07	99				4W16	99	
14W07	900	H07	1	2*15	4W16	900	I01 19
14W07	901	H07	8	2*16	4W16	901	I02 19
14W07	902	H07	28	2*17	4W16	902	I03 19
14W07	903	H08	1	2*18	4W16	903	I04 19
14W07	904	H08	8	2*19	4W16	904	I05 19
14W07	905	H08	28	2*20	4W16	905	I08 19
14W07	906	H09	1	2*21	4W16	906	I09 19
14W07	907	H09	8	2*22	4W16	907	I10 19
14W07	908				4W16	908	

14W08	00	MEMORY TO READ DISTRIBUTOR				9W17	00
14W08	90	H16	28	2*38	9W17	90	I35 19
14W08	91	H17	1	2*39	9W17	91	I36 19
14W08	92	H17	8	2*40	9W17	92	I38 19
14W08	93	H17	28	2*41	9W17	93	I39 19
14W08	94	H18	1	2*42	9W17	94	I40 19
14W08	95	H18	8	2*43	9W17	95	I41 19
14W08	96	H18	28	2*44	9W17	96	I42 19
14W08	97				9W17	97	
14W08	98				9W17	98	
14W08	99				9W17	99	
14W08	900	H13	1	2*30	9W17	900	I25 19
14W08	901	H13	8	2*31	9W17	901	I26 19
14W08	902	H13	28	2*32	9W17	902	I27 19
14W08	903	H14	1	2*33	9W17	903	I28 19
14W08	904	H14	8	2*34	9W17	904	I29 19
14W08	905	H14	28	2*35	9W17	905	I32 19
14W08	906	H16	1	2*36	9W17	906	I33 19
14W08	907	H16	8	2*37	9W17	907	I34 19
14W08	908				9W17	908	

14W09	00	MEMORY TO READ DISTRIBUTOR				10W17	00
14W09	90	H22	28	2+53	10W17	90	I11 19
14W09	91	H23	1	2+54	10W17	91	I12 19
14W09	92	H23	8	2+55	10W17	92	I14 19
14W09	93	H23	28	2+56	10W17	93	I15 19
14W09	94	H24	1	2+57	10W17	94	I16 19
14W09	95	H24	8	2+58	10W17	95	I17 19
14W09	96	H24	28	2+59	10W17	96	I18 19
14W09	97				10W17	97	
14W09	98				10W17	98	
14W09	99				10W17	99	
14W09	900	H19	1	2+45	10W17	900	I01 19
14W09	901	H19	8	2+46	10W17	901	I02 19
14W09	902	H19	28	2+47	10W17	902	I03 19
14W09	903	H21	1	2+48	10W17	903	I04 19
14W09	904	H21	8	2+49	10W17	904	I05 19
14W09	905	H21	28	2+50	10W17	905	I08 19
14W09	906	H22	1	2+51	10W17	906	I09 19
14W09	907	H22	8	2+52	10W17	907	I10 19
14W09	908				10W17	908	

14W10	00	DISTRIBUTOR FROM WRITE BUFFER				2W06	00
14W10	90	I02	4	2+15 DATA TO MEMORY FAN OUT	2W06	90	B14 27
14W10	91	I02	6	2+16 DATA TO MEMORY FAN OUT	2W06	91	B14 25
14W10	92	I02	7	2+17 DATA TO MEMORY FAN OUT	2W06	92	B14 23
14W10	93	I02	27	2+18 DATA TO MEMORY FAN OUT	2W06	93	B15 8
14W10	94	I02	25	2+19 DATA TO MEMORY FAN OUT	2W06	94	B15 6
14W10	95	I02	22	2+20 DATA TO MEMORY FAN OUT	2W06	95	B15 4
14W10	96	I03	4	2+21 DATA TO MEMORY FAN OUT	2W06	96	B15 27
14W10	97	I03	6	2+22 DATA TO MEMORY FAN OUT	2W06	97	B15 25
14W10	98	I03	7	2+23 DATA TO MEMORY FAN OUT	2W06	98	B15 23
14W10	99	I03	27	2+24 DATA TO MEMORY FAN OUT	2W06	99	B16 8
14W10	900	I03	25	2+25 DATA TO MEMORY FAN OUT	2W06	900	B16 6
14W10	901	I03	22	2+26 DATA TO MEMORY FAN OUT	2W06	901	B16 4
14W10	902	I04	4	2+27 DATA TO MEMORY FAN OUT	2W06	902	B16 27
14W10	903	I04	6	2+28 DATA TO MEMORY FAN OUT	2W06	903	B16 25
14W10	904	I04	7	2+29 DATA TO MEMORY FAN OUT	2W06	904	B16 23
14W10	905	C14	14	MEMORY MARGIN	2W06	905	B22 20
14W10	906				2W06	906	
14W10	907				2W06	907	
14W10	908				2W06	908	

14W11	00	WRITE DISTRIBUTOR TO MEMORY				13W03	00
14W11	90	I11	3	2*23	FAN OUT TO MEMORY	13W03	90 H27 24
14W11	91	I12	18	2*24	FAN OUT TO MEMORY	13W03	91 H29 2
14W11	92	I12	13	2*25	FAN OUT TO MEMORY	13W03	92 H29 19
14W11	93	I13	3	2*26	FAN OUT TO MEMORY	13W03	93 H29 24
14W11	94	I14	18	2*27	FAN OUT TO MEMORY	13W03	94 H30 2
14W11	95	I14	13	2*28	FAN OUT TO MEMORY	13W03	95 H30 19
14W11	96	I15	3	2*29	FAN OUT TO MEMORY	13W03	96 H30 24
14W11	97					13W03	97
14W11	98					13W03	98
14W11	99					13W03	99
14W11	900	I06	18	2*15	FAN OUT TO MEMORY	13W03	900 H25 2
14W11	901	I06	13	2*16	FAN OUT TO MEMORY	13W03	901 H25 19
14W11	902	I07	3	2*17	FAN OUT TO MEMORY	13W03	902 H25 24
14W11	903	I08	18	2*18	FAN OUT TO MEMORY	13W03	903 H26 2
14W11	904	I08	13	2*19	FAN OUT TO MEMORY	13W03	904 H26 19
14W11	905	I09	3	2*20	FAN OUT TO MEMORY	13W03	905 H26 24
14W11	906	I10	18	2*21	FAN OUT TO MEMORY	13W03	906 H27 2
14W11	907	I10	13	2*22	FAN OUT TO MEMORY	13W03	907 H27 19
14W11	908					13W03	908

14W12	00	WRITE DISTRIBUTOR TO MEMORY				4W04	00
14W12	90	I11	5	2*23	FAN OUT TO MEMORY	4W04	90 H09 24
14W12	91	I12	20	2*24	FAN OUT TO MEMORY	4W04	91 H11 2
14W12	92	I12	11	2*25	FAN OUT TO MEMORY	4W04	92 H11 19
14W12	93	I13	5	2*26	FAN OUT TO MEMORY	4W04	93 H11 24
14W12	94	I14	20	2*27	FAN OUT TO MEMORY	4W04	94 H12 2
14W12	95	I14	11	2*28	FAN OUT TO MEMORY	4W04	95 H12 19
14W12	96	I15	5	2*29	FAN OUT TO MEMORY	4W04	96 H12 24
14W12	97					4W04	97
14W12	98					4W04	98
14W12	99					4W04	99
14W12	900	I06	20	2*15	FAN OUT TO MEMORY	4W04	900 H07 2
14W12	901	I06	11	2*16	FAN OUT TO MEMORY	4W04	901 H07 19
14W12	902	I07	5	2*17	FAN OUT TO MEMORY	4W04	902 H07 24
14W12	903	I08	20	2*18	FAN OUT TO MEMORY	4W04	903 H08 2
14W12	904	I08	11	2*19	FAN OUT TO MEMORY	4W04	904 H08 19
14W12	905	I09	5	2*20	FAN OUT TO MEMORY	4W04	905 H08 24
14W12	906	I10	20	2*21	FAN OUT TO MEMORY	4W04	906 H09 2
14W12	907	I10	11	2*22	FAN OUT TO MEMORY	4W04	907 H09 19
14W12	908					4W04	908

14W13	00	WRITE DISTRIBUTOR TO MEMORY				3W16	00
14W13	90	I11	28	2*23	FAN OUT TO MEMORY	3W16	90 H27 24
14W13	91	I12	24	2*24	FAN OUT TO MEMORY	3W16	91 H29 2
14W13	92	I12	7	2*25	FAN OUT TO MEMORY	3W16	92 H29 19
14W13	93	I13	28	2*26	FAN OUT TO MEMORY	3W16	93 H29 24
14W13	94	I14	24	2*27	FAN OUT TO MEMORY	3W16	94 H30 2
14W13	95	I14	7	2*28	FAN OUT TO MEMORY	3W16	95 H30 19
14W13	96	I15	28	2*29	FAN OUT TO MEMORY	3W16	96 H30 24
14W13	97	G05	28		ACCEPT	3W16	97 I23 21
14W13	98					3W16	98
14W13	99					3W16	99
14W13	900	I06	24	2*15	FAN OUT TO MEMORY	3W16	900 H25 2
14W13	901	I06	7	2*16	FAN OUT TO MEMORY	3W16	901 H25 19
14W13	902	I07	28	2*17	FAN OUT TO MEMORY	3W16	902 H25 24
14W13	903	I08	24	2*18	FAN OUT TO MEMORY	3W16	903 H26 2
14W13	904	I08	7	2*19	FAN OUT TO MEMORY	3W16	904 H26 19
14W13	905	I09	28	2*20	FAN OUT TO MEMORY	3W16	905 H26 24
14W13	906	I10	24	2*21	FAN OUT TO MEMORY	3W16	906 H27 2
14W13	907	I10	7	2*22	FAN OUT TO MEMORY	3W16	907 H27 19
14W13	908					3W16	908

14W14	00	WRITE DISTRIBUTOR TO MEMORY				9W04	00
14W14	90	I11	13	2*23	FAN OUT TO MEMORY	9W04	90 H27 24
14W14	91	I12	3	2*24	FAN OUT TO MEMORY	9W04	91 H29 2
14W14	92	I13	18	2*25	FAN OUT TO MEMORY	9W04	92 H29 19
14W14	93	I13	13	2*26	FAN OUT TO MEMORY	9W04	93 H29 24
14W14	94	I14	3	2*27	FAN OUT TO MEMORY	9W04	94 H30 2
14W14	95	I15	18	2*28	FAN OUT TO MEMORY	9W04	95 H30 19
14W14	96	I15	13	2*29	FAN OUT TO MEMORY	9W04	96 H30 24
14W14	97					9W04	97
14W14	98					9W04	98
14W14	99					9W04	99
14W14	900	I06	3	2*15	FAN OUT TO MEMORY	9W04	900 H25 2
14W14	901	I07	18	2*16	FAN OUT TO MEMORY	9W04	901 H25 19
14W14	902	I07	13	2*17	FAN OUT TO MEMORY	9W04	902 H25 24
14W14	903	I08	3	2*18	FAN OUT TO MEMORY	9W04	903 H26 2
14W14	904	I09	18	2*19	FAN OUT TO MEMORY	9W04	904 H26 19
14W14	905	I09	13	2*20	FAN OUT TO MEMORY	9W04	905 H26 24
14W14	906	I10	3	2*21	FAN OUT TO MEMORY	9W04	906 H27 2
14W14	907	I11	18	2*22	FAN OUT TO MEMORY	9W04	907 H27 19
14W14	908					9W04	908

14W15	00	WRITE DISTRIBUTOR TO MEMORY				10W04	00
14W15	90	I11	11	2*23	FAN OUT TO MEMORY	10W04	90 H09 24
14W15	91	I12	9	2*24	FAN OUT TO MEMORY	10W04	91 H11 2
14W15	92	I13	20	2*25	FAN OUT TO MEMORY	10W04	92 H11 19
14W15	93	I13	11	2*26	FAN OUT TO MEMORY	10W04	93 H11 24
14W15	94	I14	9	2*27	FAN OUT TO MEMORY	10W04	94 H12 2
14W15	95	I15	20	2*28	FAN OUT TO MEMORY	10W04	95 H12 19
14W15	96	I15	11	2*29	FAN OUT TO MEMORY	10W04	96 H12 24
14W15	97					10W04	97
14W15	98					10W04	98
14W15	99					10W04	99
14W15	900	I06	9	2*15	FAN OUT TO MEMORY	10W04	900 H07 2
14W15	901	I07	20	2*16	FAN OUT TO MEMORY	10W04	901 H07 19
14W15	902	I07	11	2*17	FAN OUT TO MEMORY	10W04	902 H07 24
14W15	903	I08	9	2*18	FAN OUT TO MEMORY	10W04	903 H08 2
14W15	904	I09	20	2*19	FAN OUT TO MEMORY	10W04	904 H08 19
14W15	905	I09	11	2*20	FAN OUT TO MEMORY	10W04	905 H08 24
14W15	906	I10	9	2*21	FAN OUT TO MEMORY	10W04	906 H09 2
14W15	907	I11	20	2*22	FAN OUT TO MEMORY	10W04	907 H09 19
14W15	908					10W04	908

14W16	00	WRITE DISTRIBUTOR TO MEMORY				15W04	00
14W16	90	I12	26	2*24	FAN OUT TO MEMORY	15W04	90 H29 2
14W16	91	I13	22	2*25	FAN OUT TO MEMORY	15W04	91 H29 19
14W16	92	I13	9	2*26	FAN OUT TO MEMORY	15W04	92 H29 24
14W16	93	I14	26	2*27	FAN OUT TO MEMORY	15W04	93 H30 2
14W16	94	I15	22	2*28	FAN OUT TO MEMORY	15W04	94 H30 19
14W16	95	I15	9	2*29	FAN OUT TO MEMORY	15W04	95 H30 24
14W16	96	I11	9	2*23	FAN OUT TO MEMORY	15W04	96 H27 24
14W16	97					15W04	97
14W16	98					15W04	98
14W16	99					15W04	99
14W16	900	I06	26	2*15	FAN OUT TO MEMORY	15W04	900 H25 2
14W16	901	I07	22	2*16	FAN OUT TO MEMORY	15W04	901 H25 19
14W16	902	I07	9	2*17	FAN OUT TO MEMORY	15W04	902 H25 24
14W16	903	I08	26	2*18	FAN OUT TO MEMORY	15W04	903 H26 2
14W16	904	I09	22	2*19	FAN OUT TO MEMORY	15W04	904 H26 19
14W16	905	I09	9	2*20	FAN OUT TO MEMORY	15W04	905 H26 24
14W16	906	I10	26	2*21	FAN OUT TO MEMORY	15W04	906 H27 2
14W16	907	I11	22	2*22	FAN OUT TO MEMORY	15W04	907 H27 19
14W16	908					15W04	908

14W17	00	WRITE DISTRIBUTOR TO MEMORY				16W04	00
14W17	90	I12	28	2*24	FAN OUT TO MEMORY	16W04	90 H11 2
14W17	91	I13	24	2*25	FAN OUT TO MEMORY	16W04	91 H11 19
14W17	92	I13	7	2*26	FAN OUT TO MEMORY	16W04	92 H11 24
14W17	93	I14	28	2*27	FAN OUT TO MEMORY	16W04	93 H12 2
14W17	94	I15	24	2*28	FAN OUT TO MEMORY	16W04	94 H12 19
14W17	95	I15	7	2*29	FAN OUT TO MEMORY	16W04	95 H12 24
14W17	96					16W04	96
14W17	97					16W04	97
14W17	98					16W04	98
14W17	99					16W04	99
14W17	900	I06	28	2*15	FAN OUT TO MEMORY	16W04	900 H07 2
14W17	901	I07	24	2*16	FAN OUT TO MEMORY	16W04	901 H07 19
14W17	902	I07	7	2*17	FAN OUT TO MEMORY	16W04	902 H07 24
14W17	903	I08	28	2*18	FAN OUT TO MEMORY	16W04	903 H08 2
14W17	904	I09	24	2*19	FAN OUT TO MEMORY	16W04	904 H08 19
14W17	905	I09	7	2*20	FAN OUT TO MEMORY	16W04	905 H08 24
14W17	906	I10	28	2*21	FAN OUT TO MEMORY	16W04	906 H09 2
14W17	907	I11	24	2*22	FAN OUT TO MEMORY	16W04	907 H09 19
14W17	908	I11	7	2*23	FAN OUT TO MEMORY	16W04	908 H09 24

14W18	00	MEMORY ADDRESS FROM CONTROL				5W33	00
14W18	90	G01	18	2*8	ADDRESS TO MEMORY	5W33	90 Q42 20
14W18	91	G02	11	2*9	ADDRESS TO MEMORY	5W33	91 R40 9
14W18	92	G02	14	2*10	ADDRESS TO MEMORY	5W33	92 R40 5
14W18	93	G02	17	2*11	ADDRESS TO MEMORY	5W33	93 R40 20
14W18	94	G02	18	2*12	ADDRESS TO MEMORY	5W33	94 R41 9
14W18	95	G03	11	2*13	ADDRESS TO MEMORY	5W33	95 R41 5
14W18	96	G03	14	2*14	ADDRESS TO MEMORY	5W33	96 R41 20
14W18	97	G03	17	2*15	ADDRESS TO MEMORY	5W33	97 R42 9
14W18	98	G03	18	2*16	ADDRESS TO MEMORY	5W33	98 R42 5
14W18	99					5W33	99
14W18	900	G13	12		CLOCK	5W33	900 M32 27
14W18	901	G05	18	2*0	BANK SELECT	5W33	901 Q40 9
14W18	902	G05	22	2*1	BANK SELECT	5W33	902 Q40 5
14W18	903	G04	8	2*2	CHASSIS 14 SELECT (101)	5W33	903 Q40 20
14W18	904	G04	4	2*3	CHASSIS 14 SELECT (101)	5W33	904 Q41 9
14W18	905	G04	13	2*4	CHASSIS 14 SELECT (101)	5W33	905 Q41 5
14W18	906	G01	11	2*5	ADDRESS TO MEMORY	5W33	906 Q41 20
14W18	907	G01	17	2*6	ADDRESS TO MEMORY	5W33	907 Q42 5
14W18	908	G01	14	2*7	ADDRESS TO MEMORY	5W33	908 Q42 9

14W39	00	WRITE DISTRIBUTOR TO MEMORY				14W39	00		
14W39	90	I11	26	2*23	FAN OUT TO MEMORY	14W39	90	H09	24
14W39	91	I12	22	2*24	FAN OUT TO MEMORY	14W39	91	H11	2
14W39	92	I12	9	2*25	FAN OUT TO MEMORY	14W39	92	H11	19
14W39	93	I13	26	2*26	FAN OUT TO MEMORY	14W39	93	H11	24
14W39	94	I14	22	2*27	FAN OUT TO MEMORY	14W39	94	H12	2
14W39	95	I14	9	2*28	FAN OUT TO MEMORY	14W39	95	H12	19
14W39	96	I15	26	2*29	FAN OUT TO MEMORY	14W39	96	H12	24
14W39	97					14W39	97		
14W39	98					14W39	98		
14W39	99					14W39	99		
14W39	900	I06	22	2*15	FAN OUT TO MEMORY	14W39	900	H07	2
14W39	901	I06	9	2*16	FAN OUT TO MEMORY	14W39	901	H07	19
14W39	902	I07	26	2*17	FAN OUT TO MEMORY	14W39	902	H07	24
14W39	903	I08	22	2*18	FAN OUT TO MEMORY	14W39	903	H08	2
14W39	904	I08	9	2*19	FAN OUT TO MEMORY	14W39	904	H08	19
14W39	905	I09	26	2*20	FAN OUT TO MEMORY	14W39	905	H08	24
14W39	906	I10	22	2*21	FAN OUT TO MEMORY	14W39	906	H09	2
14W39	907	I10	9	2*22	FAN OUT TO MEMORY	14W39	907	H09	19
14W39	908					14W39	908		

14W39	00	MEMORY FROM WRITE DISTRIBUTOR				14W39	00		
14W39	90	H09	24	2*23	FAN OUT TO MEMORY	14W39	90	I11	26
14W39	91	H11	2	2*24	FAN OUT TO MEMORY	14W39	91	I12	22
14W39	92	H11	19	2*25	FAN OUT TO MEMORY	14W39	92	I12	9
14W39	93	H11	24	2*26	FAN OUT TO MEMORY	14W39	93	I13	26
14W39	94	H12	2	2*27	FAN OUT TO MEMORY	14W39	94	I14	22
14W39	95	H12	19	2*28	FAN OUT TO MEMORY	14W39	95	I14	9
14W39	96	H12	24	2*29	FAN OUT TO MEMORY	14W39	96	I15	26
14W39	97					14W39	97		
14W39	98					14W39	98		
14W39	99					14W39	99		
14W39	900	H07	2	2*15	FAN OUT TO MEMORY	14W39	900	I06	22
14W39	901	H07	19	2*16	FAN OUT TO MEMORY	14W39	901	I06	9
14W39	902	H07	24	2*17	FAN OUT TO MEMORY	14W39	902	I07	26
14W39	903	H08	2	2*18	FAN OUT TO MEMORY	14W39	903	I08	22
14W39	904	H08	19	2*19	FAN OUT TO MEMORY	14W39	904	I08	9
14W39	905	H08	24	2*20	FAN OUT TO MEMORY	14W39	905	I09	26
14W39	906	H09	2	2*21	FAN OUT TO MEMORY	14W39	906	I10	22
14W39	907	H09	19	2*22	FAN OUT TO MEMORY	14W39	907	I10	9
14W39	908					14W39	908		



15W03	00	MEMORY FROM WRITE DISTRIBUTOR				13W16	00
15W03	90	H21	24	2*8	FAN OUT TO MEMORY	13W16	90 133 9
15W03	91	H22	2	2*9	FAN OUT TO MEMORY	13W16	91 134 26
15W03	92	H22	19	2*10	FAN OUT TO MEMORY	13W16	92 135 22
15W03	93	H22	24	2*11	FAN OUT TO MEMORY	13W16	93 135 9
15W03	94	H24	2	2*12	FAN OUT TO MEMORY	13W16	94 136 26
15W03	95	H24	19	2*13	FAN OUT TO MEMORY	13W16	95 137 22
15W03	96	H24	24	2*14	FAN OUT TO MEMORY	13W16	96 137 9
15W03	97					13W16	97
15W03	98					13W16	98
15W03	99					13W16	99
15W03	900	H19	2	2*0	FAN OUT TO MEMORY	13W16	900 128 26
15W03	901	H19	19	2*1	FAN OUT TO MEMORY	13W16	901 129 22
15W03	902	H19	24	2*2	FAN OUT TO MEMORY	13W16	902 129 9
15W03	903	H20	2	2*3	FAN OUT TO MEMORY	13W16	903 130 26
15W03	904	H20	19	2*4	FAN OUT TO MEMORY	13W16	904 131 22
15W03	905	H20	24	2*5	FAN OUT TO MEMORY	13W16	905 131 9
15W03	906	H21	2	2*6	FAN OUT TO MEMORY	13W16	906 132 26
15W03	907	H21	19	2*7	FAN OUT TO MEMORY	13W16	907 133 22
15W03	908					13W16	908

15W04	00	MEMORY FROM WRITE DISTRIBUTOR				14W16	00
15W04	90	H29	2	2*24	FAN OUT TO MEMORY	14W16	90 112 26
15W04	91	H29	19	2*25	FAN OUT TO MEMORY	14W16	91 113 22
15W04	92	H29	24	2*26	FAN OUT TO MEMORY	14W16	92 113 9
15W04	93	H30	2	2*27	FAN OUT TO MEMORY	14W16	93 114 26
15W04	94	H30	19	2*28	FAN OUT TO MEMORY	14W16	94 115 22
15W04	95	H30	24	2*29	FAN OUT TO MEMORY	14W16	95 115 9
15W04	96	H27	24	2*23	FAN OUT TO MEMORY	14W16	96 111 9
15W04	97					14W16	97
15W04	98					14W16	98
15W04	99					14W16	99
15W04	900	H25	2	2*15	FAN OUT TO MEMORY	14W16	900 106 26
15W04	901	H25	19	2*16	FAN OUT TO MEMORY	14W16	901 107 22
15W04	902	H25	24	2*17	FAN OUT TO MEMORY	14W16	902 107 9
15W04	903	H26	2	2*18	FAN OUT TO MEMORY	14W16	903 108 26
15W04	904	H26	19	2*19	FAN OUT TO MEMORY	14W16	904 109 22
15W04	905	H26	24	2*20	FAN OUT TO MEMORY	14W16	905 109 9
15W04	906	H27	2	2*21	FAN OUT TO MEMORY	14W16	906 110 26
15W04	907	H27	19	2*22	FAN OUT TO MEMORY	14W16	907 111 22
15W04	908					14W16	908

15W05	00	MEMORY FROM WRITE DISTRIBUTOR				16W17	00		
15W05	90	H41	2	2*54	FAN OUT TO MEMORY	16W17	90	I12	28
15W05	91	H41	19	2*55	FAN OUT TO MEMORY	16W17	91	I13	24
15W05	92	H41	24	2*56	FAN OUT TO MEMORY	16W17	92	I13	7
15W05	93	H42	2	2*57	FAN OUT TO MEMORY	16W17	93	I14	28
15W05	94	H42	19	2*58	FAN OUT TO MEMORY	16W17	94	I15	24
15W05	95	H42	24	2*59	FAN OUT TO MEMORY	16W17	95	I15	7
15W05	96	H40	24	2*53	FAN OUT TO MEMORY	16W17	96	I11	7
15W05	97					16W17	97		
15W05	98					16W17	98		
15W05	99					16W17	99		
15W05	900	H37	2	2*45	FAN OUT TO MEMORY	16W17	900	I06	28
15W05	901	H37	19	2*46	FAN OUT TO MEMORY	16W17	901	I07	24
15W05	902	H37	24	2*47	FAN OUT TO MEMORY	16W17	902	I07	7
15W05	903	H39	2	2*48	FAN OUT TO MEMORY	16W17	903	I08	28
15W05	904	H39	19	2*49	FAN OUT TO MEMORY	16W17	904	I09	24
15W05	905	H39	24	2*50	FAN OUT TO MEMORY	16W17	905	I09	7
15W05	906	H40	2	2*51	FAN OUT TO MEMORY	16W17	906	I10	28
15W05	907	H40	19	2*52	FAN OUT TO MEMORY	16W17	907	I11	24
15W05	908					16W17	908		

15W06	00	MEMORY TO READ DISTRIBUTOR				3W11	00		
15W06	90	H21	28	2*8		3W11	90	I35	21
15W06	91	H22	1	2*9		3W11	91	I36	21
15W06	92	H22	8	2*10		3W11	92	I38	21
15W06	93	H22	28	2*11		3W11	93	I39	21
15W06	94	H24	1	2*12		3W11	94	I40	21
15W06	95	H24	8	2*13		3W11	95	I41	21
15W06	96	H24	28	2*14		3W11	96	I42	21
15W06	97					3W11	97		
15W06	98					3W11	98		
15W06	99					3W11	99		
15W06	900	H19	1	2*0		3W11	900	I25	21
15W06	901	H19	8	2*1		3W11	901	I26	21
15W06	902	H19	28	2*2		3W11	902	I27	21
15W06	903	H20	1	2*3		3W11	903	I28	21
15W06	904	H20	8	2*4		3W11	904	I29	21
15W06	905	H20	28	2*5		3W11	905	I32	21
15W06	906	H21	1	2*6		3W11	906	I33	21
15W06	907	H21	8	2*7		3W11	907	I34	21
15W06	908					3W11	908		

15W07	00	MEMORY TO READ DISTRIBUTOR			4W17	00		
15W07	90	H27	28	2*23	4W17	90	I11	21
15W07	91	H29	1	2*24	4W17	91	I12	21
15W07	92	H29	8	2*25	4W17	92	I14	21
15W07	93	H29	28	2*26	4W17	93	I15	21
15W07	94	H30	1	2*27	4W17	94	I16	21
15W07	95	H30	8	2*28	4W17	95	I17	21
15W07	96	H30	28	2*29	4W17	96	I18	21
15W07	97	G38	21	Q0	4W17	97	I21	5
15W07	98	G38	20	WRITE	4W17	98	I24	22
15W07	99				4W17	99		
15W07	900	H25	1	2*15	4W17	900	I01	21
15W07	901	H25	8	2*16	4W17	901	I02	21
15W07	902	H25	28	2*17	4W17	902	I03	21
15W07	903	H26	1	2*18	4W17	903	I04	21
15W07	904	H26	8	2*19	4W17	904	I05	21
15W07	905	H26	28	2*20	4W17	905	I08	21
15W07	906	H27	1	2*21	4W17	906	I09	21
15W07	907	H27	8	2*22	4W17	907	I10	21
15W07	908				4W17	908		

15W08	00	MEMORY TO READ DISTRIBUTOR			9W18	00		
15W08	90	H34	28	2*38	9W18	90	I35	21
15W08	91	H35	1	2*39	9W18	91	I36	21
15W08	92	H35	8	2*40	9W18	92	I38	21
15W08	93	H35	28	2*41	9W18	93	I39	21
15W08	94	H36	1	2*42	9W18	94	I40	21
15W08	95	H36	8	2*43	9W18	95	I41	21
15W08	96	H36	28	2*44	9W18	96	I42	21
15W08	97				9W18	97		
15W08	98				9W18	98		
15W08	99				9W18	99		
15W08	900	H31	1	2*30	9W18	900	I25	21
15W08	901	H31	8	2*31	9W18	901	I26	21
15W08	902	H31	28	2*32	9W18	902	I27	21
15W08	903	H32	1	2*33	9W18	903	I28	21
15W08	904	H32	8	2*34	9W18	904	I29	21
15W08	905	H32	28	2*35	9W18	905	I32	21
15W08	906	H34	1	2*36	9W18	906	I33	21
15W08	907	H34	8	2*37	9W18	907	I34	21
15W08	908				9W18	908		

15W09	00	MEMORY TO READ DISTRIBUTOR				10W18	00
15W09	90	H40	28	2*53	10W18	90	I11 21
15W09	91	H41	1	2*54	10W18	91	I12 21
15W09	92	H41	8	2*55	10W18	92	I14 21
15W09	93	H41	28	2*56	10W18	93	I15 21
15W09	94	H42	1	2*57	10W18	94	I16 21
15W09	95	H42	8	2*58	10W18	95	I17 21
15W09	96	H42	28	2*59	10W18	96	I18 21
15W09	97				10W18	97	
15W09	98				10W18	98	
15W09	99				10W18	99	
15W09	900	H37	1	2*45	10W18	900	I01 21
15W09	901	H37	8	2*46	10W18	901	I02 21
15W09	902	H37	28	2*47	10W18	902	I03 21
15W09	903	H39	1	2*48	10W18	903	I04 21
15W09	904	H39	8	2*49	10W18	904	I05 21
15W09	905	H39	28	2*50	10W18	905	I08 21
15W09	906	H40	1	2*51	10W18	906	I09 21
15W09	907	H40	8	2*52	10W18	907	I10 21
15W09	908				10W18	908	

15W10	00	DISTRIBUTOR FROM WRITE BUFFER				2W07	00
15W10	90	I39	4	2*30 DATA TO MEMORY FAN OUT	2W07	90	B17 8
15W10	91	I39	6	2*31 DATA TO MEMORY FAN OUT	2W07	91	B17 6
15W10	92	I39	7	2*32 DATA TO MEMORY FAN OUT	2W07	92	B17 4
15W10	93	I39	27	2*33 DATA TO MEMORY FAN OUT	2W07	93	B17 27
15W10	94	I39	25	2*34 DATA TO MEMORY FAN OUT	2W07	94	B17 25
15W10	95	I39	22	2*35 DATA TO MEMORY FAN OUT	2W07	95	B17 23
15W10	96	I40	4	2*36 DATA TO MEMORY FAN OUT	2W07	96	B18 8
15W10	97	I40	6	2*37 DATA TO MEMORY FAN OUT	2W07	97	B18 6
15W10	98	I40	7	2*38 DATA TO MEMORY FAN OUT	2W07	98	B18 4
15W10	99	I40	27	2*39 DATA TO MEMORY FAN OUT	2W07	99	B18 27
15W10	900	I40	25	2*40 DATA TO MEMORY FAN OUT	2W07	900	B18 25
15W10	901	I40	22	2*41 DATA TO MEMORY FAN OUT	2W07	901	B18 23
15W10	902	I41	4	2*42 DATA TO MEMORY FAN OUT	2W07	902	B19 8
15W10	903	I41	6	2*43 DATA TO MEMORY FAN OUT	2W07	903	B19 6
15W10	904	I41	7	2*44 DATA TO MEMORY FAN OUT	2W07	904	B19 4
15W10	905	C14	14	MEMORY MARGIN	2W07	905	B22 18
15W10	906				2W07	906	
15W10	907				2W07	907	
15W10	908				2W07	908	

15W11	00	WRITE DISTRIBUTOR TO MEMORY				13W04	00
15W11	90	I33	3	2*38	FAN OUT TO MEMORY	13W04	90 H34 24
15W11	91	I34	18	2*39	FAN OUT TO MEMORY	13W04	91 H35 2
15W11	92	I34	13	2*40	FAN OUT TO MEMORY	13W04	92 H35 19
15W11	93	I35	3	2*41	FAN OUT TO MEMORY	13W04	93 H35 24
15W11	94	I36	18	2*42	FAN OUT TO MEMORY	13W04	94 H36 2
15W11	95	I36	13	2*43	FAN OUT TO MEMORY	13W04	95 H36 19
15W11	96	I37	3	2*44	FAN OUT TO MEMORY	13W04	96 H36 24
15W11	97					13W04	97
15W11	98					13W04	98
15W11	99					13W04	99
15W11	900	I28	18	2*30	FAN OUT TO MEMORY	13W04	900 H31 2
15W11	901	I28	13	2*31	FAN OUT TO MEMORY	13W04	901 H31 19
15W11	902	I29	3	2*32	FAN OUT TO MEMORY	13W04	902 H31 24
15W11	903	I30	18	2*33	FAN OUT TO MEMORY	13W04	903 H32 2
15W11	904	I30	13	2*34	FAN OUT TO MEMORY	13W04	904 H32 19
15W11	905	I31	3	2*35	FAN OUT TO MEMORY	13W04	905 H32 24
15W11	906	I32	18	2*36	FAN OUT TO MEMORY	13W04	906 H34 2
15W11	907	I32	13	2*37	FAN OUT TO MEMORY	13W04	907 H34 19
15W11	908					13W04	908

15W12	00	WRITE DISTRIBUTOR TO MEMORY				4W09	00
15W12	90	I33	5	2*38	FAN OUT TO MEMORY	4W09	90 H16 24
15W12	91	I34	20	2*39	FAN OUT TO MEMORY	4W09	91 H17 2
15W12	92	I34	11	2*40	FAN OUT TO MEMORY	4W09	92 H17 19
15W12	93	I35	5	2*41	FAN OUT TO MEMORY	4W09	93 H17 24
15W12	94	I36	20	2*42	FAN OUT TO MEMORY	4W09	94 H18 2
15W12	95	I36	11	2*43	FAN OUT TO MEMORY	4W09	95 H18 19
15W12	96	I37	5	2*44	FAN OUT TO MEMORY	4W09	96 H18 24
15W12	97					4W09	97
15W12	98					4W09	98
15W12	99					4W09	99
15W12	900	I28	20	2*30	FAN OUT TO MEMORY	4W09	900 H13 2
15W12	901	I28	11	2*31	FAN OUT TO MEMORY	4W09	901 H13 19
15W12	902	I29	5	2*32	FAN OUT TO MEMORY	4W09	902 H13 24
15W12	903	I30	20	2*33	FAN OUT TO MEMORY	4W09	903 H14 2
15W12	904	I30	11	2*34	FAN OUT TO MEMORY	4W09	904 H14 19
15W12	905	I31	5	2*35	FAN OUT TO MEMORY	4W09	905 H14 24
15W12	906	I32	20	2*36	FAN OUT TO MEMORY	4W09	906 H16 2
15W12	907	I32	11	2*37	FAN OUT TO MEMORY	4W09	907 H16 19
15W12	908					4W09	908

15W13	00	WRITE DISTRIBUTOR TO MEMORY				3W17	00
15W13	90	I33	28	2*38	FAN OUT TO MEMORY	3W17	90 H34 24
15W13	91	I34	24	2*39	FAN OUT TO MEMORY	3W17	91 H35 2
15W13	92	I34	7	2*40	FAN OUT TO MEMORY	3W17	92 H35 19
15W13	93	I35	28	2*41	FAN OUT TO MEMORY	3W17	93 H35 24
15W13	94	I36	24	2*42	FAN OUT TO MEMORY	3W17	94 H36 2
15W13	95	I36	7	2*43	FAN OUT TO MEMORY	3W17	95 H36 19
15W13	96	I37	28	2*44	FAN OUT TO MEMORY	3W17	96 H36 24
15W13	97	G39	28		ACCEPT	3W17	97 I23 23
15W13	98					3W17	98
15W13	99					3W17	99
15W13	900	I28	24	2*30	FAN OUT TO MEMORY	3W17	900 H31 2
15W13	901	I28	7	2*31	FAN OUT TO MEMORY	3W17	901 H31 19
15W13	902	I29	28	2*32	FAN OUT TO MEMORY	3W17	902 H31 24
15W13	903	I30	24	2*33	FAN OUT TO MEMORY	3W17	903 H32 2
15W13	904	I30	7	2*34	FAN OUT TO MEMORY	3W17	904 H32 19
15W13	905	I31	28	2*35	FAN OUT TO MEMORY	3W17	905 H32 24
15W13	906	I32	24	2*36	FAN OUT TO MEMORY	3W17	906 H34 2
15W13	907	I32	7	2*37	FAN OUT TO MEMORY	3W17	907 H34 19
15W13	908					3W17	908

15W14	00	WRITE DISTRIBUTOR TO MEMORY				9W05	00
15W14	90	I33	13	2*38	FAN OUT TO MEMORY	9W05	90 H34 24
15W14	91	I34	3	2*39	FAN OUT TO MEMORY	9W05	91 H35 2
15W14	92	I35	18	2*40	FAN OUT TO MEMORY	9W05	92 H35 19
15W14	93	I35	13	2*41	FAN OUT TO MEMORY	9W05	93 H35 24
15W14	94	I36	3	2*42	FAN OUT TO MEMORY	9W05	94 H36 2
15W14	95	I37	18	2*43	FAN OUT TO MEMORY	9W05	95 H36 19
15W14	96	I37	13	2*44	FAN OUT TO MEMORY	9W05	96 H36 24
15W14	97					9W05	97
15W14	98					9W05	98
15W14	99					9W05	99
15W14	900	I28	3	2*30	FAN OUT TO MEMORY	9W05	900 H31 2
15W14	901	I29	18	2*31	FAN OUT TO MEMORY	9W05	901 H31 19
15W14	902	I29	13	2*32	FAN OUT TO MEMORY	9W05	902 H31 24
15W14	903	I30	3	2*33	FAN OUT TO MEMORY	9W05	903 H32 2
15W14	904	I31	18	2*34	FAN OUT TO MEMORY	9W05	904 H32 19
15W14	905	I31	13	2*35	FAN OUT TO MEMORY	9W05	905 H32 24
15W14	906	I32	3	2*36	FAN OUT TO MEMORY	9W05	906 H34 2
15W14	907	I33	18	2*37	FAN OUT TO MEMORY	9W05	907 H34 19
15W14	908					9W05	908

15W15	00	WRITE DISTRIBUTOR TO MEMORY				10W05	00
15W15	90	133	11	2*38	FAN OUT TO MEMORY	10W05	90 H16 24
15W15	91	134	9	2*39	FAN OUT TO MEMORY	10W05	91 H17 2
15W15	92	135	20	2*40	FAN OUT TO MEMORY	10W05	92 H17 19
15W15	93	135	11	2*41	FAN OUT TO MEMORY	10W05	93 H17 24
15W15	94	136	9	2*42	FAN OUT TO MEMORY	10W05	94 H18 2
15W15	95	137	20	2*43	FAN OUT TO MEMORY	10W05	95 H18 19
15W15	96	137	11	2*44	FAN OUT TO MEMORY	10W05	96 H18 24
15W15	97					10W05	97
15W15	98					10W05	98
15W15	99					10W05	99
15W15	900	128	5	2*30	FAN OUT TO MEMORY	10W05	900 H13 2
15W15	901	129	20	2*31	FAN OUT TO MEMORY	10W05	901 H13 19
15W15	902	129	11	2*32	FAN OUT TO MEMORY	10W05	902 H13 24
15W15	903	130	9	2*33	FAN OUT TO MEMORY	10W05	903 H14 2
15W15	904	131	20	2*34	FAN OUT TO MEMORY	10W05	904 H14 19
15W15	905	131	11	2*35	FAN OUT TO MEMORY	10W05	905 H14 24
15W15	906	132	9	2*36	FAN OUT TO MEMORY	10W05	906 H16 2
15W15	907	133	20	2*37	FAN OUT TO MEMORY	10W05	907 H16 19
15W15	908					10W05	908

15W16	00	WRITE DISTRIBUTOR TO MEMORY				14W04	00
15W16	90	134	26	2*39	FAN OUT TO MEMORY	14W04	90 H17 2
15W16	91	135	22	2*40	FAN OUT TO MEMORY	14W04	91 H17 19
15W16	92	135	9	2*41	FAN OUT TO MEMORY	14W04	92 H17 24
15W16	93	136	26	2*42	FAN OUT TO MEMORY	14W04	93 H18 2
15W16	94	137	22	2*43	FAN OUT TO MEMORY	14W04	94 H18 19
15W16	95	137	9	2*44	FAN OUT TO MEMORY	14W04	95 H18 24
15W16	96					14W04	96
15W16	97					14W04	97
15W16	98					14W04	98
15W16	99					14W04	99
15W16	900	128	26	2*30	FAN OUT TO MEMORY	14W04	900 H13 2
15W16	901	129	22	2*31	FAN OUT TO MEMORY	14W04	901 H13 19
15W16	902	129	9	2*32	FAN OUT TO MEMORY	14W04	902 H13 24
15W16	903	130	26	2*33	FAN OUT TO MEMORY	14W04	903 H14 2
15W16	904	131	22	2*34	FAN OUT TO MEMORY	14W04	904 H14 19
15W16	905	131	9	2*35	FAN OUT TO MEMORY	14W04	905 H14 24
15W16	906	132	26	2*36	FAN OUT TO MEMORY	14W04	906 H16 2
15W16	907	133	22	2*37	FAN OUT TO MEMORY	14W04	907 H16 19
15W16	908	133	9	2*38	FAN OUT TO MEMORY	14W04	908 H16 24

15W17	00	WRITE DISTRIBUTOR TO MEMORY				16W05	00
15W17	90	I34	28	2*39	FAN OUT TO MEMORY	16W05	90 H17 2
15W17	91	I35	24	2*40	FAN OUT TO MEMORY	16W05	91 H17 19
15W17	92	I35	7	2*41	FAN OUT TO MEMORY	16W05	92 H17 24
15W17	93	I36	28	2*42	FAN OUT TO MEMORY	16W05	93 H18 2
15W17	94	I37	24	2*43	FAN OUT TO MEMORY	16W05	94 H18 19
15W17	95	I37	7	2*44	FAN OUT TO MEMORY	16W05	95 H18 24
15W17	96					16W05	96
15W17	97					16W05	97
15W17	98					16W05	98
15W17	99					16W05	99
15W17	900	I28	28	2*30	FAN OUT TO MEMORY	16W05	900 H13 2
15W17	901	I29	24	2*31	FAN OUT TO MEMORY	16W05	901 H13 19
15W17	902	I29	7	2*32	FAN OUT TO MEMORY	16W05	902 H13 24
15W17	903	I30	28	2*33	FAN OUT TO MEMORY	16W05	903 H14 2
15W17	904	I31	24	2*34	FAN OUT TO MEMORY	16W05	904 H14 19
15W17	905	I31	7	2*35	FAN OUT TO MEMORY	16W05	905 H14 24
15W17	906	I32	28	2*36	FAN OUT TO MEMORY	16W05	906 H16 2
15W17	907	I33	24	2*37	FAN OUT TO MEMORY	16W05	907 H16 19
15W17	908	I33	7	2*38	FAN OUT TO MEMORY	16W05	908 H16 24

15W18	00	MEMORY ADDRESS FROM CONTROL				5W34	00
15W18	90	G40	18	2*8	ADDRESS TO MEMORY	5W34	90 Q42 22
15W18	91	G41	11	2*9	ADDRESS TO MEMORY	5W34	91 R40 11
15W18	92	G41	14	2*10	ADDRESS TO MEMORY	5W34	92 R40 26
15W18	93	G41	17	2*11	ADDRESS TO MEMORY	5W34	93 R40 22
15W18	94	G41	18	2*12	ADDRESS TO MEMORY	5W34	94 R41 11
15W18	95	G42	11	2*13	ADDRESS TO MEMORY	5W34	95 R41 26
15W18	96	G42	14	2*14	ADDRESS TO MEMORY	5W34	96 R41 22
15W18	97	G42	17	2*15	ADDRESS TO MEMORY	5W34	97 R42 11
15W18	98	G42	18	2*16	ADDRESS TO MEMORY	5W34	98 R42 26
15W18	99					5W34	99
15W18	900	G35	12		CLOCK	5W34	900 M32 25
15W18	901	G39	18	2*0	BANK SELECT	5W34	901 Q40 11
15W18	902	G39	22	2*1	BANK SELECT	5W34	902 Q40 26
15W18	903	G38	9	2*2	CHASSIS 15 SELECT (110)	5W34	903 Q40 22
15W18	904	G38	5	2*3	CHASSIS 15 SELECT (110)	5W34	904 Q41 11
15W18	905	G38	13	2*4	CHASSIS 15 SELECT (110)	5W34	905 Q41 26
15W18	906	G40	11	2*5	ADDRESS TO MEMORY	5W34	906 Q41 22
15W18	907	G40	14	2*6	ADDRESS TO MEMORY	5W34	907 Q42 11
15W18	908	G40	17	2*7	ADDRESS TO MEMORY	5W34	908 Q42 26

15W39	00	MEMORY FROM WRITE DISTRIBUTOR				15W39	00		
15W39	90	H34	24	2*38	FAN OUT TO MEMORY	15W39	90	I33	26
15W39	91	H35	2	2*39	FAN OUT TO MEMORY	15W39	91	I34	22
15W39	92	H35	19	2*40	FAN OUT TO MEMORY	15W39	92	I34	9
15W39	93	H35	24	2*41	FAN OUT TO MEMORY	15W39	93	I35	26
15W39	94	H36	2	2*42	FAN OUT TO MEMORY	15W39	94	I36	22
15W39	95	H36	19	2*43	FAN OUT TO MEMORY	15W39	95	I36	9
15W39	96	H36	24	2*44	FAN OUT TO MEMORY	15W39	96	I37	26
15W39	97					15W39	97		
15W39	98					15W39	98		
15W39	99					15W39	99		
15W39	900	H31	2	2*30	FAN OUT TO MEMORY	15W39	900	I28	22
15W39	901	H31	19	2*31	FAN OUT TO MEMORY	15W39	901	I28	9
15W39	902	H31	24	2*32	FAN OUT TO MEMORY	15W39	902	I29	26
15W39	903	H32	2	2*33	FAN OUT TO MEMORY	15W39	903	I30	22
15W39	904	H32	19	2*34	FAN OUT TO MEMORY	15W39	904	I30	9
15W39	905	H32	24	2*35	FAN OUT TO MEMORY	15W39	905	I31	26
15W39	906	H34	2	2*36	FAN OUT TO MEMORY	15W39	906	I32	22
15W39	907	H34	19	2*37	FAN OUT TO MEMORY	15W39	907	I32	9
15W39	908					15W39	908		

15W39	00	WRITE DISTRIBUTOR TO MEMORY				15W39	00		
15W39	90	I33	26	2*38	FAN OUT TO MEMORY	15W39	90	H34	24
15W39	91	I34	22	2*39	FAN OUT TO MEMORY	15W39	91	H35	2
15W39	92	I34	9	2*40	FAN OUT TO MEMORY	15W39	92	H35	19
15W39	93	I35	26	2*41	FAN OUT TO MEMORY	15W39	93	H35	24
15W39	94	I36	22	2*42	FAN OUT TO MEMORY	15W39	94	H36	2
15W39	95	I36	9	2*43	FAN OUT TO MEMORY	15W39	95	H36	19
15W39	96	I37	26	2*44	FAN OUT TO MEMORY	15W39	96	H36	24
15W39	97					15W39	97		
15W39	98					15W39	98		
15W39	99					15W39	99		
15W39	900	I28	22	2*30	FAN OUT TO MEMORY	15W39	900	H31	2
15W39	901	I28	9	2*31	FAN OUT TO MEMORY	15W39	901	H31	19
15W39	902	I29	26	2*32	FAN OUT TO MEMORY	15W39	902	H31	24
15W39	903	I30	22	2*33	FAN OUT TO MEMORY	15W39	903	H32	2
15W39	904	I30	9	2*34	FAN OUT TO MEMORY	15W39	904	H32	19
15W39	905	I31	26	2*35	FAN OUT TO MEMORY	15W39	905	H32	24
15W39	906	I32	22	2*36	FAN OUT TO MEMORY	15W39	906	H34	2
15W39	907	I32	9	2*37	FAN OUT TO MEMORY	15W39	907	H34	19
15W39	908					15W39	908		



16W03	00	MEMORY FROM WRITE DISTRIBUTOR				13W17	00
16W03	90	H03	24	2*8	FAN OUT TO MEMORY	13W17	90 133 7
16W03	91	H04	2	2*9	FAN OUT TO MEMORY	13W17	91 134 28
16W03	92	H04	19	2*10	FAN OUT TO MEMORY	13W17	92 135 24
16W03	93	H04	24	2*11	FAN OUT TO MEMORY	13W17	93 135 7
16W03	94	H06	2	2*12	FAN OUT TO MEMORY	13W17	94 136 28
16W03	95	H06	19	2*13	FAN OUT TO MEMORY	13W17	95 137 24
16W03	96	H06	24	2*14	FAN OUT TO MEMORY	13W17	96 137 7
16W03	97					13W17	97
16W03	98					13W17	98
16W03	99					13W17	99
16W03	900	H01	2	2*0	FAN OUT TO MEMORY	13W17	900 128 28
16W03	901	H01	19	2*1	FAN OUT TO MEMORY	13W17	901 129 24
16W03	902	H01	24	2*2	FAN OUT TO MEMORY	13W17	902 129 7
16W03	903	H02	2	2*3	FAN OUT TO MEMORY	13W17	903 130 28
16W03	904	H02	19	2*4	FAN OUT TO MEMORY	13W17	904 131 24
16W03	905	H02	24	2*5	FAN OUT TO MEMORY	13W17	905 131 7
16W03	906	H03	2	2*6	FAN OUT TO MEMORY	13W17	906 132 28
16W03	907	H03	19	2*7	FAN OUT TO MEMORY	13W17	907 133 24
16W03	908					13W17	908

16W04	00	MEMORY FROM WRITE DISTRIBUTOR				14W17	00
16W04	90	H11	2	2*24	FAN OUT TO MEMORY	14W17	90 112 28
16W04	91	H11	19	2*25	FAN OUT TO MEMORY	14W17	91 113 24
16W04	92	H11	24	2*26	FAN OUT TO MEMORY	14W17	92 113 7
16W04	93	H12	2	2*27	FAN OUT TO MEMORY	14W17	93 114 28
16W04	94	H12	19	2*28	FAN OUT TO MEMORY	14W17	94 115 24
16W04	95	H12	24	2*29	FAN OUT TO MEMORY	14W17	95 115 7
16W04	96					14W17	96
16W04	97					14W17	97
16W04	98					14W17	98
16W04	99					14W17	99
16W04	900	H07	2	2*15	FAN OUT TO MEMORY	14W17	900 106 28
16W04	901	H07	19	2*16	FAN OUT TO MEMORY	14W17	901 107 24
16W04	902	H07	24	2*17	FAN OUT TO MEMORY	14W17	902 107 7
16W04	903	H08	2	2*18	FAN OUT TO MEMORY	14W17	903 108 28
16W04	904	H08	19	2*19	FAN OUT TO MEMORY	14W17	904 109 24
16W04	905	H08	24	2*20	FAN OUT TO MEMORY	14W17	905 109 7
16W04	906	H09	2	2*21	FAN OUT TO MEMORY	14W17	906 110 28
16W04	907	H09	19	2*22	FAN OUT TO MEMORY	14W17	907 111 24
16W04	908	H09	24	2*23	FAN OUT TO MEMORY	14W17	908 111 7

16W05 00 MEMORY FROM WRITE DISTRIBUTOR				15W17 00		
16W05	90	H17	2	2*39	FAN OUT TO MEMORY	15W17 90 134 28
16W05	91	H17	19	2*40	FAN OUT TO MEMORY	15W17 91 135 24
16W05	92	H17	24	2*41	FAN OUT TO MEMORY	15W17 92 135 7
16W05	93	H18	2	2*42	FAN OUT TO MEMORY	15W17 93 136 28
16W05	94	H18	19	2*43	FAN OUT TO MEMORY	15W17 94 137 24
16W05	95	H18	24	2*44	FAN OUT TO MEMORY	15W17 95 137 7
16W05	96					15W17 96
16W05	97					15W17 97
16W05	98					15W17 98
16W05	99					15W17 99
16W05	900	H13	2	2*30	FAN OUT TO MEMORY	15W17 900 128 28
16W05	901	H13	19	2*31	FAN OUT TO MEMORY	15W17 901 129 24
16W05	902	H13	24	2*32	FAN OUT TO MEMORY	15W17 902 129 7
16W05	903	H14	2	2*33	FAN OUT TO MEMORY	15W17 903 130 28
16W05	904	H14	19	2*34	FAN OUT TO MEMORY	15W17 904 131 24
16W05	905	H14	24	2*35	FAN OUT TO MEMORY	15W17 905 131 7
16W05	906	H16	2	2*36	FAN OUT TO MEMORY	15W17 906 132 28
16W05	907	H16	19	2*37	FAN OUT TO MEMORY	15W17 907 133 24
16W05	908	H16	24	2*38	FAN OUT TO MEMORY	15W17 908 133 7

16W06 00 MEMORY TO READ DISTRIBUTOR				3W13 00		
16W06	90	H03	28	2*8		3W13 90 135 23
16W06	91	H04	1	2*9		3W13 91 136 23
16W06	92	H04	8	2*10		3W13 92 138 23
16W06	93	H04	28	2*11		3W13 93 139 23
16W06	94	H06	1	2*12		3W13 94 140 23
16W06	95	H06	8	2*13		3W13 95 141 23
16W06	96	H06	28	2*14		3W13 96 142 23
16W06	97					3W13 97
16W06	98					3W13 98
16W06	99					3W13 99
16W06	900	H01	1	2*0		3W13 900 125 23
16W06	901	H01	8	2*1		3W13 901 126 23
16W06	902	H01	28	2*2		3W13 902 127 23
16W06	903	H02	1	2*3		3W13 903 128 23
16W06	904	H02	8	2*4		3W13 904 129 23
16W06	905	H02	28	2*5		3W13 905 132 23
16W06	906	H03	1	2*6		3W13 906 133 23
16W06	907	H03	8	2*7		3W13 907 134 23
16W06	908					3W13 908

16W07	00	MEMORY TO READ DISTRIBUTOR				4W21	00
16W07	90	H09	28	2*23	4W21	90	I11 23
16W07	91	H11	1	2*24	4W21	91	I12 23
16W07	92	H11	8	2*25	4W21	92	I14 23
16W07	93	H11	28	2*26	4W21	93	I15 23
16W07	94	H12	1	2*27	4W21	94	I16 23
16W07	95	H12	8	2*28	4W21	95	I17 23
16W07	96	H12	28	2*29	4W21	96	I18 23
16W07	97				4W21	97	
16W07	98				4W21	98	
16W07	99				4W21	99	
16W07	900	H07	1	2*15	4W21	900	I01 23
16W07	901	H07	8	2*16	4W21	901	I02 23
16W07	902	H07	28	2*17	4W21	902	I03 23
16W07	903	H08	1	2*18	4W21	903	I04 23
16W07	904	H08	8	2*19	4W21	904	I05 23
16W07	905	H08	28	2*20	4W21	905	I08 23
16W07	906	H09	1	2*21	4W21	906	I09 23
16W07	907	H09	8	2*22	4W21	907	I10 23
16W07	908				4W21	908	

16W08	00	MEMORY TO READ DISTRIBUTOR				9W19	00
16W08	90	H16	28	2*38	9W19	90	I35 23
16W08	91	H17	1	2*39	9W19	91	I36 23
16W08	92	H17	8	2*40	9W19	92	I38 23
16W08	93	H17	28	2*41	9W19	93	I39 23
16W08	94	H18	1	2*42	9W19	94	I40 23
16W08	95	H18	8	2*43	9W19	95	I41 23
16W08	96	H18	28	2*44	9W19	96	I42 23
16W08	97				9W19	97	
16W08	98				9W19	98	
16W08	99				9W19	99	
16W08	900	H13	1	2*30	9W19	900	I25 23
16W08	901	H13	8	2*31	9W19	901	I26 23
16W08	902	H13	28	2*32	9W19	902	I27 23
16W08	903	H14	1	2*33	9W19	903	I28 23
16W08	904	H14	8	2*34	9W19	904	I29 23
16W08	905	H14	28	2*35	9W19	905	I32 23
16W08	906	H16	1	2*36	9W19	906	I33 23
16W08	907	H16	8	2*37	9W19	907	I34 23
16W08	908				9W19	908	

16W09	00	MEMORY TO READ DISTRIBUTOR				10W19	00
16W09	90	H22	28	2*53	10W19	90	I11 23
16W09	91	H23	1	2*54	10W19	91	I12 23
16W09	92	H23	8	2*55	10W19	92	I14 23
16W09	93	H23	28	2*56	10W19	93	I15 23
16W09	94	H24	1	2*57	10W19	94	I16 23
16W09	95	H24	8	2*58	10W19	95	I17 23
16W09	96	H24	28	2*59	10W19	96	I18 23
16W09	97				10W19	97	
16W09	98				10W19	98	
16W09	99				10W19	99	
16W09	900	H19	1	2*45	10W19	900	I01 23
16W09	901	H19	8	2*46	10W19	901	I02 23
16W09	902	H19	28	2*47	10W19	902	I03 23
16W09	903	H21	1	2*48	10W19	903	I04 23
16W09	904	H21	8	2*49	10W19	904	I05 23
16W09	905	H21	28	2*50	10W19	905	I08 23
16W09	906	H22	1	2*51	10W19	906	I09 23
16W09	907	H22	8	2*52	10W19	907	I10 23
16W09	908				10W19	908	

16W10	00	DISTRIBUTOR FROM WRITE BUFFER				2W08	00
16W10	90	I02	4	2*45 DATA TO MEMORY FAN OUT	2W08	90	B19 27
16W10	91	I02	6	2*46 DATA TO MEMORY FAN OUT	2W08	91	B19 25
16W10	92	I02	7	2*47 DATA TO MEMORY FAN OUT	2W08	92	B19 23
16W10	93	I02	27	2*48 DATA TO MEMORY FAN OUT	2W08	93	B20 8
16W10	94	I02	25	2*49 DATA TO MEMORY FAN OUT	2W08	94	B20 6
16W10	95	I02	22	2*50 DATA TO MEMORY FAN OUT	2W08	95	B20 4
16W10	96	I03	4	2*51 DATA TO MEMORY FAN OUT	2W08	96	B20 27
16W10	97	I03	6	2*52 DATA TO MEMORY FAN OUT	2W08	97	B20 25
16W10	98	I03	7	2*53 DATA TO MEMORY FAN OUT	2W08	98	B20 23
16W10	99	I03	27	2*54 DATA TO MEMORY FAN OUT	2W08	99	B21 8
16W10	900	I03	25	2*55 DATA TO MEMORY FAN OUT	2W08	900	B21 6
16W10	901	I03	22	2*56 DATA TO MEMORY FAN OUT	2W08	901	B21 4
16W10	902	I04	4	2*57 DATA TO MEMORY FAN OUT	2W08	902	B21 27
16W10	903	I04	6	2*58 DATA TO MEMORY FAN OUT	2W08	903	B21 25
16W10	904	I04	7	2*59 DATA TO MEMORY FAN OUT	2W08	904	B21 23
16W10	905	C14	14	MEMORY MARGIN	2W08	905	B22 16
16W10	906				2W08	906	
16W10	907				2W08	907	
16W10	908				2W08	908	

16W11	00	WRITE DISTRIBUTOR TO MEMORY				13W05	00
16W11	90	I11	3	2*53	FAN OUT TO MEMORY	13W05	90 H40 24
16W11	91	I12	18	2*54	FAN OUT TO MEMORY	13W05	91 H41 2
16W11	92	I12	13	2*55	FAN OUT TO MEMORY	13W05	92 H41 19
16W11	93	I13	3	2*56	FAN OUT TO MEMORY	13W05	93 H41 24
16W11	94	I14	18	2*57	FAN OUT TO MEMORY	13W05	94 H42 2
16W11	95	I14	13	2*58	FAN OUT TO MEMORY	13W05	95 H42 19
16W11	96	I15	3	2*59	FAN OUT TO MEMORY	13W05	96 H42 24
16W11	97					13W05	97
16W11	98					13W05	98
16W11	99					13W05	99
16W11	900	I06	18	2*45	FAN OUT TO MEMORY	13W05	900 H37 2
16W11	901	I06	13	2*46	FAN OUT TO MEMORY	13W05	901 H37 19
16W11	902	I07	3	2*47	FAN OUT TO MEMORY	13W05	902 H37 24
16W11	903	I08	18	2*48	FAN OUT TO MEMORY	13W05	903 H39 2
16W11	904	I08	13	2*49	FAN OUT TO MEMORY	13W05	904 H39 19
16W11	905	I09	3	2*50	FAN OUT TO MEMORY	13W05	905 H39 24
16W11	906	I10	18	2*51	FAN OUT TO MEMORY	13W05	906 H40 2
16W11	907	I10	13	2*52	FAN OUT TO MEMORY	13W05	907 H40 19
16W11	908					13W05	908

16W12	00	WRITE DISTRIBUTOR TO MEMORY				4W10	00
16W12	90	I11	5	2*53	FAN OUT TO MEMORY	4W10	90 H22 24
16W12	91	I12	20	2*54	FAN OUT TO MEMORY	4W10	91 H23 2
16W12	92	I12	11	2*55	FAN OUT TO MEMORY	4W10	92 H23 19
16W12	93	I13	5	2*56	FAN OUT TO MEMORY	4W10	93 H23 24
16W12	94	I14	20	2*57	FAN OUT TO MEMORY	4W10	94 H24 2
16W12	95	I14	11	2*58	FAN OUT TO MEMORY	4W10	95 H24 19
16W12	96	I15	5	2*59	FAN OUT TO MEMORY	4W10	96 H24 24
16W12	97					4W10	97
16W12	98	G04	20	WRITE		4W10	98 I24 24
16W12	99	G04	21	GO		4W10	99 I21 3
16W12	900	I06	20	2*45	FAN OUT TO MEMORY	4W10	900 H19 2
16W12	901	I06	11	2*46	FAN OUT TO MEMORY	4W10	901 H19 19
16W12	902	I07	5	2*47	FAN OUT TO MEMORY	4W10	902 H19 24
16W12	903	I08	20	2*48	FAN OUT TO MEMORY	4W10	903 H21 2
16W12	904	I08	11	2*49	FAN OUT TO MEMORY	4W10	904 H21 19
16W12	905	I09	5	2*50	FAN OUT TO MEMORY	4W10	905 H21 24
16W12	906	I10	20	2*51	FAN OUT TO MEMORY	4W10	906 H22 2
16W12	907	I10	11	2*52	FAN OUT TO MEMORY	4W10	907 H22 19
16W12	908					4W10	908

16W13	00	WRITE DISTRIBUTOR TO MEMORY				3W18	00
16W13	90	I11	28	2*53	FAN OUT TO MEMORY	3W18	90 H40 24
16W13	91	I12	24	2*54	FAN OUT TO MEMORY	3W18	91 H41 2
16W13	92	I12	7	2*55	FAN OUT TO MEMORY	3W18	92 H41 19
16W13	93	I13	28	2*56	FAN OUT TO MEMORY	3W18	93 H41 24
16W13	94	I14	24	2*57	FAN OUT TO MEMORY	3W18	94 H42 2
16W13	95	I14	7	2*58	FAN OUT TO MEMORY	3W18	95 H42 19
16W13	96	I15	28	2*59	FAN OUT TO MEMORY	3W18	96 H42 24
16W13	97	G05	28		ACCEPT	3W18	97 I23 25
16W13	98					3W18	98
16W13	99					3W18	99
16W13	900	I06	24	2*45	FAN OUT TO MEMORY	3W18	900 H37 2
16W13	901	I06	7	2*46	FAN OUT TO MEMORY	3W18	901 H37 19
16W13	902	I07	28	2*47	FAN OUT TO MEMORY	3W18	902 H37 24
16W13	903	I08	24	2*48	FAN OUT TO MEMORY	3W18	903 H39 2
16W13	904	I08	7	2*49	FAN OUT TO MEMORY	3W18	904 H39 19
16W13	905	I09	28	2*50	FAN OUT TO MEMORY	3W18	905 H39 24
16W13	906	I10	24	2*51	FAN OUT TO MEMORY	3W18	906 H40 2
16W13	907	I10	7	2*52	FAN OUT TO MEMORY	3W18	907 H40 19
16W13	908					3W18	908

16W14	00	WRITE DISTRIBUTOR TO MEMORY				9W06	00
16W14	90	I11	13	2*53	FAN OUT TO MEMORY	9W06	90 H40 24
16W14	91	I12	3	2*54	FAN OUT TO MEMORY	9W06	91 H41 2
16W14	92	I13	18	2*55	FAN OUT TO MEMORY	9W06	92 H41 19
16W14	93	I13	13	2*56	FAN OUT TO MEMORY	9W06	93 H41 24
16W14	94	I14	3	2*57	FAN OUT TO MEMORY	9W06	94 H42 2
16W14	95	I15	18	2*58	FAN OUT TO MEMORY	9W06	95 H42 19
16W14	96	I15	13	2*59	FAN OUT TO MEMORY	9W06	96 H42 24
16W14	97					9W06	97
16W14	98					9W06	98
16W14	99					9W06	99
16W14	900	I06	3	2*45	FAN OUT TO MEMORY	9W06	900 H37 2
16W14	901	I07	18	2*46	FAN OUT TO MEMORY	9W06	901 H37 19
16W14	902	I07	13	2*47	FAN OUT TO MEMORY	9W06	902 H37 24
16W14	903	I08	3	2*48	FAN OUT TO MEMORY	9W06	903 H39 2
16W14	904	I09	18	2*49	FAN OUT TO MEMORY	9W06	904 H39 19
16W14	905	I09	13	2*50	FAN OUT TO MEMORY	9W06	905 H39 24
16W14	906	I10	3	2*51	FAN OUT TO MEMORY	9W06	906 H40 2
16W14	907	I11	18	2*52	FAN OUT TO MEMORY	9W06	907 H40 19
16W14	908					9W06	908

16W15	00	WRITE DISTRIBUTOR TO MEMORY				10W06	00
16W15	90	I11	11	2*53	FAN OUT TO MEMORY	10W06	90 H22 24
16W15	91	I12	5	2*54	FAN OUT TO MEMORY	10W06	91 H23 2
16W15	92	I13	20	2*55	FAN OUT TO MEMORY	10W06	92 H23 19
16W15	93	I13	11	2*56	FAN OUT TO MEMORY	10W06	93 H23 24
16W15	94	I14	5	2*57	FAN OUT TO MEMORY	10W06	94 H24 2
16W15	95	I15	20	2*58	FAN OUT TO MEMORY	10W06	95 H24 19
16W15	96	I15	11	2*59	FAN OUT TO MEMORY	10W06	96 H24 24
16W15	97					10W06	97
16W15	98					10W06	98
16W15	99					10W06	99
16W15	900	I06	5	2*45	FAN OUT TO MEMORY	10W06	900 H19 2
16W15	901	I07	20	2*46	FAN OUT TO MEMORY	10W06	901 H19 19
16W15	902	I07	11	2*47	FAN OUT TO MEMORY	10W06	902 H19 24
16W15	903	I08	5	2*48	FAN OUT TO MEMORY	10W06	903 H21 2
16W15	904	I09	20	2*49	FAN OUT TO MEMORY	10W06	904 H21 19
16W15	905	I09	11	2*50	FAN OUT TO MEMORY	10W06	905 H21 24
16W15	906	I10	5	2*51	FAN OUT TO MEMORY	10W06	906 H22 2
16W15	907	I11	20	2*52	FAN OUT TO MEMORY	10W06	907 H22 19
16W15	908					10W06	908

16W16	00	WRITE DISTRIBUTOR TO MEMORY				14W05	00
16W16	90	I12	26	2*54	FAN OUT TO MEMORY	14W05	90 H23 2
16W16	91	I13	22	2*55	FAN OUT TO MEMORY	14W05	91 H23 19
16W16	92	I13	9	2*56	FAN OUT TO MEMORY	14W05	92 H23 24
16W16	93	I14	26	2*57	FAN OUT TO MEMORY	14W05	93 H24 2
16W16	94	I15	22	2*58	FAN OUT TO MEMORY	14W05	94 H24 19
16W16	95	I15	9	2*59	FAN OUT TO MEMORY	14W05	95 H24 24
16W16	96					14W05	96
16W16	97					14W05	97
16W16	98					14W05	98
16W16	99					14W05	99
16W16	900	I06	26	2*45	FAN OUT TO MEMORY	14W05	900 H19 2
16W16	901	I07	22	2*46	FAN OUT TO MEMORY	14W05	901 H19 19
16W16	902	I07	9	2*47	FAN OUT TO MEMORY	14W05	902 H19 24
16W16	903	I08	26	2*48	FAN OUT TO MEMORY	14W05	903 H21 2
16W16	904	I09	22	2*49	FAN OUT TO MEMORY	14W05	904 H21 19
16W16	905	I09	9	2*50	FAN OUT TO MEMORY	14W05	905 H21 24
16W16	906	I10	26	2*51	FAN OUT TO MEMORY	14W05	906 H22 2
16W16	907	I11	22	2*52	FAN OUT TO MEMORY	14W05	907 H22 19
16W16	908	I11	9	2*53	FAN OUT TO MEMORY	14W05	908 H22 24

16W17	00	WRITE DISTRIBUTOR TO MEMORY				15W05	00		
16W17	90	I12	28	2*54	FAN OUT TO MEMORY	15W05	90	H41	2
16W17	91	I13	24	2*55	FAN OUT TO MEMORY	15W05	91	H41	19
16W17	92	I13	7	2*56	FAN OUT TO MEMORY	15W05	92	H41	24
16W17	93	I14	28	2*57	FAN OUT TO MEMORY	15W05	93	H42	2
16W17	94	I15	24	2*58	FAN OUT TO MEMORY	15W05	94	H42	19
16W17	95	I15	7	2*59	FAN OUT TO MEMORY	15W05	95	H42	24
16W17	96	I11	7	2*53	FAN OUT TO MEMORY	15W05	96	H40	24
16W17	97					15W05	97		
16W17	98					15W05	98		
16W17	99					15W05	99		
16W17	900	I06	28	2*45	FAN OUT TO MEMORY	15W05	900	H37	2
16W17	901	I07	24	2*46	FAN OUT TO MEMORY	15W05	901	H37	19
16W17	902	I07	7	2*47	FAN OUT TO MEMORY	15W05	902	H37	24
16W17	903	I08	28	2*48	FAN OUT TO MEMORY	15W05	903	H39	2
16W17	904	I09	24	2*49	FAN OUT TO MEMORY	15W05	904	H39	19
16W17	905	I09	7	2*50	FAN OUT TO MEMORY	15W05	905	H39	24
16W17	906	I10	28	2*51	FAN OUT TO MEMORY	15W05	906	H40	2
16W17	907	I11	24	2*52	FAN OUT TO MEMORY	15W05	907	H40	19
16W17	908					15W05	908		

16W18	00	MEMORY ADDRESS FROM CONTROL				5W35	00		
16W18	90	G01	18	2*8	ADDRESS TO MEMORY	5W35	90	Q42	24
16W18	91	G02	11	2*9	ADDRESS TO MEMORY	5W35	91	R40	13
16W18	92	G02	14	2*10	ADDRESS TO MEMORY	5W35	92	R40	28
16W18	93	G02	17	2*11	ADDRESS TO MEMORY	5W35	93	R40	24
16W18	94	G02	18	2*12	ADDRESS TO MEMORY	5W35	94	R41	13
16W18	95	G03	11	2*13	ADDRESS TO MEMORY	5W35	95	R41	28
16W18	96	G03	14	2*14	ADDRESS TO MEMORY	5W35	96	R41	24
16W18	97	G03	17	2*15	ADDRESS TO MEMORY	5W35	97	R42	13
16W18	98	G03	18	2*16	ADDRESS TO MEMORY	5W35	98	R42	28
16W18	99					5W35	99		
16W18	900	G13	12		CLOCK	5W35	900	M32	23
16W18	901	G05	18	2*0	BANK SELECT	5W35	901	Q40	13
16W18	902	G05	22	2*1	BANK SELECT	5W35	902	Q40	28
16W18	903	G04	8	2*2	CHASSIS 16 SELECT (111)	5W35	903	Q40	24
16W18	904	G04	5	2*3	CHASSIS 16 SELECT (111)	5W35	904	Q41	13
16W18	905	G04	13	2*4	CHASSIS 16 SELECT (111)	5W35	905	Q41	28
16W18	906	G01	11	2*5	ADDRESS TO MEMORY	5W35	906	Q41	24
16W18	907	G01	14	2*6	ADDRESS TO MEMORY	5W35	907	Q42	13
16W18	908	G01	17	2*7	ADDRESS TO MEMORY	5W35	908	Q42	28

16W24 00 DISK SYNC PASSON (OUTPUT)

16W24	90	K02	8	2*0	OUTPUT DATA
16W24	91	K02	6	2*1	OUTPUT DATA
16W24	92	K02	4	2*2	OUTPUT DATA
16W24	93	K02	27	2*3	OUTPUT DATA
16W24	94	K02	25	2*4	OUTPUT DATA
16W24	95	K02	23	2*5	OUTPUT DATA
16W24	96	K03	8	2*6	OUTPUT DATA
16W24	97	K03	6	2*7	OUTPUT DATA
16W24	98	K03	4	2*8	OUTPUT DATA
16W24	99	K03	27	2*9	OUTPUT DATA
16W24	900	K03	25	2*10	OUTPUT DATA
16W24	901	K03	23	2*11	OUTPUT DATA
16W24	902	K01	23		ACTIVE
16W24	903	K01	8		INACTIVE
16W24	904	K01	6		FULL
16W24	905	K01	27		EMPTY
16W24	906	K01	4		FUNCTION
16W24	907	K01	25		MASTER CLEAR
16W24	908				

16W25 00 DISK SYNC PASSON (INPUT)

16W25	90	L09	5	2*0	INPUT DATA
16W25	91	L09	7	2*1	INPUT DATA
16W25	92	L09	10	2*2	INPUT DATA
16W25	93	L09	21	2*3	INPUT DATA
16W25	94	L09	24	2*4	INPUT DATA
16W25	95	L09	26	2*5	INPUT DATA
16W25	96	L08	5	2*6	INPUT DATA
16W25	97	L08	7	2*7	INPUT DATA
16W25	98	L08	10	2*8	INPUT DATA
16W25	99	L08	21	2*9	INPUT DATA
16W25	900	L08	24	2*10	INPUT DATA
16W25	901	L08	26	2*11	INPUT DATA
16W25	902				ACTIVE
16W25	903	L07	5		INACTIVE
16W25	904	L07	7		FULL
16W25	905				EMPTY
16W25	906	L26	1		
16W25	907	L26	15		
16W25	908				

16W26 00 DISK SYNC TO CHANNEL (INPUT)

16W26	90	K05	8	BIT 0
16W26	91	K05	6	BIT 1
16W26	92	K05	4	BIT 2
16W26	93	K05	27	BIT 3
16W26	94	K05	25	BIT 4
16W26	95	K05	23	BIT 5
16W26	96	K06	8	BIT 6
16W26	97	K06	6	BIT 7
16W26	98	K06	4	BIT 8
16W26	99	K06	27	BIT 9
16W26	900	K06	25	BIT 10
16W26	901	K06	23	BIT 11
16W26	902	K04	4	ACTIVE
16W26	903	K04	8	INACTIVE
16W26	904	K04	6	FULL
16W26	905	K04	27	EMPTY
16W26	906	L26	3	
16W26	907	L26	13	
16W26	908			

16W27 00 DISK SYNC TO CHANNEL (OUTPUT)

16W27	90	L01	5	2*0
16W27	91	L01	7	2*1
16W27	92	L01	10	2*2
16W27	93	L01	21	2*3
16W27	94	L01	24	2*4
16W27	95	L01	26	2*5
16W27	96	L02	5	2*6
16W27	97	L02	7	2*7
16W27	98	L02	10	2*8
16W27	99	L02	21	2*9
16W27	900	L02	24	2*10
16W27	901	L02	26	2*11
16W27	902	L03	7	ACTIVE
16W27	903	L03	21	INACTIVE
16W27	904	L03	10	FULL
16W27	905	L03	26	EMPTY
16W27	906	L03	5	FUNCTION
16W27	907	L03	24	MASTER CLEAR
16W27	908			

16W28 00 DISK SYNC TO DISK FILE (OUTPUT)

16W28	90	J06	8	2*0	WRITE DATA
16W28	91	J06	6	2*1	
16W28	92	J06	4	2*2	
16W28	93	J06	27	2*3	
16W28	94	J06	25	2*4	
16W28	95	J06	23	2*5	
16W28	96	J05	8	4*6	
16W28	97	J05	6	2*7	
16W28	98	J05	4	2*8	
16W28	99	J05	27	2*9	
16W28	900	J05	25	2*10	WRITE DATA
16W28	901	J05	23	2*11	
16W28	902	J04	8		POSITION CONTROL
16W28	903	J04	6		GROUP CONTROL
16W28	904	J04	4		WRITE CONTROL
16W28	905	J04	27		CLOCK CONTROL
16W28	906				
16W28	907				
16W28	908				

16W29 00 DISK SYNC TO DISK FILE (INPUT)

16W29	90	J01	12	2*0	READ DATA ANALOG
16W29	91	J01	5	2*1	
16W29	92	J01	26	2*2	
16W29	93	J01	19	2*3	
16W29	94	J02	12	2*4	
16W29	95	J02	5	2*5	
16W29	96	J02	26	2*6	
16W29	97	J02	19	2*7	
16W29	98	J03	12	2*8	
16W29	99	J03	5	2*9	
16W29	900	J03	26	2*10	READ DATA ANALOG
16W29	901	J03	19	2*11	
16W29	902	J04	12		REV MARK
16W29	903	J04	5		CLOCK
16W29	904	J04	26		SECTOR MARK
16W29	905				
16W29	906				
16W29	907				
16W29	908				

16W39 00 WRITE DISTRIBUTOR TO MEMORY

16W39 90 I11 26 2*53 FAN OUT TO MEMORY
 16W39 91 I12 22 2*54 FAN OUT TO MEMORY
 16W39 92 I12 9 2*55 FAN OUT TO MEMORY
 16W39 93 I13 26 2*56 FAN OUT TO MEMORY
 16W39 94 I14 22 2*57 FAN OUT TO MEMORY
 16W39 95 I14 9 2*58 FAN OUT TO MEMORY
 16W39 96 I15 26 2*59 FAN OUT TO MEMORY
 16W39 97
 16W39 98
 16W39 99
 16W39 900 I06 22 2*45 FAN OUT TO MEMORY
 16W39 901 I06 9 2*46 FAN OUT TO MEMORY
 16W39 902 I07 26 2*47 FAN OUT TO MEMORY
 16W39 903 I08 22 2*48 FAN OUT TO MEMORY
 16W39 904 I08 9 2*49 FAN OUT TO MEMORY
 16W39 905 I09 26 2*50 FAN OUT TO MEMORY
 16W39 906 I10 22 2*51 FAN OUT TO MEMORY
 16W39 907 I10 9 2*52 FAN OUT TO MEMORY
 16W39 908

16W39 00

16W39 90 H22 24
 16W39 91 H23 2
 16W39 92 H23 19
 16W39 93 H23 24
 16W39 94 H24 2
 16W39 95 H24 19
 16W39 96 H24 24
 16W39 97
 16W39 98
 16W39 99
 16W39 900 H19 2
 16W39 901 H19 19
 16W39 902 H19 24
 16W39 903 H21 2
 16W39 904 H21 19
 16W39 905 H21 24
 16W39 906 H22 2
 16W39 907 H22 19
 16W39 908

16W39 00 MEMORY FROM WRITE DISTRIBUTOR

16W39 90 H22 24 2*53 FAN OUT TO MEMORY
 16W39 91 H23 2 2*54 FAN OUT TO MEMORY
 16W39 92 H23 19 2*55 FAN OUT TO MEMORY
 16W39 93 H23 24 2*56 FAN OUT TO MEMORY
 16W39 94 H24 2 2*57 FAN OUT TO MEMORY
 16W39 95 H24 19 2*58 FAN OUT TO MEMORY
 16W39 96 H24 24 2*59 FAN OUT TO MEMORY
 16W39 97
 16W39 98
 16W39 99
 16W39 900 H19 2 2*45 FAN OUT TO MEMORY
 16W39 901 H19 19 2*46 FAN OUT TO MEMORY
 16W39 902 H19 24 2*47 FAN OUT TO MEMORY
 16W39 903 H21 2 2*48 FAN OUT TO MEMORY
 16W39 904 H21 19 2*49 FAN OUT TO MEMORY
 16W39 905 H21 24 2*50 FAN OUT TO MEMORY
 16W39 906 H22 2 2*51 FAN OUT TO MEMORY
 16W39 907 H22 19 2*52 FAN OUT TO MEMORY
 16W39 908

16W39 00

16W39 90 I11 26
 16W39 91 I12 22
 16W39 92 I12 9
 16W39 93 I13 26
 16W39 94 I14 22
 16W39 95 I14 9
 16W39 96 I15 26
 16W39 97
 16W39 98
 16W39 99
 16W39 900 I06 22
 16W39 901 I06 9
 16W39 902 I07 26
 16W39 903 I08 22
 16W39 904 I08 9
 16W39 905 I09 26
 16W39 906 I10 22
 16W39 907 I10 9
 16W39 908

30W03	00	DISK FILE FROM DISK SYNC				
30W03	90	A18	5	2*0	WRITE DATA	
30W03	91	A18	7	2*1	WRITE DATA	TO DISK
30W03	92	A18	10	2*2	WRITE DATA	SYNCHRONIZER
30W03	93	A18	21	2*3	WRITE DATA	
30W03	94	A18	24	2*4	WRITE DATA	
30W03	95	A18	26	2*5	WRITE DATA	CHASSIS 4 AND/OR 10
30W03	96	A19	5	2*6	WRITE DATA	AND/OR 16
30W03	97	A19	7	2*7	WRITE DATA	
30W03	98	A19	10	2*8	WRITE DATA	
30W03	99	A19	21	2*9	WRITE DATA	
30W03	900	A19	24	2*10	WRITE DATA	
30W03	901	A19	26	2*11	WRITE DATA	
30W03	902	A20	5		POSITION CONTROL	
30W03	903	A20	7		GROUP CONTROL	
30W03	904	A20	10		WRITE CONTROL	
30W03	905	A17	12		CLOCK	
30W03	906					
30W03	907					
30W03	908					

30W04	00	DISK FILE TO DISK SYNC				
30W04	90	A01	9	2*0	READ DATA ANALOG	
30W04	91	A01	15	2*1	READ DATA ANALOG	TO DISK
30W04	92	A01	25	2*2	READ DATA ANALOG	SYNCHRONIZER
30W04	93	A02	9	2*3	READ DATA ANALOG	
30W04	94	A02	15	2*4	READ DATA ANALOG	
30W04	95	A02	25	2*5	READ DATA ANALOG	
30W04	96	A03	9	2*6	READ DATA ANALOG	CHASSIS 4 AND/OR 10
30W04	97	A03	15	2*7	READ DATA ANALOG	AND/OR 16
30W04	98	A03	25	2*8	READ DATA ANALOG	
30W04	99	A04	9	2*9	READ DATA ANALOG	
30W04	900	A04	15	2*10	READ DATA ANALOG	
30W04	901	A04	25	2*11	READ DATA ANALOG	
30W04	902	B12	9		REV, MARK	
30W04	903	B12	25		CLOCK	
30W04	905					
30W04	906					
30W04	907					
30W04	908					

50W01 00 CONSOLE CABLE 1 FROM DISPLAY SYNC

50W01	90	A16	9	X*0	
50W01	91	A16	6	X*1	
50W01	92	A16	1	X*2	TO DISPLAY CONSOLE SYNCHRONIZER
50W01	93	A20	9	X*3	
50W01	94	A20	6	X*4	
50W01	95	A20	1	X*5	CHASSIS 12
50W01	96	A24	9	X*6	
50W01	97	A24	6	X*7	
50W01	98	A24	1	X*8	
50W01	99	A04	9	Y*0	
50W01	900	A04	6	Y*1	
50W01	901	A04	1	Y*2	
50W01	902	A08	9	Y*3	
50W01	903	A08	6	Y*4	
50W01	904	A08	1	Y*5	
50W01	905	A12	9	Y*6	
50W01	906	A12	6	Y*7	
50W01	907	A12	1	Y*8	
50W01	908	B22	9	FOCUS AND ASTIGMATISM	

50W02 00 CONSOLE CABLE 2 TO DISPLAY SYNC

50W02	90	A26	1	KEYBOARD BIT 0 (TB2 1)	
50W02	91	A26	2	KEYBOARD BIT 1 (TB2 2)	
50W02	92	A26	3	KEYBOARD BIT 2 (TB2 3)	TO DISPLAY CONSOLE SYNCHRONIZER
50W02	93	A26	4	KEYBOARD BIT 3 (TB2 4)	
50W02	94	A26	5	KEYBOARD BIT 4 (TB2 5)	
50W02	95	A26	6	KEYBOARD BIT 5 (TB2 6)	CHASSIS 12
50W02	96	A26	7	KEY DOWN (TB2 8)	
50W02	97	A26	8	KEY UP (TB2 7)	
50W02	98	B21	1	UNBLANK LEFT CONSOLE 0	
50W02	99	B21	6	UNBLANK RIGHT CONSOLE 0	
50W02	900	B22	6	SMALL SIZE	
50W02	901	B22	1	MED. SIZE	
50W02	902				
50W02	903	B24	1	VERT. PUSH ANALOG	
50W02	904	B24	7	VERT. PULL ANALOG	
50W02	905	B23	1	HORIZ. PUSH ANALOG	
50W02	906	B23	9	HORIZ. PULL ANALOG	
50W02	907				
50W02	908				



81W00	00	6681 SYNC TO CHANNEL (OUTPUT)			
81W00	90	D12	26	DATA BIT	2*0
81W00	91	D12	16	DATA BIT	2*1
81W00	92	D12	13	DATA BIT	2*2
81W00	93	D12	3	DATA BIT	2*3
81W00	94	C12	26	DATA BIT	2*4
81W00	95	C12	16	DATA BIT	2*5
81W00	96	C12	13	DATA BIT	2*6
81W00	97	C12	3	DATA BIT	2*7
81W00	98	C11	26	DATA BIT	2*8
81W00	99	C11	16	DATA BIT	2*9
81W00	900	C11	13	DATA BIT	2*10
81W00	901	C11	3	DATA BIT	2*11
81W00	902	C10	16	ACTIVE	
81W00	903	C10	26	INACTIVE	
81W00	904	C10	3	FULL	
81W00	905	C10	13	EMPTY	
81W00	906	C09	3	FUNCTION	
81W00	907	C09	13	MASTER CLEAR	
81W00	908				

81W01	00	6681 SYNC PASSON (OUTPUT)			
81W01	90	D12	28	DATA BIT	2*0
81W01	91	D12	14	DATA BIT	2*1
81W01	92	D12	15	DATA BIT	2*2
81W01	93	D12	1	DATA BIT	2*3
81W01	94	C12	28	DATA BIT	2*4
81W01	95	C12	14	DATA BIT	2*5
81W01	96	C12	15	DATA BIT	2*6
81W01	97	C12	1	DATA BIT	2*7
81W01	98	C11	28	DATA BIT	2*8
81W01	99	C11	14	DATA BIT	2*9
81W01	900	C11	15	DATA BIT	2*10
81W01	901	C11	1	DATA BIT	2*11
81W01	902	C10	14	ACTIVE	
81W01	903	C10	28	INACTIVE	
81W01	904	C10	1	FULL	
81W01	905	C10	15	EMPTY	
81W01	906	E09	1	FUNCTION	
81W01	907	C09	15	MASTER CLEAR	
81W01	908				

81W02 00 6681 SYNC TO CHANNEL (INPUT)

81W02 90 D10 24 DATA BIT 2*0
81W02 91 D10 26 DATA BIT 2*1
81W02 92 D10 28 DATA BIT 2*2
81W02 93 D10 3 DATA BIT 2*3
81W02 94 D10 5 DATA BIT 2*4
81W02 95 D10 7 DATA BIT 2*5
81W02 96 D09 24 DATA BIT 2*6
81W02 97 D09 26 DATA BIT 2*7
81W02 98 D09 28 DATA BIT 2*8
81W02 99 D09 3 DATA BIT 2*9
81W02 900 D09 5 DATA BIT 2*10
81W02 901 D09 7 DATA BIT 2*11
81W02 902 ACTIVE
81W02 903 D11 3 INACTIVE
81W02 904 D11 5 FULL
81W02 905 D11 7 EMPTY
81W02 906 E12 12 CLOCK (10 MC)
81W02 907 C09 16 CLOCK (1 MC)
81W02 908

81W03 00 6681 SYNC PASS ON (INPUT)

81W03 90 D10 8 DATA BIT 2*0
81W03 91 D10 9 DATA BIT 2*1
81W03 92 D10 10 DATA BIT 2*2
81W03 93 D10 21 DATA BIT 2*3
81W03 94 D10 22 DATA BIT 2*4
81W03 95 D10 23 DATA BIT 2*5
81W03 96 D09 8 DATA BIT 2*6
81W03 97 D09 9 DATA BIT 2*7
81W03 98 D09 10 DATA BIT 2*8
81W03 99 D09 21 DATA BIT 2*9
81W03 900 D09 22 DATA BIT 2*10
81W03 901 D09 23 DATA BIT 2*11
81W03 902 ACTIVE
81W03 903 D11 21 INACTIVE
81W03 904 D11 22 FULL
81W03 905 D11 23 EMPTY
81W03 906 D11 28 CLOCK (10 MC)
81W03 907 D11 26 CLOCK (1 MC)
81W03 908

82W00 00 6682 SYNC TO CHANNEL (OUTPUT)

82W00	90	D12	26	DATA BIT 2*0
82W00	91	D12	16	DATA BIT 2*1
82W00	92	D12	13	DATA BIT 2*2
82W00	93	D12	3	DATA BIT 2*3
82W00	94	C12	26	DATA BIT 2*4
82W00	95	C12	16	DATA BIT 2*5
82W00	96	C12	13	DATA BIT 2*6
82W00	97	C12	3	DATA BIT 2*7
82W00	98	C11	26	DATA BIT 2*8
82W00	99	C11	16	DATA BIT 2*9
82W00	900	C11	13	DATA BIT 2*10
82W00	901	C11	3	DATA BIT 2*11
82W00	902	C10	16	ACTIVE
82W00	903	C10	26	INACTIVE
82W00	904	C10	3	FULL
82W00	905	C10	13	EMPTY
82W00	906	C09	3	FUNCTION
82W00	907	C09	13	MASTER CLEAR
82W00	908			

82W01 00 6682 SYNC PASSON (OUTPUT)

82W01	90	D12	28	DATA BIT 2*0
82W01	91	D12	14	DATA BIT 2*1
82W01	92	D12	15	DATA BIT 2*2
82W01	93	D12	1	DATA BIT 2*3
82W01	94	C12	28	DATA BIT 2*4
82W01	95	C12	14	DATA BIT 2*5
82W01	96	C12	15	DATA BIT 2*6
82W01	97	C12	1	DATA BIT 2*7
82W01	98	C11	28	DATA BIT 2*8
82W01	99	C11	14	DATA BIT 2*9
82W01	900	C11	15	DATA BIT 2*10
82W01	901	C11	1	DATA BIT 2*11
82W01	902	C10	14	ACTIVE
82W01	903	C10	28	INACTIVE
82W01	904	C10	1	FULL
82W01	905	C10	15	EMPTY
82W01	906	C09	1	FUNCTION
82W01	907	C09	15	MASTER CLEAR
82W01	908			

82W02 00 6682 SYNC TO CHANNEL (INPUT)

82W02 90 D10 24 DATA BIT 2*0
82W02 91 D10 26 DATA BIT 2*1
82W02 92 D10 28 DATA BIT 2*2
82W02 93 D10 3 DATA BIT 2*3
82W02 94 D10 5 DATA BIT 2*4
82W02 95 D10 7 DATA BIT 2*5
82W02 96 D09 24 DATA BIT 2*6
82W02 97 D09 26 DATA BIT 2*7
82W02 98 D09 28 DATA BIT 2*8
82W02 99 D09 3 DATA BIT 2*9
82W02 900 D09 5 DATA BIT 2*10
82W02 901 D09 7 DATA BIT 2*11
82W02 902 ACTIVE
82W02 903 D11 3 INACTIVE
82W02 904 D11 5 FULL
82W02 905 D11 7 EMPTY
82W02 906 E12 12 CLOCK (10 MC)
82W02 907 C09 16 CLOCK (1 MC)
82W02 908

82W03 00 6682 SYNC PASS ON (INPUT)

82W03 90 D10 8 DATA BIT 2*0
82W03 91 D10 9 DATA BIT 2*1
82W03 92 D10 10 DATA BIT 2*2
82W03 93 D10 21 DATA BIT 2*3
82W03 94 D10 22 DATA BIT 2*4
82W03 95 D10 23 DATA BIT 2*5
82W03 96 D09 8 DATA BIT 2*6
82W03 97 D09 9 DATA BIT 2*7
82W03 98 D09 10 DATA BIT 2*8
82W03 99 D09 21 DATA BIT 2*9
82W03 900 D09 22 DATA BIT 2*10
82W03 901 D09 23 DATA BIT 2*11
82W03 902 ACTIVE
82W03 903 D11 21 INACTIVE
82W03 904 D11 22 FULL
82W03 905 D11 23 EMPTY
82W03 906 D11 28 CLOCK (10 MC)
82W03 907 D11 26 CLOCK (1 MC)
82W03 908

END FILE

CHASSIS 1

1W02	90	F37	5	2*0	CENTRAL PROGRAM ADDRESS
1W02	91	F37	7	2*1	CENTRAL PROGRAM ADDRESS
1W02	92	F37	10	2*2	CENTRAL PROGRAM ADDRESS
1W02	93	F37	21	2*3	CENTRAL PROGRAM ADDRESS
1W02	94	F37	24	2*4	CENTRAL PROGRAM ADDRESS
1W02	95	F37	26	2*5	CENTRAL PROGRAM ADDRESS
1W02	96	F38	5	2*6	CENTRAL PROGRAM ADDRESS
1W02	97	F38	7	2*7	CENTRAL PROGRAM ADDRESS
1W02	98	F38	10	2*8	CENTRAL PROGRAM ADDRESS
1W02	99	F38	21	2*9	CENTRAL PROGRAM ADDRESS
1W02	900	F38	24	2*10	CENTRAL PROGRAM ADDRESS
1W02	901	F38	26	2*11	CENTRAL PROGRAM ADDRESS
1W02	902	F39	5	2*12	CENTRAL PROGRAM ADDRESS
1W02	903	F39	7	2*13	CENTRAL PROGRAM ADDRESS
1W02	904	F39	10	2*14	CENTRAL PROGRAM ADDRESS
1W02	905	F39	21	2*15	CENTRAL PROGRAM ADDRESS
1W02	906	F39	24	2*16	CENTRAL PROGRAM ADDRESS
1W02	907	F39	26	2*17	CENTRAL PROGRAM ADDRESS
1W03	903	F40	4	BIT 2	
1W03	902	F40	6	BIT 1	
1W03	901	F40	8	BIT 0	
1W03	906	F40	23	BIT 5	
1W03	905	F40	25	BIT 4	
1W03	904	F40	27	BIT 3	
1W03	900	F41	4	BIT 8	
1W03	908	F41	6	BIT 7	
1W03	907	F41	8	BIT 6	
1W03	93	F41	23	BIT 11	
1W03	92	F41	25	BIT 10	
1W03	91	F41	27	BIT 9	
1W03	95	F42	6	BIT 13	
1W03	94	F42	8	BIT 12	
1W03	99	F42	23	BIT 17	
1W03	98	F42	25	BIT 16	
1W03	97	F42	27	BIT 15	
1W04	90	G33	5	2*0	CENTRAL TO PERIPHERAL DATA
1W04	91	G33	7	2*1	CENTRAL TO PERIPHERAL DATA
1W04	92	G33	10	2*2	CENTRAL TO PERIPHERAL DATA
1W04	93	G33	21	2*3	CENTRAL TO PERIPHERAL DATA
1W04	94	G33	24	2*4	CENTRAL TO PERIPHERAL DATA
1W04	95	G33	26	2*5	CENTRAL TO PERIPHERAL DATA
1W04	96	G34	5	2*6	CENTRAL TO PERIPHERAL DATA
1W04	97	G34	7	2*7	CENTRAL TO PERIPHERAL DATA
1W04	98	G34	10	2*8	CENTRAL TO PERIPHERAL DATA
1W04	99	G34	21	2*9	CENTRAL TO PERIPHERAL DATA
1W04	900	G34	24	2*10	CENTRAL TO PERIPHERAL DATA
1W04	901	G34	26	2*11	CENTRAL TO PERIPHERAL DATA
1W04	902	G35	5	2*12	CENTRAL TO PERIPHERAL DATA
1W04	903	G35	7	2*13	CENTRAL TO PERIPHERAL DATA
1W04	904	G35	10	2*14	CENTRAL TO PERIPHERAL DATA
1W05	90	G35	21	2*15	CENTRAL TO PERIPHERAL DATA
1W05	91	G35	24	2*16	CENTRAL TO PERIPHERAL DATA
1W05	92	G35	26	2*17	CENTRAL TO PERIPHERAL DATA
1W05	93	G36	5	2*18	CENTRAL TO PERIPHERAL DATA
1W05	94	G36	7	2*19	CENTRAL TO PERIPHERAL DATA
1W05	95	G36	10	2*20	CENTRAL TO PERIPHERAL DATA
1W05	96	G36	21	2*21	CENTRAL TO PERIPHERAL DATA
1W05	97	G36	24	2*22	CENTRAL TO PERIPHERAL DATA
1W05	98	G36	26	2*23	CENTRAL TO PERIPHERAL DATA
1W05	99	G37	5	2*24	CENTRAL TO PERIPHERAL DATA

1W05	900	G37	7	2+25	CENTRAL TO PERIPHERAL DATA
1W05	901	G37	10	2+26	CENTRAL TO PERIPHERAL DATA
1W05	902	G37	21	2+27	CENTRAL TO PERIPHERAL DATA
1W05	903	G37	24	2+28	CENTRAL TO PERIPHERAL DATA
1W05	904	G37	26	2+29	CENTRAL TO PERIPHERAL DATA
1W06	900	G38	5	2+30	CENTRAL TO PERIPHERAL DATA
1W06	901	G38	7	2+31	CENTRAL TO PERIPHERAL DATA
1W06	902	G38	10	2+32	CENTRAL TO PERIPHERAL DATA
1W06	903	G38	21	2+33	CENTRAL TO PERIPHERAL DATA
1W06	904	G38	24	2+34	CENTRAL TO PERIPHERAL DATA
1W06	905	G38	26	2+35	CENTRAL TO PERIPHERAL DATA
1W06	906	G39	5	2+36	CENTRAL TO PERIPHERAL DATA
1W06	907	G39	7	2+37	CENTRAL TO PERIPHERAL DATA
1W06	90	G39	10	2+38	CENTRAL TO PERIPHERAL DATA
1W06	91	G39	21	2+39	CENTRAL TO PERIPHERAL DATA
1W06	92	G39	24	2+40	CENTRAL TO PERIPHERAL DATA
1W06	93	G39	26	2+41	CENTRAL TO PERIPHERAL DATA
1W06	94	G40	5	2+42	CENTRAL TO PERIPHERAL DATA
1W06	95	G40	7	2+43	CENTRAL TO PERIPHERAL DATA
1W06	96	G40	10	2+44	CENTRAL TO PERIPHERAL DATA
1W07	900	G40	21	2+45	CENTRAL TO PERIPHERAL DATA
1W07	901	G40	24	2+46	CENTRAL TO PERIPHERAL DATA
1W07	902	G40	26	2+47	CENTRAL TO PERIPHERAL DATA
1W07	903	G41	5	2+48	CENTRAL TO PERIPHERAL DATA
1W07	904	G41	7	2+49	CENTRAL TO PERIPHERAL DATA
1W07	905	G41	10	2+50	CENTRAL TO PERIPHERAL DATA
1W07	906	G41	21	2+51	CENTRAL TO PERIPHERAL DATA
1W07	907	G41	24	2+52	CENTRAL TO PERIPHERAL DATA
1W07	90	G41	26	2+53	CENTRAL TO PERIPHERAL DATA
1W03	96	G42	4	BIT 14	
1W07	91	G42	5	2+54	CENTRAL TO PERIPHERAL DATA
1W07	92	G42	7	2+55	CENTRAL TO PERIPHERAL DATA
1W07	93	G42	10	2+56	CENTRAL TO PERIPHERAL DATA
1W07	94	G42	21	2+57	CENTRAL TO PERIPHERAL DATA
1W07	95	G42	24	2+58	CENTRAL TO PERIPHERAL DATA
1W07	96	G42	26	2+59	CENTRAL TO PERIPHERAL DATA
1W01	900	H02	12	CLOCK	
1W07	97	H29	16	MEMORY MARGIN	
1W06	97	H29	18	MEMORY MARGIN	
1W04	905	H29	20	MEMORY MARGIN	
1W05	907	H29	22	STORAGE MARGIN	
1W05	905	I25	11	RESUME CENTRAL READ	
1W05	906	I29	1	RESUME CENTRAL READ	
1W12	900	I29	5	EXCHANGE RESUME OUT	
1W08	905	I29	21	RESUME CENTRAL WRITE	
1W12	902	I30	4	CLOCK	
1W01	903	I30	8	EXCH GO	
1W12	901	I30	23	MC	
1W01	902	I30	25	WR GO	
1W01	901	I30	27	RD GO	
1W08	92	J33	4	2+50	PERIPHERAL TO CENTRAL DATA
1W08	91	J33	6	2+49	PERIPHERAL TO CENTRAL DATA
1W08	90	J33	8	2+48	PERIPHERAL TO CENTRAL DATA
1W08	95	J33	23	2+53	PERIPHERAL TO CENTRAL DATA
1W08	94	J33	25	2+52	PERIPHERAL TO CENTRAL DATA
1W08	93	J33	27	2+51	PERIPHERAL TO CENTRAL DATA
1W08	98	J34	4	2+56	PERIPHERAL TO CENTRAL DATA
1W08	97	J34	6	2+55	PERIPHERAL TO CENTRAL DATA
1W08	96	J34	8	2+54	PERIPHERAL TO CENTRAL DATA
1W08	901	J34	23	2+59	PERIPHERAL TO CENTRAL DATA
1W08	900	J34	25	2+58	PERIPHERAL TO CENTRAL DATA
1W08	99	J34	27	2+57	PERIPHERAL TO CENTRAL DATA

1W08	904	J35	4	2+38	PERIPHERAL TO CENTRAL DATA
1W08	903	J35	6	2+37	PERIPHERAL TO CENTRAL DATA
1W08	902	J35	8	2+36	PERIPHERAL TO CENTRAL DATA
1W09	92	J35	23	2+41	PERIPHERAL TO CENTRAL DATA
1W09	91	J35	25	2+40	PERIPHERAL TO CENTRAL DATA
1W09	90	J35	27	2+39	PERIPHERAL TO CENTRAL DATA
1W09	95	J36	4	2+44	PERIPHERAL TO CENTRAL DATA
1W09	94	J36	6	2+43	PERIPHERAL TO CENTRAL DATA
1W09	93	J36	8	2+42	PERIPHERAL TO CENTRAL DATA
1W09	98	J36	23	2+47	PERIPHERAL TO CENTRAL DATA
1W09	97	J36	25	2+46	PERIPHERAL TO CENTRAL DATA
1W09	96	J36	27	2+45	PERIPHERAL TO CENTRAL DATA
1W09	901	J37	4	2+26	PERIPHERAL TO CENTRAL DATA
1W09	900	J37	6	2+25	PERIPHERAL TO CENTRAL DATA
1W09	99	J37	8	2+24	PERIPHERAL TO CENTRAL DATA
1W09	904	J37	23	2+29	PERIPHERAL TO CENTRAL DATA
1W09	903	J37	25	2+28	PERIPHERAL TO CENTRAL DATA
1W09	902	J37	27	2+27	PERIPHERAL TO CENTRAL DATA
1W10	92	J38	4	2+32	PERIPHERAL TO CENTRAL DATA
1W10	91	J38	6	2+31	PERIPHERAL TO CENTRAL DATA
1W10	90	J38	8	2+30	PERIPHERAL TO CENTRAL DATA
1W10	95	J38	23	2+35	PERIPHERAL TO CENTRAL DATA
1W10	94	J38	25	2+34	PERIPHERAL TO CENTRAL DATA
1W10	93	J38	27	2+33	PERIPHERAL TO CENTRAL DATA
1W10	98	J39	4	2+14	PERIPHERAL TO CENTRAL DATA
1W10	97	J39	6	2+13	PERIPHERAL TO CENTRAL DATA
1W10	96	J39	8	2+12	PERIPHERAL TO CENTRAL DATA
1W10	901	J39	23	2+17	PERIPHERAL TO CENTRAL DATA
1W10	900	J39	25	2+16	PERIPHERAL TO CENTRAL DATA
1W10	99	J39	27	2+15	PERIPHERAL TO CENTRAL DATA
1W10	904	J40	4	2+20	PERIPHERAL TO CENTRAL DATA
1W10	903	J40	6	2+19	PERIPHERAL TO CENTRAL DATA
1W10	902	J40	8	2+18	PERIPHERAL TO CENTRAL DATA
1W11	92	J40	23	2+23	PERIPHERAL TO CENTRAL DATA
1W11	91	J40	25	2+22	PERIPHERAL TO CENTRAL DATA
1W11	90	J40	27	2+21	PERIPHERAL TO CENTRAL DATA
1W11	95	J41	4	2+2	PERIPHERAL TO CENTRAL DATA
1W11	94	J41	6	2+1	PERIPHERAL TO CENTRAL DATA
1W11	93	J41	8	2+0	PERIPHERAL TO CENTRAL DATA
1W11	98	J41	23	2+5	PERIPHERAL TO CENTRAL DATA
1W11	97	J41	25	2+4	PERIPHERAL TO CENTRAL DATA
1W11	96	J41	27	2+3	PERIPHERAL TO CENTRAL DATA
1W11	901	J42	4	2+8	PERIPHERAL TO CENTRAL DATA
1W11	900	J42	6	2+7	PERIPHERAL TO CENTRAL DATA
1W11	99	J42	8	2+6	PERIPHERAL TO CENTRAL DATA
1W11	904	J42	23	2+11	PERIPHERAL TO CENTRAL DATA
1W11	903	J42	25	2+10	PERIPHERAL TO CENTRAL DATA
1W11	902	J42	27	2+9	PERIPHERAL TO CENTRAL DATA
1W14	906	M35	3		FUNCTION
1W13	905	M35	8		EMPTY
1W14	905	M35	9		EMPTY
1W14	904	M35	13		FULL
1W13	903	M35	14		INACTIVE
1W13	904	M35	15		FULL
1W14	903	M35	18		INACTIVE
1W14	902	M35	22		ACTIVE
1W13	902	M35	25		ACTIVE
1W13	906	M36	1		CLOCK (100N SEC)
1W13	91	M36	4	2+1	INPUT DATA
1W14	90	M36	5	2+0	OUTPUT DATA
1W13	90	M36	6	2+0	INPUT DATA
1W14	91	M36	11	2+1	OUTPUT DATA

1W13	92	M36	23	2*2 INPUT DATA
1W14	93	M36	24	2*3 OUTPUT DATA
1W13	93	M36	27	2*3 INPUT DATA
1W14	92	M36	28	2*2 OUTPUT DATA
1W13	907	M37	1	CLOCK (1 US)
1W13	95	M37	4	2*5 INPUT DATA
1W14	94	M37	5	2*4 OUTPUT DATA
1W13	94	M37	6	2*4 INPUT DATA
1W14	95	M37	11	2*5 OUTPUT DATA
1W13	96	M37	23	2*6 INPUT DATA
1W14	97	M37	24	2*7 OUTPUT DATA
1W13	97	M37	27	2*7 INPUT DATA
1W14	96	M37	28	2*6 OUTPUT DATA
1W14	907	M38	1	MASTER CLEAR
1W13	99	M38	4	2*9 INPUT DATA
1W14	98	M38	5	2*8 OUTPUT DATA
1W13	98	M38	6	2*8 INPUT DATA
1W14	99	M38	11	2*9 OUTPUT DATA
1W13	900	M38	23	2*10 INPUT DATA
1W14	901	M38	24	2*11 OUTPUT DATA
1W13	901	M38	27	2*11 INPUT DATA
1W14	900	M38	28	2*10 OUTPUT DATA
1W16	906	M39	3	FUNCTION
1W15	905	M39	8	EMPTY
1W16	905	M39	9	EMPTY
1W16	904	M39	13	FULL
1W15	903	M39	14	INACTIVE
1W15	904	M39	15	FULL
1W16	903	M39	18	INACTIVE
1W16	902	M39	22	ACTIVE
1W15	902	M39	25	ACTIVE
1W15	906	M40	1	CLOCK (100NSEC)
1W15	91	M40	4	2*1 INPUT DATA
1W16	90	M40	5	2*0 OUTPUT DATA
1W15	90	M40	6	2*0 INPUT DATA
1W16	91	M40	11	2*1 OUTPUT DATA
1W15	92	M40	23	2*2 INPUT DATA
1W16	93	M40	24	2*3 OUTPUT DATA
1W15	93	M40	27	2*3 INPUT DATA
1W16	92	M40	28	2*2 OUTPUT DATA
1W15	907	M41	1	CLOCK (1US)
1W15	95	M41	4	2*5 INPUT DATA
1W16	94	M41	5	2*4 OUTPUT DATA
1W15	94	M41	6	2*4 INPUT DATA
1W16	95	M41	11	2*5 OUTPUT DATA
1W15	96	M41	23	2*6 INPUT DATA
1W16	97	M41	24	2*7 OUTPUT DATA
1W15	97	M41	27	2*7 INPUT DATA
1W16	96	M41	28	2*6 OUTPUT DATA
1W16	907	M42	1	MASTER CLEAR
1W15	99	M42	4	2*9 INPUT DATA
1W16	98	M42	5	2*8 OUTPUT DATA
1W15	98	M42	6	2*8 INPUT DATA
1W16	99	M42	11	2*9 OUTPUT DATA
1W15	900	M42	23	2*10 INPUT DATA
1W16	901	M42	24	2*11 OUTPUT DATA
1W15	901	M42	27	2*11 INPUT DATA
1W16	900	M42	28	2*10 OUTPUT DATA
1W18	906	N35	3	FUNCTION
1W17	905	N35	8	EMPTY
1W18	905	N35	9	EMPTY
1W18	904	N35	13	FULL

1W17	903	N35	14	INACTIVE
1W17	904	N35	15	FULL
1W18	903	N35	18	INACTIVE
1W18	902	N35	22	ACTIVE
1W17	902	N35	25	ACTIVE
1W17	906	N36	1	CLOCK (100NSEC)
1W17	91	N36	4	2+1 INPUT DATA
1W18	90	N36	5	2+0 OUTPUT DATA
1W17	90	N36	6	2+0 INPUT DATA
1W18	91	N36	11	2+1 OUTPUT DATA
1W17	92	N36	23	2+2 INPUT DATA
1W18	93	N36	24	2+3 OUTPUT DATA
1W17	93	N36	27	2+3 INPUT DATA
1W18	92	N36	28	2+2 OUTPUT DATA
1W17	907	N37	1	CLOCK (1US)
1W17	95	N37	4	2+5 INPUT DATA
1W18	94	N37	5	2+4 OUTPUT DATA
1W17	94	N37	6	2+4 INPUT DATA
1W18	95	N37	11	2+5 OUTPUT DATA
1W17	96	N37	23	2+6 INPUT DATA
1W18	97	N37	24	2+7 OUTPUT DATA
1W17	97	N37	27	2+7 INPUT DATA
1W18	96	N37	28	2+6 OUTPUT DATA
1W18	907	N38	1	MASTER CLEAR
1W17	99	N38	4	2+9 INPUT DATA
1W18	98	N38	5	2+8 OUTPUT DATA
1W17	98	N38	6	2+8 INPUT DATA
1W18	99	N38	11	2+9 OUTPUT DATA
1W17	900	N38	23	2+10 INPUT DATA
1W18	901	N38	24	2+11 OUTPUT DATA
1W17	901	N38	27	2+11 INPUT DATA
1W18	900	N38	28	2+10 OUTPUT DATA
1W20	906	N39	3	FUNCTION
1W19	905	N39	8	EMPTY
1W20	905	N39	9	EMPTY
1W20	904	N39	13	FULL
1W19	903	N39	14	INACTIVE
1W19	904	N39	15	FULL
1W20	903	N39	18	INACTIVE
1W20	902	N39	22	ACTIVE
1W19	902	N39	25	ACTIVE
1W19	906	N40	1	CLOCK (100NSEC)
1W19	91	N40	4	2+1 INPUT DATA
1W20	90	N40	5	2+0 OUTPUT DATA
1W19	90	N40	6	2+0 INPUT DATA
1W20	91	N40	11	2+1 OUTPUT DATA
1W19	92	N40	23	2+2 INPUT DATA
1W20	93	N40	24	2+3 OUTPUT DATA
1W19	93	N40	27	2+3 INPUT DATA
1W20	92	N40	28	2+2 OUTPUT DATA
1W19	907	N41	1	CLOCK (1USEC)
1W19	95	N41	4	2+5 INPUT DATA
1W20	94	N41	5	2+4 OUTPUT DATA
1W19	94	N41	6	2+4 INPUT DATA
1W20	95	N41	11	2+5 OUTPUT DATA
1W19	96	N41	23	2+6 INPUT DATA
1W20	97	N41	24	2+7 OUTPUT DATA
1W19	97	N41	27	2+7 INPUT DATA
1W20	96	N41	28	2+6 OUTPUT DATA
1W20	907	N42	1	MASTER CLEAR
1W19	99	N42	4	2+9 INPUT DATA
1W20	98	N42	5	2+8 OUTPUT DATA

1W19	98	N42	6	2*8 INPUT DATA
1W20	99	N42	11	2*9 OUTPUT DATA
1W19	900	N42	23	2*10 INPUT DATA
1W20	901	N42	24	2*11 OUTPUT DATA
1W19	901	N42	27	2*11 INPUT DATA
1W20	900	N42	28	2*10 OUTPUT DATA
1W22	906	035	3	FUNCTION
1W21	905	035	8	EMPTY
1W22	905	035	9	EMPTY
1W22	904	035	13	FULL
1W21	903	035	14	INACTIVE
1W21	904	035	15	FULL
1W22	903	035	18	INACTIVE
1W22	902	035	22	ACTIVE
1W21	902	035	25	ACTIVE
1W21	906	036	1	CLOCK (100NSEC)
1W21	91	036	4	2*1 INPUT DATA
1W22	90	036	5	2*0 OUTPUT DATA
1W21	90	036	6	2*0 INPUT DATA
1W22	91	036	11	2*1 OUTPUT DATA
1W21	92	036	23	2*2 INPUT DATA
1W22	93	036	24	2*3 OUTPUT DATA
1W21	93	036	27	2*3 INPUT DATA
1W22	92	036	28	2*2 OUTPUT DATA
1W21	907	037	1	CLOCK (1USEC)
1W21	95	037	4	2*5 INPUT DATA
1W22	94	037	5	2*4 OUTPUT DATA
1W21	94	037	6	2*4 INPUT DATA
1W22	95	037	11	2*5 OUTPUT DATA
1W21	96	037	23	2*6 INPUT DATA
1W22	97	037	24	2*7 OUTPUT DATA
1W21	97	037	27	2*7 INPUT DATA
1W22	96	037	28	2*6 OUTPUT DATA
1W22	907	038	1	MASTER CLEAR
1W21	99	038	4	2*9 INPUT DATA
1W22	98	038	5	2*8 OUTPUT DATA
1W21	98	038	6	2*8 INPUT DATA
1W22	99	038	11	2*9 OUTPUT DATA
1W21	900	038	23	2*10 INPUT DATA
1W22	901	038	24	2*11 OUTPUT DATA
1W21	901	038	27	2*11 INPUT DATA
1W22	900	038	28	2*10 OUTPUT DATA
1W24	906	039	3	FUNCTION
1W23	905	039	8	EMPTY
1W24	905	039	9	EMPTY
1W24	904	039	13	FULL
1W23	903	039	14	INACTIVE
1W23	904	039	15	FULL
1W24	903	039	18	INACTIVE
1W24	902	039	22	ACTIVE
1W23	902	039	25	ACTIVE
1W23	906	040	1	CLOCK (100NSEC)
1W23	91	040	4	2*1 INPUT DATA
1W24	90	040	5	2*0 OUTPUT DATA
1W23	90	040	6	2*0 INPUT DATA
1W24	91	040	11	2*1 OUTPUT DATA
1W23	92	040	23	2*2 INPUT DATA
1W24	93	040	24	2*3 OUTPUT DATA
1W23	93	040	27	2*3 INPUT DATA
1W24	92	040	28	2*2 OUTPUT DATA
1W23	907	041	1	CLOCK (1US)
1W23	95	041	4	2*5 INPUT DATA

1W24	94	041	5	2*4	OUTPUT DATA
1W23	94	041	6	2*4	INPUT DATA
1W24	95	041	11	2*5	OUTPUT DATA
1W23	96	041	23	2*6	INPUT DATA
1W24	97	041	24	2*7	OUTPUT DATA
1W23	97	041	27	2*7	INPUT DATA
1W24	96	041	28	2*6	OUTPUT DATA
1W24	907	042	1		MASTER CLEAR
1W23	99	042	4	2*9	INPUT DATA
1W24	98	042	5	2*8	OUTPUT DATA
1W23	98	042	6	2*8	INPUT DATA
1W24	99	042	11	2*9	OUTPUT DATA
1W23	900	042	23	2*10	INPUT DATA
1W24	901	042	24	2*11	OUTPUT DATA
1W23	901	042	27	2*11	INPUT DATA
1W24	900	042	28	2*10	OUTPUT DATA
1W26	906	P35	3		FUNCTION
1W25	905	P35	8		EMPTY
1W26	905	P35	9		EMPTY
1W26	904	P35	13		FULL
1W25	903	P35	14		INACTIVE
1W25	904	P35	15		FULL
1W26	903	P35	18		INACTIVE
1W26	902	P35	22		ACTIVE
1W25	902	P35	25		ACTIVE
1W25	906	P36	1		CLOCK (100NSEC)
1W25	91	P36	4	2*1	INPUT DATA
1W26	90	P36	5	2*0	OUTPUT DATA
1W25	90	P36	6	2*0	INPUT DATA
1W26	91	P36	11	2*1	OUTPUT DATA
1W25	92	P36	23	2*2	INPUT DATA
1W26	93	P36	24	2*3	OUTPUT DATA
1W25	93	P36	27	2*3	INPUT DATA
1W26	92	P36	28	2*2	OUTPUT DATA
1W25	907	P37	1		CLOCK (1USEC)
1W25	95	P37	4	2*5	INPUT DATA
1W26	94	P37	5	2*4	OUTPUT DATA
1W25	94	P37	6	2*4	INPUT DATA
1W26	95	P37	11	2*5	OUTPUT DATA
1W25	96	P37	23	2*6	INPUT DATA
1W26	97	P37	24	2*7	OUTPUT DATA
1W25	97	P37	27	2*7	INPUT DATA
1W26	96	P37	28	2*6	OUTPUT DATA
1W26	907	P38	1		MASTER CLEAR
1W25	99	P38	4	2*9	INPUT DATA
1W26	98	P38	5	2*8	OUTPUT DATA
1W25	98	P38	6	2*8	INPUT DATA
1W26	99	P38	11	2*9	OUTPUT DATA
1W25	900	P38	23	2*10	INPUT DATA
1W26	901	P38	24	2*11	OUTPUT DATA
1W25	901	P38	27	2*11	INPUT DATA
1W26	900	P38	28	2*10	OUTPUT DATA
1W28	906	P39	3		FUNCTION
1W27	905	P39	8		EMPTY
1W28	905	P39	9		EMPTY
1W28	904	P39	13		FULL
1W27	903	P39	14		INACTIVE
1W27	904	P39	15		FULL
1W28	903	P39	18		INACTIVE
1W28	902	P39	22		ACTIVE
1W27	902	P39	25		ACTIVE
1W27	906	P40	1		CLOCK (100NSEC)

1W27	91	P40	4	2*1	INPUT DATA
1W28	90	P40	5	2*0	OUTPUT DATA
1W27	90	P40	6	2*0	INPUT DATA
1W28	91	P40	11	2*1	OUTPUT DATA
1W27	92	P40	23	2*2	INPUT DATA
1W28	93	P40	24	2*3	OUTPUT DATA
1W27	93	P40	27	2*3	INPUT DATA
1W28	92	P40	28	2*2	OUTPUT DATA
1W27	907	P41	1		CLOCK (1US)
1W27	95	P41	4	2*5	INPUT DATA
1W28	94	P41	5	2*4	OUTPUT DATA
1W27	94	P41	6	2*4	INPUT DATA
1W28	95	P41	11	2*5	OUTPUT DATA
1W27	96	P41	23	2*6	INPUT DATA
1W28	97	P41	24	2*7	OUTPUT DATA
1W27	97	P41	27	2*7	INPUT DATA
1W28	96	P41	28	2*6	OUTPUT DATA
1W28	907	P42	1		MASTER CLEAR
1W27	99	P42	4	2*9	INPUT DATA
1W28	98	P42	5	2*8	OUTPUT DATA
1W27	98	P42	6	2*8	INPUT DATA
1W28	99	P42	11	2*9	OUTPUT DATA
1W27	900	P42	23	2*10	INPUT DATA
1W28	901	P42	24	2*11	OUTPUT DATA
1W27	901	P42	27	2*11	INPUT DATA
1W28	900	P42	28	2*10	OUTPUT DATA
1W30	906	Q35	3		FUNCTION
1W29	905	Q35	8		EMPTY
1W30	905	Q35	9		EMPTY
1W30	904	Q35	13		FULL
1W29	903	Q35	14		INACTIVE
1W29	904	Q35	15		FULL
1W30	903	Q35	18		INACTIVE
1W30	902	Q35	22		ACTIVE
1W29	902	Q35	25		ACTIVE
1W29	906	Q36	1		CLOCK (100NSEC)
1W29	91	Q36	4	2*1	INPUT DATA
1W30	90	Q36	5	2*0	OUTPUT DATA
1W29	90	Q36	6	2*0	INPUT DATA
1W30	91	Q36	11	2*1	OUTPUT DATA
1W29	92	Q36	23	2*2	INPUT DATA
1W30	93	Q36	24	2*3	OUTPUT DATA
1W29	93	Q36	27	2*3	INPUT DATA
1W30	92	Q36	28	2*2	OUTPUT DATA
1W29	907	Q37	1		CLOCK (1US)
1W29	95	Q37	4	2*5	INPUT DATA
1W30	94	Q37	5	2*4	OUTPUT DATA
1W29	94	Q37	6	2*4	INPUT DATA
1W30	95	Q37	11	2*5	OUTPUT DATA
1W29	96	Q37	23	2*6	INPUT DATA
1W30	97	Q37	24	2*7	OUTPUT DATA
1W29	97	Q37	27	2*7	INPUT DATA
1W30	96	Q37	28	2*6	OUTPUT DATA
1W30	907	Q38	1		MASTER CLEAR
1W29	99	Q38	4	2*9	INPUT DATA
1W30	98	Q38	5	2*8	OUTPUT DATA
1W29	98	Q38	6	2*8	INPUT DATA
1W30	99	Q38	11	2*9	OUTPUT DATA
1W29	900	Q38	23	2*10	INPUT DATA
1W30	901	Q38	24	2*11	OUTPUT DATA
1W29	901	Q38	27	2*11	INPUT DATA
1W30	900	Q38	28	2*10	OUTPUT DATA

1W32	906	Q39	3	FUNCTION
1W31	905	Q39	8	EMPTY
1W32	905	Q39	9	EMPTY
1W32	904	Q39	13	FULL
1W31	903	Q39	14	INACTIVE
1W31	904	Q39	15	FULL
1W32	903	Q39	18	INACTIVE
1W32	902	Q39	22	ACTIVE
1W31	902	Q39	25	ACTIVE
1W31	906	Q40	1	CLOCK (100US)
1W31	91	Q40	4	2*1 INPUT DATA
1W32	90	Q40	5	2*0 OUTPUT DATA
1W31	90	Q40	6	2*0 INPUT DATA
1W32	91	Q40	11	2*1 OUTPUT DATA
1W31	92	Q40	23	2*2 INPUT DATA
1W32	93	Q40	24	2*3 OUTPUT DATA
1W31	93	Q40	27	2*3 INPUT DATA
1W32	92	Q40	28	2*2 OUTPUT DATA
1W31	907	Q41	1	CLOCK (1US)
1W31	95	Q41	4	2*5 INPUT DATA
1W32	94	Q41	5	2*4 OUTPUT DATA
1W31	94	Q41	6	2*4 INPUT DATA
1W32	95	Q41	11	2*5 OUTPUT DATA
1W31	96	Q41	23	2*6 INPUT DATA
1W32	97	Q41	24	2*7 OUTPUT DATA
1W31	97	Q41	27	2*7 INPUT DATA
1W32	96	Q41	28	2*6 OUTPUT DATA
1W32	907	Q42	1	MASTER CLEAR
1W31	99	Q42	4	2*9 INPUT DATA
1W32	98	Q42	5	2*8 OUTPUT DATA
1W31	98	Q42	6	2*8 INPUT DATA
1W32	99	Q42	11	2*9 OUTPUT DATA
1W31	900	Q42	23	2*10 INPUT DATA
1W32	901	Q42	24	2*11 OUTPUT DATA
1W31	901	Q42	27	2*11 INPUT DATA
1W32	900	Q42	28	2*10 OUTPUT DATA
1W34	906	R35	3	FUNCTION
1W33	905	R35	8	EMPTY
1W34	905	R35	9	EMPTY
1W34	904	R35	13	FULL
1W33	903	R35	14	INACTIVE
1W33	904	R35	15	FULL
1W34	903	R35	18	INACTIVE
1W34	902	R35	22	ACTIVE
1W33	902	R35	25	ACTIVE
1W33	906	R36	1	CLOCK (100 NSEC)
1W33	91	R36	4	2*1 INPUT DATA
1W34	90	R36	5	2*0 OUTPUT DATA
1W33	90	R36	6	2*0 INPUT DATA
1W34	91	R36	11	2*1 OUTPUT DATA
1W33	92	R36	23	2*2 INPUT DATA
1W34	93	R36	24	2*3 OUTPUT DATA
1W33	93	R36	27	2*3 INPUT DATA
1W34	92	R36	28	2*2 OUTPUT DATA
1W33	907	R37	1	CLOCK (1US)
1W33	95	R37	4	2*5 INPUT DATA
1W34	94	R37	5	2*4 OUTPUT DATA
1W33	94	R37	6	2*4 INPUT DATA
1W34	95	R37	11	2*5 OUTPUT DATA
1W33	96	R37	23	2*6 INPUT DATA
1W34	97	R37	24	2*7 OUTPUT DATA
1W33	97	R37	27	2*7 INPUT DATA

1W34	96	R37	28	2*6 OUTPUT DATA
1W34	907	R38	1	MASTER CLEAR
1W33	99	R38	4	2*9 INPUT DATA
1W34	98	R38	5	2*8 OUTPUT DATA
1W33	98	R38	6	2*8 INPUT DATA
1W34	99	R38	11	2*9 OUTPUT DATA
1W33	900	R38	23	2*10 INPUT DATA
1W34	901	R38	24	2*11 OUTPUT DATA
1W33	901	R38	27	2*11 INPUT DATA
1W34	900	R38	28	2*10 OUTPUT DATA
1W36	906	R39	3	FUNCTION
1W35	905	R39	8	EMPTY
1W36	905	R39	9	EMPTY
1W36	904	R39	13	FULL
1W35	903	R39	14	INACTIVE
1W35	904	R39	15	FULL
1W36	903	R39	18	INACTIVE
1W36	902	R39	22	ACTIVE
1W35	902	R39	25	ACTIVE
1W35	906	R40	1	CLOCK (100NS)
1W35	91	R40	4	2*1 INPUT DATA
1W36	90	R40	5	2*0 OUTPUT DATA
1W35	90	R40	6	2*0 INPUT DATA
1W36	91	R40	11	2*1 OUTPUT DATA
1W35	92	R40	23	2*2 INPUT DATA
1W36	93	R40	24	2*3 OUTPUT DATA
1W35	93	R40	27	2*3 INPUT DATA
1W36	92	R40	28	2*2 OUTPUT DATA
1W35	907	R41	1	CLOCK (1US)
1W35	95	R41	4	2*5 INPUT DATA
1W36	94	R41	5	2*4 OUTPUT DATA
1W35	94	R41	6	2*4 INPUT DATA
1W36	95	R41	11	2*5 OUTPUT DATA
1W35	96	R41	23	2*6 INPUT DATA
1W36	97	R41	24	2*7 OUTPUT DATA
1W35	97	R41	27	2*7 INPUT DATA
1W36	96	R41	28	2*6 OUTPUT DATA
1W36	907	R42	1	MASTER CLEAR
1W35	99	R42	4	2*9 INPUT DATA
1W36	98	R42	5	2*8 OUTPUT DATA
1W35	98	R42	6	2*8 INPUT DATA
1W36	99	R42	11	2*9 OUTPUT DATA
1W35	900	R42	23	2*10 INPUT DATA
1W36	901	R42	24	2*11 OUTPUT DATA
1W35	901	R42	27	2*11 INPUT DATA
1W36	900	R42	28	2*10 OUTPUT DATA

CHASSIS 2

2W14	90	A01	5	2*30		
2W14	91	A01	7	2*31		
2W14	92	A01	10	2*32		
2W14	93	A01	21	2*33		
2W14	94	A01	24	2*34		
2W14	95	A01	26	2*35		
2W13	901	A02	5	STORE CONTROL	BIT	2*36
2W13	902	A02	7	STORE CONTROL	BIT	2*37
2W13	903	A02	10	STORE CONTROL	BIT	2*38
2W13	904	A02	21	STORE CONTROL	BIT	2*39
2W13	905	A02	24	STORE CONTROL	BIT	2*40
2W13	906	A02	26	STORE CONTROL	BIT	2*41
2W13	907	A03	5	STORE CONTROL	BIT	2*42
2W13	900	A03	7	STORE CONTROL	BIT	2*43
2W13	90	A03	10	STORE CONTROL	BIT	2*44
2W13	91	A03	21	STORE CONTROL	BIT	2*45
2W13	92	A03	24	STORE CONTROL	BIT	2*46
2W13	93	A03	26	STORE CONTROL	BIT	2*47
2W13	94	A04	5	STORE CONTROL	BIT	2*48
2W13	95	A04	7	STORE CONTROL	BIT	2*49
2W13	96	A04	10	STORE CONTROL	BIT	2*50
2W13	97	A04	21	STORE CONTROL	BIT	2*51
2W13	98	A04	24	STORE CONTROL	BIT	2*52
2W13	99	A04	26	STORE CONTROL	BIT	2*53
2W14	96	A05	10	2*56		
2W14	904	A07	5	STORE PERIPHERAL		
2W14	902	A07	7	STORE CONTROL		
2W14	903	A07	10	STORE REGISTERS		
2W09	900	A08	5	DATA 2*0 REG.	TO MEM.	
2W09	901	A08	7	DATA 2*1 REG.	TO MEM.	
2W09	902	A08	10	DATA 2*2 REG.	TO MEM.	
2W09	903	A08	21	DATA 2*3 REG.	TO MEM.	
2W09	904	A08	24	DATA 2*4 REG.	TO MEM.	
2W09	905	A08	26	DATA 2*5 REG.	TO MEM.	
2W09	906	A09	5	DATA 2*6 REG.	TO MEM.	
2W09	907	A09	7	DATA 2*7 REG.	TO MEM.	
2W09	90	A09	10	DATA 2*8 REG.	TO MEM.	
2W09	91	A09	21	DATA 2*9 REG.	TO MEM.	
2W09	92	A09	24	DATA 2*10 REG.	TO MEM.	
2W09	93	A09	26	DATA 2*11 REG.	TO MEM.	
2W09	94	A10	5	DATA 2*12 REG.	TO MEM.	
2W09	95	A10	7	DATA 2*13 REG.	TO MEM.	
2W09	96	A10	10	DATA 2*14 REG.	TO MEM.	
2W09	97	A10	21	DATA 2*15 REG.	TO MEM.	
2W09	98	A10	24	DATA 2*16 REG.	TO MEM.	
2W09	99	A10	26	DATA 2*17 REG.	TO MEM.	
2W10	900	A11	5	DATA 2*18 REG.	TO MEM.	
2W10	901	A11	7	DATA 2*19 REG.	TO MEM.	
2W10	902	A11	10	DATA 2*20 REG.	TO MEM.	
2W10	903	A11	21	DATA 2*21 REG.	TO MEM.	
2W10	904	A11	24	DATA 2*22 REG.	TO MEM.	
2W10	905	A11	26	DATA 2*23 REG.	TO MEM.	
2W10	906	A12	5	DATA 2*24 REG.	TO MEM.	
2W10	907	A12	7	DATA 2*25 REG.	TO MEM.	
2W10	90	A12	10	DATA 2*26 REG.	TO MEM.	
2W10	91	A12	21	DATA 2*27 REG.	TO MEM.	
2W10	92	A12	24	DATA 2*28 REG.	TO MEM.	
2W10	93	A12	26	DATA 2*29 REG.	TO MEM.	
2W10	94	A13	5	DATA 2*30 REG.	TO MEM.	
2W10	95	A13	7	DATA 2*31 REG.	TO MEM.	

2W10	96	A13	10	DATA 2*32	REG. TO MEM.
2W10	97	A13	21	DATA 2*33	REG. TO MEM.
2W10	98	A13	24	DATA 2*34	REG. TO MEM.
2W10	99	A13	26	DATA 2*35	REG. TO MEM.
2W11	906	A14	5	2*36	REGISTER TO DISTRIBUTOR
2W11	907	A14	7	2*37	REGISTER TO DISTRIBUTOR
2W11	90	A14	10	2*38	REGISTER TO DISTRIBUTOR
2W11	91	A14	21	2*39	REGISTER TO DISTRIBUTOR
2W11	92	A14	24	2*40	REGISTER TO DISTRIBUTOR
2W11	93	A14	26	2*41	REGISTER TO DISTRIBUTOR
2W11	94	A15	5	2*42	REGISTER TO DISTRIBUTOR
2W11	95	A15	7	2*43	REGISTER TO DISTRIBUTOR
2W11	96	A15	10	2*44	REGISTER TO DISTRIBUTOR
2W12	900	A15	21	2*45	REGISTER TO DISTRIBUTOR
2W12	901	A15	24	2*46	REGISTER TO DISTRIBUTOR
2W12	902	A15	26	2*47	REGISTER TO DISTRIBUTOR
2W12	903	A16	5	2*48	REGISTER TO DISTRIBUTOR
2W12	904	A16	7	2*49	REGISTER TO DISTRIBUTOR
2W12	905	A16	10	2*50	REGISTER TO DISTRIBUTOR
2W12	906	A16	21	2*51	REGISTER TO DISTRIBUTOR
2W12	907	A16	24	2*52	REGISTER TO DISTRIBUTOR
2W12	90	A16	26	2*53	REGISTER TO DISTRIBUTOR
2W12	91	A17	5	2*54	REGISTER TO DISTRIBUTOR
2W12	92	A17	7	2*55	REGISTER TO DISTRIBUTOR
2W12	93	A17	10	2*56	REGISTER TO DISTRIBUTOR
2W12	94	A17	21	2*57	REGISTER TO DISTRIBUTOR
2W12	95	A17	24	2*58	REGISTER TO DISTRIBUTOR
2W12	96	A17	26	2*59	REGISTER TO DISTRIBUTOR
2W04	93	B01	5	2*0	PERIPHERAL TO CENTRAL DATA
2W04	94	B01	7	2*1	PERIPHERAL TO CENTRAL DATA
2W04	95	B01	10	2*2	PERIPHERAL TO CENTRAL DATA
2W04	96	B01	21	2*3	PERIPHERAL TO CENTRAL DATA
2W04	97	B01	24	2*4	PERIPHERAL TO CENTRAL DATA
2W04	98	B01	26	2*5	PERIPHERAL TO CENTRAL DATA
2W04	99	B02	5	2*6	PERIPHERAL TO CENTRAL DATA
2W04	900	B02	7	2*7	PERIPHERAL TO CENTRAL DATA
2W04	901	B02	10	2*8	PERIPHERAL TO CENTRAL DATA
2W04	902	B02	21	2*9	PERIPHERAL TO CENTRAL DATA
2W04	903	B02	24	2*10	PERIPHERAL TO CENTRAL DATA
2W04	904	B02	26	2*11	PERIPHERAL TO CENTRAL DATA
2W03	96	B03	5	2*12	PERIPHERAL TO CENTRAL DATA
2W03	97	B03	7	2*13	PERIPHERAL TO CENTRAL DATA
2W03	98	B03	10	2*14	PERIPHERAL TO CENTRAL DATA
2W03	99	B03	21	2*15	PERIPHERAL TO CENTRAL DATA
2W03	900	B03	24	2*16	PERIPHERAL TO CENTRAL DATA
2W03	901	B03	26	2*17	PERIPHERAL TO CENTRAL DATA
2W03	902	B04	5	2*18	PERIPHERAL TO CENTRAL DATA
2W03	903	B04	7	2*19	PERIPHERAL TO CENTRAL DATA
2W03	904	B04	10	2*20	PERIPHERAL TO CENTRAL DATA
2W04	90	B04	21	2*21	PERIPHERAL TO CENTRAL DATA
2W04	91	B04	24	2*22	PERIPHERAL TO CENTRAL DATA
2W04	92	B04	26	2*23	PERIPHERAL TO CENTRAL DATA
2W02	99	B05	5	2*24	PERIPHERAL TO CENTRAL DATA
2W02	900	B05	7	2*25	PERIPHERAL TO CENTRAL DATA
2W02	901	B05	10	2*26	PERIPHERAL TO CENTRAL DATA
2W02	902	B05	21	2*27	PERIPHERAL TO CENTRAL DATA
2W02	903	B05	24	2*28	PERIPHERAL TO CENTRAL DATA
2W02	904	B05	26	2*29	PERIPHERAL TO CENTRAL DATA
2W03	90	B06	5	2*30	PERIPHERAL TO CENTRAL DATA
2W03	91	B06	7	2*31	PERIPHERAL TO CENTRAL DATA
2W03	92	B06	10	2*32	PERIPHERAL TO CENTRAL DATA
2W03	93	B06	21	2*33	PERIPHERAL TO CENTRAL DATA

2W03	94	B06	24	2*34	PERIPHERAL TO CENTRAL DATA
2W03	95	B06	26	2*35	PERIPHERAL TO CENTRAL DATA
2W01	902	B07	5	2*36	PERIPHERAL TO CENTRAL DATA
2W01	903	B07	7	2*37	PERIPHERAL TO CENTRAL DATA
2W01	904	B07	10	2*38	PERIPHERAL TO CENTRAL DATA
2W02	90	B07	21	2*39	PERIPHERAL TO CENTRAL DATA
2W02	91	B07	24	2*40	PERIPHERAL TO CENTRAL DATA
2W02	92	B07	26	2*41	PERIPHERAL TO CENTRAL DATA
2W02	93	B08	5	2*42	PERIPHERAL TO CENTRAL DATA
2W02	94	B08	7	2*43	PERIPHERAL TO CENTRAL DATA
2W02	95	B08	10	2*44	PERIPHERAL TO CENTRAL DATA
2W02	96	B08	21	2*45	PERIPHERAL TO CENTRAL DATA
2W02	97	B08	24	2*46	PERIPHERAL TO CENTRAL DATA
2W02	98	B08	26	2*47	PERIPHERAL TO CENTRAL DATA
2W01	90	B09	5	2*48	PERIPHERAL TO CENTRAL DATA
2W01	91	B09	7	2*49	PERIPHERAL TO CENTRAL DATA
2W01	92	B09	10	2*50	PERIPHERAL TO CENTRAL DATA
2W01	93	B09	21	2*51	PERIPHERAL TO CENTRAL DATA
2W01	94	B09	24	2*52	PERIPHERAL TO CENTRAL DATA
2W01	95	B09	26	2*53	PERIPHERAL TO CENTRAL DATA
2W01	96	B10	5	2*54	PERIPHERAL TO CENTRAL DATA
2W01	97	B10	7	2*55	PERIPHERAL TO CENTRAL DATA
2W01	98	B10	10	2*56	PERIPHERAL TO CENTRAL DATA
2W01	99	B10	21	2*57	PERIPHERAL TO CENTRAL DATA
2W01	900	B10	24	2*58	PERIPHERAL TO CENTRAL DATA
2W01	901	B10	26	2*59	PERIPHERAL TO CENTRAL DATA
2W05	92	B12	4	2*2	DATA TO MEMORY FAN OUT
2W05	91	B12	6	2*1	DATA TO MEMORY FAN OUT
2W05	90	B12	8	2*0	DATA TO MEMORY FAN OUT
2W05	95	B12	23	2*5	DATA TO MEMORY FAN OUT
2W05	94	B12	25	2*4	DATA TO MEMORY FAN OUT
2W05	93	B12	27	2*3	DATA TO MEMORY FAN OUT
2W05	98	B13	4	2*8	DATA TO MEMORY FAN OUT
2W05	97	B13	6	2*7	DATA TO MEMORY FAN OUT
2W05	96	B13	8	2*6	DATA TO MEMORY FAN OUT
2W05	901	B13	23	2*11	DATA TO MEMORY FAN OUT
2W05	900	B13	25	2*10	DATA TO MEMORY FAN OUT
2W05	99	B13	27	2*9	DATA TO MEMORY FAN OUT
2W05	904	B14	4	2*14	DATA TO MEMORY FAN OUT
2W05	903	B14	6	2*13	DATA TO MEMORY FAN OUT
2W05	902	B14	8	2*12	DATA TO MEMORY FAN OUT
2W05	92	B14	23	2*17	DATA TO MEMORY FAN OUT
2W06	91	B14	25	2*16	DATA TO MEMORY FAN OUT
2W06	90	B14	27	2*15	DATA TO MEMORY FAN OUT
2W06	95	B15	4	2*20	DATA TO MEMORY FAN OUT
2W06	94	B15	6	2*19	DATA TO MEMORY FAN OUT
2W06	93	B15	8	2*18	DATA TO MEMORY FAN OUT
2W06	98	B15	23	2*23	DATA TO MEMORY FAN OUT
2W06	97	B15	25	2*22	DATA TO MEMORY FAN OUT
2W06	96	B15	27	2*21	DATA TO MEMORY FAN OUT
2W06	901	B16	4	2*26	DATA TO MEMORY FAN OUT
2W06	900	B16	6	2*25	DATA TO MEMORY FAN OUT
2W06	99	B16	8	2*24	DATA TO MEMORY FAN OUT
2W06	904	B16	23	2*29	DATA TO MEMORY FAN OUT
2W06	903	B16	25	2*28	DATA TO MEMORY FAN OUT
2W06	902	B16	27	2*27	DATA TO MEMORY FAN OUT
2W07	92	B17	4	2*32	DATA TO MEMORY FAN OUT
2W07	91	B17	6	2*31	DATA TO MEMORY FAN OUT
2W07	90	B17	8	2*30	DATA TO MEMORY FAN OUT
2W07	95	B17	23	2*35	DATA TO MEMORY FAN OUT
2W07	94	B17	25	2*34	DATA TO MEMORY FAN OUT
2W07	93	B17	27	2*33	DATA TO MEMORY FAN OUT

2W07	98	B18	4	2*38	DATA TO MEMORY FAN OUT
2W07	97	B18	6	2*37	DATA TO MEMORY FAN OUT
2W07	96	B18	8	2*36	DATA TO MEMORY FAN OUT
2W07	901	B18	23	2*41	DATA TO MEMORY FAN OUT
2W07	900	B18	25	2*40	DATA TO MEMORY FAN OUT
2W07	99	B18	27	2*39	DATA TO MEMORY FAN OUT
2W07	904	B19	4	2*44	DATA TO MEMORY FAN OUT
2W07	903	B19	6	2*43	DATA TO MEMORY FAN OUT
2W07	902	B19	8	2*42	DATA TO MEMORY FAN OUT
2W08	92	B19	23	2*47	DATA TO MEMORY FAN OUT
2W08	91	B19	25	2*46	DATA TO MEMORY FAN OUT
2W08	90	B19	27	2*45	DATA TO MEMORY FAN OUT
2W08	95	B20	4	2*50	DATA TO MEMORY FAN OUT
2W08	94	B20	6	2*49	DATA TO MEMORY FAN OUT
2W08	93	B20	8	2*48	DATA TO MEMORY FAN OUT
2W08	98	B20	23	2*53	DATA TO MEMORY FAN OUT
2W08	97	B20	25	2*52	DATA TO MEMORY FAN OUT
2W08	96	B20	27	2*51	DATA TO MEMORY FAN OUT
2W08	901	B21	4	2*56	DATA TO MEMORY FAN OUT
2W08	900	B21	6	2*55	DATA TO MEMORY FAN OUT
2W08	99	B21	8	2*54	DATA TO MEMORY FAN OUT
2W08	904	B21	23	2*59	DATA TO MEMORY FAN OUT
2W08	903	B21	25	2*58	DATA TO MEMORY FAN OUT
2W08	902	B21	27	2*57	DATA TO MEMORY FAN OUT
2W08	905	B22	16		MEMORY MARGIN
2W07	905	B22	18		MEMORY MARGIN
2W06	905	B22	20		MEMORY MARGIN
2W05	905	B22	22		MEMORY MARGIN
2W01	905	C03	8		RESUME CENTRAL WRITE
2W20	907	C16	12		MASTER CLEAR
2W20	905	C17	12		EMPTY
2W19	903	C32	6		INACTIVE
2W19	904	C32	8		FULL
2W19	92	C33	4	2*2	INPUT DATA
2W19	91	C33	6	2*1	INPUT DATA
2W19	90	C33	8	2*0	INPUT DATA
2W19	95	C33	23	2*5	INPUT DATA
2W19	94	C33	25	2*4	INPUT DATA
2W19	93	C33	27	2*3	INPUT DATA
2W19	98	C34	4	2*8	INPUT DATA
2W19	97	C34	6	2*7	INPUT DATA
2W19	96	C34	8	2*6	INPUT DATA
2W19	901	C34	23	2*11	INPUT DATA
2W19	900	C34	25	2*10	INPUT DATA
2W19	99	C34	27	2*9	INPUT DATA
2W21	906	E01	6		BOOLEAN REQUEST RELEASE
2W21	905	E01	8		DIVIDE REQUEST RELEASE
2W14	906	E01	23		EXP = 3777 ERROR
2W14	905	E01	25		EXP = 1777 ERROR
2W22	908	E02	5		ERROR (EXP = 1777 FROM CHASSIS 7)
2W23	908	E02	7		ERROR (EXP = 3777 FROM CHASSIS 8)
2W14	901	I22	12		CLOCK
2W21	93	J01	5		SUM
2W21	94	J01	7		DIFFERENCE
2W21	95	J01	10		PRODUCT
2W21	98	J01	21		TRANSFER
2W21	97	J01	24		COMPLEMENT
2W21	91	J02	7		ROUND
2W21	96	J02	10		POP. COUNT
2W32	98	J03	1		ERROR MULT. 1
2W32	900	J03	6		ERROR MULT. 1
2W32	99	J03	23		ERROR MULT. 2

2W32	901	J03	28	ERROR MULT. 2
2W24	90	K01	1	2*0 DIVIDE TO MULTIPLY RESULT
2W24	91	K01	6	2*1 DIVIDE TO MULTIPLY RESULT
2W24	92	K01	23	2*2 DIVIDE TO MULTIPLY RESULT
2W24	93	K01	28	2*3 DIVIDE TO MULTIPLY RESULT
2W24	94	K02	1	2*4 DIVIDE TO MULTIPLY RESULT
2W24	95	K02	6	2*5 DIVIDE TO MULTIPLY RESULT
2W24	96	K02	23	2*6 DIVIDE TO MULTIPLY RESULT
2W24	97	K02	28	2*7 DIVIDE TO MULTIPLY RESULT
2W24	98	K03	1	2*8 DIVIDE TO MULTIPLY RESULT
2W24	99	K03	6	2*9 DIVIDE TO MULTIPLY RESULT
2W24	900	K03	23	2*10 DIVIDE TO MULTIPLY RESULT
2W24	901	K03	28	2*11 DIVIDE TO MULTIPLY RESULT
2W24	902	K04	1	2*12 DIVIDE TO MULTIPLY RESULT
2W24	903	K04	6	2*13 DIVIDE TO MULTIPLY RESULT
2W24	904	K04	23	2*14 DIVIDE TO MULTIPLY RESULT
2W24	905	K04	28	2*15 DIVIDE TO MULTIPLY RESULT
2W24	906	K05	1	2*16 DIVIDE TO MULTIPLY RESULT
2W24	907	K05	6	2*17 DIVIDE TO MULTIPLY RESULT
2W25	90	K05	23	2*18 DIVIDE TO MULTIPLY RESULT
2W25	91	K05	28	2*19 DIVIDE TO MULTIPLY RESULT
2W25	92	K06	1	2*20 DIVIDE TO MULTIPLY RESULT
2W25	93	K06	6	2*21 DIVIDE TO MULTIPLY RESULT
2W25	94	K06	23	2*22 DIVIDE TO MULTIPLY RESULT
2W25	95	K06	28	2*23 DIVIDE TO MULTIPLY RESULT
2W25	96	K07	1	2*24 DIVIDE TO MULTIPLY RESULT
2W25	97	K07	6	2*25 DIVIDE TO MULTIPLY RESULT
2W25	98	K07	23	2*26 DIVIDE TO MULTIPLY RESULT
2W25	99	K07	28	2*27 DIVIDE TO MULTIPLY RESULT
2W25	900	K08	1	2*28 DIVIDE TO MULTIPLY RESULT
2W25	901	K08	6	2*29 DIVIDE TO MULTIPLY RESULT
2W25	902	K08	23	2*30 DIVIDE TO MULTIPLY RESULT
2W25	903	K08	28	2*31 DIVIDE TO MULTIPLY RESULT
2W25	904	K09	1	2*32 DIVIDE TO MULTIPLY RESULT
2W25	905	K09	6	2*33 DIVIDE TO MULTIPLY RESULT
2W25	906	K09	23	2*34 DIVIDE TO MULTIPLY RESULT
2W25	907	K09	28	2*35 DIVIDE TO MULTIPLY RESULT
2W26	90	K10	1	2*36 DIVIDE TO MULTIPLY RESULT
2W26	91	K10	6	2*37 DIVIDE TO MULTIPLY RESULT
2W26	92	K10	23	2*38 DIVIDE TO MULTIPLY RESULT
2W26	93	K10	28	2*39 DIVIDE TO MULTIPLY RESULT
2W26	94	K11	1	2*40 DIVIDE TO MULTIPLY RESULT
2W26	95	K11	6	2*41 DIVIDE TO MULTIPLY RESULT
2W26	96	K11	23	2*42 DIVIDE TO MULTIPLY RESULT
2W26	97	K11	28	2*43 DIVIDE TO MULTIPLY RESULT
2W26	98	K12	1	2*44 DIVIDE TO MULTIPLY RESULT
2W26	99	K12	6	2*45 DIVIDE TO MULTIPLY RESULT
2W26	900	K12	23	2*46 DIVIDE TO MULTIPLY RESULT
2W26	901	K12	28	2*47 DIVIDE TO MULTIPLY RESULT
2W22	902	K13	1	2*48 EXPONENT RESULT-REG.
2W22	903	K13	6	2*49 EXPONENT RESULT-REG.
2W22	904	K13	23	2*50 EXPONENT RESULT-REG.
2W22	905	K13	28	2*51 EXPONENT RESULT-REG.
2W22	906	K14	1	2*52 EXPONENT RESULT-REG.
2W22	907	K14	6	2*53 EXPONENT RESULT-REG.
2W23	902	K14	23	2*54 EXPONENT RESULT-REG.
2W23	903	K14	28	2*55 EXPONENT RESULT-REG.
2W23	904	K15	1	2*56 EXPONENT RESULT-REG.
2W23	905	K15	6	2*57 EXPONENT RESULT-REG.
2W23	906	K15	23	2*58 EXPONENT RESULT-REG.
2W23	907	K15	28	2*59 EXPONENT RESULT-REG.
2W31	907	L06	12	2*40 MULTIPLY TO DIVIDE OPERAND X**J

2W32	90	L06	14	2*41	MULTIPLY TO DIVIDE	X**J
2W32	91	L06	16	2*42	MULTIPLY TO DIVIDE	X**J
2W32	92	L06	18	2*43	MULTIPLY TO DIVIDE	X**J
2W32	93	L07	12	2*44	MULTIPLY TO DIVIDE	X**J
2W32	94	L07	14	2*45	MULTIPLY TO DIVIDE	X**J
2W32	95	L07	16	2*46	MULTIPLY TO DIVIDE	X**J
2W32	96	L07	18	2*47	MULTIPLY TO DIVIDE	X**J
2W23	90	L08	12	2*48	X**J (EXPONENT)	
2W23	91	L08	14	2*49	X**J (EXPONENT)	
2W23	92	L08	16	2*50	X**J (EXPONENT)	
2W23	93	L08	18	2*51	X**J (EXPONENT)	
2W23	94	L09	12	2*52	X**J (EXPONENT)	
2W23	95	L09	14	2*53	X**J (EXPONENT)	
2W23	96	L09	16	2*54	X**J (EXPONENT)	
2W23	97	L09	18	2*55	X**J (EXPONENT)	
2W23	98	L10	12	2*56	X**J (EXPONENT)	
2W23	99	L10	14	2*57	X**J (EXPONENT)	
2W23	900	L10	16	2*58	X**J (EXPONENT)	
2W23	901	L10	18	2*59	X**J (EXPONENT)	
2W21	903	L11	12	GO	DIVIDE	
2W21	900	L11	14	TRANSMIT	DIVIDE	
2W21	902	L11	16	GO	BOOLEAN	
2W21	99	L11	18	TRANSMIT	BOOLEAN	
2W29	94	M06	12	2*40	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	95	M06	14	2*41	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	96	M06	16	2*42	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	97	M06	18	2*43	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	98	M07	12	2*44	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	99	M07	14	2*45	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	900	M07	16	2*46	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W29	901	M07	18	2*47	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W22	90	M08	12	2*48	X**K (EXPONENT)	
2W22	91	M08	14	2*49	X**K (EXPONENT)	
2W22	92	M08	16	2*50	X**K (EXPONENT)	
2W22	93	M08	18	2*51	X**K (EXPONENT)	
2W22	94	M09	12	2*52	X**K (EXPONENT)	
2W22	95	M09	14	2*53	X**K (EXPONENT)	
2W22	96	M09	16	2*54	X**K (EXPONENT)	
2W22	97	M09	18	2*55	X**K (EXPONENT)	
2W22	98	M10	12	2*56	X**K (EXPONENT)	
2W22	99	M10	14	2*57	X**K (EXPONENT)	
2W22	900	M10	16	2*58	X**K (EXPONENT)	
2W22	901	M10	18	2*59	X**K (EXPONENT)	
2W26	905	M11	14	GO	MULT 1	
2W26	906	M11	16	GO	MULT 2	
2W30	905	N06	12	2*20	MULTIPLY TO DIVIDE OPERAND	X**J
2W30	906	N06	14	2*21	MULTIPLY TO DIVIDE OPERAND	X**J
2W30	907	N06	16	2*22	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	90	N06	18	2*23	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	91	N07	12	2*24	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	92	N07	14	2*25	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	93	N07	16	2*26	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	94	N07	18	2*27	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	95	N08	12	2*28	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	96	N08	14	2*29	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	97	N08	16	2*30	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	98	N08	18	2*31	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	99	N09	12	2*32	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	900	N09	14	2*33	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	901	N09	16	2*34	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	902	N09	18	2*35	MULTIPLY TO DIVIDE OPERAND	X**J
2W31	903	N10	12	2*36	MULTIPLY TO DIVIDE OPERAND	X**J

2W31	904	N10	14	2*37	MULTIPLY TO DIVIDE OPERAND X**J
2W31	905	N10	16	2*38	MULTIPLY TO DIVIDE OPERAND X**J
2W31	906	N10	18	2*39	MULTIPLY TO DIVIDE OPERAND X**J
2W21	907	009	7	TRANSMIT	MUIT 1
2W21	908	009	21	TRANSMIT	MUIT 2
2W28	92	P06	12	2*20	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	93	P06	14	2*21	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	94	P06	16	2*22	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	95	P06	18	2*23	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	96	P07	12	2*24	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	97	P07	14	2*25	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	98	P07	16	2*26	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	99	P07	18	2*27	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	900	P08	12	2*28	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	901	P08	14	2*29	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	902	P08	16	2*30	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	903	P08	18	2*31	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	904	P09	12	2*32	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	905	P09	14	2*33	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	906	P09	16	2*34	MULTIPLY TO DIVIDE (OPERAND) X**K
2W28	907	P09	18	2*35	MULTIPLY TO DIVIDE (OPERAND) X**K
2W29	90	P10	12	2*36	MULTIPLY TO DIVIDE (OPERAND) X**K
2W29	91	P10	14	2*37	MULTIPLY TO DIVIDE (OPERAND) X**K
2W29	92	P10	16	2*38	MULTIPLY TO DIVIDE (OPERAND) X**K
2W29	93	P10	18	2*39	MULTIPLY TO DIVIDE (OPERAND) X**K
2W29	903	Q06	12	2*0	MULTIPLY TO DIVIDE (OPERAND) X**J
2W29	904	Q06	14	2*1	MULTIPLY TO DIVIDE (OPERAND) X**J
2W29	905	Q06	16	2*2	MULTIPLY TO DIVIDE (OPERAND) X**J
2W29	906	Q06	18	2*3	MULTIPLY TO DIVIDE (OPERAND) X**J
2W29	907	Q07	12	2*4	MULTIPLY TO DIVIDE (OPERAND) X**J
2W30	90	Q07	14	2*5	MULTIPLY TO DIVIDE OPERAND X**J
2W30	91	Q07	16	2*6	MULTIPLY TO DIVIDE OPERAND X**J
2W30	92	Q07	18	2*7	MULTIPLY TO DIVIDE OPERAND X**J
2W30	93	Q08	12	2*8	MULTIPLY TO DIVIDE OPERAND X**J
2W30	94	Q08	14	2*9	MULTIPLY TO DIVIDE OPERAND X**J
2W30	95	Q08	16	2*10	MULTIPLY TO DIVIDE OPERAND X**J
2W30	96	Q08	18	2*11	MULTIPLY TO DIVIDE OPERAND X**J
2W30	97	Q09	12	2*12	MULTIPLY TO DIVIDE OPERAND X**J
2W30	98	Q09	14	2*13	MULTIPLY TO DIVIDE OPERAND X**J
2W30	99	Q09	16	2*14	MULTIPLY TO DIVIDE OPERAND X**J
2W30	900	Q09	18	2*15	MULTIPLY TO DIVIDE OPERAND X**J
2W30	901	Q10	12	2*16	MULTIPLY TO DIVIDE OPERAND X**J
2W30	902	Q10	14	2*17	MULTIPLY TO DIVIDE OPERAND X**J
2W30	903	Q10	16	2*18	MULTIPLY TO DIVIDE OPERAND X**J
2W30	904	Q10	18	2*19	MULTIPLY TO DIVIDE OPERAND X**J
2W26	907	Q11	5	D P MULT	1
2W26	908	Q11	7	REDUCE MULT.	1
2W26	908	Q12	5	D P MULT	2
2W26	902	Q12	7	REDUCE MULT	2
2W27	90	R06	12	2*0	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	91	R06	14	2*1	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	92	R06	16	2*2	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	93	R06	18	2*3	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	94	R07	12	2*4	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	95	R07	14	2*5	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	96	R07	16	2*6	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	97	R07	18	2*7	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	98	R08	12	2*8	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	99	R08	14	2*9	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	900	R08	16	2*10	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	901	R08	18	2*11	MULTIPLY TO DIVIDE (OPERAND) X**K
2W27	902	R09	12	2*12	MULTIPLY TO DIVIDE (OPERAND) X**K

2W27	903	R09	14	2*13	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W27	904	R09	16	2*14	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W27	905	R09	18	2*15	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W27	906	R10	12	2*16	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W27	907	R10	14	2*17	MULTIPLY TO DIVIDE (OPERAND)	X**K
2W28	90	R10	16			
2W28	91	R10	18			

CHASSIS 3

3W03	905	C14	14	MEMORY MARGIN
3W20	900	G35	12	CLOCK
3W20	904	G38	4	2*3 CHASSIS 3 SELECT (000)
3W20	903	G38	9	2*2 CHASSIS 3 SELECT (000)
3W20	905	G38	14	2*4
3W05	906	G38	20	WRITE
3W05	905	G38	21	GO
3W20	901	G39	18	2*0 BANK SELECT
3W20	902	G39	22	2*1 BANK SELECT
3W39	97	G39	28	ACCEPT
3W20	906	G40	11	2*5 ADDRESS TO MEMORY
3W20	907	G40	14	2*6 ADDRESS TO MEMORY
3W20	908	G40	17	2*7 ADDRESS TO MEMORY
3W20	90	G40	18	2*8 ADDRESS TO MEMORY
3W20	91	G41	11	2*9 ADDRESS TO MEMORY
3W20	92	G41	14	2*10 ADDRESS TO MEMORY
3W20	93	G41	17	2*11 ADDRESS TO MEMORY
3W20	94	G41	18	2*12 ADDRESS TO MEMORY
3W20	95	G42	11	2*13 ADDRESS TO MEMORY
3W20	96	G42	14	2*14 ADDRESS TO MEMORY
3W20	97	G42	17	2*15 ADDRESS TO MEMORY
3W20	98	G42	18	2*16 ADDRESS TO MEMORY
3W39	900	H19	1	2*0
3W15	900	H19	2	2*0 FAN OUT TO MEMORY
3W39	901	H19	8	2*1
3W15	901	H19	19	2*1 FAN OUT TO MEMORY
3W15	902	H19	24	2*2 FAN OUT TO MEMORY
3W39	902	H19	28	2*2
3W39	903	H20	1	2*3
3W15	903	H20	2	2*3 FAN OUT TO MEMORY
3W39	904	H20	8	2*4
3W15	904	H20	19	2*4 FAN OUT TO MEMORY
3W15	905	H20	24	2*5 FAN OUT TO MEMORY
3W39	905	H20	28	2*5
3W39	906	H21	1	2*6
3W15	906	H21	2	2*6 FAN OUT TO MEMORY
3W39	907	H21	8	2*7
3W15	907	H21	19	2*7 FAN OUT TO MEMORY
3W15	90	H21	24	2*8 FAN OUT TO MEMORY
3W39	90	H21	28	2*8
3W39	91	H22	1	2*9
3W15	91	H22	2	2*9 FAN OUT TO MEMORY
3W39	92	H22	8	2*10
3W15	92	H22	19	2*10 FAN OUT TO MEMORY
3W15	93	H22	24	2*11 FAN OUT TO MEMORY
3W39	93	H22	28	2*11
3W39	94	H24	1	2*12
3W15	94	H24	2	2*12 FAN OUT TO MEMORY
3W39	95	H24	8	2*13
3W15	95	H24	19	2*13 FAN OUT TO MEMORY
3W15	96	H24	24	2*14 FAN OUT TO MEMORY
3W39	96	H24	28	2*14
3W06	900	H25	1	2*15
3W16	900	H25	2	2*15 FAN OUT TO MEMORY
3W06	901	H25	8	2*16
3W16	901	H25	19	2*16 FAN OUT TO MEMORY
3W16	902	H25	24	2*17 FAN OUT TO MEMORY
3W06	902	H25	28	2*17
3W06	903	H26	1	2*18
3W16	903	H26	2	2*18 FAN OUT TO MEMORY

3W06	904	H26	8	2*19	
3W16	904	H26	19	2*19	FAN OUT TO MEMORY
3W16	905	H26	24	2*20	FAN OUT TO MEMORY
3W06	905	H26	28	2*20	
3W06	906	H27	1	2*21	
3W16	906	H27	2	2*21	FAN OUT TO MEMORY
3W06	907	H27	8	2*22	
3W16	907	H27	19	2*22	FAN OUT TO MEMORY
3W16	90	H27	24	2*23	FAN OUT TO MEMORY
3W06	90	H27	28	2*23	
3W06	91	H29	1	2*24	
3W16	91	H29	2	2*24	FAN OUT TO MEMORY
3W06	92	H29	8	2*25	
3W16	92	H29	19	2*25	FAN OUT TO MEMORY
3W16	93	H29	24	2*26	FAN OUT TO MEMORY
3W06	93	H29	28	2*26	
3W06	94	H30	1	2*27	
3W16	94	H30	2	2*27	FAN OUT TO MEMORY
3W06	95	H30	8	2*28	
3W16	95	H30	19	2*28	FAN OUT TO MEMORY
3W16	96	H30	24	2*29	FAN OUT TO MEMORY
3W06	96	H30	28	2*29	
3W02	900	H31	1	2*30	
3W17	900	H31	2	2*30	FAN OUT TO MEMORY
3W02	901	H31	8	2*31	
3W17	901	H31	19	2*31	FAN OUT TO MEMORY
3W17	902	H31	24	2*32	FAN OUT TO MEMORY
3W02	902	H31	28	2*32	
3W02	903	H32	1	2*33	
3W17	903	H32	2	2*33	FAN OUT TO MEMORY
3W02	904	H32	8	2*34	
3W17	904	H32	19	2*34	FAN OUT TO MEMORY
3W17	905	H32	24	2*35	FAN OUT TO MEMORY
3W02	905	H32	28	2*35	
3W02	906	H34	1	2*36	
3W17	906	H34	2	2*36	FAN OUT TO MEMORY
3W02	907	H34	8	2*37	
3W17	907	H34	19	2*37	FAN OUT TO MEMORY
3W17	90	H34	24	2*38	FAN OUT TO MEMORY
3W02	90	H34	28	2*38	
3W02	91	H35	1	2*39	
3W17	91	H35	2	2*39	FAN OUT TO MEMORY
3W02	92	H35	8	2*40	
3W17	92	H35	19	2*40	FAN OUT TO MEMORY
3W17	93	H35	24	2*41	FAN OUT TO MEMORY
3W02	93	H35	28	2*41	
3W02	94	H36	1	2*42	
3W17	94	H36	2	2*42	FAN OUT TO MEMORY
3W02	95	H36	8	2*43	
3W17	95	H36	19	2*43	FAN OUT TO MEMORY
3W17	96	H36	24	2*44	FAN OUT TO MEMORY
3W02	96	H36	28	2*44	
3W04	900	H37	1	2*45	
3W18	900	H37	2	2*45	FAN OUT TO MEMORY
3W04	901	H37	8	2*46	
3W18	901	H37	19	2*46	FAN OUT TO MEMORY
3W18	902	H37	24	2*47	FAN OUT TO MEMORY
3W04	902	H37	28	2*47	
3W04	903	H39	1	2*48	
3W18	903	H39	2	2*48	FAN OUT TO MEMORY
3W04	904	H39	8	2*49	
3W18	904	H39	19	2*49	FAN OUT TO MEMORY

3W18	905	H39	24	2*50	FAN OUT TO MEMORY
3W04	905	H39	28	2*50	
3W04	906	H40	1	2*51	
3W18	906	H40	2	2*51	FAN OUT TO MEMORY
3W04	907	H40	8	2*52	
3W18	907	H40	19	2*52	FAN OUT TO MEMORY
3W18	90	H40	24	2*53	FAN OUT TO MEMORY
3W04	90	H40	28	2*53	
3W04	91	H41	1	2*54	
3W18	91	H41	2	2*54	FAN OUT TO MEMORY
3W04	92	H41	8	2*55	
3W18	92	H41	19	2*55	FAN OUT TO MEMORY
3W18	93	H41	24	2*56	FAN OUT TO MEMORY
3W04	93	H41	28	2*56	
3W04	94	H42	1	2*57	
3W18	94	H42	2	2*57	FAN OUT TO MEMORY
3W04	95	H42	8	2*58	
3W18	95	H42	19	2*58	FAN OUT TO MEMORY
3W18	96	H42	24	2*59	FAN OUT TO MEMORY
3W04	96	H42	28	2*59	
3W06	98	I23	3		ACCEPT
3W39	97	I23	5		ACCEPT
3W07	99	I23	7		ACCEPT
3W04	97	I23	9		ACCEPT FROM CHAS. 10
3W15	97	I23	19		MEMORY ACCEPT
3W16	97	I23	21		ACCEPT
3W17	97	I23	23		ACCEPT
3W18	97	I23	25		ACCEPT
3W20	99	I23	28		ACCEPT
3W06	97	I24	12		READ PERIPHERAL
3W03	90	I25	1	2*0	CENTRAL TO PERIPHERAL DATA
3W05	90	I25	3	2*0	
3W39	900	I25	5	2*0	
3W07	900	I25	7	2*0	
3W08	900	I25	9	2*0	
3W09	900	I25	11	2*0	
3W10	900	I25	19	2*0	
3W11	900	I25	21	2*0	
3W13	900	I25	23	2*0	
3W14	904	I25	26	2*0	
3W12	900	I25	28	2*0	MEMORY TO REGISTER
3W03	91	I26	1	2*1	CENTRAL TO PERIPHERAL DATA
3W05	91	I26	3	2*1	
3W39	901	I26	5	2*1	
3W07	901	I26	7	2*1	
3W08	901	I26	9	2*1	
3W09	901	I26	11	2*1	
3W10	901	I26	19	2*1	
3W11	901	I26	21	2*1	
3W13	901	I26	23	2*1	
3W14	905	I26	26	2*1	
3W12	901	I26	28	2*1	MEMORY TO REGISTER
3W03	92	I27	1	2*2	CENTRAL TO PERIPHERAL DATA
3W05	92	I27	3	2*2	
3W39	902	I27	5	2*2	
3W07	902	I27	7	2*2	
3W08	902	I27	9	2*2	
3W09	902	I27	11	2*2	
3W10	902	I27	19	2*2	
3W11	902	I27	21	2*2	
3W13	902	I27	23	2*2	
3W14	906	I27	26	2*2	

3W12	902	127	28	2*2	MEMORY TO REGISTER
3W03	93	128	1	2*3	CENTRAL TO PERIPHERAL DATA
3W05	93	128	3	2*3	
3W39	903	128	5	2*3	
3W07	903	128	7	2*3	
3W08	903	128	9	2*3	
3W09	903	128	11	2*3	
3W10	903	128	19	2*3	
3W11	903	128	21	2*3	
3W13	903	128	23	2*3	
3W14	907	128	26	2*3	
3W12	903	128	28	2*3	MEMORY TO REGISTER
3W03	94	129	1	2*4	CENTRAL TO PERIPHERAL DATA
3W05	94	129	3	2*4	
3W39	904	129	5	2*4	
3W07	904	129	7	2*4	
3W08	904	129	9	2*4	
3W09	904	129	11	2*4	
3W10	904	129	19	2*4	
3W11	904	129	21	2*4	
3W13	904	129	23	2*4	
3W14	900	129	26	2*4	
3W12	904	129	28	2*4	MEMORY TO REGISTER
3W03	95	132	1	2*5	CENTRAL TO PERIPHERAL DATA
3W05	95	132	3	2*5	
3W39	905	132	5	2*5	
3W07	905	132	7	2*5	
3W08	905	132	9	2*5	
3W09	905	132	11	2*5	
3W10	905	132	19	2*5	
3W11	905	132	21	2*5	
3W13	905	132	23	2*5	
3W14	90	132	26	2*5	
3W12	905	132	28	2*5	MEMORY TO REGISTER
3W03	96	133	1	2*6	CENTRAL TO PERIPHERAL DATA
3W05	96	133	3	2*6	
3W39	906	133	5	2*6	
3W07	906	133	7	2*6	
3W08	906	133	9	2*6	
3W09	906	133	11	2*6	
3W10	906	133	19	2*6	
3W11	906	133	21	2*6	
3W13	906	133	23	2*6	
3W14	91	133	26	2*6	
3W12	906	133	28	2*6	MEMORY TO REGISTER
3W03	97	134	1	2*7	CENTRAL TO PERIPHERAL DATA
3W05	97	134	3	2*7	
3W39	907	134	5	2*7	
3W07	907	134	7	2*7	
3W08	907	134	9	2*7	
3W09	907	134	11	2*7	
3W10	907	134	19	2*7	
3W11	907	134	21	2*7	
3W13	907	134	23	2*7	
3W14	92	134	26	2*7	
3W12	907	134	28	2*7	MEMORY TO REGISTER
3W03	98	135	1	2*8	CENTRAL TO PERIPHERAL DATA
3W05	98	135	3	2*8	
3W39	90	135	5	2*8	
3W07	90	135	7	2*8	
3W08	90	135	9	2*8	
3W09	90	135	11	2*8	

3W10	90	135	19	2*8	
3W11	90	135	21	2*8	
3W13	90	135	23	2*8	
3W14	93	135	26	2*8	
3W12	90	135	28	2*8	MEMORY TO REGISTER
3W03	99	136	1	2*9	CENTRAL TO PERIPHERAL DATA
3W05	99	136	3	2*9	
3W39	91	136	5	2*9	
3W07	91	136	7	2*9	
3W08	91	136	9	2*9	
3W09	91	136	11	2*9	
3W10	91	136	19	2*9	
3W11	91	136	21	2*9	
3W13	91	136	23	2*9	
3W14	94	136	26	2*9	
3W12	91	136	28	2*9	MEMORY TO REGISTER
3W03	900	138	1	2*10	CENTRAL TO PERIPHERAL DATA
3W05	900	138	3	2*10	
3W39	92	138	5	2*10	
3W07	92	138	7	2*10	
3W08	92	138	9	2*10	
3W09	92	138	11	2*10	
3W10	92	138	19	2*10	
3W11	92	138	21	2*10	
3W13	92	138	23	2*10	
3W14	95	138	26	2*10	
3W12	92	138	28	2*10	MEMORY TO REGISTER
3W03	901	139	1	2*11	CENTRAL TO PERIPHERAL DATA
3W05	901	139	3	2*11	
3W39	93	139	5	2*11	
3W07	93	139	7	2*11	
3W08	93	139	9	2*11	
3W09	93	139	11	2*11	
3W10	93	139	19	2*11	
3W11	93	139	21	2*11	
3W13	93	139	23	2*11	
3W14	96	139	26	2*11	
3W12	93	139	28	2*11	MEMORY TO REGISTER
3W03	902	140	1	2*12	CENTRAL TO PERIPHERAL DATA
3W05	902	140	3	2*12	
3W39	94	140	5	2*12	
3W07	94	140	7	2*12	
3W08	94	140	9	2*12	
3W09	94	140	11	2*12	
3W10	94	140	19	2*12	
3W11	94	140	21	2*12	
3W13	94	140	23	2*12	
3W14	97	140	26	2*12	
3W12	94	140	28	2*12	MEMORY TO REGISTER
3W03	903	141	1	2*13	CENTRAL TO PERIPHERAL DATA
3W05	903	141	3	2*13	
3W39	95	141	5	2*13	
3W07	95	141	7	2*13	
3W08	95	141	9	2*13	
3W09	95	141	11	2*13	
3W10	95	141	19	2*13	
3W11	95	141	21	2*13	
3W13	95	141	23	2*13	
3W14	98	141	26	2*13	
3W12	95	141	28	2*13	MEMORY TO REGISTER
3W03	904	142	1	2*14	CENTRAL TO PERIPHERAL DATA
3W05	904	142	3	2*14	

3W39	96	I42	5	2*14
3W07	96	I42	7	2*14
3W08	96	I42	9	2*14
3W09	96	I42	11	2*14
3W10	96	I42	19	2*14
3W11	96	I42	21	2*14
3W13	96	I42	23	2*14
3W14	99	I42	26	2*14
3W12	96	I42	28	2*14 MEMORY TO REGISTER
3W21	900	J37	27	CLOCK
3W21	903	J39	8	EXCHANGE GO
3W21	902	J39	9	WRITE GO
3W21	901	J39	10	READ GO
3W24	902	J39	24	EXCHANGE GO (PERIPH)
3W24	901	J39	26	WRITE GO (PERIPH)
3W24	900	J39	28	READ GO (PERIPH)
3W23	903	J40	3	BIT 2
3W23	902	J40	5	BIT 1
3W23	901	J40	7	BIT 0
3W22	906	J40	8	BIT 5
3W22	905	J40	9	BIT 4
3W22	904	J40	10	BIT 3
3W22	903	J40	21	BIT 2
3W22	902	J40	22	BIT 1
3W22	901	J40	23	BIT 0
3W23	906	J40	24	BIT 5
3W23	905	J40	26	BIT 4
3W23	904	J40	28	BIT 3
3W23	900	J41	3	BIT 8
3W23	908	J41	5	BIT 7
3W23	907	J41	7	BIT 6
3W22	93	J41	8	BIT 11
3W22	92	J41	9	BIT 10
3W22	91	J41	10	BIT 9
3W22	900	J41	21	BIT 8
3W22	908	J41	22	BIT 7
3W22	907	J41	23	BIT 6
3W23	93	J41	24	BIT 11
3W23	92	J41	26	BIT 10
3W23	91	J41	28	BIT 9
3W23	96	J42	3	BIT 14
3W23	95	J42	5	BIT 13
3W23	94	J42	7	BIT 12
3W22	99	J42	8	BIT 17
3W22	98	J42	9	BIT 16
3W22	97	J42	10	BIT 15
3W22	96	J42	21	BIT 14
3W22	95	J42	22	BIT 13
3W22	94	J42	23	BIT 12
3W23	99	J42	24	BIT 17
3W23	98	J42	26	BIT 16
3W23	97	J42	28	BIT 15

CHASSIS 4

4W07	907	C14	14	STORAGE MARGIN
4W20	906	G01	11	2+5 ADDRESS TO MEMORY
4W20	907	G01	14	2+6 ADDRESS TO MEMORY
4W20	908	G01	17	2+7 ADDRESS TO MEMORY
4W20	90	G01	18	2+8 ADDRESS TO MEMORY
4W20	91	G02	11	2+9 ADDRESS TO MEMORY
4W20	92	G02	14	2+10 ADDRESS TO MEMORY
4W20	93	G02	17	2+11 ADDRESS TO MEMORY
4W20	94	G02	18	2+12 ADDRESS TO MEMORY
4W20	95	G03	11	2+13 ADDRESS TO MEMORY
4W20	96	G03	14	2+14 ADDRESS TO MEMORY
4W20	97	G03	17	2+15 ADDRESS TO MEMORY
4W20	98	G03	18	2+16 ADDRESS TO MEMORY
4W20	904	G04	4	2+3 CHASSIS 4 SELECT (001)
4W20	903	G04	8	
4W20	905	G04	14	2+4
4W39	906	G04	20	WRITE
4W39	905	G04	21	GO
4W20	901	G05	18	2+0 BANK SELECT
4W20	902	G05	22	2+1 BANK SELECT
4W06	98	G05	28	ACCEPT
4W20	900	G13	12	CLOCK
4W05	90	H01	1	2+0
4W03	900	H01	2	2+0 FAN OUT TO MEMORY
4W05	91	H01	8	2+1
4W03	901	H01	19	2+1 FAN OUT TO MEMORY
4W03	902	H01	24	2+2 FAN OUT TO MEMORY
4W05	92	H01	28	2+2
4W05	93	H02	1	2+3
4W03	903	H02	2	2+3 FAN OUT TO MEMORY
4W05	94	H02	8	2+4
4W03	904	H02	19	2+4 FAN OUT TO MEMORY
4W03	905	H02	24	2+5 FAN OUT TO MEMORY
4W05	95	H02	28	2+5
4W05	96	H03	1	2+6
4W03	906	H03	2	2+6 FAN OUT TO MEMORY
4W05	97	H03	8	2+7
4W03	907	H03	19	2+7 FAN OUT TO MEMORY
4W03	90	H03	24	2+8 FAN OUT TO MEMORY
4W05	98	H03	28	2+8
4W05	99	H04	1	2+9
4W03	91	H04	2	2+9 FAN OUT TO MEMORY
4W05	900	H04	8	2+10
4W03	92	H04	19	2+10 FAN OUT TO MEMORY
4W03	93	H04	24	2+11 FAN OUT TO MEMORY
4W05	901	H04	28	2+11
4W05	902	H06	1	2+12
4W03	94	H06	2	2+12 FAN OUT TO MEMORY
4W05	903	H06	8	2+13
4W03	95	H06	19	2+13 FAN OUT TO MEMORY
4W03	96	H06	24	2+14 FAN OUT TO MEMORY
4W05	904	H06	28	2+14
4W39	90	H07	1	2+15
4W04	900	H07	2	2+15 FAN OUT TO MEMORY
4W39	91	H07	8	2+16
4W04	901	H07	19	2+16 FAN OUT TO MEMORY
4W04	902	H07	24	2+17 FAN OUT TO MEMORY
4W39	92	H07	28	2+17
4W39	93	H08	1	2+18
4W04	903	H08	2	2+18 FAN OUT TO MEMORY

4W39	94	H08	8	2*19	
4W04	904	H08	19	2*19	FAN OUT TO MEMORY
4W04	905	H08	24	2*20	FAN OUT TO MEMORY
4W39	95	H08	28	2*20	
4W39	96	H09	1	2*21	
4W04	906	H09	2	2*21	FAN OUT TO MEMORY
4W39	97	H09	8	2*22	
4W04	907	H09	19	2*22	FAN OUT TO MEMORY
4W04	90	H09	24	2*23	FAN OUT TO MEMORY
4W39	98	H09	28	2*23	
4W39	99	H11	1	2*24	
4W04	91	H11	2	2*24	FAN OUT TO MEMORY
4W39	900	H11	8	2*25	
4W04	92	H11	19	2*25	FAN OUT TO MEMORY
4W04	93	H11	24	2*26	FAN OUT TO MEMORY
4W39	901	H11	28	2*26	
4W39	902	H12	1	2*27	
4W04	94	H12	2	2*27	FAN OUT TO MEMORY
4W39	903	H12	8	2*28	
4W04	95	H12	19	2*28	FAN OUT TO MEMORY
4W04	96	H12	24	2*29	FAN OUT TO MEMORY
4W39	904	H12	28	2*29	
4W08	900	H13	1	2*30	
4W09	900	H13	2	2*30	FAN OUT TO MEMORY
4W08	901	H13	8	2*31	
4W09	901	H13	19	2*31	FAN OUT TO MEMORY
4W09	902	H13	24	2*32	FAN OUT TO MEMORY
4W08	902	H13	28	2*32	
4W08	903	H14	1	2*33	
4W09	903	H14	2	2*33	FAN OUT TO MEMORY
4W08	904	H14	8	2*34	
4W09	904	H14	19	2*34	FAN OUT TO MEMORY
4W09	905	H14	24	2*35	FAN OUT TO MEMORY
4W08	905	H14	28	2*35	
4W08	906	H16	1	2*36	
4W09	906	H16	2	2*36	FAN OUT TO MEMORY
4W08	907	H16	8	2*37	
4W09	907	H16	19	2*37	FAN OUT TO MEMORY
4W09	90	H16	24	2*38	FAN OUT TO MEMORY
4W08	90	H16	28	2*38	
4W08	91	H17	1	2*39	
4W09	91	H17	2	2*39	FAN OUT TO MEMORY
4W08	92	H17	8	2*40	
4W09	92	H17	19	2*40	FAN OUT TO MEMORY
4W09	93	H17	24	2*41	FAN OUT TO MEMORY
4W08	93	H17	28	2*41	
4W08	94	H18	1	2*42	
4W09	94	H18	2	2*42	FAN OUT TO MEMORY
4W08	95	H18	8	2*43	
4W09	95	H18	19	2*43	FAN OUT TO MEMORY
4W09	96	H18	24	2*44	FAN OUT TO MEMORY
4W08	96	H18	28	2*44	
4W11	900	H19	1	2*45	
4W10	900	H19	2	2*45	FAN OUT TO MEMORY
4W11	901	H19	8	2*46	
4W10	901	H19	19	2*46	FAN OUT TO MEMORY
4W10	902	H19	24	2*47	FAN OUT TO MEMORY
4W11	902	H19	28	2*47	
4W11	903	H21	1	2*48	
4W10	903	H21	2	2*48	FAN OUT TO MEMORY
4W11	904	H21	8	2*49	
4W10	904	H21	19	2*49	FAN OUT TO MEMORY

4W10	905	H21	24	2*50	FAN OUT TO MEMORY
4W11	905	H21	28	2*50	
4W11	906	H22	1	2*51	
4W10	906	H22	2	2*51	FAN OUT TO MEMORY
4W11	907	H22	8	2*52	
4W10	907	H22	19	2*52	FAN OUT TO MEMORY
4W10	90	H22	24	2*53	FAN OUT TO MEMORY
4W11	90	H22	28	2*53	
4W11	91	H23	1	2*54	
4W10	91	H23	2	2*54	FAN OUT TO MEMORY
4W11	92	H23	8	2*55	
4W10	92	H23	19	2*55	FAN OUT TO MEMORY
4W10	93	H23	24	2*56	FAN OUT TO MEMORY
4W11	93	H23	28	2*56	
4W11	94	H24	1	2*57	
4W10	94	H24	2	2*57	FAN OUT TO MEMORY
4W11	95	H24	8	2*58	
4W10	95	H24	19	2*58	FAN OUT TO MEMORY
4W10	96	H24	24	2*59	FAN OUT TO MEMORY
4W11	96	H24	28	2*59	
4W07	90	I01	1	2*15	CENTRAL TO PERIPHERAL DATA
4W39	90	I01	3	2*15	
4W06	900	I01	5	2*15	
4W12	900	I01	7	2*15	
4W13	900	I01	9	2*15	
4W15	900	I01	11	2*15	
4W16	900	I01	19	2*15	
4W17	900	I01	21	2*15	
4W21	900	I01	23	2*15	
4W14	904	I01	26	2*15	
4W18	900	I01	28	2*15	MEMORY TO REGISTER
4W07	91	I02	1	2*16	CENTRAL TO PERIPHERAL DATA
4W39	91	I02	3	2*16	
4W06	901	I02	5	2*16	
4W12	901	I02	7	2*16	
4W13	901	I02	9	2*16	
4W15	901	I02	11	2*16	
4W16	901	I02	19	2*16	
4W17	901	I02	21	2*16	
4W21	901	I02	23	2*16	
4W14	905	I02	26	2*16	
4W18	901	I02	28	2*16	MEMORY TO REGISTER
4W07	92	I03	1	2*17	CENTRAL TO PERIPHERAL DATA
4W39	92	I03	3	2*17	
4W06	902	I03	5	2*17	
4W12	902	I03	7	2*17	
4W13	902	I03	9	2*17	
4W15	902	I03	11	2*17	
4W16	902	I03	19	2*17	
4W17	902	I03	21	2*17	
4W21	902	I03	23	2*17	
4W14	906	I03	26	2*17	
4W18	902	I03	28	2*17	MEMORY TO REGISTER
4W07	93	I04	1	2*18	CENTRAL TO PERIPHERAL DATA
4W39	93	I04	3	2*18	
4W06	903	I04	5	2*18	
4W12	903	I04	7	2*18	
4W13	903	I04	9	2*18	
4W15	903	I04	11	2*18	
4W16	903	I04	19	2*18	
4W17	903	I04	21	2*18	
4W21	903	I04	23	2*18	

4W14	907	I04	26	2*18	
4W18	903	I04	28	2*18	MEMORY TO REGISTER
4W07	94	I05	1	2*19	CENTRAL TO PERIPHERAL DATA
4W39	94	I05	3	2*19	
4W06	904	I05	5	2*19	
4W12	904	I05	7	2*19	
4W13	904	I05	9	2*19	
4W15	904	I05	11	2*19	
4W16	904	I05	19	2*19	
4W17	904	I05	21	2*19	
4W21	904	I05	23	2*19	
4W14	900	I05	26	2*19	
4W18	904	I05	28	2*19	MEMORY TO REGISTER
4W07	95	I08	1	2*20	CENTRAL TO PERIPHERAL DATA
4W39	95	I08	3	2*20	
4W06	905	I08	5	2*20	
4W12	905	I08	7	2*20	
4W13	905	I08	9	2*20	
4W15	905	I08	11	2*20	
4W16	905	I08	19	2*20	
4W17	905	I08	21	2*20	
4W21	905	I08	23	2*20	
4W14	90	I08	26	2*20	
4W18	905	I08	28	2*20	MEMORY TO REGISTER
4W07	96	I09	1	2*21	CENTRAL TO PERIPHERAL DATA
4W39	96	I09	3	2*21	
4W06	906	I09	5	2*21	
4W12	906	I09	7	2*21	
4W13	906	I09	9	2*21	
4W15	906	I09	11	2*21	
4W16	906	I09	19	2*21	
4W17	906	I09	21	2*21	
4W21	906	I09	23	2*21	
4W14	91	I09	26	2*21	
4W18	906	I09	28	2*21	MEMORY TO REGISTER
4W07	97	I10	1	2*22	CENTRAL TO PERIPHERAL DATA
4W39	97	I10	3	2*22	
4W06	907	I10	5	2*22	
4W12	907	I10	7	2*22	
4W13	907	I10	9	2*22	
4W15	907	I10	11	2*22	
4W16	907	I10	19	2*22	
4W17	907	I10	21	2*22	
4W21	907	I10	23	2*22	
4W14	92	I10	26	2*22	
4W18	907	I10	28	2*22	MEMORY TO REGISTER
4W07	98	I11	1	2*23	CENTRAL TO PERIPHERAL DATA
4W39	98	I11	3	2*23	
4W06	90	I11	5	2*23	
4W12	90	I11	7	2*23	
4W13	90	I11	9	2*23	
4W15	90	I11	11	2*23	
4W16	90	I11	19	2*23	
4W17	90	I11	21	2*23	
4W21	90	I11	23	2*23	
4W14	93	I11	26	2*23	
4W18	90	I11	28	2*23	MEMORY TO REGISTER
4W07	99	I12	1	2*24	CENTRAL TO PERIPHERAL DATA
4W39	99	I12	3	2*24	
4W06	91	I12	5	2*24	
4W12	91	I12	7	2*24	
4W13	91	I12	9	2*24	

4W15	91	I12	11	2*24	
4W16	91	I12	19	2*24	
4W17	91	I12	21	2*24	
4W21	91	I12	23	2*24	
4W14	94	I12	26	2*24	
4W18	91	I12	28	2*24	MEMORY TO REGISTER
4W07	900	I14	1	2*25	CENTRAL TO PERIPHERAL DATA
4W39	900	I14	3	2*25	
4W06	92	I14	5	2*25	
4W12	92	I14	7	2*25	
4W13	92	I14	9	2*25	
4W15	92	I14	11	2*25	
4W16	92	I14	19	2*25	
4W17	92	I14	21	2*25	
4W21	92	I14	23	2*25	
4W14	95	I14	26	2*25	
4W18	92	I14	28	2*25	MEMORY TO REGISTER
4W07	901	I15	1	2*26	CENTRAL TO PERIPHERAL DATA
4W39	901	I15	3	2*26	
4W06	93	I15	5	2*26	
4W12	93	I15	7	2*26	
4W13	93	I15	9	2*26	
4W15	93	I15	11	2*26	
4W16	93	I15	19	2*26	
4W17	93	I15	21	2*26	
4W21	93	I15	23	2*26	
4W14	96	I15	26	2*26	
4W18	93	I15	28	2*26	MEMORY TO REGISTER
4W07	902	I16	1	2*27	CENTRAL TO PERIPHERAL DATA
4W39	902	I16	3	2*27	
4W06	94	I16	5	2*27	
4W12	94	I16	7	2*27	
4W13	94	I16	9	2*27	
4W15	94	I16	11	2*27	
4W16	94	I16	19	2*27	
4W17	94	I16	21	2*27	
4W21	94	I16	23	2*27	
4W14	97	I16	26	2*27	
4W18	94	I16	28	2*27	MEMORY TO REGISTER
4W07	903	I17	1	2*28	CENTRAL TO PERIPHERAL DATA
4W39	903	I17	3	2*28	
4W06	95	I17	5	2*28	
4W12	95	I17	7	2*28	
4W13	95	I17	9	2*28	
4W15	95	I17	11	2*28	
4W16	95	I17	19	2*28	
4W17	95	I17	21	2*28	
4W21	95	I17	23	2*28	
4W14	98	I17	26	2*28	
4W18	95	I17	28	2*28	MEMORY TO REGISTER
4W07	904	I18	1	2*29	CENTRAL TO PERIPHERAL DATA
4W39	904	I18	3	2*29	
4W06	96	I18	5	2*29	
4W12	96	I18	7	2*29	
4W13	96	I18	9	2*29	
4W15	96	I18	11	2*29	
4W16	96	I18	19	2*29	
4W17	96	I18	21	2*29	
4W21	96	I18	23	2*29	
4W14	99	I18	26	2*29	
4W18	96	I18	28	2*29	MEMORY TO REGISTER
4W39	907	I19	12	READ PERIPHERAL	

4W19	901	I20	5	MEMORY GO
4W19	902	I20	7	MEMORY WRITE
4W19	904	I20	24	READ TO PERIPHERAL
4W10	99	I21	3	GO
4W17	97	I21	5	GO
4W11	99	I21	7	READ PERIPHERAL
4W08	99	I21	9	READ PERIPHERAL
4W06	97	I21	11	READ PERIPHERAL
4W39	907	I21	13	READ PERIPHERAL
4W11	97	I21	18	GO
4W08	97	I21	20	GO
4W05	905	I21	22	GO
4W39	905	I21	24	GO
4W16	97	I21	26	GO MEMORY
4W15	97	I21	28	MEMORY GO
4W11	98	I24	3	WRITE
4W08	98	I24	5	WRITE
4W07	905	I24	7	RESUME CENTRAL READ
4W07	906	I24	9	RESUME CENTRAL READ
4W15	98	I24	18	MEMORY WRITE
4W16	98	I24	20	WRITE MEMORY
4W17	98	I24	22	WRITE
4W10	98	I24	24	WRITE
4W05	906	I24	26	WRITE
4W39	906	I24	28	WRITE
4W29	91	J01	5	2*1 READ DATA ANALOG
4W29	90	J01	12	2*0 READ DATA ANALOG
4W29	93	J01	19	2*3 READ DATA ANALOG
4W29	92	J01	26	2*2 READ DATA ANALOG
4W29	95	J02	5	2*5 READ DATA ANALOG
4W29	94	J02	12	2*4 READ DATA ANALOG
4W29	97	J02	19	2*7 READ DATA ANALOG
4W29	96	J02	26	2*6 READ DATA ANALOG
4W29	99	J03	5	2*9 READ DATA ANALOG
4W29	98	J03	12	2*8 READ DATA ANALOG
4W29	901	J03	19	2*11 READ DATA ANALOG
4W29	900	J03	26	2*10 READ DATA ANALOG
4W29	903	J04	5	CLOCK
4W29	902	J04	12	REV. MARK
4W28	98	J05	4	2*8 WRITE DATA
4W28	97	J05	6	2*7 WRITE DATA
4W28	96	J05	8	2*6 WRITE DATA
4W28	901	J05	23	2*11 WRITE DATA
4W28	900	J05	25	2*10 WRITE DATA
4W28	99	J05	27	2*9 WRITE DATA
4W28	92	J06	4	2*2 WRITE DATA
4W28	91	J06	6	2*1 WRITE DATA
4W28	90	J06	8	2*0 WRITE DATA
4W28	95	J06	23	2*5 WRITE DATA
4W28	94	J06	25	2*4 WRITE DATA
4W28	93	J06	27	2*3 WRITE DATA
4W28	904	J07	4	
4W28	903	J07	6	
4W28	902	J07	8	
4W28	905	J07	27	
4W24	906	K01	4	FUNCTION
4W24	904	K01	6	FULL
4W24	903	K01	8	INACTIVE
4W24	902	K01	23	ACTIVE
4W24	907	K01	25	MASTER CLEAR
4W24	905	K01	27	EMPTY
4W24	92	K02	4	2*2 OUTPUT DATA

4W24	91	K02	6	2*1	OUTPUT DATA
4W24	90	K02	8	2*0	OUTPUT DATA
4W24	95	K02	23	2*5	OUTPUT DATA
4W24	94	K02	25	2*4	OUTPUT DATA
4W24	93	K02	27	2*3	OUTPUT DATA
4W24	98	K03	4	2*8	OUTPUT DATA
4W24	97	K03	6	2*7	OUTPUT DATA
4W24	96	K03	8	2*6	OUTPUT DATA
4W24	901	K03	23	2*11	OUTPUT DATA
4W24	900	K03	25	2*10	OUTPUT DATA
4W24	99	K03	27	2*9	OUTPUT DATA
4W26	902	K04	4		ACTIVE
4W26	904	K04	6		FULL
4W26	903	K04	8		INACTIVE
4W26	905	K04	27		EMPTY
4W26	92	K05	4	2*2	INPUT DATA
4W26	91	K05	6	2*1	INPUT DATA
4W26	90	K05	8	2*0	INPUT DATA
4W26	95	K05	23	2*5	INPUT DATA
4W26	94	K05	25	2*4	INPUT DATA
4W26	93	K05	27	2*3	INPUT DATA
4W26	98	K06	4	2*8	INPUT DATA
4W26	97	K06	6	2*7	INPUT DATA
4W26	96	K06	8	2*6	INPUT DATA
4W26	901	K06	23	2*11	INPUT DATA
4W26	900	K06	25	2*10	INPUT DATA
4W26	99	K06	27	2*9	INPUT DATA
4W27	90	L01	5	2*0	OUTPUT DATA
4W27	91	L01	7	2*1	OUTPUT DATA
4W27	92	L01	10	2*2	OUTPUT DATA
4W27	93	L01	21	2*3	OUTPUT DATA
4W27	94	L01	24	2*4	OUTPUT DATA
4W27	95	L01	26	2*5	OUTPUT DATA
4W27	96	L02	5	2*6	OUTPUT DATA
4W27	97	L02	7	2*7	OUTPUT DATA
4W27	98	L02	10	2*8	OUTPUT DATA
4W27	99	L02	21	2*9	OUTPUT DATA
4W27	900	L02	24	2*10	OUTPUT DATA
4W27	901	L02	26	2*11	OUTPUT DATA
4W27	906	L03	5		FUNCTION
4W27	902	L03	7		ACTIVE
4W27	904	L03	10		FULL
4W27	903	L03	21		INACTIVE
4W27	907	L03	24		MASTER CLEAR
4W27	905	L03	26		EMPTY
4W25	903	L07	5		INACTIVE
4W25	904	L07	7		FULL
4W25	96	L08	5	2*6	INPUT DATA
4W25	97	L08	7	2*7	INPUT DATA
4W25	98	L08	10	2*8	INPUT DATA
4W25	99	L08	21	2*9	INPUT DATA
4W25	900	L08	24	2*10	INPUT DATA
4W25	901	L08	26	2*11	INPUT DATA
4W25	90	L09	5	2*0	INPUT DATA
4W25	91	L09	7	2*1	INPUT DATA
4W25	92	L09	10	2*2	INPUT DATA
4W25	93	L09	21	2*3	INPUT DATA
4W25	94	L09	24	2*4	INPUT DATA
4W25	95	L09	26	2*5	INPUT DATA
4W25	906	L26	1		
4W26	906	L26	3		
4W26	907	L26	13		

4W25 907 L26 15

CHASSIS 5

5W03	900	A41	4	2*45	
5W03	901	A41	6	2*46	
5W03	902	A41	7	2*47	
5W03	903	A41	22	2*48	
5W03	904	A41	25	2*49	
5W03	905	A41	27	2*50	
5W03	906	A42	4	2*51	
5W03	907	A42	6	2*52	
5W03	90	A42	7	2*53	
5W03	91	A42	22	2*54	
5W03	92	A42	25	2*55	
5W03	93	A42	27	2*56	
5W03	94	B41	4	2*57	
5W03	95	B41	6	2*58	
5W03	96	B41	7	2*59	
5W04	900	B41	22	2*30	BUFFER TO CONTROL
5W04	901	B41	25	2*31	BUFFER TO CONTROL
5W04	902	B41	27	2*32	BUFFER TO CONTROL
5W04	903	B42	4	2*33	BUFFER TO CONTROL
5W04	904	B42	6	2*34	BUFFER TO CONTROL
5W04	905	B42	7	2*35	BUFFER TO CONTROL
5W04	906	B42	22	2*36	BUFFER TO CONTROL
5W04	907	B42	25	2*37	BUFFER TO CONTROL
5W04	90	B42	27	2*38	BUFFER TO CONTROL
5W04	91	C41	4	2*39	BUFFER TO CONTROL
5W04	92	C41	6	2*40	BUFFER TO CONTROL
5W04	93	C41	7	2*41	BUFFER TO CONTROL
5W04	94	C41	22	2*42	BUFFER TO CONTROL
5W04	95	C41	25	2*43	BUFFER TO CONTROL
5W04	96	C41	27	2*44	BUFFER TO CONTROL
5W01	904	C42	4	2*15	
5W01	905	C42	6	2*16	
5W01	906	C42	7	2*17	
5W01	907	C42	22	2*18	
5W01	900	C42	25	2*19	
5W01	90	C42	27	2*20	
5W01	91	D41	4	2*21	
5W01	92	D41	6	2*22	
5W01	93	D41	7	2*23	
5W01	94	D41	22	2*24	
5W01	95	D41	25	2*25	
5W01	96	D41	27	2*26	
5W01	97	D42	4	2*27	
5W01	98	D42	6	2*28	
5W01	99	D42	7	2*29	
5W02	904	D42	22	2*0	
5W02	905	D42	25	2*1	
5W02	906	D42	27	2*2	
5W13	98	E38	4	TRANSFER	
5W13	97	E38	6	COMPLEMENT	
5W13	93	E38	8	SUM	
5W13	94	E38	10	DIFFERENCE	
5W13	95	E38	12	PRODUCT	
5W12	97	E38	19	SHIFT (JK)	
5W12	99	E38	21	SHIFT LEFT	
5W12	98	E38	23	SHIFT NOMINAL	
5W12	908	E39	4	NORM. (SHIFT)	
5W12	900	E39	6	ROUND SHIFT	
5W12	95	E39	8	PACK	
5W12	96	E39	10	UN-PACK	

5W12	92	E39	12	MASK			
5W12	94	E39	19	ADD			
5W12	93	E39	21	ROUND ADD			
5W11	92	E39	23	ADD PLUS			
5W12	905	E39	27	LONG ADD - PLUS			
5W14	91	E40	4	ROUND NO. 1			
5W14	901	E40	6	ROUND			
5W14	92	E40	8	DOUBLE NO. 1			
5W14	902	E40	10	DOUBLE			
5W13	91	E40	12	ROUND			
5W13	96	E40	19	POP. COUNT			
5W13	902	E40	21	GO BOOLEAN			
5W12	907	E40	23	SHIFT IN			
5W12	91	E40	25	ADD IN			
5W12	90	E40	27	LONG ADD IN			
5W02	907	E41	4	2*3			
5W02	900	E41	6	2*4			
5W02	90	E41	7	2*5			
5W02	91	E41	22	2*6			
5W02	92	E41	25	2*7			
5W02	93	E41	27	2*8			
5W02	94	E42	4	2*9			
5W02	95	E42	6	2*10			
5W02	96	E42	7	2*11			
5W02	97	E42	22	2*12			
5W02	98	E42	25	2*13			
5W02	99	E42	27	2*14			
5W08	900	F41	4	BIT 0			
5W08	901	F41	6	BIT 1			
5W08	902	F41	8	BIT 2			
5W08	903	F41	10	BIT 3			
5W08	904	F41	12	BIT 4			
5W08	905	F41	19	BIT 5			
5W08	906	F41	21	BIT 6			
5W08	907	F41	23	BIT 7			
5W08	908	F41	25	BIT 8			
5W08	90	F41	27	BIT 9			
5W08	91	F42	4	BIT 10			
5W08	92	F42	6	BIT 11			
5W08	93	F42	8	BIT 12			
5W08	94	F42	10	BIT 13			
5W08	95	F42	12	BIT 14			
5W08	96	F42	19	BIT 15			
5W08	97	F42	21	BIT 16			
5W08	98	F42	23	BIT 17			
5W11	90	F42	25	INCR SIGN			
5W18	901	F42	27	MEMORY GO			
5W09	90	G41	4	2*0 CENTRAL PROGRAM ADDRESS			
5W09	91	G41	6	2*1 CENTRAL PROGRAM ADDRESS			
5W09	92	G41	8	2*2 CENTRAL PROGRAM ADDRESS			
5W09	93	G41	10	2*3 CENTRAL PROGRAM ADDRESS			
5W09	94	G41	12	2*4 CENTRAL PROGRAM ADDRESS			
5W09	95	G41	19	2*5 CENTRAL PROGRAM ADDRESS			
5W09	96	G41	21	2*6 CENTRAL PROGRAM ADDRESS			
5W09	97	G41	23	2*7 CENTRAL PROGRAM ADDRESS			
5W09	98	G41	25	2*8 CENTRAL PROGRAM ADDRESS			
5W09	99	G41	27	2*9 CENTRAL PROGRAM ADDRESS			
5W09	900	G42	4	2*10 CENTRAL PROGRAM ADDRESS			
5W09	901	G42	6	2*11 CENTRAL PROGRAM ADDRESS			
5W09	902	G42	8	2*12 CENTRAL PROGRAM ADDRESS			
5W09	903	G42	10	2*13 CENTRAL PROGRAM ADDRESS			
5W09	904	G42	12	2*14 CENTRAL PROGRAM ADDRESS			

5W09	905	G42	19	2*15	CENTRAL PROGRAM ADDRESS		
5W09	906	G42	21	2*16	CENTRAL PROGRAM ADDRESS		
5W09	907	G42	23	2*17	CENTRAL PROGRAM ADDRESS		
5W22	903	H38	1	MULTIPLY TO X		0	
5W15	902	H38	3	MULTIPLY TO X	0		
5W22	902	H38	5	MULTIPLY TO X		GO	
5W15	901	H38	7	MULTIPLY TO X	GO		
5W22	905	H38	22	MULTIPLY TO X		2	
5W15	904	H38	24	MULTIPLY TO X	2		
5W22	904	H38	26	MULTIPLY TO X		1	
5W15	903	H38	28	MULTIPLY TO X	1		
5W22	907	H39	1	ADD TO X		0	
5W15	906	H39	3	ADD TO X	0		
5W22	906	H39	5	ADD TO X		GO	
5W15	905	H39	7	ADD TO X	GO		
5W22	90	H39	22	ADD TO X		2	
5W15	98	H39	24	ADD TO X	2		
5W20	93	H39	26	ADD TO X		1	
5W15	907	H39	28	ADD TO X	1		
5W22	92	H40	1	INCREMENT TO X		0	
5W17	95	H40	3	INCR TO X	0		
5W22	91	H40	5	INCREMENT TO X		GO	
5W17	94	H40	7	INCR TO X	GO		
5W22	94	H40	22	INCREMENT TO X		2	
5W17	97	H40	24	INCR TO X	2		
5W22	93	H40	26	INCREMENT TO X		1	
5W17	96	H40	28	INCR TO X	1		
5W22	96	H41	1	INCREMENT TO B		0	
5W22	95	H41	5	INCREMENT TO B		GO	
5W22	98	H41	22	INCREMENT TO B		2	
5W22	97	H41	26	INCREMENT TO B		1	
5W16	901	H42	1	INCREMENT TO A		0	
5W16	900	H42	5	INCREMENT TO A		GO	
5W16	903	H42	22	INCREMENT TO A		2	
5W16	902	H42	26	INCREMENT TO A		1	
5W23	902	I22	12	CLOCK			
5W27	94	I38	4	2*34			
5W27	93	I38	6	2*33			
5W27	92	I38	8	2*32			
5W27	91	I38	10	2*31			
5W27	90	I38	12	2*30			
5W10	904	I38	19	STORE CONTROL BIT	2*39		
5W10	903	I38	21	STORE CONTROL BIT	2*38		
5W10	902	I38	23	STORE CONTROL BIT	2*37		
5W10	901	I38	25	STORE CONTROL BIT	2*36		
5W27	95	I38	27	2*35			
5W10	90	I39	4	STORE CONTROL BIT	2*44		
5W10	900	I39	6	STORE CONTROL BIT	2*43		
5W10	907	I39	8	STORE CONTROL BIT	2*42		
5W10	906	I39	10	STORE CONTROL BIT	2*41		
5W10	905	I39	12	STORE CONTROL BIT	2*40		
5W10	95	I39	19	STORE CONTROL BIT	2*49		
5W10	94	I39	21	STORE CONTROL BIT	2*48		
5W10	93	I39	23	STORE CONTROL BIT	2*47		
5W10	92	I39	25	STORE CONTROL BIT	2*46		
5W10	91	I39	27	STORE CONTROL BIT	2*45		
5W10	96	I40	4	STORE CONTROL BIT	2*50		
5W27	96	I40	19	2*56			
5W10	98	I40	23	STORE CONTROL BIT	2*52		
5W10	99	I40	23	STORE CONTROL BIT	2*53		
5W10	97	I40	27	STORE CONTROL BIT	2*51		
5W16	905	I41	1	SHIFT TO B		0	

5W16	904	I41	5	SHIFT TO B	GO
5W16	907	I41	22	SHIFT TO B	2
5W16	906	I41	26	SHIFT TO B	1
5W16	90	I42	1	MEMORY TO D	0
5W15	91	I42	3	MEMORY TO D	0
5W20	94	I42	5	MEM. TO D	GO
5W15	90	I42	7	MEMORY TO D	GO
5W16	92	I42	22	MEMORY TO D	2
5W15	93	I42	24	MEMORY TO D	2
5W16	91	I42	26	MEMORY TO D	1
5W15	92	I42	28	MEMORY TO D	1
5W12	904	J22	19	REQUEST RELEASE SHIFT	
5W13	906	J23	19	BOOLEAN REQUEST RELEASE	
5W13	905	J24	19	DIVIDE REQUEST RELEASE	
5W14	95	J25	19	REQUEST RELEASE	
5W22	901	J39	4	CLOCK	
5W28	900	J39	6	CLOCK	
5W27	901	J39	8	CLOCK	
5W29	900	J39	23	CLOCK	
5W15	900	J39	25	CLOCK	
5W14	96	J39	27	CLOCK	
5W14	90	J40	4	GO UNIT NO. 1.	
5W14	900	J40	6	GO UNIT NO. 2	
5W13	903	J40	8	GO DIVIDE	
5W13	99	J40	10	TRANSMIT BCOLEAN	
5W12	902	J40	12	TRANSMIT SHIFT	
5W12	903	J40	19	TRANSMIT ADD	
5W12	901	J40	21	TRANSMIT LONG ADD	
5W14	93	J40	23	XMIT RESULT NO. 1	
5W14	94	J40	25	XMIT RESULT NO. 2	
5W13	900	J40	27	TRANSMIT DIVIDE	
5W16	97	J41	1	J TO MULTIPLY (2)	0
5W17	901	J41	3	J TO MULTIPLY 0	
5W22	99	J41	5	XJ TO MULTIPLY (2)	GO
5W17	900	J41	7	J TO MULTIPLY	GO
5W16	99	J41	22	J TO MULTIPLY (2)	2
5W17	903	J41	24	J TO MULTIPLY 2	
5W16	98	J41	26	J TO MULTIPLY (2)	1
5W17	902	J41	28	J TO MULTIPLY 1	
5W16	94	J42	1	K TO MULTIPLY (1)	0
5W15	95	J42	3	K TO MULTIPLY 0	
5W16	93	J42	5	K TO MULTIPLY (1)	GO
5W15	94	J42	7	K TO MULTIPLY 0	
5W16	96	J42	22	K TO MULTIPLY (1)	2
5W15	97	J42	24	K TO MULTIPLY 2	
5W16	95	J42	26	K TO MULTIPLY (1)	1
5W15	96	J42	28	K TO MULTIPLY 1	
5W14	905	K21	19	REQUEST RELEASE	
5W12	906	K22	19	REQUEST RELEASE LONG ADD	
5W26	98	K23	19	REQUEST RELEASE ADD	
5W18	904	K38	4	READ TO PERIPHERAL	
5W18	902	K38	6	MEMORY WRITE	
5W27	902	K38	23	STORE CONTROL	
5W27	903	K38	25	STORE REGISTERS	
5W27	904	K38	27	STORE PERIPHERAL	
5W19	90	K40	1	X TO INCREMENT	0
5W20	95	K40	5	X TO INCR.	GO
5W19	92	K40	22	X TO INCREMENT	2
5W19	91	K40	26	X TO INCREMENT	1
5W19	901	K41	1	XJ TO ADD (1)	0
5W17	905	K41	3	X TO ADD	0
5W19	900	K41	5	XJ TO ADD (1)	GO

5W17	904	K41	7	X TO ADD	GO	
5W19	903	K41	22	XJ TO ADD (1)		2
5W17	907	K41	24	X TO ADD	2	
5W19	902	K41	26	XJ TO ADD (1)		1
5W17	906	K41	28	X TO ADD	1	
5W19	905	K42	1	XL TO ADD (2)		0
5W17	90	K42	3	X TO ADD	0	
5W19	904	K42	5	XL TO ADD (2)		GO
5W17	93	K42	7	X TO ADD	GO	
5W19	907	K42	22	XL TO ADD (2)		2
5W17	92	K42	24	X TO ADD	2	
5W19	906	K42	26	XL TO ADD (2)		1
5W17	91	K42	28	X TO ADD	1	
5W20	901	L41	1	BJ TO INCREMENT		0
5W20	900	L41	5	BJ TO INCREMENT		GO
5W20	903	L41	22	BJ TO INCREMENT		2
5W20	902	L41	26	BJ TO INCREMENT		1
5W20	905	L42	1	BK TO INCREMENT		0
5W20	904	L42	5	BK TO INCREMENT		GO
5W20	907	L42	22	BK TO INCREMENT		2
5W20	906	L42	26	BK TO INCREMENT		1
5W32	900	M32	4	CLOCK		
5W31	900	M32	6	CLOCK		
5W30	900	M32	8	CLOCK		
5W35	900	M32	23	CLOCK		
5W34	900	M32	25	CLOCK		
5W33	900	M32	27	CLOCK		
5W19	94	M41	1	B TO ADD (1)		0
5W19	93	M41	5	B TO ADD (1)		GO
5W19	96	M41	22	B TO ADD (1)		2
5W19	95	M41	26	B TO ADD (1)		1
5W20	90	M42	1	A TO INCREMENT		0
5W20	96	M42	5	A TO INCR.		GO
5W20	92	M42	22	A TO INCREMENT		2
5W20	91	M42	26	A TO INCREMENT		1
5W27	906	N23	7	EXP = 3777 ERROR		
5W27	905	N23	10	EXP = 1777 ERROR		
5W24	901	N40	5	BIT 0		
5W24	902	N40	7	BIT 1		
5W24	903	N40	10	BIT 2		
5W24	904	N40	21	BIT 3		
5W24	905	N40	24	BIT 4		
5W24	906	N40	26	BIT 5		
5W24	907	N41	5	BIT 6		
5W24	908	N41	7	BIT 7		
5W24	900	N41	10	BIT 8		
5W24	91	N41	21	BIT 9		
5W24	92	N41	24	BIT 10		
5W24	93	N41	26	BIT 11		
5W24	94	N42	5	BIT 12		
5W24	95	N42	7	BIT 13		
5W24	96	N42	10	BIT 14		
5W24	97	N42	21	BIT 15		
5W24	98	N42	24	BIT 16		
5W24	99	N42	26	BIT 17		
5W37	901	022	3	INCREMENT OPERAND	K BIT	2*1
5W36	901	022	7	INCREMENT OPERAND	J BIT	2*1
5W36	900	022	23	INCREMENT OPERAND	J BIT	2*0
5W37	900	022	27	INCREMENT OPERAND	K BIT	2*0
5W37	903	023	3	INCREMENT OPERAND	K BIT	2*3
5W36	903	023	7	INCREMENT OPERAND	J BIT	2*3
5W36	902	023	23	INCREMENT OPERAND	J BIT	2*2

5W37	902	023	27	INCREMENT	OPERAND	K	BIT	2*2
5W37	905	024	3	INCREMENT	OPERAND	K	BIT	2*5
5W36	905	024	7	INCREMENT	OPERAND	J	BIT	2*5
5W36	904	024	23	INCREMENT	OPERAND	J	BIT	2*4
5W37	904	024	27	INCREMENT	OPERAND	K	BIT	2*4
5W37	907	025	3	INCREMENT	OPERAND	K	BIT	2*7
5W36	907	025	7	INCREMENT	OPERAND	J	BIT	2*7
5W36	906	025	23	INCREMENT	OPERAND	J	BIT	2*6
5W37	906	025	27	INCREMENT	OPERAND	K	BIT	2*6
5W37	90	026	3	INCREMENT	OPERAND	K	BIT	2*9
5W36	90	026	7	INCREMENT	OPERAND	J	BIT	2*9
5W36	908	026	23	INCREMENT	OPERAND	J	BIT	2*8
5W37	908	026	27	INCREMENT	OPERAND	K	BIT	2*8
5W37	92	027	3	INCREMENT	OPERAND	K	BIT	2*11
5W36	92	027	7	INCREMENT	OPERAND	J	BIT	2*11
5W36	91	027	23	INCREMENT	OPERAND	J	BIT	2*10
5W37	91	027	27	INCREMENT	OPERAND	K	BIT	2*10
5W37	94	028	3	INCREMENT	OPERAND	K	BIT	2*13
5W36	94	028	7	INCREMENT	OPERAND	J	BIT	2*13
5W36	93	028	23	INCREMENT	OPERAND	J	BIT	2*12
5W37	93	028	27	INCREMENT	OPERAND	K	BIT	2*12
5W37	96	029	3	INCREMENT	OPERAND	K	BIT	2*15
5W36	96	029	7	INCREMENT	OPERAND	J	BIT	2*15
5W36	95	029	23	INCREMENT	OPERAND	J	BIT	2*14
5W37	95	029	27	INCREMENT	OPERAND	K	BIT	2*14
5W37	97	030	3					
5W36	97	030	7					
5W36	98	030	24					
5W37	98	030	25					
5W26	905	031	3	X TO MEMORY		0		
5W25	905	031	5					
5W25	904	031	6					
5W26	904	031	8					
5W25	907	031	23					
5W26	907	031	25					
5W25	906	031	26	X TO MEMORY		1		
5W26	906	031	28	X TO MEMORY		1		
5W25	93	033	6					
5W26	91	033	8					
5W25	98	034	4					
5W25	903	034	6					
5W25	902	034	8					
5W25	901	034	10					
5W25	900	034	12					
5W26	94	035	4	ENTER (JK)		2*2		
5W26	93	035	6	ENTER (JK)		2*1		
5W26	92	035	8	ENTER (JK)		2*0		
5W13	907	035	10	TRANSMIT MUIT		1		
5W13	908	035	12	TRANSMIT MUIT		2		
5W26	97	035	23	ENTER (JK)		2*5		
5W26	96	035	25	ENTER (JK)		2*4		
5W26	95	035	27	ENTER (JK)		2*3		
5W23	900	040	2	EXCHANGE RESUME		OUT		
5W21	902	040	15	EXCHANGE GO		(PERIPH		
5W29	99	040	17	ACCEPT				
5W21	900	041	18	READ/GO		(PERIPH)		
5W21	901	041	20	WRITE GO		(PERIPH)		
5W26	99	042	5	LONG ADD SER		CONTROL		
5W17	99	042	7	LONG ADD SIGN		CH 5		
5W17	98	042	10	LONG ADD ZERO		CH 5		
5W11	901	042	21	LONG ADD INFINITE				
5W11	900	042	24	LONG ADD INDEFINITE				

5W23	901	042	26	MC	
5W28	902	Q34	3	2*1	BANK SELECT
5W29	902	Q34	5	2*1	BANK SELECT
5W28	901	Q34	7	2*0	BANK SELECT
5W29	901	Q34	9	2*0	BANK SELECT
5W30	901	Q34	11	2*0	BANK SELECT
5W31	901	Q34	13	2*0	BANK SELECT
5W28	903	Q34	18	2*2	CHASSIS 4 SELECT (001)
5W29	903	Q34	20	2*2	CHASSIS 3 SELECT(000)
5W30	903	Q34	22	2*2	CHASSIS 9 SELECT (010)
5W31	903	Q34	24	2*2	CHASSIS 10 SELECT (011)
5W30	902	Q34	26	2*1	BANK SELECT
5W31	902	Q34	28	2*1	BANK SELECT
5W28	905	Q35	3	2*4	CHASSIS 4 SELECT (001)
5W29	905	Q35	5	2*4	CHASSIS 3 SELECT (000)
5W28	904	Q35	7	2*3	CHASSIS 4 SELECT (001)
5W29	904	Q35	9	2*3	CHASSIS 3 SELECT (000)
5W30	904	Q35	11	2*3	CHASSIS 9 SELECT (010)
5W31	904	Q35	13	2*3	CHASSIS 10 SELECT (011)
5W28	906	Q35	18	2*5	ADDRESS TO MEMORY
5W29	906	Q35	20	2*5	ADDRESS TO MEMORY
5W30	906	Q35	22	2*5	ADDRESS TO MEMORY
5W31	906	Q35	24	2*5	ADDRESS TO MEMORY
5W30	905	Q35	26	2*4	CHASSIS 9 SELECT (010)
5W31	905	Q35	28	2*4	CHASSIS 10 SELECT (011)
5W28	908	Q36	3	2*7	ADDRESS TO MEMORY
5W29	908	Q36	5	2*7	ADDRESS TO MEMORY
5W28	907	Q36	7	2*6	ADDRESS TO MEMORY
5W29	907	Q36	9	2*6	ADDRESS TO MEMORY
5W30	907	Q36	11	2*6	ADDRESS TO MEMORY
5W31	907	Q36	13	2*6	ADDRESS TO MEMORY
5W28	90	Q36	18	2*8	ADDRESS TO MEMORY
5W29	90	Q36	20	2*8	ADDRESS TO MEMORY
5W30	90	Q36	22	2*8	ADDRESS TO MEMORY
5W31	90	Q36	24	2*8	ADDRESS TO MEMORY
5W30	908	Q36	26	2*7	ADDRESS TO MEMORY
5W31	908	Q36	28	2*7	ADDRESS TO MEMORY
5W28	92	Q37	3	2*10	ADDRESS TO MEMORY
5W29	92	Q37	5	2*10	ADDRESS TO MEMORY
5W28	91	Q37	7	2*9	ADDRESS TO MEMORY
5W29	91	Q37	9	2*9	ADDRESS TO MEMORY
5W30	91	Q37	11	2*9	ADDRESS TO MEMORY
5W31	91	Q37	13	2*9	ADDRESS TO MEMORY
5W28	93	Q37	18	2*11	ADDRESS TO MEMORY
5W29	93	Q37	20	2*11	ADDRESS TO MEMORY
5W30	93	Q37	22	2*11	ADDRESS TO MEMORY
5W31	93	Q37	24	2*11	ADDRESS TO MEMORY
5W30	92	Q37	26	2*10	ADDRESS TO MEMORY
5W31	92	Q37	28	2*10	ADDRESS TO MEMORY
5W28	95	Q38	3	2*13	ADDRESS TO MEMORY
5W29	95	Q38	5	2*13	ADDRESS TO MEMORY
5W28	94	Q38	7	2*12	ADDRESS TO MEMORY
5W29	94	Q38	9	2*12	ADDRESS TO MEMORY
5W30	94	Q38	11	2*12	ADDRESS TO MEMORY
5W31	94	Q38	13	2*12	ADDRESS TO MEMORY
5W28	96	Q38	18	2*14	ADDRESS TO MEMORY
5W29	96	Q38	20	2*14	ADDRESS TO MEMORY
5W30	96	Q38	22	2*14	ADDRESS TO MEMORY
5W31	96	Q38	24	2*14	ADDRESS TO MEMORY
5W30	95	Q38	26	2*13	ADDRESS TO MEMORY
5W31	95	Q38	28	2*13	ADDRESS TO MEMORY
5W28	98	Q39	3	2*16	ADDRESS TO MEMORY

5W29	98	Q39	5	2+16	
5W28	97	Q39	7	2+15	ADDRESS TO MEMORY
5W29	97	Q39	9	2+15	ADDRESS TO MEMORY
5W30	97	Q39	11	2+15	ADDRESS TO MEMORY
5W31	97	Q39	13	2+15	ADDRESS TO MEMORY
5W30	98	Q39	26	2+16	ADDRESS TO MEMORY
5W31	98	Q39	28	2+16	ADDRESS TO MEMORY
5W32	902	Q40	3	2+1	BANK SELECT
5W33	902	Q40	5	2+1	BANK SELECT
5W32	901	Q40	7	2+0	BANK SELECT
5W33	901	Q40	9	2+0	BANK SELECT
5W34	901	Q40	11	2+0	BANK SELECT
5W35	901	Q40	13	2+0	BANK SELECT
5W32	903	Q40	18	2+2	CHASSIS 13 SELECT (100)
5W33	903	Q40	20	2+2	CHASSIS 14 SELECT (101)
5W34	903	Q40	22	2+2	CHASSIS 15 SELECT (110)
5W35	903	Q40	24	2+2	CHASSIS 16 SELECT (111)
5W34	902	Q40	26	2+1	BANK SELECT
5W35	902	Q40	28	2+1	BANK SELECT
5W32	905	Q41	3	2+4	CHASSIS 13 SELECT (100)
5W33	905	Q41	5	2+4	CHASSIS 14 SELECT (101)
5W32	904	Q41	7	2+3	CHASSIS 13 SELECT (100)
5W33	904	Q41	9	2+3	CHASSIS 14 SELECT (101)
5W34	904	Q41	11	2+3	CHASSIS 15 SELECT (110)
5W35	904	Q41	13	2+3	CHASSIS 16 SELECT (111)
5W32	906	Q41	18	2+5	ADDRESS TO MEMORY
5W33	906	Q41	20	2+5	ADDRESS TO MEMORY
5W34	906	Q41	22	2+5	ADDRESS TO MEMORY
5W35	906	Q41	24	2+5	ADDRESS TO MEMORY
5W34	905	Q41	26	2+4	CHASSIS 15 SELECT (110)
5W35	905	Q41	28	2+4	CHASSIS 16 SELECT (111)
5W32	908	Q42	3	2+7	ADDRESS TO MEMORY
5W33	907	Q42	5	2+6	ADDRESS TO MEMORY
5W32	907	Q42	7	2+6	ADDRESS TO MEMORY
5W33	908	Q42	9	2+7	ADDRESS TO MEMORY
5W34	907	Q42	11	2+6	ADDRESS TO MEMORY
5W35	907	Q42	13	2+6	ADDRESS TO MEMORY
5W32	90	Q42	18	2+8	ADDRESS TO MEMORY
5W33	90	Q42	20	2+8	ADDRESS TO MEMORY
5W34	90	Q42	22	2+8	ADDRESS TO MEMORY
5W35	90	Q42	24	2+8	ADDRESS TO MEMORY
5W34	908	Q42	26	2+7	ADDRESS TO MEMORY
5W35	908	Q42	28	2+7	ADDRESS TO MEMORY
5W32	92	R40	3	2+10	ADDRESS TO MEMORY
5W33	92	R40	5	2+10	ADDRESS TO MEMORY
5W32	91	R40	7	2+9	ADDRESS TO MEMORY
5W33	91	R40	9	2+9	ADDRESS TO MEMORY
5W34	91	R40	11	2+9	ADDRESS TO MEMORY
5W35	91	R40	13	2+9	ADDRESS TO MEMORY
5W32	93	R40	18	2+11	ADDRESS TO MEMORY
5W33	93	R40	20	2+11	ADDRESS TO MEMORY
5W34	93	R40	22	2+11	ADDRESS TO MEMORY
5W35	93	R40	24	2+11	ADDRESS TO MEMORY
5W34	92	R40	26	2+10	ADDRESS TO MEMORY
5W35	92	R40	28	2+10	ADDRESS TO MEMORY
5W32	95	R41	3	2+13	ADDRESS TO MEMORY
5W33	95	R41	5	2+13	ADDRESS TO MEMORY
5W32	94	R41	7	2+12	ADDRESS TO MEMORY
5W33	94	R41	9	2+12	ADDRESS TO MEMORY
5W34	94	R41	11	2+12	ADDRESS TO MEMORY
5W35	94	R41	13	2+12	ADDRESS TO MEMORY
5W32	96	R41	18	2+14	ADDRESS TO MEMORY

5W33	96	R41	20	2+14	ADDRESS	TO	MEMORY
5W34	96	R41	22	2+14	ADDRESS	TO	MEMORY
5W35	96	R41	24	2+14	ADDRESS	TO	MEMORY
5W34	95	R41	26	2+13	ADDRESS	TO	MEMORY
5W35	95	R41	28	2+13	ADDRESS	TO	MEMORY
5W32	98	R42	3	2+16	ADDRESS	TO	MEMORY
5W33	98	R42	5	2+16	ADDRESS	TO	MEMORY
5W32	97	R42	7	2+15	ADDRESS	TO	MEMORY
5W33	97	R42	9	2+15	ADDRESS	TO	MEMORY
5W34	97	R42	11	2+15	ADDRESS	TO	MEMORY
5W35	97	R42	13	2+15	ADDRESS	TO	MEMORY
5W34	98	R42	26	2+16	ADDRESS	TO	MEMORY
5W35	98	R42	28	2+16	ADDRESS	TO	MEMORY

CHASSIS 6

6W10	90	E01	1	2*0	MULTIPLY TO REGISTER
6W10	91	E01	5	2*1	MULTIPLY TO REGISTER
6W10	92	E01	25	2*2	MULTIPLY TO REGISTER
6W10	93	E01	27	2*3	MULTIPLY TO REGISTER
6W10	94	E02	1	2*4	MULTIPLY TO REGISTER
6W10	95	E02	5	2*5	MULTIPLY TO REGISTER
6W10	96	E02	25	2*6	MULTIPLY TO REGISTER
6W10	97	E02	27	2*7	MULTIPLY TO REGISTER
6W10	98	E03	1	2*8	MULTIPLY TO REGISTER
6W10	99	E03	5	2*9	MULTIPLY TO REGISTER
6W10	900	E03	25	2*10	MULTIPLY TO REGISTER
6W10	901	E03	27	2*11	MULTIPLY TO REGISTER
6W10	902	E04	1	2*12	MULTIPLY TO REGISTER
6W10	903	E04	5	2*13	MULTIPLY TO REGISTER
6W10	904	E04	25	2*14	MULTIPLY TO REGISTER
6W10	905	E04	27	2*15	MULTIPLY TO REGISTER
6W10	906	E05	1	2*16	MULTIPLY TO REGISTER
6W10	907	E05	5	2*17	MULTIPLY TO REGISTER
6W11	90	E05	25	2*18	MULTIPLY TO REGISTER
6W11	91	E05	27	2*19	MULTIPLY TO REGISTER
6W11	92	E06	1	2*20	MULTIPLY TO REGISTER
6W11	93	E06	5	2*21	MULTIPLY TO REGISTER
6W11	94	E06	25	2*22	MULTIPLY TO REGISTER
6W11	95	E06	27	2*23	MULTIPLY TO REGISTER
6W11	96	E07	1	2*24	MULTIPLY TO REGISTER
6W11	97	E07	5	2*25	MULTIPLY TO REGISTER
6W11	98	E07	25	2*26	MULTIPLY TO REGISTER
6W11	99	E07	27	2*27	MULTIPLY TO REGISTER
6W11	900	F01	1	2*28	MULTIPLY TO REGISTER
6W11	901	F01	5	2*29	MULTIPLY TO REGISTER
6W11	902	F01	25	2*30	MULTIPLY TO REGISTER
6W11	903	F01	27	2*31	MULTIPLY TO REGISTER
6W11	904	F02	1	2*32	MULTIPLY TO REGISTER
6W11	905	F02	5	2*33	MULTIPLY TO REGISTER
6W11	906	F02	25	2*34	MULTIPLY TO REGISTER
6W11	907	F02	27	2*35	MULTIPLY TO REGISTER
6W08	90	F03	1	2*36	MULTIPLY TO REGISTER
6W08	91	F03	5	2*37	MULTIPLY TO REGISTER
6W08	92	F03	25	2*38	MULTIPLY TO REGISTER
6W08	93	F03	27	2*39	MULTIPLY TO REGISTER
6W08	94	F04	1	2*40	MULTIPLY TO REGISTER
6W08	95	F04	5	2*41	MULTIPLY TO REGISTER
6W08	96	F04	25	2*42	MULTIPLY TO REGISTER
6W08	97	F04	27	2*43	MULTIPLY TO REGISTER
6W08	98	F05	1	2*44	MULTIPLY TO REGISTER
6W08	99	F05	5	2*45	MULTIPLY TO REGISTER
6W08	900	F05	25	2*46	MULTIPLY TO REGISTER
6W08	901	F05	27	2*47	MULTIPLY TO REGISTER
6W22	902	F06	1	REDUCE MULT. 2	
6W09	902	F06	15	2*59 REGISTER TO MULTIPLY, OPERAND (XJ)	
6W21	908	F07	1	REDUCE MULT. 1	
6W07	902	F07	15	2*59 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	90	G01	12	2*0 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	91	G01	14	2*1 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	92	G01	16	2*2 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	93	G01	18	2*3 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	94	G02	12	2*4 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	95	G02	14	2*5 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	96	G02	16	2*6 REGISTER TO MULTIPLY, OPERAND 1(XK)	
6W12	97	G02	18	2*7 REGISTER TO MULTIPLY, OPERAND 1(XK)	

6W12	98	G03	12	2*8	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	99	G03	14	2*9	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	900	G03	16	2*10	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	901	G03	18	2*11	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	902	G04	12	2*12	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	903	G04	14	2*13	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	904	G04	16	2*14	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	905	G04	18	2*15	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	906	G05	12	2*16	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12	907	G05	14	2*17	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	90	G05	16	2*18	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	91	G05	18	2*19	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	92	G06	12	2*20	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	93	G06	14	2*21	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	94	G06	16	2*22	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	95	G06	18	2*23	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	96	H01	12	2*24	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	97	H01	14	2*25	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	98	H01	16	2*26	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	99	H01	18	2*27	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	900	H02	12	2*28	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	901	H02	14	2*29	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	902	H02	16	2*30	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	903	H02	18	2*31	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	904	H03	12	2*32	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	905	H03	14	2*33	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	906	H03	16	2*34	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15	907	H03	18	2*35	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	90	H04	12		
6W07	91	H04	14		
6W07	92	H04	16		
6W07	93	H04	18		
6W07	94	H05	12	2*40	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	95	H05	14	2*41	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	96	H05	16	2*42	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	97	H05	18	2*43	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	98	H06	12	2*44	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	99	H06	14	2*45	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	900	H06	16	2*46	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07	901	H06	18	2*47	REGISTER TO MULTIPLY, OPERAND 1(XK)
6W16	95	H09	1		REQUEST RELEASE
6W22	905	H10	1		GO MULT 1
6W14	90	I01	12	2*0	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	91	I01	14	2*1	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	92	I01	16	2*2	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	93	I01	18	2*3	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	94	I02	12	2*4	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	95	I02	14	2*5	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	96	I02	16	2*6	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	97	I02	18	2*7	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	98	I03	12	2*8	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	99	I03	14	2*9	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	900	I03	16	2*10	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	901	I03	18	2*11	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	902	I04	12	2*12	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	903	I04	14	2*13	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	904	I04	16	2*14	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	905	I04	18	2*15	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	906	I05	12	2*16	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14	907	I05	14	2*17	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	90	I05	16	2*18	REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	91	I05	18	2*19	REGISTER TO MULTIPLY, OPERAND 2(XJ)

6W13	92	I06	12	2*20 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	93	I06	14	2*21 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	94	I06	16	2*22 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	95	I06	18	2*23 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W28	98	I07	23	ERROR MULT. 1
6W16	92	I08	23	DOUBLE NO. 1
6W28	900	I08	27	ERROR MULT. 1
6W16	91	I09	3	ROUND NO. 1
6W16	90	I09	23	GO UNIT NO. 1
6W16	96	I22	12	CLOCK
6W13	96	J01	12	2*24 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	97	J01	14	2*25 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	98	J01	16	2*26 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	99	J01	18	2*27 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	900	J02	12	2*28 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	901	J02	14	2*29 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	902	J02	16	2*30 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	903	J02	18	2*31 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	904	J03	12	2*32 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	905	J03	14	2*33 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	906	J03	16	2*34 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13	907	J03	18	2*35 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W09	90	J04	12	2*36 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	91	J04	14	2*37 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	92	J04	16	2*38 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	93	J04	18	2*39 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	94	J05	12	2*40 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	95	J05	14	2*41 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	96	J05	16	2*42 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	97	J05	18	2*43 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	98	J06	12	2*44 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	99	J06	14	2*45 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	900	J06	16	2*46 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09	901	J06	18	2*47 REGISTER TO MULTIPLY, OPERAND (XJ)
6W28	99	J07	23	ERROR MULT. 2
6W16	902	J08	23	DOUBLE
6W28	901	J08	27	ERROR MULT. 2
6W16	901	J09	3	ROUND
6W16	900	J09	23	GO UNIT NO. 2
6W23	90	K01	1	2*0 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	92	K01	2	2*2 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	91	K01	3	2*1 MULTIPLY TO DIVIDE (OPERAND) X**K
6W20	91	K01	10	2*1 DIVIDE TO MULTIPLY RESULT
6W20	90	K01	11	2*0 DIVIDE TO MULTIPLY RESULT
6W23	93	K02	1	2*3 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	95	K02	2	2*5 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	94	K02	3	2*4 MULTIPLY TO DIVIDE (OPERAND) X**K
6W20	93	K02	10	2*3 DIVIDE TO MULTIPLY RESULT
6W20	92	K02	11	2*2 DIVIDE TO MULTIPLY RESULT
6W23	96	K03	1	2*6 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	98	K03	2	2*8 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	97	K03	3	2*7 MULTIPLY TO DIVIDE (OPERAND) X**K
6W20	95	K03	10	2*5 DIVIDE TO MULTIPLY RESULT
6W20	94	K03	11	2*4 DIVIDE TO MULTIPLY RESULT
6W23	99	K04	1	2*9 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	901	K04	2	2*11 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	900	K04	3	2*10 MULTIPLY TO DIVIDE (OPERAND) X**K
6W20	97	K04	10	2*7 DIVIDE TO MULTIPLY RESULT
6W20	96	K04	11	2*6 DIVIDE TO MULTIPLY RESULT
6W23	902	K05	1	2*12 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	904	K05	2	2*14 MULTIPLY TO DIVIDE (OPERAND) X**K
6W23	903	K05	3	2*13 MULTIPLY TO DIVIDE (OPERAND) X**K

6W20	99	K05	10	2*9	DIVIDE TO MULTIPLY RESULT	
6W20	98	K05	11	2*8	DIVIDE TO MULTIPLY RESULT	
6W23	905	K06	1	2*15	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W23	907	K06	2	2*17	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W23	906	K06	3	2*16	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W20	901	K06	10	2*11	DIVIDE TO MULTIPLY RESULT	
6W20	900	K06	11	2*10	DIVIDE TO MULTIPLY RESULT	
6W24	90	K07	1	2*18	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	92	K07	2	2*20	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	91	K07	3	2*19	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W20	903	K07	10	2*13	DIVIDE TO MULTIPLY RESULT	
6W20	902	K07	11	2*12	DIVIDE TO MULTIPLY RESULT	
6W16	905	K09	1		REQUEST RELEASE	
6W22	906	K10	1			
6W24	93	L01	1	2*21	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	95	L01	2	2*23	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	94	L01	3	2*22	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W20	905	L01	10	2*15	DIVIDE TO MULTIPLY RESULT	
6W20	904	L01	11	2*14	DIVIDE TO MULTIPLY RESULT	
6W24	96	L02	1	2*24	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	98	L02	2	2*26	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	97	L02	3	2*25	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W20	907	L02	10	2*17	DIVIDE TO MULTIPLY RESULT	
6W20	906	L02	11	2*16	DIVIDE TO MULTIPLY RESULT	
6W24	99	L03	1	2*27	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	901	L03	2	2*29	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	900	L03	3	2*28	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	91	L03	10	2*19	DIVIDE TO MULTIPLY RESULT	
6W21	90	L03	11	2*18	DIVIDE TO MULTIPLY RESULT	
6W24	902	L04	1	2*30	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	904	L04	2	2*32	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	903	L04	3	2*31	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	93	L04	10	2*21	DIVIDE TO MULTIPLY RESULT	
6W21	92	L04	11	2*20	DIVIDE TO MULTIPLY RESULT	
6W24	905	L05	1	2*33	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	907	L05	2	2*35	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W24	906	L05	3	2*34	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	95	L05	10	2*23	DIVIDE TO MULTIPLY RESULT	
6W21	94	L05	11	2*22	DIVIDE TO MULTIPLY RESULT	
6W25	90	L06	1	2*36	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	92	L06	2	2*38	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	91	L06	3	2*37	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	97	L06	10	2*25	DIVIDE TO MULTIPLY RESULT	
6W21	96	L06	11	2*24	DIVIDE TO MULTIPLY RESULT	
6W25	93	L07	1	2*39	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	95	L07	2	2*41	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	94	L07	3	2*40	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	99	L07	10	2*27	DIVIDE TO MULTIPLY RESULT	
6W21	98	L07	11	2*26	DIVIDE TO MULTIPLY RESULT	
6W16	94	L08	2		XMIT RESULT NO. 2	
6W16	93	L08	26		XMIT RESULT NO. 1	
6W25	96	M01	1	2*42	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	98	M01	2	2*44	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	97	M01	3	2*43	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	901	M01	10	2*29	DIVIDE TO MULTIPLY RESULT	
6W21	900	M01	11	2*28	DIVIDE TO MULTIPLY RESULT	
6W25	99	M02	1	2*45	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	901	M02	2	2*47	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W25	900	M02	3	2*46	MULTIPLY TO DIVIDE (OPERAND)	X**K
6W21	903	M02	10	2*31	DIVIDE TO MULTIPLY RESULT	
6W21	902	M02	11	2*30	DIVIDE TO MULTIPLY RESULT	
6W25	903	M03	1	2*0	MULTIPLY TO DIVIDE (OPERAND)	X**J

6W25	905	M03	2	2*2 MULTIPLY TO DIVIDE (OPERAND) X**J
6W25	904	M03	3	2*1 MULTIPLY TO DIVIDE (OPERAND) X**J
6W21	905	M03	10	2*33 DIVIDE TO MULTIPLY RESULT
6W21	904	M03	11	2*32 DIVIDE TO MULTIPLY RESULT
6W25	906	M04	1	2*3 MULTIPLY TO DIVIDE (OPERAND) X**J
6W26	90	M04	2	2*5 MULTIPLY TO DIVIDE OPERAND X**J
6W25	907	M04	3	2*4 MULTIPLY TO DIVIDE (OPERAND) X**J
6W21	907	M04	10	2*35 DIVIDE TO MULTIPLY RESULT
6W21	906	M04	11	2*34 DIVIDE TO MULTIPLY RESULT
6W26	91	M05	1	
6W26	93	M05	2	
6W26	92	M05	3	
6W22	91	M05	10	2*37 DIVIDE TO MULTIPLY RESULT
6W22	90	M05	11	2*36 DIVIDE TO MULTIPLY RESULT
6W26	94	M06	1	2*9 MULTIPLY TO DIVIDE OPERAND X**J
6W26	96	M06	2	2*11 MULTIPLY TO DIVIDE OPERAND X**J
6W26	95	M06	3	2*10 MULTIPLY TO DIVIDE OPERAND X**J
6W22	93	M06	10	2*39 DIVIDE TO MULTIPLY RESULT
6W22	92	M06	11	2*38 DIVIDE TO MULTIPLY RESULT
6W26	97	M07	1	2*12 MULTIPLY TO DIVIDE OPERAND X**J
6W26	99	M07	2	2*14 MULTIPLY TO DIVIDE OPERAND X**J
6W26	98	M07	3	2*13 MULTIPLY TO DIVIDE OPERAND X**J
6W22	95	M07	10	2*41 DIVIDE TO MULTIPLY RESULT
6W22	94	M07	11	2*40 DIVIDE TO MULTIPLY RESULT
6W26	900	N01	1	2*15 MULTIPLY TO DIVIDE OPERAND X**J
6W26	902	N01	2	2*17 MULTIPLY TO DIVIDE OPERAND X**J
6W26	901	N01	3	2*16 MULTIPLY TO DIVIDE OPERAND X**J
6W22	97	N01	10	2*43 DIVIDE TO MULTIPLY RESULT
6W22	96	N01	11	2*42 DIVIDE TO MULTIPLY RESULT
6W26	903	N02	1	2*18 MULTIPLY TO DIVIDE OPERAND X**J
6W26	905	N02	2	2*20 MULTIPLY TO DIVIDE OPERAND X**J
6W26	904	N02	3	2*19 MULTIPLY TO DIVIDE OPERAND X**J
6W22	99	N02	10	2*45 DIVIDE TO MULTIPLY RESULT
6W22	98	N02	11	2*44 DIVIDE TO MULTIPLY RESULT
6W26	906	N03	1	2*21 MULTIPLY TO DIVIDE OPERAND X**J
6W27	90	N03	2	2*23 MULTIPLY TO DIVIDE OPERAND X**J
6W26	907	N03	3	2*22 MULTIPLY TO DIVIDE OPERAND X**J
6W22	901	N03	10	2*47 DIVIDE TO MULTIPLY RESULT
6W22	900	N03	11	2*46 DIVIDE TO MULTIPLY RESULT
6W27	93	N04	4	2*26 MULTIPLY TO DIVIDE OPERAND X**J
6W27	92	N04	6	2*25 MULTIPLY TO DIVIDE OPERAND X**J
6W27	91	N04	8	2*24 MULTIPLY TO DIVIDE OPERAND X**J
6W27	96	N04	23	2*29 MULTIPLY TO DIVIDE OPERAND X**J
6W27	95	N04	25	2*28 MULTIPLY TO DIVIDE OPERAND X**J
6W27	94	N04	27	2*27 MULTIPLY TO DIVIDE OPERAND X**J
6W27	99	N05	4	2*32 MULTIPLY TO DIVIDE OPERAND X**J
6W27	98	N05	6	2*31 MULTIPLY TO DIVIDE OPERAND X**J
6W27	97	N05	8	2*30 MULTIPLY TO DIVIDE OPERAND X**J
6W27	902	N05	23	2*35 MULTIPLY TO DIVIDE OPERAND X**J
6W27	901	N05	25	2*34 MULTIPLY TO DIVIDE OPERAND X**J
6W27	900	N05	27	2*33 MULTIPLY TO DIVIDE OPERAND X**J
6W27	905	N06	4	2*38 MULTIPLY TO DIVIDE OPERAND X**J
6W27	904	N06	6	2*37 MULTIPLY TO DIVIDE OPERAND X**J
6W27	903	N06	8	2*36 MULTIPLY TO DIVIDE OPERAND X**J
6W28	90	N06	23	2*41 MULTIPLY TO DIVIDE X**J
6W27	907	N06	25	2*40 MULTIPLY TO DIVIDE OPERAND X**J
6W27	906	N06	27	2*39 MULTIPLY TO DIVIDE OPERAND X**J
6W28	93	N07	4	2*44 MULTIPLY TO DIVIDE X**J
6W28	92	N07	6	2*43 MULTIPLY TO DIVIDE X**J
6W28	91	N07	8	2*42 MULTIPLY TO DIVIDE X**J
6W22	907	N07	12	D P MULT 1
6W22	908	N07	19	D P MULT 2

6W28	96	N07	23	2*47	MULTIPLY	TO	DIVIDE	X**J
6W28	95	N07	25	2*46	MULTIPLY	TO	DIVIDE	X**J
6W28	94	N07	27	2*45	MULTIPLY	TO	DIVIDE	X**J

CHASSIS 7

7W05	900	A37	2	2*0	MEMORY TO REGISTER
7W05	901	A37	26	2*1	MEMORY TO REGISTER
7W05	902	A38	2	2*2	MEMORY TO REGISTER
7W05	903	A38	26	2*3	MEMORY TO REGISTER
7W05	904	A39	2	2*4	MEMORY TO REGISTER
7W05	905	A39	26	2*5	MEMORY TO REGISTER
7W05	906	A40	2	2*6	MEMORY TO REGISTER
7W05	907	A40	26	2*7	MEMORY TO REGISTER
7W05	90	A41	2	2*8	MEMORY TO REGISTER
7W05	91	A41	26	2*9	MEMORY TO REGISTER
7W05	92	A42	2	2*10	MEMORY TO REGISTER
7W05	93	A42	26	2*11	MEMORY TO REGISTER
7W05	94	B37	2	2*12	MEMORY TO REGISTER
7W05	95	B37	26	2*13	MEMORY TO REGISTER
7W05	96	B38	2	2*14	MEMORY TO REGISTER
7W06	900	B38	26	2*15	MEMORY TO REGISTER
7W06	901	B39	2	2*16	MEMORY TO REGISTER
7W06	902	B39	26	2*17	MEMORY TO REGISTER
7W06	903	B40	2	2*18	MEMORY TO REGISTER
7W06	904	B40	26	2*19	MEMORY TO REGISTER
7W06	905	B41	2	2*20	MEMORY TO REGISTER
7W06	906	B41	26	2*21	MEMORY TO REGISTER
7W06	907	B42	2	2*22	MEMORY TO REGISTER
7W06	90	B42	26	2*23	MEMORY TO REGISTER
7W06	91	C37	2	2*24	MEMORY TO REGISTER
7W06	92	C37	26	2*25	MEMORY TO REGISTER
7W06	93	C38	2	2*26	MEMORY TO REGISTER
7W06	94	C38	26	2*27	MEMORY TO REGISTER
7W06	95	C39	2	2*28	MEMORY TO REGISTER
7W06	96	C39	26	2*29	MEMORY TO REGISTER
7W07	900	C40	2	2*30	MEMORY TO REGISTER
7W07	901	C40	26	2*31	MEMORY TO REGISTER
7W07	902	C41	2	2*32	MEMORY TO REGISTER
7W07	903	C41	26	2*33	MEMORY TO REGISTER
7W07	904	C42	2	2*34	MEMORY TO REGISTER
7W07	905	C42	26	2*35	MEMORY TO REGISTER
7W10	90	D34	2	2*0	MULTIPLY TO REGISTER
7W10	91	D34	26	2*1	MULTIPLY TO REGISTER
7W10	92	D35	2	2*2	MULTIPLY TO REGISTER
7W10	93	D35	26	2*3	MULTIPLY TO REGISTER
7W10	94	D36	2	2*4	MULTIPLY TO REGISTER
7W10	95	D36	26	2*5	MULTIPLY TO REGISTER
7W10	96	D37	2	2*6	MULTIPLY TO REGISTER
7W10	97	D37	26	2*7	MULTIPLY TO REGISTER
7W10	98	D38	2	2*8	MULTIPLY TO REGISTER
7W10	99	D38	26	2*9	MULTIPLY TO REGISTER
7W10	900	D39	2	2*10	MULTIPLY TO REGISTER
7W10	901	D39	26	2*11	MULTIPLY TO REGISTER
7W10	902	D40	2	2*12	MULTIPLY TO REGISTER
7W10	903	D40	26	2*13	MULTIPLY TO REGISTER
7W10	904	D41	2	2*14	MULTIPLY TO REGISTER
7W10	905	D41	26	2*15	MULTIPLY TO REGISTER
7W10	906	D42	2	2*16	MULTIPLY TO REGISTER
7W10	907	D42	26	2*17	MULTIPLY TO REGISTER
7W09	94	E26	3	DATA 2*30	REG. TO MEM.
7W09	96	E26	21	DATA 2*32	REG. TO MEM.
7W09	95	E26	25	DATA 2*31	REG. TO MEM.
7W09	97	E27	3	DATA 2*33	REG. TO MEM.
7W09	99	E27	21	DATA 2*35	REG. TO MEM.
7W09	98	E27	25	DATA 2*34	REG. TO MEM.

7W11	90	E34	2	2*18	MULTIPLY	TO REGISTER
7W11	91	E34	26	2*19	MULTIPLY	TO REGISTER
7W11	92	E35	2	2*20	MULTIPLY	TO REGISTER
7W11	93	E35	26	2*21	MULTIPLY	TO REGISTER
7W11	94	E36	2	2*22	MULTIPLY	TO REGISTER
7W11	95	E36	26	2*23	MULTIPLY	TO REGISTER
7W11	96	E37	2	2*24	MULTIPLY	TO REGISTER
7W11	97	E37	26	2*25	MULTIPLY	TO REGISTER
7W11	98	E38	2	2*26	MULTIPLY	TO REGISTER
7W11	99	E38	26	2*27	MULTIPLY	TO REGISTER
7W11	900	E39	2	2*28	MULTIPLY	TO REGISTER
7W11	901	E39	26	2*29	MULTIPLY	TO REGISTER
7W11	902	E40	2	2*30	MULTIPLY	TO REGISTER
7W11	903	E40	26	2*31	MULTIPLY	TO REGISTER
7W11	904	E41	2	2*32	MULTIPLY	TO REGISTER
7W11	905	E41	26	2*33	MULTIPLY	TO REGISTER
7W11	906	E42	2	2*34	MULTIPLY	TO REGISTER
7W11	907	E42	26	2*35	MULTIPLY	TO REGISTER
7W08	900	F26	3	DATA 2*0	REG.	TO MEM.
7W08	902	F26	21	DATA 2*2	REG.	TO MEM.
7W08	901	F26	25	DATA 2*1	REG.	TO MEM.
7W08	903	F27	3	DATA 2*3	REG.	TO MEM.
7W08	905	F27	21	DATA 2*5	REG.	TO MEM.
7W08	904	F27	25	DATA 2*4	REG.	TO MEM.
7W08	906	F28	3	DATA 2*6	REG.	TO MEM.
7W08	90	F28	21	DATA 2*8	REG.	TO MEM.
7W08	907	F28	25	DATA 2*7	REG.	TO MEM.
7W08	91	F29	3	DATA 2*9	REG.	TO MEM.
7W08	93	F29	21	DATA 2*11	REG.	TO MEM.
7W08	92	F29	25	DATA 2*10	REG.	TO MEM.
7W08	94	F30	3	DATA 2*12	REG.	TO MEM.
7W08	96	F30	21	DATA 2*14	REG.	TO MEM.
7W08	95	F30	25	DATA 2*13	REG.	TO MEM.
7W08	97	F31	3	DATA 2*15	REG.	TO MEM.
7W08	99	F31	21	DATA 2*17	REG.	TO MEM.
7W08	98	F31	25	DATA 2*16	REG.	TO MEM.
7W09	900	F32	3	DATA 2*18	REG.	TO MEM.
7W09	902	F32	21	DATA 2*20	REG.	TO MEM.
7W09	901	F32	25	DATA 2*19	REG.	TO MEM.
7W09	903	F33	3	DATA 2*21	REG.	TO MEM.
7W09	905	F33	21	DATA 2*23	REG.	TO MEM.
7W09	904	F33	25	DATA 2*22	REG.	TO MEM.
7W09	906	F34	3	DATA 2*24	REG.	TO MEM.
7W09	90	F34	21	DATA 2*26	REG.	TO MEM.
7W09	907	F34	25	DATA 2*25	REG.	TO MEM.
7W09	91	F35	3	DATA 2*27	REG.	TO MEM.
7W09	93	F35	21	DATA 2*29	REG.	TO MEM.
7W09	92	F35	25	DATA 2*28	REG.	TO MEM.
7W21	900	F38	2	INCREMENT	RESULT BIT 2*0	
7W21	901	F38	26	INCREMENT	RESULT BIT 2*1	
7W21	902	F39	2	INCREMENT	RESULT BIT 2*2	
7W21	903	F39	26	INCREMENT	RESULT BIT 2*3	
7W21	904	F40	2	INCREMENT	RESULT BIT 2*4	
7W21	905	F40	26	INCREMENT	RESULT BIT 2*5	
7W21	906	F41	2	INCREMENT	RESULT BIT 2*6	
7W21	907	F41	26	INCREMENT	RESULT BIT 2*7	
7W21	908	F42	2	INCREMENT	RESULT BIT 2*8	
7W21	90	F42	26	INCREMENT	RESULT BIT 2*9	
7W19	96	G26	5	A TO INCR.		GO
7W19	91	G26	7	A TO INCREMENT		1
7W19	90	G26	10	A TO INCREMENT		0
7W19	92	G26	26	A TO INCREMENT		2

7W16	904	G27	4	MULTIPLY TO X	1
7W16	905	G27	5	MULTIPLY TO X	2
7W16	903	G27	16	MULTIPLY TO X	0
7W16	902	G27	17	MULTIPLY TO X	GO
7W17	90	G28	4	MEMORY TO D	0
7W17	91	G28	5	MEMORY TO D	1
7W17	92	G28	16	MEMORY TO D	2
7W19	94	G28	17	MEM. TO D	GO
7W12	90	G29	3	2*0 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	92	G29	21	2*2 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	91	G29	25	2*1 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	93	G30	3	2*3 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	95	G30	21	2*5 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	94	G30	25	2*4 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	96	G31	3	2*6 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	98	G31	21	2*8 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	97	G31	25	2*7 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	99	G32	3	2*9 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	901	G32	21	2*11 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	900	G32	25	2*10 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	902	G33	3	2*12 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	904	G33	21	2*14 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	903	G33	25	2*13 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	905	G34	3	2*15 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	907	G34	21	2*17 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W12	906	G34	25	2*16 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W20	93	G37	5	EXCHANGE	
7W21	91	G38	2	INCREMENT RESULT BIT 2*10	
7W21	92	G38	26	INCREMENT RESULT BIT 2*11	
7W21	93	G39	2	INCREMENT RESULT BIT 2*12	
7W21	94	G39	26	INCREMENT RESULT BIT 2*13	
7W21	95	G40	2	INCREMENT RESULT BIT 2*14	
7W21	96	G40	26	INCREMENT RESULT BIT 2*15	
7W21	97	G41	2	INCREMENT RESULT BIT 2*16	
7W21	98	G41	26	INCREMENT RESULT BIT 2*17	
7W20	904	H26	4	X TO MEMORY	GO
7W20	905	H26	6	X TO MEMORY	0
7W20	906	H26	7	X TO MEMORY	1
7W20	907	H26	27	X TO MEMORY	2
7W17	902	H27	4	INCREMENT TO A	1
7W17	903	H27	5	INCREMENT TO A	2
7W17	901	H27	16	INCREMENT TO A	0
7W17	900	H27	17	INCREMENT TO A	GO
7W16	93	H28	4	INCREMENT TO X	1
7W16	94	H28	5	INCREMENT TO X	2
7W16	92	H28	16	INCREMENT TO X	0
7W16	91	H28	17	INCREMENT TO X	GO
7W15	90	H29	3	2*18 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	92	H29	21	2*20 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	91	H29	25	2*19 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	93	H30	3	2*21 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	95	H30	21	2*23 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	94	H30	25	2*22 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	96	H31	3	2*24 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	98	H31	21	2*26 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	97	H31	25	2*25 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	99	H32	3	2*27 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	901	H32	21	2*29 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	900	H32	25	2*28 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	902	H33	3	2*30 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	904	H33	21	2*32 REGISTER TO MULTIPLY,	OPERAND 1(XK)
7W15	903	H33	25	2*31 REGISTER TO MULTIPLY,	OPERAND 1(XK)

7W15	905	H34	3	2*33 REGISTER TO MULTIPLY, OPERAND 1(XK)	
7W15	907	H34	21	2*35 REGISTER TO MULTIPLY, OPERAND 1(XK)	
7W15	906	H34	25	2*34 REGISTER TO MULTIPLY, OPERAND 1(XK)	
7W26	90	H37	2	2*0 ADD TO REGISTER	
7W26	91	H37	26	2*1 ADD TO REGISTER	
7W26	92	H38	2	2*2 ADD TO REGISTER	
7W26	93	H38	26	2*3 ADD TO REGISTER	
7W26	94	H39	2	2*4 ADD TO REGISTER	
7W26	95	H39	26	2*5 ADD TO REGISTER	
7W26	96	H40	2	2*6 ADD TO REGISTER	
7W26	97	H40	26	2*7 ADD TO REGISTER	
7W26	98	H41	2	2*8 ADD TO REGISTER	
7W26	99	H41	26	2*9 ADD TO REGISTER	
7W26	900	H42	2	2*10 ADD TO REGISTER	
7W26	901	H42	26	2*11 ADD TO REGISTER	
7W16	901	I22	12	CLOCK	
7W18	904	I26	5	XL TO ADD (2)	GO
7W18	906	I26	7	XL TO ADD (2)	1
7W18	905	I26	10	XL TO ADD (2)	0
7W17	93	I26	21	K TO MULTIPLY (1)	GO
7W17	94	I26	24	K TO MULTIPLY (1)	0
7W18	907	I26	26	XL TO ADD (2)	2
7W17	95	I27	5	K TO MULTIPLY (1)	1
7W16	99	I27	7	XJ TO MULTIPLY (2)	GO
7W17	96	I27	10	K TO MULTIPLY (1)	2
7W17	98	I27	21	J TO MULTIPLY (2)	1
7W17	99	I27	24	J TO MULTIPLY (2)	2
7W17	97	I27	26	J TO MULTIPLY (2)	0
7W19	93	I28	4	ADD TO X	1
7W16	90	I28	5	ADD TO X	2
7W16	907	I28	16	ADD TO X	0
7W16	906	I28	17	ADD TO X	GO
7W14	90	I29	3	2*0 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	92	I29	21	2*2 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	91	I29	25	2*1 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	93	I30	3	2*3 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	95	I30	21	2*5 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	94	I30	25	2*4 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	96	I31	3	2*6 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	98	I31	21	2*8 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	97	I31	25	2*7 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	99	I32	3	2*9 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	901	I32	21	2*11 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	900	I32	25	2*10 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	902	I33	3	2*12 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	904	I33	21	2*14 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	903	I33	25	2*13 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	905	I34	3	2*15 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	907	I34	21	2*17 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W14	906	I34	25	2*16 REGISTER TO MULTIPLY, OPERAND 2(XJ)	
7W20	900	J26	5	D**1 TO X**1	
7W20	901	J26	7	D**2 TO X**2	
7W20	902	J26	10	D**3 TO X**3	
7W20	903	J26	21	D**4 TO X**4	
7W20	98	J26	24	D**5 TO X**5	
7W19	95	J27	21	X TO INCR.	GO
7W18	90	J27	24	X TO INCREMENT	0
7W18	91	J28	5	X TO INCREMENT	1
7W18	900	J28	7	XJ TO ADD (1)	GO
7W18	92	J28	10	X TO INCREMENT	2
7W18	902	J28	21	XJ TO ADD (1)	1
7W18	903	J28	24	XJ TO ADD (1)	2

7W18	901	J28	26	XJ TO ADD (1)	0
7W13	90	J29	3	2*18 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	92	J29	21	2*20 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	91	J29	25	2*19 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	93	J30	3	2*21 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	95	J30	21	2*23 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	94	J30	25	2*22 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	96	J31	3	2*24 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	98	J31	21	2*26 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	97	J31	25	2*25 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	99	J32	3	2*27 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	901	J32	21	2*29 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	900	J32	25	2*28 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	902	J33	3	2*30 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	904	J33	21	2*32 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	903	J33	25	2*31 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	905	J34	3	2*33 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	907	J34	21	2*35 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W13	906	J34	25	2*34 REGISTER TO MULTIPLY,	OPERAND 2(XJ)
7W26	902	J37	2	2*12 ADD TO REGISTER	
7W26	903	J37	26	2*13 ADD TO REGISTER	
7W26	904	J38	2	2*14 ADD TO REGISTER	
7W26	905	J38	26	2*15 ADD TO REGISTER	
7W26	906	J39	2	2*16 ADD TO REGISTER	
7W26	907	J39	26	2*17 ADD TO REGISTER	
7W27	90	J40	2	2*18 ADD TO REGISTER	
7W27	91	J40	26	2*19 ADD TO REGISTER	
7W27	92	J41	2	2*20 ADD TO REGISTER	
7W27	93	J41	26	2*21 ADD TO REGISTER	
7W27	94	J42	2	2*22 ADD TO REGISTER	
7W27	95	J42	26	2*23 ADD TO REGISTER	
7W22	900	K27	3	INCREMENT OPERAND 1 BIT	2*0
7W22	902	K27	21	INCREMENT OPERAND 1 BIT	2*2
7W22	901	K27	25	INCREMENT OPERAND 1 BIT	2*1
7W22	903	K28	3	INCREMENT OPERAND 1 BIT	2*3
7W22	905	K28	21	INCREMENT OPERAND 1 BIT	2*5
7W22	904	K28	25	INCREMENT OPERAND 1 BIT	2*4
7W22	906	K29	3	INCREMENT OPERAND 1 BIT	2*6
7W22	908	K29	21	INCREMENT OPERAND 1 BIT	2*8
7W22	907	K29	25	INCREMENT OPERAND 1 BIT	2*7
7W22	90	K30	3	INCREMENT OPERAND 1 BIT	2*9
7W22	92	K30	21	INCREMENT OPERAND 1 BIT	2*11
7W22	91	K30	25	INCREMENT OPERAND 1 BIT	2*10
7W22	93	K31	3	INCREMENT OPERAND 1 BIT	2*12
7W22	95	K31	21	INCREMENT OPERAND 1 BIT	2*14
7W22	94	K31	25	INCREMENT OPERAND 1 BIT	2*13
7W22	96	K32	3	INCREMENT OPERAND 1 BIT	2*15
7W22	98	K32	21	INCREMENT OPERAND 1 BIT	2*17
7W22	97	K32	25	INCREMENT OPERAND 1 BIT	2*16
7W16	97	K33	4	INCREMENT TO B	1
7W16	98	K33	5	INCREMENT TO B	2
7W16	96	K33	16	INCREMENT TO B	0
7W16	95	K33	17	INCREMENT TO B	GO
7W17	906	K34	4	SHIFT TO B	1
7W17	907	K34	5	SHIFT TO B	2
7W17	905	K34	16	SHIFT TO B	0
7W17	904	K34	17	SHIFT TO B	GO
7W27	96	K37	2	2*24 ADD TO REGISTER	
7W27	97	K37	26	2*25 ADD TO REGISTER	
7W27	98	K38	2	2*26 ADD TO REGISTER	
7W27	99	K38	26	2*27 ADD TO REGISTER	
7W27	900	K39	2	2*28 ADD TO REGISTER	

7W27	901	K39	26	2*29	ADD TO REGISTER				
7W27	902	K40	2	2*30	ADD TO REGISTER				
7W27	903	K40	26	2*31	ADD TO REGISTER				
7W27	904	K41	2	2*32	ADD TO REGISTER				
7W27	905	K41	26	2*33	ADD TO REGISTER				
7W27	906	K42	2	2*34	ADD TO REGISTER				
7W27	907	K42	26	2*35	ADD TO REGISTER				
7W30	92	L26	4	D*3	TO X*3 UPPER				
7W30	91	L26	6	D*2	TO X*2 UPPER				
7W30	90	L26	8	D*1	TO X*1 UPPER				
7W30	94	L26	25	D*5	TO X*5 UPPER				
7W30	93	L26	27	D*4	TO X*4 UPPER				
7W23	900	L27	3	INCREMENT	OPERAND K BIT 2*0				
7W23	902	L27	21	INCREMENT	OPERAND K BIT 2*2				
7W23	901	L27	25	INCREMENT	OPERAND K BIT 2*1				
7W23	903	L28	3	INCREMENT	OPERAND K BIT 2*3				
7W23	905	L28	21	INCREMENT	OPERAND K BIT 2*5				
7W23	904	L28	25	INCREMENT	OPERAND K BIT 2*4				
7W23	906	L29	3	INCREMENT	OPERAND K BIT 2*6				
7W23	908	L29	21	INCREMENT	OPERAND K BIT 2*8				
7W23	907	L29	25	INCREMENT	OPERAND K BIT 2*7				
7W23	90	L30	3	INCREMENT	OPERAND K BIT 2*9				
7W23	92	L30	21	INCREMENT	OPERAND K BIT 2*11				
7W23	91	L30	25	INCREMENT	OPERAND K BIT 2*10				
7W23	93	L31	3	INCREMENT	OPERAND K BIT 2*12				
7W23	95	L31	21	INCREMENT	OPERAND K BIT 2*14				
7W23	94	L31	25	INCREMENT	OPERAND K BIT 2*13				
7W23	96	L32	3	INCREMENT	OPERAND K BIT 2*15				
7W23	98	L32	21	INCREMENT	OPERAND K BIT 2*17				
7W23	97	L32	25	INCREMENT	OPERAND K BIT 2*16				
7W19	900	L34	5	BJ TO INCREMENT	GO				
7W19	902	L34	7	BJ TO INCREMENT	1				
7W19	901	L34	10	BJ TO INCREMENT	0				
7W19	904	L34	21	BK TO INCREMENT	GO				
7W19	905	L34	24	BK TO INCREMENT	0				
7W19	903	L34	26	BJ TO INCREMENT	2				
7W28	90	L35	3	2*0	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	92	L35	21	2*2	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	91	L35	25	2*1	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	93	L36	3	2*3	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	95	L36	21	2*5	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	94	L36	25	2*4	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	96	L37	3	2*6	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	98	L37	21	2*8	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	97	L37	25	2*7	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	99	L38	3	2*9	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	901	L38	21	2*11	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	900	L38	25	2*10	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	902	L39	3	2*12	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	904	L39	21	2*14	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	903	L39	25	2*13	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	905	L40	3	2*15	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	907	L40	21	2*17	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W28	906	L40	25	2*16	REGISTER TO ADD, OPERAND 2(BJ+XJ)				
7W29	90	L41	3	2*18	REGISTER TO ADD, OPERAND 2(XJ+BJ)				
7W29	92	L41	21	2*20	REGISTER TO ADD, OPERAND 2(XJ+BJ)				
7W29	91	L41	25	2*19	REGISTER TO ADD, OPERAND 2(XJ+BJ)				
7W29	93	L42	3	2*21	REGISTER TO ADD, OPERAND 2(XJ+BJ)				
7W29	95	L42	21	2*23	REGISTER TO ADD, OPERAND 2(XJ+BJ)				
7W29	94	L42	25	2*22	REGISTER TO ADD, OPERAND 2(XJ+BJ)				
7W19	906	M34	5	BK TO INCREMENT	1				
7W18	93	M34	7	B TO ADD (1)	GO				

7W19	907	M34	10	BK TO INCREMENT	2	
7W18	95	M34	21	B TO ADD (1)	1	
7W18	96	M34	24	B TO ADD (1)	2	
7W18	94	M34	26	B TO ADD (1)	0	
7W29	96	M35	3	2*24 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	98	M35	21	2*26 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	97	M35	25	2*25 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	99	M36	3	2*27 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	901	M36	21	2*29 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	900	M36	25	2*28 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	902	M37	3	2*30 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	904	M37	21	2*32 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	903	M37	25	2*31 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	905	M38	3	2*33 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	907	M38	21	2*35 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W29	906	M38	25	2*34 REGISTER TO ADD,	OPERAND	2(XJ+BJ)
7W24	90	M39	3	2*0 REGISTER TO ADD,	OPERAND	1(XK)
7W24	92	M39	21	2*2 REGISTER TO ADD,	OPERAND	1(XK)
7W24	91	M39	25	2*1 REGISTER TO ADD,	OPERAND	1(XK)
7W24	93	M40	3	2*3 REGISTER TO ADD,	OPERAND	1(XK)
7W24	95	M40	21	2*5 REGISTER TO ADD,	OPERAND	1(XK)
7W24	94	M40	25	2*4 REGISTER TO ADD,	OPERAND	1(XK)
7W24	96	M41	3	2*6 REGISTER TO ADD,	OPERAND	1(XK)
7W24	98	M41	21	2*8 REGISTER TO ADD,	OPERAND	1(XK)
7W24	97	M41	25	2*7 REGISTER TO ADD,	OPERAND	1(XK)
7W24	99	M42	3	2*9 REGISTER TO ADD,	OPERAND	1(XK)
7W24	901	M42	21	2*11 REGISTER TO ADD,	OPERAND	1(XK)
7W24	900	M42	25	2*10 REGISTER TO ADD,	OPERAND	1(XK)
7W24	902	N35	3	2*12 REGISTER TO ADD,	OPERAND	1(XK)
7W24	904	N35	21	2*14 REGISTER TO ADD,	OPERAND	1(XK)
7W24	903	N35	25	2*13 REGISTER TO ADD,	OPERAND	1(XK)
7W24	905	N36	3	2*15 REGISTER TO ADD,	OPERAND	1(XK)
7W24	907	N36	21	2*17 REGISTER TO ADD,	OPERAND	1(XK)
7W24	906	N36	25	2*16 REGISTER TO ADD,	OPERAND	1(XK)
7W25	90	N37	3	2*18 REGISTER TO ADD,	OPERAND	1(XK)
7W25	92	N37	21	2*20 REGISTER TO ADD,	OPERAND	1(XK)
7W25	91	N37	25	2*19 REGISTER TO ADD,	OPERAND	1(XK)
7W25	93	N38	3	2*21 REGISTER TO ADD,	OPERAND	1(XK)
7W25	95	N38	21	2*23 REGISTER TO ADD,	OPERAND	1(XK)
7W25	94	N38	25	2*22 REGISTER TO ADD,	OPERAND	1(XK)
7W25	96	N39	3	2*24 REGISTER TO ADD,	OPERAND	1(XK)
7W25	98	N39	21	2*26 REGISTER TO ADD,	OPERAND	1(XK)
7W25	97	N39	25	2*25 REGISTER TO ADD,	OPERAND	1(XK)
7W25	99	N40	3	2*27 REGISTER TO ADD,	OPERAND	1(XK)
7W25	901	N40	21	2*29 REGISTER TO ADD,	OPERAND	1(XK)
7W25	900	N40	25	2*28 REGISTER TO ADD,	OPERAND	1(XK)
7W25	902	N41	3	2*30 REGISTER TO ADD,	OPERAND	1(XK)
7W25	904	N41	21	2*32 REGISTER TO ADD,	OPERAND	1(XK)
7W25	903	N41	25	2*31 REGISTER TO ADD,	OPERAND	1(XK)
7W25	905	N42	3	2*33 REGISTER TO ADD,	OPERAND	1(XK)
7W25	907	N42	21	2*35 REGISTER TO ADD,	OPERAND	1(XK)
7W25	906	N42	25	2*34 REGISTER TO ADD,	OPERAND	1(XK)
7W31	90	O38	2	2*0 SHIFT TO REGISTER		
7W31	91	O38	26	2*1 SHIFT TO REGISTER		
7W31	92	O39	2	2*2 SHIFT TO REGISTER		
7W31	93	O39	26	2*3 SHIFT TO REGISTER		
7W31	94	O40	2	2*4 SHIFT TO REGISTER		
7W31	95	O40	26	2*5 SHIFT TO REGISTER		
7W31	96	O41	2	2*6 SHIFT TO REGISTER		
7W31	97	O41	26	2*7 SHIFT TO REGISTER		
7W31	98	O42	2	2*8 SHIFT TO REGISTER		
7W31	99	O42	26	2*9 SHIFT TO REGISTER		

7W31	900	P38	2	2*10	SHIFT TO REGISTER
7W31	901	P38	26	2*11	SHIFT TO REGISTER
7W31	902	P39	2	2*12	SHIFT TO REGISTER
7W31	903	P39	26	2*13	SHIFT TO REGISTER
7W31	904	P40	2	2*14	SHIFT TO REGISTER
7W31	905	P40	26	2*15	SHIFT TO REGISTER
7W31	906	P41	2	2*16	SHIFT TO REGISTER
7W31	907	P41	26	2*17	SHIFT TO REGISTER

CHASSIS 8

8W05	900	A01	2	2*36	MEMORY TO REGISTER
8W05	901	A01	26	2*37	MEMORY TO REGISTER
8W05	902	A02	2	2*38	MEMORY TO REGISTER
8W05	903	A02	26	2*39	MEMORY TO REGISTER
8W05	904	A03	2	2*40	MEMORY TO REGISTER
8W05	905	A03	26	2*41	MEMORY TO REGISTER
8W05	906	A04	2	2*42	MEMORY TO REGISTER
8W05	907	A04	26	2*43	MEMORY TO REGISTER
8W05	908	A05	2	2*44	MEMORY TO REGISTER
8W06	90	A05	26	2*45	MEMORY REGISTER
8W06	91	A06	2	2*46	MEMORY REGISTER
8W06	92	A06	26	2*47	MEMORY REGISTER
8W06	93	A07	2	2*48	MEMORY REGISTER
8W06	94	A07	26	2*49	MEMORY REGISTER
8W06	95	A08	2	2*50	MEMORY REGISTER
8W06	96	A08	26	2*51	MEMORY REGISTER
8W10	906	A09	3	2*36	REGISTER TO DISTRIBUTOR
8W10	907	A09	21	2*37	REGISTER TO DISTRIBUTOR
8W10	90	A09	25	2*38	REGISTER TO DISTRIBUTOR
8W10	91	A10	3	2*39	REGISTER TO DISTRIBUTOR
8W10	92	A10	21	2*40	REGISTER TO DISTRIBUTOR
8W10	93	A10	25	2*41	REGISTER TO DISTRIBUTOR
8W10	94	A11	3	2*42	REGISTER TO DISTRIBUTOR
8W10	95	A11	21	2*43	REGISTER TO DISTRIBUTOR
8W10	96	A11	25	2*44	REGISTER TO DISTRIBUTOR
8W11	900	A12	3	2*45	REGISTER TO DISTRIBUTOR
8W11	901	A12	21	2*46	REGISTER TO DISTRIBUTOR
8W11	902	A12	25	2*47	REGISTER TO DISTRIBUTOR
8W06	97	B01	2	2*52	MEMORY REGISTER
8W06	98	B01	26	2*53	MEMORY REGISTER
8W06	99	B02	2	2*54	MEMORY REGISTER
8W06	900	B02	26	2*55	MEMORY REGISTER
8W06	901	B03	2	2*56	MEMORY REGISTER
8W06	902	B03	26	2*57	MEMORY REGISTER
8W06	903	B04	2	2*58	MEMORY REGISTER
8W06	904	B04	26	2*59	MEMORY REGISTER
8W08	90	B05	2	2*36	MULTIPLY TO REGISTER
8W08	91	B05	26	2*37	MULTIPLY TO REGISTER
8W08	92	B06	2	2*38	MULTIPLY TO REGISTER
8W08	93	B06	26	2*39	MULTIPLY TO REGISTER
8W08	94	B07	2	2*40	MULTIPLY TO REGISTER
8W08	95	B07	26	2*41	MULTIPLY TO REGISTER
8W08	96	B08	2	2*42	MULTIPLY TO REGISTER
8W08	97	B08	26	2*43	MULTIPLY TO REGISTER
8W11	903	B09	3	2*48	REGISTER TO DISTRIBUTOR
8W11	904	B09	21	2*49	REGISTER TO DISTRIBUTOR
8W11	905	B09	25	2*50	REGISTER TO DISTRIBUTOR
8W11	906	B10	3	2*51	REGISTER TO DISTRIBUTOR
8W11	907	B10	21	2*52	REGISTER TO DISTRIBUTOR
8W11	90	B10	25	2*53	REGISTER TO DISTRIBUTOR
8W11	91	B11	3	2*54	REGISTER TO DISTRIBUTOR
8W11	92	B11	21	2*55	REGISTER TO DISTRIBUTOR
8W11	93	B11	25	2*56	REGISTER TO DISTRIBUTOR
8W11	94	B12	3	2*57	REGISTER TO DISTRIBUTOR
8W11	95	B12	21	2*58	REGISTER TO DISTRIBUTOR
8W11	96	B12	25	2*59	REGISTER TO DISTRIBUTOR
8W08	98	C01	2	2*44	MULTIPLY TO REGISTER
8W08	99	C01	26	2*45	MULTIPLY TO REGISTER
8W08	900	C02	2	2*46	MULTIPLY TO REGISTER
8W08	901	C02	26	2*47	MULTIPLY TO REGISTER

8W18	902	C03	2	2+48	EXPONENT	RESULT-REG.	
8W18	903	C03	26	2+49	EXPONENT	RESULT-REG.	
8W18	904	C04	2	2+50	EXPONENT	RESULT-REG.	
8W18	905	C04	26	2+51	EXPONENT	RESULT-REG.	
8W18	906	C05	2	2+52	EXPONENT	RESULT-REG.	
8W18	907	C05	26	2+53	EXPONENT	RESULT-REG.	
8W19	902	C06	2	2+54	EXPONENT	RESULT-REG.	
8W19	903	C06	26	2+55	EXPONENT	RESULT-REG.	
8W19	904	C07	2	2+56	EXPONENT	RESULT-REG.	
8W19	905	C07	26	2+57	EXPONENT	RESULT-REG.	
8W19	906	C08	2	2+58	EXPONENT	RESULT-REG.	
8W19	907	C08	26	2+59	EXPONENT	RESULT-REG.	
8W07	90	C09	3	2+36	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	91	C09	21	2+37	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	92	C09	25	2+38	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	93	C10	3	2+39	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	94	C10	21	2+40	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	95	C10	25	2+41	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	96	C11	3	2+42	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	97	C11	21	2+43	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	98	C11	25	2+44	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	99	C12	3	2+45	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	900	C12	21	2+46	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W07	901	C12	25	2+47	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W18	90	D09	3	2+48	X**K	(EXPONENT)	
8W18	91	D09	21	2+49	X**K	(EXPONENT)	
8W18	92	D09	25	2+50	X**K	(EXPONENT)	
8W18	93	D10	3	2+51	X**K	(EXPONENT)	
8W18	94	D10	21	2+52	X**K	(EXPONENT)	
8W18	95	D10	25	2+53	X**K	(EXPONENT)	
8W18	96	D11	3	2+54	X**K	(EXPONENT)	
8W18	97	D11	21	2+55	X**K	(EXPONENT)	
8W18	98	D11	25	2+56	X**K	(EXPONENT)	
8W18	99	D12	3	2+57	X**K	(EXPONENT)	
8W18	900	D12	21	2+58	X**K	(EXPONENT)	
8W18	901	D12	25	2+59	X**K	(EXPONENT)	
8W07	902	D12	25	2+59	REGISTER	TO MULTIPLY,	OPERAND 1(XK)
8W21	905	E01	5	X TO	ADD	0	
8W21	906	E01	7	X TO	ADD	1	
8W21	907	E01	10	X TO	ADD	2	
8W21	904	E01	21	X TO	ADD	GO	
8W22	91	E01	24	EXCHANGE			
8W13	90	E06	26	INCR	SIGN		
8W09	90	E09	3	2+36	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	91	E09	21	2+37	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	92	E09	25	2+38	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	93	E10	3	2+39	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	94	E10	21	2+40	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	95	E10	25	2+41	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	96	E11	3	2+42	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	97	E11	21	2+43	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	98	E11	25	2+44	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	99	E12	3	2+45	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	900	E12	21	2+46	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W09	901	E12	25	2+47	REGISTER	TO MULTIPLY,	OPERAND (XJ)
8W21	90	F01	5	X TO	ADD	0	
8W21	91	F01	7	X TO	ADD	1	
8W21	92	F01	10	X TO	ADD	2	
8W21	93	F01	21	X TO	ADD	GO	
8W12	901	F01	24	TRANSMIT	LONG	ADD	
8W20	95	F02	5	K TO	MULTIPLY	0	
8W20	96	F02	7	K TO	MULTIPLY	1	

8W20	97	F02	10	K TO MULTIPLY 2
8W20	94	F02	21	K TO MULTIPLY GO
8W19	90	F09	3	2*48 X**J (EXPONENT)
8W19	91	F09	21	2*49 X**J (EXPONENT)
8W19	92	F09	25	2*50 X**J (EXPONENT)
8W19	93	F10	3	2*51 X**J (EXPONENT)
8W19	94	F10	21	2*52 X**J (EXPONENT)
8W19	95	F10	25	2*53 X**J (EXPONENT)
8W19	96	F11	3	2*54 X**J (EXPONENT)
8W19	97	F11	21	2*55 X**J (EXPONENT)
8W19	98	F11	25	2*56 X**J (EXPONENT)
8W19	99	F12	3	2*57 X**J (EXPONENT)
8W19	900	F12	21	2*58 X**J (EXPONENT)
8W19	901	F12	23	2*59 X**J (EXPONENT)
8W09	902	F12	25	2*59 REGISTER TO MULTIPLY, OPERAND (XJ)
8W21	901	G01	5	J TO MULTIPLY 0
8W21	902	G01	7	J TO MULTIPLY 1
8W21	903	G01	10	J TO MULTIPLY 2
8W21	900	G01	21	J TO MULTIPLY GO
8W12	902	G01	24	TRANSMIT SHIFT
8W13	92	G01	26	ADD MINUS
8W22	905	G02	5	X TO MEMORY 0
8W22	906	G02	7	X TO MEMORY 1
8W22	907	G02	10	X TO MEMORY 2
8W22	904	G02	21	X TO MEMORY GO
8W12	903	G02	24	TRANSMIT ADD
8W20	92	G05	4	MEMORY TO D 1
8W20	91	G05	5	MEMORY TO D 0
8W20	93	G05	16	MEMORY TO D 2
8W20	90	G05	17	MEMORY TO D GO
8W20	903	G06	4	MULTIPLY TO X 1
8W20	902	G06	5	MULTIPLY TO X 0
8W20	904	G06	16	MULTIPLY TO X 2
8W20	901	G06	17	MULTIPLY TO X GO
8W20	907	G07	4	ADD TO X 1
8W20	906	G07	5	ADD TO X 0
8W20	98	G07	16	ADD TO X 2
8W20	905	G07	17	ADD TO X GO
8W21	96	G08	4	INCR TO X 1
8W21	95	G08	5	INCR TO X 0
8W21	97	G08	16	INCR TO X 2
8W21	94	G08	17	INCR TO X GO
8W23	90	G11	5	D*1 TO X*1 UPPER
8W23	91	G11	7	D*2 TO X*2 UPPER
8W23	92	G11	10	D*3 TO X*3 UPPER
8W23	93	G11	21	D*4 TO X*4 UPPER
8W23	94	G11	24	D*5 TO X*5 UPPER
8W12	95	G16	5	PACK
8W12	908	G16	7	NORM. (SHIFT)
8W12	96	G16	10	UN-PACK
8W12	97	G16	21	SHIFT (JK)
8W12	99	G16	24	SHIFT LEFT
8W12	98	G16	26	SHIFT NOMINAL
8W12	90	H01	5	LONG ADD IN
8W12	900	H01	7	ROUND SHIFT
8W12	91	H01	10	ADD IN
8W12	93	H01	21	ROUND ADD
8W12	94	H01	24	ADD
8W12	907	H01	26	SHIFT IN
8W22	92	H02	4	ENTER (JK) 2*0
8W22	93	H02	6	ENTER (JK) 2*1
8W22	94	H02	7	ENTER (JK) 2*2

8W22	95	H02	22	ENTER (JK) 2*3
8W22	96	H02	25	ENTER (JK) 2*4
8W22	97	H02	27	ENTER (JK) 2*5
8W30	90	H03	1	2*0 SHIFT TO REGISTER
8W30	91	H03	5	2*1 SHIFT TO REGISTER
8W30	92	H03	25	2*2 SHIFT TO REGISTER
8W30	93	H03	27	2*3 SHIFT TO REGISTER
8W30	94	H04	1	2*4 SHIFT TO REGISTER
8W30	95	H04	5	2*5 SHIFT TO REGISTER
8W30	96	H04	25	2*6 SHIFT TO REGISTER
8W30	97	H04	27	2*7 SHIFT TO REGISTER
8W30	98	H05	1	2*8 SHIFT TO REGISTER
8W30	99	H05	5	2*9 SHIFT TO REGISTER
8W12	904	H06	4	REQUEST RELEASE SHIFT
8W22	98	H06	6	REQUEST RELEASE ADD
8W12	906	H06	8	REQUEST RELEASE LONG ADD
8W13	901	H06	10	LONG ADD INFINITE
8W13	900	H06	12	LONG ADD INDEFINITE
8W18	908	H06	19	ERROR (EXP = 1777 FROM CHASSIS 7)
8W19	908	H06	21	ERROR (EXP = 3777 FROM CHASSIS 8)
8W21	98	H06	23	LONG ADD ZERO CH 5
8W21	99	H06	25	LONG ADD SIGN CH 5
8W22	99	H06	27	LONG ADD SER CONTROL
8W30	900	H17	1	2*10 SHIFT TO REGISTER
8W30	901	H17	5	2*11 SHIFT TO REGISTER
8W30	902	H17	25	2*12 SHIFT TO REGISTER
8W30	903	H17	27	2*13 SHIFT TO REGISTER
8W30	904	H18	1	2*14 SHIFT TO REGISTER
8W30	905	H18	5	2*15 SHIFT TO REGISTER
8W30	906	H18	25	2*16 SHIFT TO REGISTER
8W30	907	H18	27	2*17 SHIFT TO REGISTER
8W28	907	I17	23	2*17 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W12	905	I19	5	LONG ADD - PLUS
8W12	92	I19	9	MASK
8W20	900	I21	12	CLOCK
8W26	90	J01	11	2*0 REGISTER TO ADD, OPERAND 1(XK)
8W26	91	J01	13	2*1 REGISTER TO ADD, OPERAND 1(XK)
8W26	92	J02	11	2*2 REGISTER TO ADD, OPERAND 1(XK)
8W26	93	J02	13	2*3 REGISTER TO ADD, OPERAND 1(XK)
8W26	94	J03	11	2*4 REGISTER TO ADD, OPERAND 1(XK)
8W26	95	J03	13	2*5 REGISTER TO ADD, OPERAND 1(XK)
8W28	96	J04	7	2*6 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	96	J04	8	2*6 REGISTER TO ADD, O-E-AND 100K)
8W28	97	J04	21	2*7 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	97	J04	24	2*7 REGISTER TO ADD, OPERAND 1(XK)
8W28	98	J05	7	2*8 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	98	J05	8	2*8 REGISTER TO ADD, OPERAND 1(XK)
8W28	99	J05	21	2*9 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	99	J05	24	2*9 REGISTER TO ADD, OPERAND 1(XK)
8W28	900	J06	7	2*10 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	900	J06	8	2*10 REGISTER TO ADD, OPERAND 1(XK)
8W28	901	J06	21	2*11 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	901	J06	24	2*11 REGISTER TO ADD, OPERAND 1(XK)
8W26	902	J07	6	2*12 REGISTER TO ADD, OPERAND 1(XK)
8W28	902	J07	7	2*12 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	903	J07	21	2*13 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	903	J07	24	2*13 REGISTER TO ADD, OPERAND 1(XK)
8W26	904	J08	6	2*14 REGISTER TO ADD, OPERAND 1(XK)
8W28	904	J08	7	2*14 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	905	J08	21	2*15 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	905	J08	24	2*15 REGISTER TO ADD, OPERAND 1(XK)
8W26	906	J09	6	2*16 REGISTER TO ADD, OPERAND 1(XK)

8W28	906	J09	7	2*16	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W26	907	J09	24	2*17	REGISTER TO ADD, OPERAND 1(XK)
8W27	90	J10	6	2*18	REGISTER TO ADD, OPERAND 1(XK)
8W29	90	J10	7	2*18	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	91	J10	21	2*19	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	91	J10	24	2*19	REGISTER TO ADD, OPERAND 1(XK)
8W27	92	J11	6	2*20	REGISTER TO ADD, OPERAND 1(XK)
8W29	92	J11	7	2*20	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	93	J11	21	2*21	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	93	J11	24	2*21	REGISTER TO ADD, OPERAND 1(XK)
8W27	94	K01	6	2*22	REGISTER TO ADD, OPERAND 1(XK)
8W29	94	K01	7	2*22	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	95	K01	21	2*23	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	95	K01	24	2*23	REGISTER TO ADD, OPERAND 1(XK)
8W27	96	K02	6	2*24	REGISTER TO ADD, OPERAND 1(XK)
8W29	96	K02	7	2*24	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	97	K02	21	2*25	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	97	K02	24	2*25	REGISTER TO ADD, OPERAND 1(XK)
8W27	98	K03	6	2*26	REGISTER TO ADD, OPERAND 1(XK)
8W29	98	K03	7	2*26	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	99	K03	21	2*27	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	99	K03	24	2*27	REGISTER TO ADD, OPERAND 1(XK)
8W27	900	K04	6	2*28	REGISTER TO ADD, OPERAND 1(XK)
8W29	900	K04	7	2*28	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	901	K04	21	2*29	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	901	K04	24	2*29	REGISTER TO ADD, OPERAND 1(XK)
8W27	902	K05	6	2*30	REGISTER TO ADD, OPERAND 1(XK)
8W29	902	K05	7	2*30	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	903	K05	21	2*31	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	903	K05	24	2*31	REGISTER TO ADD, OPERAND 1(XK)
8W27	904	K06	6	2*32	REGISTER TO ADD, OPERAND 1(XK)
8W29	904	K06	7	2*32	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	905	K06	21	2*33	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	905	K06	24	2*33	REGISTER TO ADD, OPERAND 1(XK)
8W27	906	K07	6	2*34	REGISTER TO ADD, OPERAND 1(XK)
8W29	906	K07	7	2*34	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29	907	K07	21	2*35	REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W27	907	K07	24	2*35	REGISTER TO ADD, OPERAND 1(XK)
8W28	90	L09	14	2*0	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	91	L09	18	2*1	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	92	L10	14	2*2	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	93	L10	18	2*3	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	94	L11	14	2*4	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28	95	L11	18	2*5	REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W24	90	M01	1	2*0	ADD TO REGISTER
8W24	91	M01	6	2*1	ADD TO REGISTER
8W24	92	M01	23	2*2	ADD TO REGISTER
8W24	93	M01	28	2*3	ADD TO REGISTER
8W24	94	M02	1	2*4	ADD TO REGISTER
8W24	95	M02	6	2*5	ADD TO REGISTER
8W24	96	M02	23	2*6	ADD TO REGISTER
8W24	97	M02	28	2*7	ADD TO REGISTER
8W24	98	M03	1	2*8	ADD TO REGISTER
8W24	99	M03	6	2*9	ADD TO REGISTER
8W24	900	M03	23	2*10	ADD TO REGISTER
8W24	901	M03	28	2*11	ADD TO REGISTER
8W24	902	M04	1	2*12	ADD TO REGISTER
8W24	903	M04	6	2*13	ADD TO REGISTER
8W24	904	M04	23	2*14	ADD TO REGISTER
8W24	905	M04	28	2*15	ADD TO REGISTER
8W24	906	M05	1	2*16	ADD TO REGISTER
8W24	907	M05	6	2*17	ADD TO REGISTER

8W25	90	M05	23	2*18	ADD	TO	REGISTER
8W25	91	M05	28	2*19	ADD	TO	REGISTER
8W25	92	N01	1	2*20	ADD	TO	REGISTER
8W25	93	N01	6	2*21	ADD	TO	REGISTER
8W25	94	N01	23	2*22	ADD	TO	REGISTER
8W25	95	N01	28	2*23	ADD	TO	REGISTER
8W25	96	N02	1	2*24	ADD	TO	REGISTER
8W25	97	N02	6	2*25	ADD	TO	REGISTER
8W25	98	N02	23	2*26	ADD	TO	REGISTER
8W25	99	N02	28	2*27	ADD	TO	REGISTER
8W25	900	N03	1	2*28	ADD	TO	REGISTER
8W25	901	N03	6	2*29	ADD	TO	REGISTER
8W25	902	N03	23	2*30	ADD	TO	REGISTER
8W25	903	N03	28	2*31	ADD	TO	REGISTER
8W25	904	N04	1	2*32	ADD	TO	REGISTER
8W25	905	N04	6	2*33	ADD	TO	REGISTER
8W25	906	N04	23	2*34	ADD	TO	REGISTER
8W25	907	N04	28	2*35	ADD	TO	REGISTER

CHASSIS 9

9W11	97	C14	14	MEMORY MARGIN
9W20	900	G35	12	CLOCK
9W20	904	G38	5	2*3 CHASSIS 9 SELECT (010)
9W20	903	G38	9	2*2 CHASSIS 9 SELECT (010)
9W20	905	G38	14	2*4 CHASSIS 9 SELECT (010)
9W14	98	G38	20	WRIT
9W14	97	G38	21	GO
9W20	901	G39	18	2*0 BANK SELECT
9W20	902	G39	22	2*1 BANK SELECT
9W07	99	G39	28	ACCEPT
9W20	906	G40	11	2*5 ADDRESS TO MEMORY
9W20	907	G40	14	2*6 ADDRESS TO MEMORY
9W20	908	G40	17	2*7 ADDRESS TO MEMORY
9W20	90	G40	18	2*8 ADDRESS TO MEMORY
9W20	91	G41	11	2*9 ADDRESS TO MEMORY
9W20	92	G41	14	2*10 ADDRESS TO MEMORY
9W20	93	G41	17	2*11 ADDRESS TO MEMORY
9W20	94	G41	18	2*12 ADDRESS TO MEMORY
9W20	95	G42	11	2*13 ADDRESS TO MEMORY
9W20	96	G42	14	2*14 ADDRESS TO MEMORY
9W20	97	G42	17	2*15 ADDRESS TO MEMORY
9W20	98	G42	18	2*16 ADDRESS TO MEMORY
9W07	900	H19	1	2*0
9W03	900	H19	2	2*0 FAN OUT TO MEMORY
9W07	901	H19	8	2*1
9W03	901	H19	19	2*1 FAN OUT TO MEMORY
9W03	902	H19	24	2*2 FAN OUT TO MEMORY
9W07	902	H19	28	2*2
9W07	903	H20	1	2*3
9W03	903	H20	2	2*3 FAN OUT TO MEMORY
9W07	904	H20	8	2*4
9W03	904	H20	19	2*4 FAN OUT TO MEMORY
9W03	905	H20	24	2*5 FAN OUT TO MEMORY
9W07	905	H20	28	2*5
9W07	906	H21	1	2*6
9W03	906	H21	2	2*6 FAN OUT TO MEMORY
9W07	907	H21	8	2*7
9W03	907	H21	19	2*7 FAN OUT TO MEMORY
9W03	90	H21	24	2*8 FAN OUT TO MEMORY
9W07	90	H21	28	2*8
9W07	91	H22	1	2*9
9W03	91	H22	2	2*9 FAN OUT TO MEMORY
9W07	92	H22	8	2*10
9W03	92	H22	19	2*10 FAN OUT TO MEMORY
9W03	93	H22	24	2*11 FAN OUT TO MEMORY
9W07	93	H22	28	2*11
9W07	94	H24	1	2*12
9W03	94	H24	2	2*12 FAN OUT TO MEMORY
9W07	95	H24	8	2*13
9W03	95	H24	19	2*13 FAN OUT TO MEMORY
9W03	96	H24	24	2*14 FAN OUT TO MEMORY
9W07	96	H24	28	2*14
9W08	900	H25	1	2*15
9W04	900	H25	2	2*15 FAN OUT TO MEMORY
9W08	901	H25	8	2*16
9W04	901	H25	19	2*16 FAN OUT TO MEMORY
9W04	902	H25	24	2*17 FAN OUT TO MEMORY
9W08	902	H25	28	2*17
9W08	903	H26	1	2*18
9W04	903	H26	2	2*18 FAN OUT TO MEMORY

9W08	904	H26	8	2*19	
9W04	904	H26	19	2*19	FAN OUT TO MEMORY
9W04	905	H26	24	2*20	FAN OUT TO MEMORY
9W08	905	H26	28	2*20	
9W08	906	H27	1	2*21	
9W04	906	H27	2	2*21	FAN OUT TO MEMORY
9W08	907	H27	8	2*22	
9W04	907	H27	19	2*22	FAN OUT TO MEMORY
9W04	90	H27	24	2*23	FAN OUT TO MEMORY
9W08	90	H27	28	2*23	
9W08	91	H29	1	2*24	
9W04	91	H29	2	2*24	FAN OUT TO MEMORY
9W08	92	H29	8	2*25	
9W04	92	H29	19	2*25	FAN OUT TO MEMORY
9W04	93	H29	24	2*26	FAN OUT TO MEMORY
9W08	93	H29	28	2*26	
9W08	94	H30	1	2*27	
9W04	94	H30	2	2*27	FAN OUT TO MEMORY
9W08	95	H30	8	2*28	
9W04	95	H30	19	2*28	FAN OUT TO MEMORY
9W04	96	H30	24	2*29	FAN OUT TO MEMORY
9W08	96	H30	28	2*29	
9W39	900	H31	1	2*30	
9W05	900	H31	2	2*30	FAN OUT TO MEMORY
9W39	901	H31	8	2*31	
9W05	901	H31	19	2*31	FAN OUT TO MEMORY
9W05	902	H31	24	2*32	FAN OUT TO MEMORY
9W39	902	H31	28	2*32	
9W39	903	H32	1	2*33	
9W05	903	H32	2	2*33	FAN OUT TO MEMORY
9W39	904	H32	8	2*34	
9W05	904	H32	19	2*34	FAN OUT TO MEMORY
9W05	905	H32	24	2*35	FAN OUT TO MEMORY
9W39	905	H32	28	2*35	
9W39	906	H34	1	2*36	
9W05	906	H34	2	2*36	FAN OUT TO MEMORY
9W39	907	H34	8	2*37	
9W05	907	H34	19	2*37	FAN OUT TO MEMORY
9W05	90	H34	24	2*38	FAN OUT TO MEMORY
9W39	90	H34	28	2*38	
9W39	91	H35	1	2*39	
9W05	91	H35	2	2*39	FAN OUT TO MEMORY
9W39	92	H35	8	2*40	
9W05	92	H35	19	2*40	FAN OUT TO MEMORY
9W05	93	H35	24	2*41	FAN OUT TO MEMORY
9W39	93	H35	28	2*41	
9W39	94	H36	1	2*42	
9W05	94	H36	2	2*42	FAN OUT TO MEMORY
9W39	95	H36	8	2*43	
9W05	95	H36	19	2*43	FAN OUT TO MEMORY
9W05	96	H36	24	2*44	FAN OUT TO MEMORY
9W39	96	H36	28	2*44	
9W09	900	H37	1	2*45	
9W06	900	H37	2	2*45	FAN OUT TO MEMORY
9W09	901	H37	8	2*46	
9W06	901	H37	19	2*46	FAN OUT TO MEMORY
9W06	902	H37	24	2*47	FAN OUT TO MEMORY
9W09	902	H37	28	2*47	
9W09	903	H39	1	2*48	
9W06	903	H39	2	2*48	FAN OUT TO MEMORY
9W09	904	H39	8	2*49	
9W06	904	H39	19	2*49	FAN OUT TO MEMORY

9W06	905	H39	24	2*50	FAN OUT TO MEMORY
9W09	905	H39	28	2*50	
9W09	906	H40	1	2*51	
9W06	906	H40	2	2*51	FAN OUT TO MEMORY
9W09	907	H40	8	2*52	
9W06	907	H40	19	2*52	FAN OUT TO MEMORY
9W06	90	H40	24	2*53	FAN OUT TO MEMORY
9W09	90	H40	28	2*53	
9W09	91	H41	1	2*54	
9W06	91	H41	2	2*54	FAN OUT TO MEMORY
9W09	92	H41	8	2*55	
9W06	92	H41	19	2*55	FAN OUT TO MEMORY
9W06	93	H41	24	2*56	FAN OUT TO MEMORY
9W09	93	H41	28	2*56	
9W09	94	H42	1	2*57	
9W06	94	H42	2	2*57	FAN OUT TO MEMORY
9W09	95	H42	8	2*58	
9W06	95	H42	19	2*58	FAN OUT TO MEMORY
9W06	96	H42	24	2*59	FAN OUT TO MEMORY
9W09	96	H42	28	2*59	
9W14	99	I24	12	READ	PERIPHERAL
9W11	900	I25	1	2*30	CENTRAL TO PERIPHERAL DATA
9W14	900	I25	3	2*30	
9W13	900	I25	5	2*30	
9W39	900	I25	7	2*30	
9W15	900	I25	9	2*30	
9W16	900	I25	11	2*30	
9W17	900	I25	19	2*30	
9W18	900	I25	21	2*30	
9W19	900	I25	23	2*30	
9W10	900	I25	26	2*30	BUFFER TO CONTROL
9W12	900	I25	28	2*30	MEMORY TO REGISTER
9W11	901	I26	1	2*31	CENTRAL TO PERIPHERAL DATA
9W14	901	I26	3	2*31	
9W13	901	I26	5	2*31	
9W39	901	I26	7	2*31	
9W15	901	I26	9	2*31	
9W16	901	I26	11	2*31	
9W17	901	I26	19	2*31	
9W18	901	I26	21	2*31	
9W19	901	I26	23	2*31	
9W10	901	I26	26	2*31	BUFFER TO CONTROL
9W12	901	I26	28	2*31	MEMORY TO REGISTER
9W11	902	I27	1	2*32	CENTRAL TO PERIPHERAL DATA
9W14	902	I27	3	2*32	
9W13	902	I27	5	2*32	
9W39	902	I27	7	2*32	
9W15	902	I27	9	2*32	
9W16	902	I27	11	2*32	
9W17	902	I27	19	2*32	
9W18	902	I27	21	2*32	
9W19	902	I27	23	2*32	
9W10	902	I27	26	2*32	BUFFER TO CONTROL
9W12	902	I27	28	2*32	MEMORY TO REGISTER
9W11	903	I28	1	2*33	CENTRAL TO PERIPHERAL DATA
9W14	903	I28	3	2*33	
9W13	903	I28	5	2*33	
9W39	903	I28	7	2*33	
9W15	903	I28	9	2*33	
9W16	903	I28	11	2*33	
9W17	903	I28	19	2*33	
9W18	903	I28	21	2*33	

9W19	903	128	23	2*33	
9W10	903	128	26	2*33	BUFFER TO CONTROL
9W12	903	128	28	2*33	MEMORY TO REGISTER
9W11	904	129	1	2*34	CENTRAL TO PERIPHERAL DATA
9W14	904	129	3	2*34	
9W13	904	129	5	2*34	
9W39	904	129	7	2*34	
9W15	904	129	9	2*34	
9W16	904	129	11	2*34	
9W17	904	129	19	2*34	
9W18	904	129	21	2*34	
9W19	904	129	23	2*34	
9W10	904	129	26	2*34	BUFFER TO CONTROL
9W12	904	129	28	2*34	MEMORY TO REGISTER
9W11	905	132	1	2*35	CENTRAL TO PERIPHERAL DATA
9W14	905	132	3	2*35	
9W13	905	132	5	2*35	
9W39	905	132	7	2*35	
9W15	905	132	9	2*35	
9W16	905	132	11	2*35	
9W17	905	132	19	2*35	
9W18	905	132	21	2*35	
9W19	905	132	23	2*35	
9W10	905	132	26	2*35	BUFFER TO CONTROL
9W12	905	132	28	2*35	MEMORY TO REGISTER
9W11	906	133	1	2*36	CENTRAL TO PERIPHERAL DATA
9W14	906	133	3	2*36	
9W13	906	133	5	2*36	
9W39	906	133	7	2*36	
9W15	906	133	9	2*36	
9W16	906	133	11	2*36	
9W17	906	133	19	2*36	
9W18	906	133	21	2*36	
9W19	906	133	23	2*36	
9W10	906	133	26	2*36	BUFFER TO CONTROL
9W12	906	133	28	2*36	MEMORY TO REGISTER
9W11	907	134	1	2*37	CENTRAL TO PERIPHERAL DATA
9W14	907	134	3	2*37	
9W13	907	134	5	2*37	
9W39	907	134	7	2*37	
9W15	907	134	9	2*37	
9W16	907	134	11	2*37	
9W17	907	134	19	2*37	
9W18	907	134	21	2*37	
9W19	907	134	23	2*37	
9W10	907	134	26	2*37	BUFFER TO CONTROL
9W21	901	134	28	2*37	MEMORY TO REGISTER
9W11	90	135	1	2*38	CENTRAL TO PERIPHERAL DATA
9W14	90	135	3	2*38	
9W13	90	135	5	2*38	
9W39	90	135	7	2*38	
9W15	90	135	9	2*38	
9W16	90	135	11	2*38	
9W17	90	135	19	2*38	
9W18	90	135	21	2*38	
9W19	90	135	23	2*38	
9W10	90	135	26	2*38	BUFFER TO CONTROL
9W21	902	135	28	2*38	MEMORY TO REGISTER
9W11	91	136	1	2*39	CENTRAL TO PERIPHERAL DATA
9W14	91	136	3	2*39	
9W13	91	136	5	2*39	
9W39	91	136	7	2*39	

9W15	91	I36	9	2*39	
9W16	91	I36	11	2*39	
9W17	91	I36	19	2*39	
9W18	91	I36	21	2*39	
9W19	91	I36	23	2*39	
9W10	91	I36	26	2*39	BUFFER TO CONTROL
9W21	903	I36	28		
9W11	92	I38	1	2*40	CENTRAL TO PERIPHERAL DATA
9W14	92	I38	3	2*40	
9W13	92	I38	5	2*40	
9W39	92	I38	7	2*40	
9W15	92	I38	9	2*40	
9W16	92	I38	11	2*40	
9W17	92	I38	19	2*40	
9W18	92	I38	21	2*40	
9W19	92	I38	23	2*40	
9W10	92	I38	26	2*40	BUFFER TO CONTROL
9W21	904	I38	28	2*40	MEMORY TO REGISTER
9W11	93	I39	1	2*41	CENTRAL TO PERIPHERAL DATA
9W14	93	I39	3	2*41	
9W13	93	I39	5	2*41	
9W39	93	I39	7	2*41	
9W15	93	I39	9	2*41	
9W16	93	I39	11	2*41	
9W17	93	I39	19	2*41	
9W18	93	I39	21	2*41	
9W19	93	I39	23	2*41	
9W10	93	I39	26	2*41	BUFFER TO CONTROL
9W21	905	I39	28	2*41	MEMORY TO REGISTER
9W11	94	I40	1	2*42	CENTRAL TO PERIPHERAL DATA
9W14	94	I40	3	2*42	
9W13	94	I40	5	2*42	
9W39	94	I40	7	2*42	
9W15	94	I40	9	2*42	
9W16	94	I40	11	2*42	
9W17	94	I40	19	2*42	
9W18	94	I40	21	2*42	
9W19	94	I40	23	2*42	
9W10	94	I40	26	2*42	BUFFER TO CONTROL
9W21	906	I40	28	2*42	MEMORY TO REGISTER
9W11	95	I41	1	2*43	CENTRAL TO PERIPHERAL DATA
9W14	95	I41	3	2*43	
9W13	95	I41	5	2*43	
9W39	95	I41	7	2*43	
9W15	95	I41	9	2*43	
9W16	95	I41	11	2*43	
9W17	95	I41	19	2*43	
9W18	95	I41	21	2*43	
9W19	95	I41	23	2*43	
9W10	95	I41	26	2*43	BUFFER TO CONTROL
9W21	907	I41	28	2*43	MEMORY TO REGISTER
9W11	96	I42	1	2*44	CENTRAL TO PERIPHERAL DATA
9W14	96	I42	3	2*44	
9W13	96	I42	5	2*44	
9W39	96	I42	7	2*44	
9W15	96	I42	9	2*44	
9W16	96	I42	11	2*44	
9W17	96	I42	19	2*44	
9W18	96	I42	21	2*44	
9W19	96	I42	23	2*44	
9W10	96	I42	26	2*44	BUFFER TO CONTROL
9W21	908	I42	28	2*44	MEMORY TO REGISTER

9W36	907	J31	1	CLOCK (1 M SEC)
9W34	907	J31	3	CLOCK (1US)
9W34	906	J31	13	
9W36	906	J31	15	
9W34	903	J33	3	INACTIVE
9W34	905	J33	5	EMPTY
9W34	904	J33	7	FULL
9W36	902	J33	10	ACTIVE
9W36	903	J33	21	INACTIVE
9W36	905	J33	22	EMPTY
9W36	904	J33	23	FULL
9W34	902	J33	28	ACTIVE
9W34	92	J35	3	2*2 INPUT DATA
9W34	91	J35	5	2*1 INPUT DATA
9W34	90	J35	7	2*0 INPUT DATA
9W36	95	J35	8	2*5 INPUT DATA
9W36	94	J35	9	2*4 INPUT DATA
9W36	93	J35	10	2*3 INPUT DATA
9W36	92	J35	21	2*2 INPUT DATA
9W36	91	J35	22	2*1 INPUT DATA
9W36	90	J35	23	2*0 INPUT DATA
9W34	95	J35	24	2*5 INPUT DATA
9W34	94	J35	26	2*4 INPUT DATA
9W34	93	J35	28	2*3 INPUT DATA
9W34	98	J37	3	2*8 INPUT DATA
9W34	97	J37	5	2*7 INPUT DATA
9W34	96	J37	7	2*6 INPUT DATA
9W36	901	J37	8	2*11 INPUT DATA
9W36	900	J37	9	2*10 INPUT DATA
9W36	99	J37	10	2*9 INPUT DATA
9W36	98	J37	21	2*8 INPUT DATA
9W36	97	J37	22	2*7 INPUT DATA
9W36	96	J37	23	2*6 INPUT DATA
9W34	901	J37	24	2*11 INPUT DATA
9W34	900	J37	26	2*10 INPUT DATA
9W34	99	J37	28	2*9 INPUT DATA
9W37	906	J38	1	FUNCTION
9W35	906	J38	3	FUNCTION
9W35	902	J38	13	ACTIVE
9W37	903	J38	14	INACTIVE
9W37	902	J38	15	ACTIVE
9W35	903	J38	16	INACTIVE
9W35	904	J38	26	FULL
9W37	904	J38	28	FULL
9W37	905	J39	1	EMPTY
9W35	905	J39	3	EMPTY
9W35	907	J39	13	MASTER CLEAR
9W37	907	J39	15	MASTER CLEAR
9W37	90	J40	1	2*0 OUTPUT DATA
9W35	90	J40	3	2*0 OUTPUT DATA
9W35	91	J40	13	2*1 OUTPUT DATA
9W37	92	J40	14	2*2 OUTPUT DATA
9W37	91	J40	15	2*1 OUTPUT DATA
9W35	92	J40	16	2*2 OUTPUT DATA
9W35	93	J40	26	2*3 OUTPUT DATA
9W37	93	J40	28	2*3 OUTPUT DATA
9W37	94	J41	1	2*4 OUTPUT DATA
9W35	94	J41	3	2*4 OUTPUT DATA
9W35	95	J41	13	2*5 OUTPUT DATA
9W37	96	J41	14	2*6 OUTPUT DATA
9W37	95	J41	15	2*5 OUTPUT DATA
9W35	96	J41	16	2*6 OUTPUT DATA

9W35	97	J41	26	2*7	OUTPUT DATA
9W37	97	J41	28	2*7	OUTPUT DATA
9W37	98	J42	1	2*8	OUTPUT DATA
9W35	98	J42	3	2*8	OUTPUT DATA
9W35	99	J42	13	2*9	OUTPUT DATA
9W37	900	J42	14	2*10	OUTPUT DATA
9W37	99	J42	15	2*9	OUTPUT DATA
9W35	900	J42	16	2*10	OUTPUT DATA
9W35	901	J42	26	2*11	OUTPUT DATA
9W37	901	J42	28	2*11	OUTPUT DATA



CHASSIS 10

10W11	97	C14	14	MEMORY MARGIN
10W20	906	G01	11	2+5 ADDRESS TO MEMORY
10W20	907	G01	14	2+6 ADDRESS TO MEMORY
10W20	908	G01	17	2+7 ADDRESS TO MEMORY
10W20	90	G01	18	2+8 ADDRESS TO MEMORY
10W20	91	G02	11	2+9 ADDRESS TO MEMORY
10W20	92	G02	14	2+10 ADDRESS TO MEMORY
10W20	93	G02	17	2+11 ADDRESS TO MEMORY
10W20	94	G02	18	2+12 ADDRESS TO MEMORY
10W20	95	G03	11	2+13 ADDRESS TO MEMORY
10W20	96	G03	14	2+14 ADDRESS TO MEMORY
10W20	97	G03	17	2+15 ADDRESS TO MEMORY
10W20	98	G03	18	2+16 ADDRESS TO MEMORY
10W20	904	G04	5	2+3 CHASSIS 10 SELECT (011)
10W20	903	G04	8	2+2 CHASSIS 10 SELECT (011)
10W20	905	G04	14	2+4 CHASSIS 10 SELECT (011)
10W14	98	G04	20	WRITE
10W14	97	G04	21	GO
10W20	901	G05	18	2+0 BANK SELECT
10W20	902	G05	22	2+1 BANK SELECT
10W13	97	G05	28	ACCEPT FROM CHAS, 10
10W20	900	G13	12	CLOCK
10W07	900	H01	1	2+0
10W03	900	H01	2	2+0 FAN OUT TO MEMORY
10W07	901	H01	8	2+1
10W03	901	H01	19	2+1 FAN OUT TO MEMORY
10W03	902	H01	24	2+2 FAN OUT TO MEMORY
10W07	902	H01	28	2+2
10W07	903	H02	1	2+3
10W03	903	H02	2	2+3 FAN OUT TO MEMORY
10W07	904	H02	8	2+4
10W03	904	H02	19	2+4 FAN OUT TO MEMORY
10W03	905	H02	24	2+5 FAN OUT TO MEMORY
10W07	905	H02	28	2+5
10W07	906	H03	1	2+6
10W03	906	H03	2	2+6 FAN OUT TO MEMORY
10W07	907	H03	8	2+7
10W03	907	H03	19	2+7 FAN OUT TO MEMORY
10W03	90	H03	24	2+8 FAN OUT TO MEMORY
10W07	90	H03	28	2+8
10W07	91	H04	1	2+9
10W03	91	H04	2	2+9 FAN OUT TO MEMORY
10W07	92	H04	8	2+10
10W03	92	H04	19	2+10 FAN OUT TO MEMORY
10W03	93	H04	24	2+11 FAN OUT TO MEMORY
10W07	93	H04	28	2+11
10W07	94	H06	1	2+12
10W03	94	H06	2	2+12 FAN OUT TO MEMORY
10W07	95	H06	8	2+13
10W03	95	H06	19	2+13 FAN OUT TO MEMORY
10W03	96	H06	24	2+14 FAN OUT TO MEMORY
10W07	96	H06	28	2+14
10W08	900	H07	1	2+15
10W04	900	H07	2	2+15 FAN OUT TO MEMORY
10W08	901	H07	8	2+16
10W04	901	H07	19	2+16 FAN OUT TO MEMORY
10W04	902	H07	24	2+17 FAN OUT TO MEMORY
10W08	902	H07	28	2+17
10W08	903	H08	1	2+18
10W04	903	H08	2	2+18 FAN OUT TO MEMORY

10W08	904	H08	8	2*19	
10W04	904	H08	19	2*19	FAN OUT TO MEMORY
10W04	905	H08	24	2*20	FAN OUT TO MEMORY
10W08	905	H08	28	2*20	
10W08	906	H09	1	2*21	
10W04	906	H09	2	2*21	FAN OUT TO MEMORY
10W08	907	H09	8	2*22	
10W04	907	H09	19	2*22	FAN OUT TO MEMORY
10W04	90	H09	24	2*23	FAN OUT TO MEMORY
10W08	90	H09	28	2*23	
10W08	91	H11	1	2*24	
10W04	91	H11	2	2*24	FAN OUT TO MEMORY
10W08	92	H11	8	2*25	
10W04	92	H11	19	2*25	FAN OUT TO MEMORY
10W04	93	H11	24	2*26	FAN OUT TO MEMORY
10W08	93	H11	28	2*26	
10W08	94	H12	1	2*27	
10W04	94	H12	2	2*27	FAN OUT TO MEMORY
10W08	95	H12	8	2*28	
10W04	95	H12	19	2*28	FAN OUT TO MEMORY
10W04	96	H12	24	2*29	FAN OUT TO MEMORY
10W08	96	H12	28	2*29	
10W09	900	H13	1	2*30	
10W05	900	H13	2	2*30	FAN OUT TO MEMORY
10W09	901	H13	8	2*31	
10W05	901	H13	19	2*31	FAN OUT TO MEMORY
10W05	902	H13	24	2*32	FAN OUT TO MEMORY
10W09	902	H13	28	2*32	
10W09	903	H14	1	2*33	
10W05	903	H14	2	2*33	FAN OUT TO MEMORY
10W09	904	H14	8	2*34	
10W05	904	H14	19	2*34	FAN OUT TO MEMORY
10W05	905	H14	24	2*35	FAN OUT TO MEMORY
10W09	905	H14	28	2*35	
10W09	906	H16	1	2*36	
10W05	906	H16	2	2*36	FAN OUT TO MEMORY
10W09	907	H16	8	2*37	
10W05	907	H16	19	2*37	FAN OUT TO MEMORY
10W05	90	H16	24	2*38	FAN OUT TO MEMORY
10W09	90	H16	28	2*38	
10W09	91	H17	1	2*39	
10W05	91	H17	2	2*39	FAN OUT TO MEMORY
10W09	92	H17	8	2*40	
10W05	92	H17	19	2*40	FAN OUT TO MEMORY
10W05	93	H17	24	2*41	FAN OUT TO MEMORY
10W09	93	H17	28	2*41	
10W09	94	H18	1	2*42	
10W05	94	H18	2	2*42	FAN OUT TO MEMORY
10W09	95	H18	8	2*43	
10W05	95	H18	19	2*43	FAN OUT TO MEMORY
10W05	96	H18	24	2*44	FAN OUT TO MEMORY
10W09	96	H18	28	2*44	
10W39	900	H19	1	2*45	
10W06	900	H19	2	2*45	FAN OUT TO MEMORY
10W39	901	H19	8	2*46	
10W06	901	H19	19	2*46	FAN OUT TO MEMORY
10W06	902	H19	24	2*47	FAN OUT TO MEMORY
10W39	902	H19	28	2*47	
10W39	903	H21	1	2*48	
10W06	903	H21	2	2*48	FAN OUT TO MEMORY
10W39	904	H21	8	2*49	
10W06	904	H21	19	2*49	FAN OUT TO MEMORY

10W06	905	H21	24	2+50	FAN OUT TO MEMORY
10W39	905	H21	28	2+50	
10W39	906	H22	1	2+51	
10W06	906	H22	2	2+51	FAN OUT TO MEMORY
10W39	907	H22	8	2+52	
10W06	907	H22	19	2+52	FAN OUT TO MEMORY
10W06	90	H22	24	2+53	FAN OUT TO MEMORY
10W39	90	H22	28	2+53	
10W39	91	H23	1	2+54	
10W06	91	H23	2	2+54	FAN OUT TO MEMORY
10W39	92	H23	8	2+55	
10W06	92	H23	19	2+55	FAN OUT TO MEMORY
10W06	93	H23	24	2+56	FAN OUT TO MEMORY
10W39	93	H23	28	2+56	
10W39	94	H24	1	2+57	
10W06	94	H24	2	2+57	FAN OUT TO MEMORY
10W39	95	H24	8	2+58	
10W06	95	H24	19	2+58	FAN OUT TO MEMORY
10W06	96	H24	24	2+59	FAN OUT TO MEMORY
10W39	96	H24	28	2+59	
10W11	900	I01	1	2+45	CENTRAL TO PERIPHERAL DATA
10W14	900	I01	3	2+45	
10W13	900	I01	5	2+45	
10W15	900	I01	7	2+45	
10W39	900	I01	9	2+45	
10W16	900	I01	11	2+45	
10W17	900	I01	19	2+45	
10W18	900	I01	21	2+45	
10W19	900	I01	23	2+45	
10W10	900	I01	26	2+45	
10W12	90	I01	28	2+45	MEMORY REGISTER
10W11	901	I02	1	2+46	CENTRAL TO PERIPHERAL DATA
10W14	901	I02	3	2+46	
10W13	901	I02	5	2+46	
10W15	901	I02	7	2+46	
10W39	901	I02	9	2+46	
10W16	901	I02	11	2+46	
10W17	901	I02	19	2+46	
10W18	901	I02	21	2+46	
10W19	901	I02	23	2+46	
10W10	901	I02	26	2+46	
10W12	91	I02	28	2+46	MEMORY REGISTER
10W11	902	I03	1	2+47	CENTRAL TO PERIPHERAL DATA
10W14	902	I03	3	2+47	
10W13	902	I03	5	2+47	
10W15	902	I03	7	2+47	
10W39	902	I03	9	2+47	
10W16	902	I03	11	2+47	
10W17	902	I03	19	2+47	
10W18	902	I03	21	2+47	
10W19	902	I03	23	2+47	
10W10	902	I03	26	2+47	
10W12	92	I03	28	2+47	MEMORY REGISTER
10W11	903	I04	1	2+48	CENTRAL TO PERIPHERAL DATA
10W14	903	I04	3	2+48	
10W13	903	I04	5	2+48	
10W15	903	I04	7	2+48	
10W39	903	I04	9	2+48	
10W16	903	I04	11	2+48	
10W17	903	I04	19	2+48	
10W18	903	I04	21	2+48	
10W19	903	I04	23	2+48	

10W10	903	I04	26	2*48	
10W12	93	I04	28	2*48	MEMORY REGISTER
10W11	904	I05	1	2*49	CENTRAL TO PERIPHERAL DATA
10W14	904	I05	3	2*49	
10W13	904	I05	5	2*49	
10W15	904	I05	7	2*49	
10W39	904	I05	9	2*49	
10W16	904	I05	11	2*49	
10W17	904	I05	19	2*49	
10W18	904	I05	21	2*49	
10W19	904	I05	23	2*49	
10W10	904	I05	26	2*49	
10W12	94	I05	28	2*49	MEMORY REGISTER
10W11	905	I08	1	2*50	CENTRAL TO PERIPHERAL DATA
10W14	905	I08	3	2*50	
10W13	905	I08	5	2*50	
10W15	905	I08	7	2*50	
10W39	905	I08	9	2*50	
10W16	905	I08	11	2*50	
10W17	905	I08	19	2*50	
10W18	905	I08	21	2*50	
10W19	905	I08	23	2*50	
10W10	905	I08	26	2*50	
10W12	95	I08	28	2*50	MEMORY REGISTER
10W11	906	I09	1	2*51	CENTRAL TO PERIPHERAL DATA
10W14	906	I09	3	2*51	
10W13	906	I09	5	2*51	
10W15	906	I09	7	2*51	
10W39	906	I09	9	2*51	
10W16	906	I09	11	2*51	
10W17	906	I09	19	2*51	
10W18	906	I09	21	2*51	
10W19	906	I09	23	2*51	
10W10	906	I09	26	2*51	
10W12	96	I09	28	2*51	MEMORY REGISTER
10W11	907	I10	1	2*52	CENTRAL TO PERIPHERAL DATA
10W14	907	I10	3	2*52	
10W13	907	I10	5	2*52	
10W15	907	I10	7	2*52	
10W39	907	I10	9	2*52	
10W16	907	I10	11	2*52	
10W17	907	I10	19	2*52	
10W18	907	I10	21	2*52	
10W19	907	I10	23	2*52	
10W10	907	I10	26	2*52	
10W12	97	I10	28	2*52	MEMORY REGISTER
10W11	90	I11	1	2*53	CENTRAL TO PERIPHERAL DATA
10W14	90	I11	3	2*53	
10W13	90	I11	5	2*53	
10W15	90	I11	7	2*53	
10W39	90	I11	9	2*53	
10W16	90	I11	11	2*53	
10W17	90	I11	19	2*53	
10W18	90	I11	21	2*53	
10W19	90	I11	23	2*53	
10W10	90	I11	26	2*53	
10W12	98	I11	28	2*53	MEMORY REGISTER
10W11	91	I12	1	2*54	CENTRAL TO PERIPHERAL DATA
10W14	91	I12	3	2*54	
10W13	91	I12	5	2*54	
10W15	91	I12	7	2*54	
10W39	91	I12	9	2*54	

10W16	91	I12	11	2+54	
10W17	91	I12	19	2+54	
10W18	91	I12	21	2+54	
10W19	91	I12	23	2+54	
10W10	91	I12	26	2+54	
10W12	99	I12	28	2+54	MEMORY REGISTER
10W11	92	I14	1	2+55	CENTRAL TO PERIPHERAL DATA
10W14	92	I14	3	2+55	
10W13	92	I14	5	2+55	
10W15	92	I14	7	2+55	
10W39	92	I14	9	2+55	
10W16	92	I14	11	2+55	
10W17	92	I14	19	2+55	
10W18	92	I14	21	2+55	
10W19	92	I14	23	2+55	
10W10	92	I14	26	2+55	
10W12	900	I14	28	2+55	MEMORY REGISTER
10W11	93	I15	1	2+56	CENTRAL TO PERIPHERAL DATA
10W14	93	I15	3	2+56	
10W13	93	I15	5	2+56	
10W15	93	I15	7	2+56	
10W39	93	I15	9	2+56	
10W16	93	I15	11	2+56	
10W17	93	I15	19	2+56	
10W18	93	I15	21	2+56	
10W19	93	I15	23	2+56	
10W10	93	I15	26	2+56	
10W12	901	I15	28	2+56	MEMORY REGISTER
10W11	94	I16	1	2+57	CENTRAL TO PERIPHERAL DATA
10W14	94	I16	3	2+57	
10W13	94	I16	5	2+57	
10W15	94	I16	7	2+57	
10W39	94	I16	9	2+57	
10W16	94	I16	11	2+57	
10W17	94	I16	19	2+57	
10W18	94	I16	21	2+57	
10W19	94	I16	23	2+57	
10W10	94	I16	26	2+57	
10W12	902	I16	28	2+57	MEMORY REGISTER
10W11	95	I17	1	2+58	CENTRAL TO PERIPHERAL DATA
10W14	95	I17	3	2+58	
10W13	95	I17	5	2+58	
10W15	95	I17	7	2+58	
10W39	95	I17	9	2+58	
10W16	95	I17	11	2+58	
10W17	95	I17	19	2+58	
10W18	95	I17	21	2+58	
10W19	95	I17	23	2+58	
10W10	95	I17	26	2+58	
10W12	903	I17	28	2+58	MEMORY REGISTER
10W11	96	I18	1	2+59	CENTRAL TO PERIPHERAL DATA
10W14	96	I18	3	2+59	
10W13	96	I18	5	2+59	
10W15	96	I18	7	2+59	
10W39	96	I18	9	2+59	
10W16	96	I18	11	2+59	
10W17	96	I18	19	2+59	
10W18	96	I18	21	2+59	
10W19	96	I18	23	2+59	
10W10	96	I18	26	2+59	
10W12	904	I18	28	2+59	MEMORY REGISTER
10W14	99	I19	12	READ	PERIPHERAL

10W23	91	J01	5	2*1	READ DATA
10W23	90	J01	12	2*0	READ DATA
10W23	93	J01	19	2*3	READ DATA
10W23	92	J01	26	2*2	READ DATA
10W23	95	J02	5	2*5	READ DATA
10W23	94	J02	12	2*4	READ DATA
10W23	97	J02	19	2*7	READ DATA
10W23	96	J02	26	2*6	READ DATA
10W23	99	J03	5	2*9	READ DATA
10W23	98	J03	12	2*8	READ DATA
10W23	901	J03	19	2*11	
10W23	900	J03	26	2*10	
10W23	903	J04	5	CLOCK	
10W23	902	J04	12	REV MARK	
10W24	98	J05	4	2*8	WRITE DATA
10W24	97	J05	6	2*7	WRITE DATA
10W24	96	J05	8	2*6	WRITE DATA
10W24	901	J05	23	2*11	
10W24	900	J05	25	2*10	
10W24	99	J05	27	2*9	WRITE DATA
10W24	92	J06	4	2*2	WRITE DATA
10W24	91	J06	6	2*1	WRITE DATA
10W24	90	J06	8	2*0	WRITE DATA
10W24	95	J06	23	2*5	WRITE DATA
10W24	94	J06	25	2*4	WRITE DATA
10W24	93	J06	27	2*3	WRITE DATA
10W24	904	J07	4	WRITE	
10W24	903	J07	6	GROUP	
10W24	902	J07	8	POS CONTROL	
10W24	905	J07	27	CLOCK	
10W25	906	K01	4	FUNCTION	
10W25	904	K01	6	FULL	
10W25	903	K01	8	INACTIVE	
10W25	902	K01	23	ACTIVE	
10W25	907	K01	25	MASTER CLEAR	
10W25	905	K01	27	EMPTY	
10W25	92	K02	4	2*2	OUTPUT DATA
10W25	91	K02	6	2*1	OUTPUT DATA
10W25	90	K02	8	2*0	OUTPUT DATA
10W25	95	K02	23	2*5	OUTPUT DATA
10W25	94	K02	25	2*4	OUTPUT DATA
10W25	93	K02	27	2*3	OUTPUT DATA
10W25	98	K03	4	2*8	OUTPUT DATA
10W25	97	K03	6	2*7	OUTPUT DATA
10W25	96	K03	8	2*6	OUTPUT DATA
10W25	901	K03	23	2*11	OUTPUT DATA
10W25	900	K03	25	2*10	OUTPUT DATA
10W25	99	K03	27	2*9	OUTPUT DATA
10W27	902	K04	4	ACTIVE	
10W27	904	K04	6	FULL	
10W27	903	K04	8	INACTIVE	
10W27	905	K04	27	EMPTY	
10W27	92	K05	4	2*2	INPUT DATA
10W27	91	K05	6	2*1	INPUT DATA
10W27	90	K05	8	2*0	INPUT DATA
10W27	95	K05	23	2*5	INPUT DATA
10W27	94	K05	25	2*4	INPUT DATA
10W27	93	K05	27	2*3	INPUT DATA
10W27	98	K06	4	2*8	INPUT DATA
10W27	97	K06	6	2*7	INPUT DATA
10W27	96	K06	8	2*6	INPUT DATA
10W27	901	K06	23	2*11	INPUT DATA

10W27	900	K06	25	2*10	INPUT DATA
10W27	99	K06	27	2*9	INPUT DATA
10W28	90	L01	5	2*0	OUTPUT DATA
10W28	91	L01	7	2*1	OUTPUT DATA
10W28	92	L01	10	2*2	OUTPUT DATA
10W28	93	L01	21	2*3	OUTPUT DATA
10W28	94	L01	24	2*4	OUTPUT DATA
10W28	95	L01	26	2*5	OUTPUT DATA
10W28	96	L02	5	2*6	OUTPUT DATA
10W28	97	L02	7	2*7	OUTPUT DATA
10W28	98	L02	10	2*8	OUTPUT DATA
10W28	99	L02	21	2*9	OUTPUT DATA
10W28	900	L02	24	2*10	OUTPUT DATA
10W28	901	L02	26	2*11	OUTPUT DATA
10W28	906	L03	5		FUNCTION
10W28	902	L03	7		ACTIVE
10W28	904	L03	10		FULL
10W28	903	L03	21		INACTIVE
10W28	907	L03	24		MASTER CLEAR
10W28	905	L03	26		EMPTY
10W26	903	L07	5		INACTIVE
10W26	904	L07	7		FULL
10W26	96	L08	5	2*6	INPUT DATA
10W26	97	L08	7	2*7	INPUT DATA
10W26	98	L08	10	2*8	INPUT DATA
10W26	99	L08	21	2*9	INPUT DATA
10W26	900	L08	24	2*10	INPUT DATA
10W26	901	L08	26	2*11	INPUT DATA
10W26	90	L09	5	2*0	INPUT DATA
10W26	91	L09	7	2*1	INPUT DATA
10W26	92	L09	10	2*2	INPUT DATA
10W26	93	L09	21	2*3	INPUT DATA
10W26	94	L09	24	2*4	INPUT DATA
10W26	95	L09	26	2*5	INPUT DATA
10W26	906	L26	1		
10W27	906	L26	3		
10W27	907	L26	13		
10W26	907	L26	15		

1

2

3

4

5

6

7

8

9

10

11

12

13

14

CHASSIS 12

12W03	90	A01	1	2*0	OUTPUT DATA
12W01	90	A01	3	2*0	OUTPUT DATA
12W01	91	A01	13	2*1	OUTPUT DATA
12W03	92	A01	14	2*2	OUTPUT DATA
12W03	91	A01	15	2*1	OUTPUT DATA
12W01	92	A01	16	2*2	OUTPUT DATA
12W01	93	A01	26	2*3	OUTPUT DATA
12W03	93	A01	28	2*3	OUTPUT DATA
12W03	94	A02	1	2*4	OUTPUT DATA
12W01	94	A02	3	2*4	OUTPUT DATA
12W01	95	A02	13	2*5	OUTPUT DATA
12W03	96	A02	14	2*6	OUTPUT DATA
12W03	95	A02	15	2*5	OUTPUT DATA
12W01	96	A02	16	2*6	OUTPUT DATA
12W01	97	A02	26	2*7	OUTPUT DATA
12W03	97	A02	28	2*7	OUTPUT DATA
12W03	98	A03	1	2*8	OUTPUT DATA
12W01	98	A03	3	2*8	OUTPUT DATA
12W01	99	A03	13	2*9	OUTPUT DATA
12W03	900	A03	14	2*10	OUTPUT DATA
12W03	99	A03	15	2*9	OUTPUT DATA
12W01	900	A03	16	2*10	OUTPUT DATA
12W01	901	A03	26	2*11	OUTPUT DATA
12W03	901	A03	28	2*11	OUTPUT DATA
12W03	902	B01	1		ACTIVE
12W01	902	B01	3		ACTIVE
12W01	903	B01	13		INACTIVE
12W03	904	B01	14		FULL
12W03	903	B01	15		INACTIVE
12W01	904	B01	16		FULL
12W01	905	B01	26		EMPTY
12W03	905	B01	28		EMPTY
12W03	906	B02	1		FUNCTION
12W01	906	B02	3		FUNCTION
12W01	907	B02	13		MASTER CLEAR
12W03	907	B02	15		MASTER CLEAR
12W02	906	D01	12		CLOCK (100NSEC)
12W02	907	D03	12		RESYNC (1US)
12W04	907	D07	5		
12W04	906	D07	7		
12W02	98	E01	3	2*8	INPUT DATA
12W02	97	E01	5	2*7	INPUT DATA
12W02	96	E01	7	2*6	INPUT DATA
12W04	901	E01	8	2*11	INPUT DATA
12W04	900	E01	9	2*10	INPUT DATA
12W04	99	E01	10	2*9	INPUT DATA
12W04	98	E01	21	2*8	INPUT DATA
12W04	97	E01	22	2*7	INPUT DATA
12W04	96	E01	23	2*6	INPUT DATA
12W02	901	E01	24	2*11	INPUT DATA
12W02	900	E01	26	2*10	INPUT DATA
12W02	99	E01	28	2*9	INPUT DATA
12W02	92	E02	3	2*2	INPUT DATA
12W02	91	E02	5	2*1	INPUT DATA
12W02	90	E02	7	2*0	INPUT DATA
12W04	95	E02	8	2*5	INPUT DATA
12W04	94	E02	9	2*4	INPUT DATA
12W04	93	E02	10	2*3	INPUT DATA
12W04	92	E02	21	2*2	INPUT DATA
12W04	91	E02	22	2*1	INPUT DATA

12W04	90	E02	23	2*0 INPUT DATA
12W02	95	E02	24	2*5 INPUT DATA
12W02	94	E02	26	2*4 INPUT DATA
12W02	93	E02	28	2*3 INPUT DATA
12W02	904	E03	3	FULL
12W02	903	E03	5	INACTIVE
12W02	902	E03	7	ACTIVE
12W04	905	E03	10	EMPTY
12W04	904	E03	21	FULL
12W04	903	E03	22	INACTIVE
12W04	902	E03	23	ACTIVE
12W02	905	E03	28	EMPTY
12W08	98	E34	14	UNBLANK LEFT
12W08	99	E34	20	UNBLANK RIGHT
12W06	98	E35	14	UNBLANK LEFT CONSOLE 0
12W06	99	E35	20	UNBLANK RIGHT CONSOLE 0
12W06	90	F05	1	KEYBOARD BIT 0
12W06	91	F05	4	KEYBOARD BIT 1
12W06	92	F05	5	KEYBOARD BIT 2
12W06	97	F05	12	KEY UP
12W06	96	F05	19	KEY DOWN
12W06	93	F05	24	KEYBOARD BIT 3
12W06	94	F05	25	KEYBOARD BIT 4
12W06	95	F05	28	KEYBOARD BIT 5
12W08	90	F06	1	KEYBOARD INPUT BIT 0
12W08	91	F06	4	KEYBOARD INPUT BIT 1
12W08	92	F06	5	KEYBOARD INPUT BIT 2
12W08	97	F06	12	KEY UP
12W08	96	F06	19	KEY DOWN
12W08	93	F06	24	KEYBOARD INPUT BIT 3
12W08	94	F06	25	KEYBOARD INPUT BIT 4
12W08	95	F06	28	KEYBOARD INPUT BIT 5
12W05	908	F07	25	FOCUS AND ASTIGMATISM
12W07	908	F07	27	FOCUS AND ASTIGMATISM
12W06	900	F13	10	SMALL SIZE
12W06	901	F13	11	MED. SIZE
12W08	900	F13	12	SMALL SIZE
12W08	901	F13	13	MED. SIZE
12W05	90	F16	1	X*0
12W05	91	F16	2	X*1
12W07	90	F16	3	X*0
12W07	91	F16	6	X*1
12W07	92	F16	10	X*2
12W05	92	F16	14	X*2
12W07	99	F16	17	Y*0
12W05	99	F16	19	Y*0
12W07	900	F16	25	Y*1
12W07	901	F16	26	Y*2
12W05	900	F16	27	Y*1
12W05	901	F16	28	Y*2
12W07	93	F19	4	X*3
12W07	95	F19	7	X*5
12W05	95	F19	8	X*5
12W05	93	F19	10	X*3
12W05	94	F19	11	X*4
12W07	94	F19	13	X*4
12W07	98	F19	15	X*8
12W07	96	F19	17	X*6
12W05	97	F19	18	X*7
12W05	96	F19	19	X*6
12W07	97	F19	20	X*7
12W05	98	F19	21	X*8

12W05	902	F22	1	Y*3
12W05	903	F22	2	Y*4
12W07	902	F22	3	Y*3
12W07	903	F22	6	Y*4
12W07	904	F22	10	Y*5
12W05	904	F22	14	Y*5
12W07	905	F22	17	Y*6
12W05	905	F22	19	Y*6
12W07	906	F22	25	Y*7
12W07	907	F22	26	Y*8
12W05	906	F22	27	Y*7
12W05	907	F22	28	Y*8
12W08	906	F26	13	HORIZ. PULL ANALOG
12W08	905	F26	14	HORIZ. PUSH ANALOG
12W08	904	F26	15	VERT. PULL ANALOG
12W08	903	F26	16	VERT. PUSH ANALOG
12W06	906	F27	13	HORIZ. PULL ANALOG
12W06	905	F27	14	HORIZ. PUSH ANALOG
12W06	904	F27	15	VERT. PULL ANALOG
12W06	903	F27	16	VERT. PUSH ANALOG
12W16	90	G01	1	2*0 OUTPUT DATA
12W14	90	G01	3	2*0 OUTPUT DATA
12W14	91	G01	13	2*1 OUTPUT DATA
12W16	92	G01	14	2*2 OUTPUT DATA
12W16	91	G01	15	2*1 OUTPUT DATA
12W14	92	G01	16	2*2 OUTPUT DATA
12W14	93	G01	26	2*3 OUTPUT DATA
12W16	93	G01	28	2*3 OUTPUT DATA
12W16	94	G02	1	2*4 OUTPUT DATA
12W14	94	G02	3	2*4 OUTPUT DATA
12W14	95	G02	13	2*5 OUTPUT DATA
12W16	96	G02	14	2*6 OUTPUT DATA
12W16	95	G02	15	2*5 OUTPUT DATA
12W14	96	G02	16	2*6 OUTPUT DATA
12W14	97	G02	26	2*7 OUTPUT DATA
12W16	97	G02	28	2*7 OUTPUT DATA
12W16	98	G03	1	2*8 OUTPUT DATA
12W14	98	G03	3	2*8 OUTPUT DATA
12W14	99	G03	13	2*9 OUTPUT DATA
12W16	900	G03	14	2*10 OUTPUT DATA
12W16	99	G03	15	2*9 OUTPUT DATA
12W14	900	G03	16	2*10 OUTPUT DATA
12W14	901	G03	26	2*11 OUTPUT DATA
12W16	901	G03	28	2*11 OUTPUT DATA
12W16	902	H01	1	ACTIVE
12W14	902	H01	3	ACTIVE
12W14	903	H01	13	INACTIVE
12W16	904	H01	14	FULL
12W16	903	H01	15	INACTIVE
12W14	904	H01	16	FULL
12W14	905	H01	26	EMPTY
12W16	905	H01	28	EMPTY
12W16	906	H02	1	FUNCTION
12W14	906	H02	3	FUNCTION
12W14	907	H02	13	MASTER CLEAR
12W16	907	H02	15	MASTER CLEAR
12W15	906	J01	12	CLOCK (100US)
12W15	907	J03	12	RESYNC (1US)
12W17	907	J07	5	
12W17	906	J07	7	
12W15	98	K01	3	2*8 INPUT DATA
12W15	97	K01	5	2*7 INPUT DATA

12W15	96	K01	7	2*6 INPUT DATA
12W17	901	K01	8	2*11 INPUT DATA
12W17	900	K01	9	2*10 INPUT DATA
12W17	99	K01	10	2*9 INPUT DATA
12W17	98	K01	21	2*8 INPUT DATA
12W17	97	K01	22	2*7 INPUT DATA
12W17	96	K01	23	2*6 INPUT DATA
12W15	901	K01	24	2*11 INPUT DATA
12W15	900	K01	26	2*10 INPUT DATA
12W15	99	K01	28	2*9 INPUT DATA
12W15	92	K02	3	2*2 INPUT DATA
12W15	91	K02	5	2*1 INPUT DATA
12W15	90	K02	7	2*0 INPUT DATA
12W17	95	K02	8	2*5 INPUT DATA
12W17	94	K02	9	2*4 INPUT DATA
12W17	93	K02	10	2*3 INPUT DATA
12W17	92	K02	21	2*2 INPUT DATA
12W17	91	K02	22	2*1 INPUT DATA
12W17	90	K02	23	2*0 INPUT DATA
12W15	95	K02	24	2*5 INPUT DATA
12W15	94	K02	26	2*4 INPUT DATA
12W15	93	K02	28	2*3 INPUT DATA
12W15	904	K03	3	FULL
12W15	903	K03	5	INACTIVE
12W15	902	K03	7	ACTIVE
12W17	905	K03	10	EMPTY
12W17	904	K03	21	FULL
12W17	903	K03	22	INACTIVE
12W17	902	K03	23	ACTIVE
12W15	905	K03	28	EMPTY
12W21	98	K34	14	UNBLANK LEFT
12W21	99	K34	20	UNBLANK RIGHT
12W19	98	K35	14	UNBLANK LEFT
12W19	99	K35	20	UNBLANK RIGHT
12W19	90	L05	1	KEYBOARD BIT 0
12W19	91	L05	4	KEYBOARD BIT 1
12W19	92	L05	5	KEYBOARD BIT 2
12W19	97	L05	12	KEY UP
12W19	96	L05	19	KEY DOWN
12W19	93	L05	24	KEYBOARD BIT 3
12W19	94	L05	25	KEYBOARD BIT 4
12W19	95	L05	28	KEYBOARD BIT 5
12W21	90	L06	1	KEYBOARD BIT 0
12W21	91	L06	4	KEYBOARD BIT 1
12W21	92	L06	5	KEYBOARD BIT 2
12W21	97	L06	12	KEY UP
12W21	96	L06	19	KEY DOWN
12W21	93	L06	24	KEYBOARD BIT 3
12W21	94	L06	25	KEYBOARD BIT 4
12W21	95	L06	28	KEYBOARD BIT 5
12W18	908	L07	25	FOCUS AND ASTIGMATISM
12W20	908	L07	27	FOCUS AND ASTIGMATISM
12W19	900	L13	10	SMALL SIZE
12W19	901	L13	11	MED. SIZE
12W21	900	L13	12	SMALL SIZE
12W21	901	L13	13	MED. SIZE
12W18	90	L16	1	X*0
12W18	91	L16	2	X*1
12W20	90	L16	3	X*0
12W20	91	L16	6	X*1
12W20	92	L16	10	X*2
12W18	92	L16	14	X*2

12W20	99	L16	17	Y*0
12W18	99	L16	19	Y*0
12W20	900	L16	25	Y*1
12W20	901	L16	26	Y*2
12W18	900	L16	27	Y*1
12W18	901	L16	28	Y*2
12W20	93	L19	4	X*3
12W20	95	L19	7	X*5
12W18	95	L19	8	X*5
12W18	93	L19	10	X*3
12W18	94	L19	11	X*4
12W20	94	L19	13	X*4
12W20	98	L19	15	X*8
12W20	96	L19	17	X*6
12W18	97	L19	18	X*7
12W18	96	L19	19	X*6
12W20	97	L19	20	X*7
12W18	98	L19	21	X*8
12W18	902	L22	1	Y*3
12W18	903	L22	2	Y*4
12W20	902	L22	3	Y*3
12W20	903	L22	6	Y*4
12W20	904	L22	10	Y*5
12W18	904	L22	14	Y*5
12W20	905	L22	17	Y*6
12W18	905	L22	19	Y*6
12W20	906	L22	25	Y*7
12W20	907	L22	26	Y*8
12W18	906	L22	27	Y*7
12W18	907	L22	28	Y*8
12W21	906	L26	13	HORIZ. PULL ANALOG
12W21	905	L26	14	HORIZ. PUSH ANALOG
12W21	904	L26	15	VERT. PULL ANALOG
12W21	903	L26	16	VERT. PUSH ANALOG
12W19	906	L27	13	HORIZ. PULL ANALOG
12W19	905	L27	14	HORIZ. PUSH ANALOG
12W19	904	L27	15	VERT. PULL ANALOG
12W19	903	L27	16	VERT. PUSH ANALOG



CHASSIS 13

13W10	905	C14	14	MEMORY MARGIN
13W18	900	G35	12	CLOCK
13W18	904	G38	4	2*3 CHASSIS 13 SELECT (100)
13W18	903	G38	9	2*2 CHASSIS 13 SELECT (100)
13W18	905	G38	13	2*4 CHASSIS 13 SELECT (100)
13W07	98	Q38	20	MEMORY WRITE
13W07	97	G38	21	MEMORY GO
13W18	901	G39	18	2*0 BANK SELECT
13W18	902	G39	22	2*1 BANK SELECT
13W13	97	G39	28	
13W18	906	G40	11	2*5 ADDRESS TO MEMORY
13W18	907	G40	14	2*6 ADDRESS TO MEMORY
13W18	908	G40	17	2*7 ADDRESS TO MEMORY
13W18	90	G40	18	2*8 ADDRESS TO MEMORY
13W18	91	G41	11	2*9 ADDRESS TO MEMORY
13W18	92	G41	14	2*10 ADDRESS TO MEMORY
13W18	93	G41	17	2*11 ADDRESS TO MEMORY
13W18	94	G41	18	2*12 ADDRESS TO MEMORY
13W18	95	G42	11	2*13 ADDRESS TO MEMORY
13W18	96	G42	14	2*14 ADDRESS TO MEMORY
13W18	97	G42	17	2*15 ADDRESS TO MEMORY
13W18	98	G42	18	2*16 ADDRESS TO MEMORY
13W06	900	H19	1	2*0
13W39	900	H19	2	2*0 FAN OUT TO MEMORY
13W06	901	H19	8	2*1
13W39	901	H19	19	2*1 FAN OUT TO MEMORY
13W39	902	H19	24	2*2 FAN OUT TO MEMORY
13W06	902	H19	28	2*2
13W06	903	H20	1	2*3
13W39	903	H20	2	2*3 FAN OUT TO MEMORY
13W06	904	H20	8	2*4
13W39	904	H20	19	2*4 FAN OUT TO MEMORY
13W39	905	H20	24	2*5 FAN OUT TO MEMORY
13W06	905	H20	28	2*5
13W06	906	H21	1	2*6
13W39	906	H21	2	2*6 FAN OUT TO MEMORY
13W06	907	H21	8	2*7
13W39	907	H21	19	2*7 FAN OUT TO MEMORY
13W39	90	H21	24	2*8 FAN OUT TO MEMORY
13W06	90	H21	28	2*8
13W06	91	H22	1	2*9
13W39	91	H22	2	2*9 FAN OUT TO MEMORY
13W06	92	H22	8	2*10
13W39	92	H22	19	2*10 FAN OUT TO MEMORY
13W39	93	H22	24	2*11 FAN OUT TO MEMORY
13W06	93	H22	28	2*11
13W06	94	H24	1	2*12
13W39	94	H24	2	2*12 FAN OUT TO MEMORY
13W06	95	H24	8	2*13
13W39	95	H24	19	2*13 FAN OUT TO MEMORY
13W39	96	H24	24	2*14 FAN OUT TO MEMORY
13W06	96	H24	28	2*14
13W07	900	H25	1	2*15
13W03	900	H25	2	2*15 FAN OUT TO MEMORY
13W07	901	H25	8	2*16
13W03	901	H25	19	2*16 FAN OUT TO MEMORY
13W03	902	H25	24	2*17 FAN OUT TO MEMORY
13W07	902	H25	28	2*17
13W07	903	H26	1	2*18
13W03	903	H26	2	2*18 FAN OUT TO MEMORY

13W07	904	H26	8	2*19	
13W03	904	H26	19	2*19	FAN OUT TO MEMORY
13W03	905	H26	24	2*20	FAN OUT TO MEMORY
13W07	905	H26	28	2*20	
13W07	906	H27	1	2*21	
13W03	906	H27	2	2*21	FAN OUT TO MEMORY
13W07	907	H27	8	2*22	
13W03	907	H27	19	2*22	FAN OUT TO MEMORY
13W03	90	H27	24	2*23	FAN OUT TO MEMORY
13W07	90	H27	28	2*23	
13W07	91	H29	1	2*24	
13W03	91	H29	2	2*24	FAN OUT TO MEMORY
13W07	92	H29	8	2*25	
13W03	92	H29	19	2*25	FAN OUT TO MEMORY
13W03	93	H29	24	2*26	FAN OUT TO MEMORY
13W07	93	H29	28	2*26	
13W07	94	H30	1	2*27	
13W03	94	H30	2	2*27	FAN OUT TO MEMORY
13W07	95	H30	8	2*28	
13W03	95	H30	19	2*28	FAN OUT TO MEMORY
13W03	96	H30	24	2*29	FAN OUT TO MEMORY
13W07	96	H30	28	2*29	
13W08	900	H31	1	2*30	
13W04	900	H31	2	2*30	FAN OUT TO MEMORY
13W08	901	H31	8	2*31	
13W04	901	H31	19	2*31	FAN OUT TO MEMORY
13W04	902	H31	24	2*32	FAN OUT TO MEMORY
13W08	902	H31	28	2*32	
13W08	903	H32	1	2*33	
13W04	903	H32	2	2*33	FAN OUT TO MEMORY
13W08	904	H32	8	2*34	
13W04	904	H32	19	2*34	FAN OUT TO MEMORY
13W04	905	H32	24	2*35	FAN OUT TO MEMORY
13W08	905	H32	28	2*35	
13W08	906	H34	1	2*36	
13W04	906	H34	2	2*36	FAN OUT TO MEMORY
13W08	907	H34	8	2*37	
13W04	907	H34	19	2*37	FAN OUT TO MEMORY
13W04	90	H34	24	2*38	FAN OUT TO MEMORY
13W08	90	H34	28	2*38	
13W08	91	H35	1	2*39	
13W04	91	H35	2	2*39	FAN OUT TO MEMORY
13W08	92	H35	8	2*40	
13W04	92	H35	19	2*40	FAN OUT TO MEMORY
13W04	93	H35	24	2*41	FAN OUT TO MEMORY
13W08	93	H35	28	2*41	
13W08	94	H36	1	2*42	
13W04	94	H36	2	2*42	FAN OUT TO MEMORY
13W08	95	H36	8	2*43	
13W04	95	H36	19	2*43	FAN OUT TO MEMORY
13W04	96	H36	24	2*44	FAN OUT TO MEMORY
13W08	96	H36	28	2*44	
13W09	900	H37	1	2*45	
13W05	900	H37	2	2*45	FAN OUT TO MEMORY
13W09	901	H37	8	2*46	
13W05	901	H37	19	2*46	FAN OUT TO MEMORY
13W05	902	H37	24	2*47	FAN OUT TO MEMORY
13W09	902	H37	28	2*47	
13W09	903	H39	1	2*48	
13W05	903	H39	2	2*48	FAN OUT TO MEMORY
13W09	904	H39	8	2*49	
13W05	904	H39	19	2*49	FAN OUT TO MEMORY

13W05	905	H39	24	2*50	FAN OUT TO MEMORY
13W09	905	H39	28	2*50	
13W09	906	H40	1	2*51	
13W05	906	H40	2	2*51	FAN OUT TO MEMORY
13W09	907	H40	8	2*52	
13W05	907	H40	19	2*52	FAN OUT TO MEMORY
13W05	90	H40	24	2*53	FAN OUT TO MEMORY
13W09	90	H40	28	2*53	
13W09	91	H41	1	2*54	
13W05	91	H41	2	2*54	FAN OUT TO MEMORY
13W09	92	H41	8	2*55	
13W05	92	H41	19	2*55	FAN OUT TO MEMORY
13W05	93	H41	24	2*56	FAN OUT TO MEMORY
13W09	93	H41	28	2*56	
13W09	94	H42	1	2*57	
13W05	94	H42	2	2*57	FAN OUT TO MEMORY
13W09	95	H42	8	2*58	
13W05	95	H42	19	2*58	FAN OUT TO MEMORY
13W05	96	H42	24	2*59	FAN OUT TO MEMORY
13W09	96	H42	28	2*59	
13W15	900	I28	3	2*0	FAN OUT TO MEMORY
13W11	900	I28	5	2*0	FAN OUT TO MEMORY
13W14	901	I28	7	2*1	FAN OUT TO MEMORY
13W39	901	I28	9	2*1	FAN OUT TO MEMORY
13W12	901	I28	11	2*1	FAN OUT TO MEMORY
13W13	901	I28	13	2*1	FAN OUT TO MEMORY
13W13	900	I28	18	2*0	FAN OUT TO MEMORY
13W12	900	I28	20	2*0	FAN OUT TO MEMORY
13W39	900	I28	22	2*0	FAN OUT TO MEMORY
13W14	900	I28	24	2*0	FAN OUT TO MEMORY
13W16	900	I28	26	2*0	FAN OUT TO MEMORY
13W17	900	I28	28	2*0	FAN OUT TO MEMORY
13W13	902	I29	3	2*2	FAN OUT TO MEMORY
13W12	902	I29	5	2*2	FAN OUT TO MEMORY
13W17	902	I29	7	2*2	FAN OUT TO MEMORY
13W16	902	I29	9	2*2	FAN OUT TO MEMORY
13W11	902	I29	11	2*2	FAN OUT TO MEMORY
13W15	902	I29	13	2*2	FAN OUT TO MEMORY
13W15	901	I29	18	2*1	FAN OUT TO MEMORY
13W11	901	I29	20	2*1	FAN OUT TO MEMORY
13W16	901	I29	22	2*1	FAN OUT TO MEMORY
13W17	901	I29	24	2*1	FAN OUT TO MEMORY
13W39	902	I29	26	2*2	FAN OUT TO MEMORY
13W14	902	I29	28	2*2	FAN OUT TO MEMORY
13W15	903	I30	3	2*3	FAN OUT TO MEMORY
13W11	903	I30	5	2*3	FAN OUT TO MEMORY
13W14	904	I30	7	2*4	FAN OUT TO MEMORY
13W39	904	I30	9	2*4	FAN OUT TO MEMORY
13W12	904	I30	11	2*4	FAN OUT TO MEMORY
13W13	904	I30	13	2*4	FAN OUT TO MEMORY
13W13	903	I30	18	2*3	FAN OUT TO MEMORY
13W12	903	I30	20	2*3	FAN OUT TO MEMORY
13W39	903	I30	22	2*3	FAN OUT TO MEMORY
13W14	903	I30	24	2*3	FAN OUT TO MEMORY
13W16	903	I30	26	2*3	FAN OUT TO MEMORY
13W17	903	I30	28	2*3	FAN OUT TO MEMORY
13W13	905	I31	3	2*5	FAN OUT TO MEMORY
13W12	905	I31	5	2*5	FAN OUT TO MEMORY
13W17	905	I31	7	2*5	FAN OUT TO MEMORY
13W16	905	I31	9	2*5	FAN OUT TO MEMORY
13W11	905	I31	11	2*5	FAN OUT TO MEMORY
13W15	905	I31	13	2*5	FAN OUT TO MEMORY

13W15	904	131	18	2*4	FAN	OUT	TO	MEMORY
13W11	904	131	20	2*4	FAN	OUT	TO	MEMORY
13W16	904	131	22	2*4	FAN	OUT	TO	MEMORY
13W17	904	131	24	2*4	FAN	OUT	TO	MEMORY
13W39	905	131	26	2*5	FAN	OUT	TO	MEMORY
13W14	905	131	28	2*5	FAN	OUT	TO	MEMORY
13W15	906	132	3	2*6	FAN	OUT	TO	MEMORY
13W11	906	132	5	2*6	FAN	OUT	TO	MEMORY
13W14	907	132	7	2*7	FAN	OUT	TO	MEMORY
13W39	907	132	9	2*7	FAN	OUT	TO	MEMORY
13W12	907	132	11	2*7	FAN	OUT	TO	MEMORY
13W13	907	132	13	2*7	FAN	OUT	TO	MEMORY
13W13	906	132	18	2*6	FAN	OUT	TO	MEMORY
13W12	906	132	20	2*6	FAN	OUT	TO	MEMORY
13W39	906	132	22	2*6	FAN	OUT	TO	MEMORY
13W14	906	132	24	2*6	FAN	OUT	TO	MEMORY
13W16	906	132	26	2*6	FAN	OUT	TO	MEMORY
13W17	906	132	28	2*6	FAN	OUT	TO	MEMORY
13W13	90	133	3	2*8	FAN	OUT	TO	MEMORY
13W12	90	133	5	2*8	FAN	OUT	TO	MEMORY
13W17	90	133	7	2*8	FAN	OUT	TO	MEMORY
13W16	90	133	9	2*8	FAN	OUT	TO	MEMORY
13W11	90	133	11	2*8	FAN	OUT	TO	MEMORY
13W15	90	133	13	2*8	FAN	OUT	TO	MEMORY
13W15	907	133	18	2*7	FAN	OUT	TO	MEMORY
13W11	907	133	20	2*7	FAN	OUT	TO	MEMORY
13W16	907	133	22	2*7	FAN	OUT	TO	MEMORY
13W17	907	133	24	2*7	FAN	OUT	TO	MEMORY
13W39	90	133	26	2*8	FAN	OUT	TO	MEMORY
13W14	90	133	28	2*8	FAN	OUT	TO	MEMORY
13W15	91	134	3	2*9	FAN	OUT	TO	MEMORY
13W11	91	134	5	2*9	FAN	OUT	TO	MEMORY
13W14	92	134	7	2*10	FAN	OUT	TO	MEMORY
13W39	92	134	9	2*10	FAN	OUT	TO	MEMORY
13W12	92	134	11	2*10	FAN	OUT	TO	MEMORY
13W13	92	134	13	2*10	FAN	OUT	TO	MEMORY
13W13	91	134	18	2*9	FAN	OUT	TO	MEMORY
13W12	91	134	20	2*9	FAN	OUT	TO	MEMORY
13W39	91	134	22	2*9	FAN	OUT	TO	MEMORY
13W14	91	134	24	2*9	FAN	OUT	TO	MEMORY
13W16	91	134	26	2*9	FAN	OUT	TO	MEMORY
13W17	91	134	28	2*9	FAN	OUT	TO	MEMORY
13W13	93	135	3	2*11	FAN	OUT	TO	MEMORY
13W12	93	135	5	2*11	FAN	OUT	TO	MEMORY
13W17	93	135	7	2*11	FAN	OUT	TO	MEMORY
13W16	93	135	9	2*11	FAN	OUT	TO	MEMORY
13W11	93	135	11	2*11	FAN	OUT	TO	MEMORY
13W15	93	135	13	2*11	FAN	OUT	TO	MEMORY
13W15	92	135	18	2*10	FAN	OUT	TO	MEMORY
13W11	92	135	20	2*10	FAN	OUT	TO	MEMORY
13W16	92	135	22	2*10	FAN	OUT	TO	MEMORY
13W17	92	135	24	2*10	FAN	OUT	TO	MEMORY
13W39	93	135	26	2*11	FAN	OUT	TO	MEMORY
13W14	93	135	28	2*11	FAN	OUT	TO	MEMORY
13W15	94	136	3	2*12	FAN	OUT	TO	MEMORY
13W11	94	136	5	2*12	FAN	OUT	TO	MEMORY
13W14	95	136	7	2*13	FAN	OUT	TO	MEMORY
13W39	95	136	9	2*13	FAN	OUT	TO	MEMORY
13W12	95	136	11	2*13	FAN	OUT	TO	MEMORY
13W13	95	136	13	2*13	FAN	OUT	TO	MEMORY
13W13	94	136	18	2*12	FAN	OUT	TO	MEMORY
13W12	94	136	20	2*12	FAN	OUT	TO	MEMORY

13W39	94	I36	22	2*12	FAN	OUT	TO	MEMORY	
13W14	94	I36	24	2*12	FAN	OUT	TO	MEMORY	
13W16	94	I36	26	2*12	FAN	OUT	TO	MEMORY	
13W17	94	I36	28	2*12	FAN	OUT	TO	MEMORY	
13W13	96	I37	3	2*14	FAN	OUT	TO	MEMORY	
13W12	96	I37	5	2*14	FAN	OUT	TO	MEMORY	
13W17	96	I37	7	2*14	FAN	OUT	TO	MEMORY	
13W16	96	I37	9	2*14	FAN	OUT	TO	MEMORY	
13W11	96	I37	11	2*14	FAN	OUT	TO	MEMORY	
13W15	96	I37	13	2*14	FAN	OUT	TO	MEMORY	
13W15	95	I37	18	2*13	FAN	OUT	TO	MEMORY	
13W11	95	I37	20	2*13	FAN	OUT	TO	MEMORY	
13W16	95	I37	22	2*13	FAN	OUT	TO	MEMORY	
13W17	95	I37	24	2*13	FAN	OUT	TO	MEMORY	
13W39	96	I37	26	2*14	FAN	OUT	TO	MEMORY	
13W14	96	I37	28	2*14	FAN	OUT	TO	MEMORY	
13W10	90	I39	4	2*0	DATA	TO	MEMORY	FAN	OUT
13W10	91	I39	6	2*1	DATA	TO	MEMORY	FAN	OUT
13W10	92	I39	7	2*2	DATA	TO	MEMORY	FAN	OUT
13W10	95	I39	22	2*5	DATA	TO	MEMORY	FAN	OUT
13W10	94	I39	25	2*4	DATA	TO	MEMORY	FAN	OUT
13W10	93	I39	27	2*3	DATA	TO	MEMORY	FAN	OUT
13W10	96	I40	4	2*6	DATA	TO	MEMORY	FAN	OUT
13W10	97	I40	6	2*7	DATA	TO	MEMORY	FAN	OUT
13W10	98	I40	7	2*8	DATA	TO	MEMORY	FAN	OUT
13W10	901	I40	22	2*11	DATA	TO	MEMORY	FAN	OUT
13W10	900	I40	25	2*10	DATA	TO	MEMORY	FAN	OUT
13W10	99	I40	27	2*9	DATA	TO	MEMORY	FAN	OUT
13W10	902	I41	4	2*12	DATA	TO	MEMORY	FAN	OUT
13W10	903	I41	6	2*13	DATA	TO	MEMORY	FAN	OUT
13W10	904	I41	7	2*14	DATA	TO	MEMORY	FAN	OUT
13W36	907	J31	1	CLOCK (1 M SEC)					
13W34	906	J31	13						
13W36	906	J31	15						
13W34	903	J33	3	INACTIVE					
13W34	905	J33	5	EMPTY					
13W34	904	J33	7	FULL					
13W34	907	J33	9	CLOCK (1 MS)					
13W36	902	J33	10	ACTIVE					
13W36	903	J33	21	INACTIVE					
13W36	905	J33	22	EMPTY					
13W36	904	J33	23	FULL					
13W34	902	J33	28	ACTIVE					
13W34	92	J35	3	2*2	INPUT	DATA			
13W34	91	J35	5	2*1	INPUT	DATA			
13W34	90	J35	7	2*0	INPUT	DATA			
13W36	95	J35	8	2*5	INPUT	DATA			
13W36	94	J35	9	2*4	INPUT	DATA			
13W36	93	J35	10	2*3	INPUT	DATA			
13W36	92	J35	21	2*2	INPUT	DATA			
13W36	91	J35	22	2*1	INPUT	DATA			
13W36	90	J35	23	2*0	INPUT	DATA			
13W34	95	J35	24	2*5	INPUT	DATA			
13W34	94	J35	26	2*4	INPUT	DATA			
13W34	93	J35	28	2*3	INPUT	DATA			
13W34	98	J37	3	2*8	INPUT	DATA			
13W34	97	J37	5	2*7	INPUT	DATA			
13W34	96	J37	7	2*6	INPUT	DATA			
13W36	901	J37	8	2*11	INPUT	DATA			
13W36	900	J37	9	2*10	INPUT	DATA			
13W36	99	J37	10	2*9	INPUT	DATA			
13W36	98	J37	21	2*8	INPUT	DATA			

13W36	97	J37	22	2*7	INPUT DATA
13W36	96	J37	23	2*6	INPUT DATA
13W34	901	J37	24	2*11	INPUT DATA
13W34	900	J37	26	2*10	INPUT DATA
13W34	99	J37	28	2*9	INPUT DATA
13W37	906	J38	1		FUNCTION
13W35	906	J38	3		FUNCTION
13W35	902	J38	13		ACTIVE
13W37	903	J38	14		INACTIVE
13W37	902	J38	15		ACTIVE
13W35	903	J38	16		INACTIVE
13W35	904	J38	26		FULL
13W37	904	J38	28		FULL
13W37	905	J39	1		EMPTY
13W35	905	J39	3		EMPTY
13W35	907	J39	13		MASTER CLEAR
13W37	907	J39	15		MASTER CLEAR
13W37	90	J40	1	2*0	OUTPUT DATA
13W35	90	J40	3	2*0	OUTPUT DATA
13W35	91	J40	13	2*1	OUTPUT DATA
13W37	92	J40	14	2*2	OUTPUT DATA
13W37	91	J40	15	2*1	OUTPUT DATA
13W35	92	J40	16	2*2	OUTPUT DATA
13W35	93	J40	26	2*3	OUTPUT DATA
13W37	93	J40	28	2*3	OUTPUT DATA
13W37	94	J41	1	2*4	OUTPUT DATA
13W35	94	J41	3	2*4	OUTPUT DATA
13W35	95	J41	13	2*5	OUTPUT DATA
13W37	96	J41	14	2*6	OUTPUT DATA
13W37	95	J41	15	2*5	OUTPUT DATA
13W35	96	J41	16	2*6	OUTPUT DATA
13W35	97	J41	26	2*7	OUTPUT DATA
13W37	97	J41	28	2*7	OUTPUT DATA
13W37	98	J42	1	2*8	OUTPUT DATA
13W35	98	J42	3	2*8	OUTPUT DATA
13W35	99	J42	13	2*9	OUTPUT DATA
13W37	900	J42	14	2*10	OUTPUT DATA
13W37	99	J42	15	2*9	OUTPUT DATA
13W35	900	J42	16	2*10	OUTPUT DATA
13W35	901	J42	26	2*11	OUTPUT DATA
13W37	901	J42	28	2*11	OUTPUT DATA

CHASSIS 14

14W10	905	C14	14	MEMORY MARGIN
14W18	906	G01	11	2*5 ADDRESS TO MEMORY
14W18	908	G01	14	2*7 ADDRESS TO MEMORY
14W18	907	G01	17	2*6 ADDRESS TO MEMORY
14W18	90	G01	18	2*8 ADDRESS TO MEMORY
14W18	91	G02	11	2*9 ADDRESS TO MEMORY
14W18	92	G02	14	2*10 ADDRESS TO MEMORY
14W18	93	G02	17	2*11 ADDRESS TO MEMORY
14W18	94	G02	18	2*12 ADDRESS TO MEMORY
14W18	95	G03	11	2*13 ADDRESS TO MEMORY
14W18	96	G03	14	2*14 ADDRESS TO MEMORY
14W18	97	G03	17	2*15 ADDRESS TO MEMORY
14W18	98	G03	18	2*16 ADDRESS TO MEMORY
14W18	904	G04	4	2*3 CHASSIS 14 SELECT (101)
14W18	903	G04	8	2*2 CHASSIS 14 SELECT (101)
14W18	905	G04	13	2*4 CHASSIS 14 SELECT (101)
14W07	98	G04	20	WRITE MEMORY
14W07	97	G04	21	GO MEMORY
14W18	901	G05	18	2*0 BANK SELECT
14W18	902	G05	22	2*1 BANK SELECT
14W13	97	G05	28	ACCEPT
14W18	900	G13	12	CLOCK
14W06	900	H01	1	2*0
14W03	900	H01	2	2*0 FAN OUT TO MEMORY
14W06	901	H01	8	2*1
14W03	901	H01	19	2*1 FAN OUT TO MEMORY
14W03	902	H01	24	2*2 FAN OUT TO MEMORY
14W06	902	H01	28	2*2
14W06	903	H02	1	2*3
14W03	903	H02	2	2*3 FAN OUT TO MEMORY
14W06	904	H02	8	2*4
14W03	904	H02	19	2*4 FAN OUT TO MEMORY
14W03	905	H02	24	2*5 FAN OUT TO MEMORY
14W06	905	H02	28	2*5
14W06	906	H03	1	2*6
14W03	906	H03	2	2*6 FAN OUT TO MEMORY
14W06	907	H03	8	2*7
14W03	907	H03	19	2*7 FAN OUT TO MEMORY
14W03	90	H03	24	2*8 FAN OUT TO MEMORY
14W06	90	H03	28	2*8
14W06	91	H04	1	2*9
14W03	91	H04	2	2*9 FAN OUT TO MEMORY
14W06	92	H04	8	2*10
14W03	92	H04	19	2*10 FAN OUT TO MEMORY
14W03	93	H04	24	2*11 FAN OUT TO MEMORY
14W06	93	H04	28	2*11
14W06	94	H06	1	2*12
14W03	94	H06	2	2*12 FAN OUT TO MEMORY
14W06	95	H06	8	2*13
14W03	95	H06	19	2*13 FAN OUT TO MEMORY
14W03	96	H06	24	2*14 FAN OUT TO MEMORY
14W06	96	H06	28	2*14
14W07	900	H07	1	2*15
14W39	900	H07	2	2*15 FAN OUT TO MEMORY
14W07	901	H07	8	2*16
14W39	901	H07	19	2*16 FAN OUT TO MEMORY
14W39	902	H07	24	2*17 FAN OUT TO MEMORY
14W07	902	H07	28	2*17
14W07	903	H08	1	2*18
14W39	903	H08	2	2*18 FAN OUT TO MEMORY

14W07	904	H08	8	2*19	
14W39	904	H08	19	2*19	FAN OUT TO MEMORY
14W39	905	H08	24	2*20	FAN OUT TO MEMORY
14W07	905	H08	28	2*20	
14W07	906	H09	1	2*21	
14W39	906	H09	2	2*21	FAN OUT TO MEMORY
14W07	907	H09	8	2*22	
14W39	907	H09	19	2*22	FAN OUT TO MEMORY
14W39	90	H09	24	2*23	FAN OUT TO MEMORY
14W07	90	H09	28	2*23	
14W07	91	H11	1	2*24	
14W39	91	H11	2	2*24	FAN OUT TO MEMORY
14W07	92	H11	8	2*25	
14W39	92	H11	19	2*25	FAN OUT TO MEMORY
14W39	93	H11	24	2*26	FAN OUT TO MEMORY
14W07	93	H11	28	2*26	
14W07	94	H12	1	2*27	
14W39	94	H12	2	2*27	FAN OUT TO MEMORY
14W07	95	H12	8	2*28	
14W39	95	H12	19	2*28	FAN OUT TO MEMORY
14W39	96	H12	24	2*29	FAN OUT TO MEMORY
14W07	96	H12	28	2*29	
14W08	900	H13	1	2*30	
14W04	900	H13	2	2*30	FAN OUT TO MEMORY
14W08	901	H13	8	2*31	
14W04	901	H13	19	2*31	FAN OUT TO MEMORY
14W04	902	H13	24	2*32	FAN OUT TO MEMORY
14W08	902	H13	28	2*32	
14W08	903	H14	1	2*33	
14W04	903	H14	2	2*33	FAN OUT TO MEMORY
14W08	904	H14	8	2*34	
14W04	904	H14	19	2*34	FAN OUT TO MEMORY
14W04	905	H14	24	2*35	FAN OUT TO MEMORY
14W08	905	H14	28	2*35	
14W08	906	H16	1	2*36	
14W04	906	H16	2	2*36	FAN OUT TO MEMORY
14W08	907	H16	8	2*37	
14W04	907	H16	19	2*37	FAN OUT TO MEMORY
14W04	908	H16	24	2*38	FAN OUT TO MEMORY
14W08	90	H16	28	2*38	
14W08	91	H17	1	2*39	
14W04	90	H17	2	2*39	FAN OUT TO MEMORY
14W08	92	H17	8	2*40	
14W04	91	H17	19	2*40	FAN OUT TO MEMORY
14W04	92	H17	24	2*41	FAN OUT TO MEMORY
14W08	93	H17	28	2*41	
14W08	94	H18	1	2*42	
14W04	93	H18	2	2*42	FAN OUT TO MEMORY
14W08	95	H18	8	2*43	
14W04	94	H18	19	2*43	FAN OUT TO MEMORY
14W04	95	H18	24	2*44	FAN OUT TO MEMORY
14W08	96	H18	28	2*44	
14W09	900	H19	1	2*45	
14W05	900	H19	2	2*45	FAN OUT TO MEMORY
14W09	901	H19	8	2*46	
14W05	901	H19	19	2*46	FAN OUT TO MEMORY
14W05	902	H19	24	2*47	FAN OUT TO MEMORY
14W09	902	H19	28	2*47	
14W09	903	H21	1	2*48	
14W05	903	H21	2	2*48	FAN OUT TO MEMORY
14W09	904	H21	8	2*49	
14W05	904	H21	19	2*49	FAN OUT TO MEMORY

14W05	905	H21	24	2*50	FAN OUT TO MEMORY	
14W09	905	H21	28	2*50		
14W09	906	H22	1	2*51		
14W05	906	H22	2	2*51	FAN OUT TO MEMORY	
14W09	907	H22	8	2*52		
14W05	907	H22	19	2*52	FAN OUT TO MEMORY	
14W05	908	H22	24	2*53	FAN OUT TO MEMORY	
14W09	90	H22	28	2*53		
14W09	91	H23	1	2*54		
14W05	90	H23	2	2*54	FAN OUT TO MEMORY	
14W09	92	H23	8	2*55		
14W05	91	H23	19	2*55	FAN OUT TO MEMORY	
14W05	92	H23	24	2*56	FAN OUT TO MEMORY	
14W09	93	H23	28	2*56		
14W09	94	H24	1	2*57		
14W05	93	H24	2	2*57	FAN OUT TO MEMORY	
14W09	95	H24	8	2*58		
14W05	94	H24	19	2*58	FAN OUT TO MEMORY	
14W05	95	H24	24	2*59	FAN OUT TO MEMORY	
14W09	96	H24	28	2*59		
14W10	90	I02	4	2*15	DATA TO MEMORY	FAN OUT
14W10	91	I02	6	2*16	DATA TO MEMORY	FAN OUT
14W10	92	I02	7	2*17	DATA TO MEMORY	FAN OUT
14W10	95	I02	22	2*20	DATA TO MEMORY	FAN OUT
14W10	94	I02	25	2*19	DATA TO MEMORY	FAN OUT
14W10	93	I02	27	2*18	DATA TO MEMORY	FAN OUT
14W10	96	I03	4	2*21	DATA TO MEMORY	FAN OUT
14W10	97	I03	6	2*22	DATA TO MEMORY	FAN OUT
14W10	98	I03	7	2*23	DATA TO MEMORY	FAN OUT
14W10	901	I03	22	2*26	DATA TO MEMORY	FAN OUT
14W10	900	I03	25	2*25	DATA TO MEMORY	FAN OUT
14W10	99	I03	27	2*24	DATA TO MEMORY	FAN OUT
14W10	902	I04	4	2*27	DATA TO MEMORY	FAN OUT
14W10	903	I04	6	2*28	DATA TO MEMORY	FAN OUT
14W10	904	I04	7	2*29	DATA TO MEMORY	FAN OUT
14W14	900	I06	3	2*15	FAN OUT TO MEMORY	
14W15	900	I06	5	2*15	FAN OUT TO MEMORY	
14W13	901	I06	7	2*16	FAN OUT TO MEMORY	
14W39	901	I06	9	2*16	FAN OUT TO MEMORY	
14W12	901	I06	11	2*16	FAN OUT TO MEMORY	
14W11	901	I06	13	2*16	FAN OUT TO MEMORY	
14W11	900	I06	18	2*15	FAN OUT TO MEMORY	
14W12	900	I06	20	2*15	FAN OUT TO MEMORY	
14W39	900	I06	22	2*15	FAN OUT TO MEMORY	
14W13	900	I06	24	2*15	FAN OUT TO MEMORY	
14W16	900	I06	26	2*15	FAN OUT TO MEMORY	
14W17	900	I06	28	2*15	FAN OUT TO MEMORY	
14W11	902	I07	3	2*17	FAN OUT TO MEMORY	
14W12	902	I07	5	2*17	FAN OUT TO MEMORY	
14W17	902	I07	7	2*17	FAN OUT TO MEMORY	
14W16	902	I07	9	2*17	FAN OUT TO MEMORY	
14W15	902	I07	11	2*17	FAN OUT TO MEMORY	
14W14	902	I07	13	2*17	FAN OUT TO MEMORY	
14W14	901	I07	18	2*16	FAN OUT TO MEMORY	
14W15	901	I07	20	2*16	FAN OUT TO MEMORY	
14W16	901	I07	22	2*16	FAN OUT TO MEMORY	
14W17	901	I07	24	2*16	FAN OUT TO MEMORY	
14W39	902	I07	26	2*17	FAN OUT TO MEMORY	
14W13	902	I07	28	2*17	FAN OUT TO MEMORY	
14W14	903	I08	3	2*18	FAN OUT TO MEMORY	
14W15	903	I08	5	2*18	FAN OUT TO MEMORY	
14W13	904	I08	7	2*19	FAN OUT TO MEMORY	

14W39	904	I08	9	2*19	FAN	OUT	TO	MEMORY
14W12	904	I08	11	2*19	FAN	OUT	TO	MEMORY
14W11	904	I08	13	2*19	FAN	OUT	TO	MEMORY
14W11	903	I08	18	2*18	FAN	OUT	TO	MEMORY
14W12	903	I08	20	2*18	FAN	OUT	TO	MEMORY
14W39	903	I08	22	2*18	FAN	OUT	TO	MEMORY
14W13	903	I08	24	2*18	FAN	OUT	TO	MEMORY
14W16	903	I08	26	2*18	FAN	OUT	TO	MEMORY
14W17	903	I08	28	2*18	FAN	OUT	TO	MEMORY
14W11	905	I09	3	2*20	FAN	OUT	TO	MEMORY
14W12	905	I09	5	2*20	FAN	OUT	TO	MEMORY
14W17	905	I09	7	2*20	FAN	OUT	TO	MEMORY
14W16	905	I09	9	2*20	FAN	OUT	TO	MEMORY
14W15	905	I09	11	2*20	FAN	OUT	TO	MEMORY
14W14	905	I09	13	2*20	FAN	OUT	TO	MEMORY
14W14	904	I09	18	2*19	FAN	OUT	TO	MEMORY
14W15	904	I09	20	2*19	FAN	OUT	TO	MEMORY
14W16	904	I09	22	2*19	FAN	OUT	TO	MEMORY
14W17	904	I09	24	2*19	FAN	OUT	TO	MEMORY
14W39	905	I09	26	2*20	FAN	OUT	TO	MEMORY
14W13	905	I09	28	2*20	FAN	OUT	TO	MEMORY
14W14	906	I10	3	2*21	FAN	OUT	TO	MEMORY
14W15	906	I10	5	2*21	FAN	OUT	TO	MEMORY
14W13	907	I10	7	2*22	FAN	OUT	TO	MEMORY
14W39	907	I10	9	2*22	FAN	OUT	TO	MEMORY
14W12	907	I10	11	2*22	FAN	OUT	TO	MEMORY
14W11	907	I10	13	2*22	FAN	OUT	TO	MEMORY
14W11	906	I10	18	2*21	FAN	OUT	TO	MEMORY
14W12	906	I10	20	2*21	FAN	OUT	TO	MEMORY
14W39	906	I10	22	2*21	FAN	OUT	TO	MEMORY
14W13	906	I10	24	2*21	FAN	OUT	TO	MEMORY
14W16	906	I10	26	2*21	FAN	OUT	TO	MEMORY
14W17	906	I10	28	2*21	FAN	OUT	TO	MEMORY
14W11	90	I11	3	2*23	FAN	OUT	TO	MEMORY
14W12	90	I11	5	2*23	FAN	OUT	TO	MEMORY
14W17	908	I11	7	2*23	FAN	OUT	TO	MEMORY
14W16	96	I11	9	2*23	FAN	OUT	TO	MEMORY
14W15	90	I11	11	2*23	FAN	OUT	TO	MEMORY
14W14	90	I11	13	2*23	FAN	OUT	TO	MEMORY
14W14	907	I11	18	2*22	FAN	OUT	TO	MEMORY
14W15	907	I11	20	2*22	FAN	OUT	TO	MEMORY
14W16	907	I11	22	2*22	FAN	OUT	TO	MEMORY
14W17	907	I11	24	2*22	FAN	OUT	TO	MEMORY
14W39	90	I11	26	2*23	FAN	OUT	TO	MEMORY
14W13	90	I11	28	2*23	FAN	OUT	TO	MEMORY
14W14	91	I12	3	2*24	FAN	OUT	TO	MEMORY
14W15	91	I12	5	2*24	FAN	OUT	TO	MEMORY
14W13	92	I12	7	2*25	FAN	OUT	TO	MEMORY
14W39	92	I12	9	2*25	FAN	OUT	TO	MEMORY
14W12	92	I12	11	2*25	FAN	OUT	TO	MEMORY
14W11	92	I12	13	2*25	FAN	OUT	TO	MEMORY
14W11	91	I12	18	2*24	FAN	OUT	TO	MEMORY
14W12	91	I12	20	2*24	FAN	OUT	TO	MEMORY
14W39	91	I12	22	2*24	FAN	OUT	TO	MEMORY
14W13	91	I12	24	2*24	FAN	OUT	TO	MEMORY
14W16	90	I12	26	2*24	FAN	OUT	TO	MEMORY
14W17	90	I12	28	2*24	FAN	OUT	TO	MEMORY
14W11	93	I13	3	2*26	FAN	OUT	TO	MEMORY
14W12	93	I13	5	2*26	FAN	OUT	TO	MEMORY
14W17	92	I13	7	2*26	FAN	OUT	TO	MEMORY
14W16	92	I13	9	2*26	FAN	OUT	TO	MEMORY
14W15	93	I13	11	2*26	FAN	OUT	TO	MEMORY

14W14	93	I13	13	2*26	FAN	OUT	TO	MEMORY
14W14	92	I13	18	2*25	FAN	OUT	TO	MEMORY
14W15	92	I13	20	2*25	FAN	OUT	TO	MEMORY
14W16	91	I13	22	2*25	FAN	OUT	TO	MEMORY
14W17	91	I13	24	2*25	FAN	OUT	TO	MEMORY
14W39	93	I13	26	2*26	FAN	OUT	TO	MEMORY
14W13	93	I13	28	2*26	FAN	OUT	TO	MEMORY
14W14	94	I14	3	2*27	FAN	OUT	TO	MEMORY
14W15	94	I14	5	2*27	FAN	OUT	TO	MEMORY
14W13	95	I14	7	2*28	FAN	OUT	TO	MEMORY
14W39	95	I14	9	2*28	FAN	OUT	TO	MEMORY
14W12	95	I14	11	2*28	FAN	OUT	TO	MEMORY
14W11	95	I14	13	2*28	FAN	OUT	TO	MEMORY
14W11	94	I14	18	2*27	FAN	OUT	TO	MEMORY
14W12	94	I14	20	2*27	FAN	OUT	TO	MEMORY
14W39	94	I14	22	2*27	FAN	OUT	TO	MEMORY
14W13	94	I14	24	2*27	FAN	OUT	TO	MEMORY
14W16	93	I14	26	2*27	FAN	OUT	TO	MEMORY
14W17	93	I14	28	2*27	FAN	OUT	TO	MEMORY
14W11	96	I15	3	2*29	FAN	OUT	TO	MEMORY
14W12	96	I15	5	2*29	FAN	OUT	TO	MEMORY
14W17	95	I15	7	2*29	FAN	OUT	TO	MEMORY
14W16	95	I15	9	2*29	FAN	OUT	TO	MEMORY
14W15	96	I15	11	2*29	FAN	OUT	TO	MEMORY
14W14	96	I15	13	2*29	FAN	OUT	TO	MEMORY
14W14	95	I15	18	2*28	FAN	OUT	TO	MEMORY
14W15	95	I15	20	2*28	FAN	OUT	TO	MEMORY
14W16	94	I15	22	2*28	FAN	OUT	TO	MEMORY
14W17	94	I15	24	2*28	FAN	OUT	TO	MEMORY
14W39	96	I15	26	2*29	FAN	OUT	TO	MEMORY
14W13	96	I15	28	2*29	FAN	OUT	TO	MEMORY



15W10	905	C14	14	MEMORY MARGIN
15W18	900	G35	12	CLOCK
15W18	904	G38	5	2+3 CHASSIS 15 SELECT (110)
15W18	903	G38	9	2+2 CHASSIS 15 SELECT (110)
15W18	905	G38	13	2+4 CHASSIS 15 SELECT (110)
15W07	98	G38	20	WRITE
15W07	97	G38	21	GO
15W18	901	G39	18	2+0 BANK SELECT
15W18	902	G39	22	2+1 BANK SELECT
15W13	97	G39	28	ACCEPT
15W18	906	G40	11	2+5 ADDRESS TO MEMORY
15W18	907	G40	14	2+6 ADDRESS TO MEMORY
15W18	908	G40	17	2+7 ADDRESS TO MEMORY
15W18	90	G40	18	2+8 ADDRESS TO MEMORY
15W18	91	G41	11	2+9 ADDRESS TO MEMORY
15W18	92	G41	14	2+10 ADDRESS TO MEMORY
15W18	93	G41	17	2+11 ADDRESS TO MEMORY
15W18	94	G41	18	2+12 ADDRESS TO MEMORY
15W18	95	G42	11	2+13 ADDRESS TO MEMORY
15W18	96	G42	14	2+14 ADDRESS TO MEMORY
15W18	97	G42	17	2+15 ADDRESS TO MEMORY
15W18	98	G42	18	2+16 ADDRESS TO MEMORY
15W06	900	H19	1	2+0
15W03	900	H19	2	2+0 FAN OUT TO MEMORY
15W06	901	H19	8	2+1
15W03	901	H19	19	2+1 FAN OUT TO MEMORY
15W03	902	H19	24	2+2 FAN OUT TO MEMORY
15W06	902	H19	28	2+2
15W06	903	H20	1	2+3
15W03	903	H20	2	2+3 FAN OUT TO MEMORY
15W06	904	H20	8	2+4
15W03	904	H20	19	2+4 FAN OUT TO MEMORY
15W03	905	H20	24	2+5 FAN OUT TO MEMORY
15W06	905	H20	28	2+5
15W06	906	H21	1	2+6
15W03	906	H21	2	2+6 FAN OUT TO MEMORY
15W06	907	H21	8	2+7
15W03	907	H21	19	2+7 FAN OUT TO MEMORY
15W03	90	H21	24	2+8 FAN OUT TO MEMORY
15W06	90	H21	28	2+8
15W06	91	H22	1	2+9
15W03	91	H22	2	2+9 FAN OUT TO MEMORY
15W06	92	H22	8	2+10
15W03	92	H22	19	2+10 FAN OUT TO MEMORY
15W03	93	H22	24	2+11 FAN OUT TO MEMORY
15W06	93	H22	28	2+11
15W06	94	H24	1	2+12
15W03	94	H24	2	2+12 FAN OUT TO MEMORY
15W06	95	H24	8	2+13
15W03	95	H24	19	2+13 FAN OUT TO MEMORY
15W03	96	H24	24	2+14 FAN OUT TO MEMORY
15W06	96	H24	28	2+14
15W07	900	H25	1	2+15
15W04	900	H25	2	2+15 FAN OUT TO MEMORY
15W07	901	H25	8	2+16
15W04	901	H25	19	2+16 FAN OUT TO MEMORY
15W04	902	H25	24	2+17 FAN OUT TO MEMORY
15W07	902	H25	28	2+17
15W07	903	H26	1	2+18
15W04	903	H26	2	2+18 FAN OUT TO MEMORY
15W07	904	H26	8	2+19

15W04	904	H26	19	2*19	FAN OUT TO MEMORY
15W04	905	H26	24	2*20	FAN OUT TO MEMORY
15W07	905	H26	28	2*20	
15W07	906	H27	1	2*21	
15W04	906	H27	2	2*21	FAN OUT TO MEMORY
15W07	907	H27	8	2*22	
15W04	907	H27	19	2*22	FAN OUT TO MEMORY
15W04	96	H27	24	2*23	FAN OUT TO MEMORY
15W07	90	H27	28	2*23	
15W07	91	H29	1	2*24	
15W04	90	H29	2	2*24	FAN OUT TO MEMORY
15W07	92	H29	8	2*25	
15W04	91	H29	19	2*25	FAN OUT TO MEMORY
15W04	92	H29	24	2*26	FAN OUT TO MEMORY
15W07	93	H29	28	2*26	
15W07	94	H30	1	2*27	
15W04	93	H30	2	2*27	FAN OUT TO MEMORY
15W07	95	H30	8	2*28	
15W04	94	H30	19	2*28	FAN OUT TO MEMORY
15W04	95	H30	24	2*29	FAN OUT TO MEMORY
15W07	96	H30	28	2*29	
15W08	900	H31	1	2*30	
15W39	900	H31	2	2*30	FAN OUT TO MEMORY
15W08	901	H31	8	2*31	
15W39	901	H31	19	2*31	FAN OUT TO MEMORY
15W39	902	H31	24	2*32	FAN OUT TO MEMORY
15W08	902	H31	28	2*32	
15W08	903	H32	1	2*33	
15W39	903	H32	2	2*33	FAN OUT TO MEMORY
15W08	904	H32	8	2*34	
15W39	904	H32	19	2*34	FAN OUT TO MEMORY
15W39	905	H32	24	2*35	FAN OUT TO MEMORY
15W08	905	H32	28	2*35	
15W08	906	H34	1	2*36	
15W39	906	H34	2	2*36	FAN OUT TO MEMORY
15W08	907	H34	8	2*37	
15W39	907	H34	19	2*37	FAN OUT TO MEMORY
15W39	90	H34	24	2*38	FAN OUT TO MEMORY
15W08	90	H34	28	2*38	
15W08	91	H35	1	2*39	
15W39	91	H35	2	2*39	FAN OUT TO MEMORY
15W08	92	H35	8	2*40	
15W39	92	H35	19	2*40	FAN OUT TO MEMORY
15W39	93	H35	24	2*41	FAN OUT TO MEMORY
15W08	93	H35	28	2*41	
15W08	94	H36	1	2*42	
15W39	94	H36	2	2*42	FAN OUT TO MEMORY
15W08	95	H36	8	2*43	
15W39	95	H36	19	2*43	FAN OUT TO MEMORY
15W39	96	H36	24	2*44	FAN OUT TO MEMORY
15W08	96	H36	28	2*44	
15W09	900	H37	1	2*45	
15W05	900	H37	2	2*45	FAN OUT TO MEMORY
15W09	901	H37	8	2*46	
15W05	901	H37	19	2*46	FAN OUT TO MEMORY
15W05	902	H37	24	2*47	FAN OUT TO MEMORY
15W09	902	H37	28	2*47	
15W09	903	H39	1	2*48	
15W05	903	H39	2	2*48	FAN OUT TO MEMORY
15W09	904	H39	8	2*49	
15W05	904	H39	19	2*49	FAN OUT TO MEMORY
15W05	905	H39	24	2*50	FAN OUT TO MEMORY

15W09	905	H39	28	2+50	
15W09	906	H40	1	2+51	
15W05	906	H40	2	2+51	FAN OUT TO MEMORY
15W09	907	H40	8	2+52	
15W05	907	H40	19	2+52	FAN OUT TO MEMORY
15W05	96	H40	24	2+53	FAN OUT TO MEMORY
15W09	90	H40	28	2+53	
15W09	91	H41	1	2+54	
15W05	90	H41	2	2+54	FAN OUT TO MEMORY
15W09	92	H41	8	2+55	
15W05	91	H41	19	2+55	FAN OUT TO MEMORY
15W05	92	H41	24	2+56	FAN OUT TO MEMORY
15W09	93	H41	28	2+56	
15W09	94	H42	1	2+57	
15W05	93	H42	2	2+57	FAN OUT TO MEMORY
15W09	95	H42	8	2+58	
15W05	94	H42	19	2+58	FAN OUT TO MEMORY
15W05	95	H42	24	2+59	FAN OUT TO MEMORY
15W09	96	H42	28	2+59	
15W14	900	I28	3	2+30	FAN OUT TO MEMORY
15W15	900	I28	5	2+30	FAN OUT TO MEMORY
15W13	901	I28	7	2+31	FAN OUT TO MEMORY
15W39	901	I28	9	2+31	FAN OUT TO MEMORY
15W12	901	I28	11	2+31	FAN OUT TO MEMORY
15W11	901	I28	13	2+31	FAN OUT TO MEMORY
15W11	900	I28	18	2+30	FAN OUT TO MEMORY
15W12	900	I28	20	2+30	FAN OUT TO MEMORY
15W39	900	I28	22	2+30	FAN OUT TO MEMORY
15W13	900	I28	24	2+30	FAN OUT TO MEMORY
15W16	900	I28	26	2+30	FAN OUT TO MEMORY
15W17	900	I28	28	2+30	FAN OUT TO MEMORY
15W11	902	I29	3	2+32	FAN OUT TO MEMORY
15W12	902	I29	5	2+32	FAN OUT TO MEMORY
15W17	902	I29	7	2+32	FAN OUT TO MEMORY
15W16	902	I29	9	2+32	FAN OUT TO MEMORY
15W15	902	I29	11	2+32	FAN OUT TO MEMORY
15W14	902	I29	13	2+32	FAN OUT TO MEMORY
15W14	901	I29	18	2+31	FAN OUT TO MEMORY
15W15	901	I29	20	2+31	FAN OUT TO MEMORY
15W16	901	I29	22	2+31	FAN OUT TO MEMORY
15W17	901	I29	24	2+31	FAN OUT TO MEMORY
15W39	902	I29	26	2+32	FAN OUT TO MEMORY
15W13	902	I29	28	2+32	FAN OUT TO MEMORY
15W14	903	I30	3	2+33	FAN OUT TO MEMORY
15W15	903	I30	5	2+33	FAN OUT TO MEMORY
15W13	904	I30	7	2+34	FAN OUT TO MEMORY
15W39	904	I30	9	2+34	FAN OUT TO MEMORY
15W12	904	I30	11	2+34	FAN OUT TO MEMORY
15W11	904	I30	13	2+34	FAN OUT TO MEMORY
15W11	903	I30	18	2+33	FAN OUT TO MEMORY
15W12	903	I30	20	2+33	FAN OUT TO MEMORY
15W39	903	I30	22	2+33	FAN OUT TO MEMORY
15W13	903	I30	24	2+33	FAN OUT TO MEMORY
15W16	903	I30	26	2+33	FAN OUT TO MEMORY
15W17	903	I30	28	2+33	FAN OUT TO MEMORY
15W11	905	I31	3	2+35	FAN OUT TO MEMORY
15W12	905	I31	5	2+35	FAN OUT TO MEMORY
15W17	905	I31	7	2+35	FAN OUT TO MEMORY
15W16	905	I31	9	2+35	FAN OUT TO MEMORY
15W15	905	I31	11	2+35	FAN OUT TO MEMORY
15W14	905	I31	13	2+35	FAN OUT TO MEMORY
15W14	904	I31	18	2+34	FAN OUT TO MEMORY

15W15	904	I31	20	2*34	FAN	OUT	TO	MEMORY
15W16	904	I31	22	2*34	FAN	OUT	TO	MEMORY
15W17	904	I31	24	2*34	FAN	OUT	TO	MEMORY
15W39	905	I31	26	2*35	FAN	OUT	TO	MEMORY
15W13	905	I31	28	2*35	FAN	OUT	TO	MEMORY
15W14	906	I32	3	2*36	FAN	OUT	TO	MEMORY
15W15	906	I32	5	2*36	FAN	OUT	TO	MEMORY
15W13	907	I32	7	2*37	FAN	OUT	TO	MEMORY
15W39	907	I32	9	2*37	FAN	OUT	TO	MEMORY
15W12	907	I32	11	2*37	FAN	OUT	TO	MEMORY
15W11	907	I32	13	2*37	FAN	OUT	TO	MEMORY
15W11	906	I32	18	2*36	FAN	OUT	TO	MEMORY
15W12	906	I32	20	2*36	FAN	OUT	TO	MEMORY
15W39	906	I32	22	2*36	FAN	OUT	TO	MEMORY
15W13	906	I32	24	2*36	FAN	OUT	TO	MEMORY
15W16	906	I32	26	2*36	FAN	OUT	TO	MEMORY
15W17	906	I32	28	2*36	FAN	OUT	TO	MEMORY
15W11	90	I33	3	2*38	FAN	OUT	TO	MEMORY
15W12	90	I33	5	2*38	FAN	OUT	TO	MEMORY
15W17	908	I33	7	2*38	FAN	OUT	TO	MEMORY
15W16	908	I33	9	2*38	FAN	OUT	TO	MEMORY
15W15	90	I33	11	2*38	FAN	OUT	TO	MEMORY
15W14	90	I33	13	2*38	FAN	OUT	TO	MEMORY
15W14	907	I33	18	2*37	FAN	OUT	TO	MEMORY
15W15	907	I33	20	2*37	FAN	OUT	TO	MEMORY
15W16	907	I33	22	2*37	FAN	OUT	TO	MEMORY
15W17	907	I33	24	2*37	FAN	OUT	TO	MEMORY
15W39	90	I33	26	2*38	FAN	OUT	TO	MEMORY
15W13	90	I33	28	2*38	FAN	OUT	TO	MEMORY
15W14	91	I34	3	2*39	FAN	OUT	TO	MEMORY
15W15	91	I34	5	2*39	FAN	OUT	TO	MEMORY
15W13	92	I34	7	2*40	FAN	OUT	TO	MEMORY
15W39	92	I34	9	2*40	FAN	OUT	TO	MEMORY
15W12	92	I34	11	2*40	FAN	OUT	TO	MEMORY
15W11	92	I34	13	2*40	FAN	OUT	TO	MEMORY
15W11	91	I34	18	2*39	FAN	OUT	TO	MEMORY
15W12	91	I34	20	2*39	FAN	OUT	TO	MEMORY
15W39	91	I34	22	2*39	FAN	OUT	TO	MEMORY
15W13	91	I34	24	2*39	FAN	OUT	TO	MEMORY
15W16	90	I34	26	2*39	FAN	OUT	TO	MEMORY
15W17	90	I34	28	2*39	FAN	OUT	TO	MEMORY
15W11	93	I35	3	2*41	FAN	OUT	TO	MEMORY
15W12	93	I35	5	2*41	FAN	OUT	TO	MEMORY
15W17	92	I35	7	2*41	FAN	OUT	TO	MEMORY
15W16	92	I35	9	2*41	FAN	OUT	TO	MEMORY
15W15	93	I35	11	2*41	FAN	OUT	TO	MEMORY
15W14	93	I35	13	2*41	FAN	OUT	TO	MEMORY
15W14	92	I35	18	2*40	FAN	OUT	TO	MEMORY
15W15	92	I35	20	2*40	FAN	OUT	TO	MEMORY
15W16	91	I35	22	2*40	FAN	OUT	TO	MEMORY
15W17	91	I35	24	2*40	FAN	OUT	TO	MEMORY
15W39	93	I35	26	2*41	FAN	OUT	TO	MEMORY
15W13	93	I35	28	2*41	FAN	OUT	TO	MEMORY
15W14	94	I36	3	2*42	FAN	OUT	TO	MEMORY
15W15	94	I36	5	2*42	FAN	OUT	TO	MEMORY
15W13	95	I36	7	2*43	FAN	OUT	TO	MEMORY
15W39	95	I36	9	2*43	FAN	OUT	TO	MEMORY
15W12	95	I36	11	2*43	FAN	OUT	TO	MEMORY
15W11	95	I36	13	2*43	FAN	OUT	TO	MEMORY
15W11	94	I36	18	2*42	FAN	OUT	TO	MEMORY
15W12	94	I36	20	2*42	FAN	OUT	TO	MEMORY
15W39	94	I36	22	2*42	FAN	OUT	TO	MEMORY

15W13	94	136	24	2*42	FAN	OUT	TO	MEMORY	
15W16	93	136	26	2*42	FAN	OUT	TO	MEMORY	
15W17	93	136	28	2*42	FAN	OUT	TO	MEMORY	
15W11	96	137	3	2*44	FAN	OUT	TO	MEMORY	
15W12	96	137	5	2*44	FAN	OUT	TO	MEMORY	
15W17	95	137	7	2*44	FAN	OUT	TO	MEMORY	
15W16	95	137	9	2*44	FAN	OUT	TO	MEMORY	
15W15	96	137	11	2*44	FAN	OUT	TO	MEMORY	
15W14	96	137	13	2*44	FAN	OUT	TO	MEMORY	
15W14	95	137	18	2*43	FAN	OUT	TO	MEMORY	
15W15	95	137	20	2*43	FAN	OUT	TO	MEMORY	
15W16	94	137	22	2*43	FAN	OUT	TO	MEMORY	
15W17	94	137	24	2*43	FAN	OUT	TO	MEMORY	
15W39	96	137	26	2*44	FAN	OUT	TO	MEMORY	
15W13	96	137	28	2*44	FAN	OUT	TO	MEMORY	
15W10	90	139	4	2*30	DATA	TO	MEMORY	FAN	OUT
15W10	91	139	6	2*31	DATA	TO	MEMORY	FAN	OUT
15W10	92	139	7	2*32	DATA	TO	MEMORY	FAN	OUT
15W10	95	139	22	2*35	DATA	TO	MEMORY	FAN	OUT
15W10	94	139	25	2*34	DATA	TO	MEMORY	FAN	OUT
15W10	93	139	27	2*33	DATA	TO	MEMORY	FAN	OUT
15W10	96	140	4	2*36	DATA	TO	MEMORY	FAN	OUT
15W10	97	140	6	2*37	DATA	TO	MEMORY	FAN	OUT
15W10	98	140	7	2*38	DATA	TO	MEMORY	FAN	OUT
15W10	901	140	22	2*41	DATA	TO	MEMORY	FAN	OUT
15W10	900	140	25	2*40	DATA	TO	MEMORY	FAN	OUT
15W10	99	140	27	2*39	DATA	TO	MEMORY	FAN	OUT
15W10	902	141	4	2*42	DATA	TO	MEMORY	FAN	OUT
15W10	903	141	6	2*43	DATA	TO	MEMORY	FAN	OUT
15W10	904	141	7	2*44	DATA	TO	MEMORY	FAN	OUT



CHASSIS 16

16W10	905	C14	14	MEMORY MARGIN
16W18	906	G01	11	2+5 ADDRESS TO MEMORY
16W18	907	G01	14	2+6 ADDRESS TO MEMORY
16W18	908	G01	17	2+7 ADDRESS TO MEMORY
16W18	90	G01	18	2+8 ADDRESS TO MEMORY
16W18	91	G02	11	2+9 ADDRESS TO MEMORY
16W18	92	G02	14	2+10 ADDRESS TO MEMORY
16W18	93	G02	17	2+11 ADDRESS TO MEMORY
16W18	94	G02	18	2+12 ADDRESS TO MEMORY
16W18	95	G03	11	2+13 ADDRESS TO MEMORY
16W18	96	G03	14	2+14 ADDRESS TO MEMORY
16W18	97	G03	17	2+15 ADDRESS TO MEMORY
16W18	98	G03	18	2+16 ADDRESS TO MEMORY
16W18	904	G04	5	2+3 CHASSIS 16 SELECT (111)
16W18	903	G04	8	2+2 CHASSIS 16 SFLECT (111)
16W18	905	G04	13	2+4 CHASSIS 16 SELECT (111)
16W12	98	G04	20	WRITE
16W12	99	G04	21	GO
16W18	901	G05	18	2+0 BANK SELECT
16W18	902	G05	22	2+1 BANK SELECT
16W13	97	G05	28	ACCEPT
16W18	900	G13	12	CLOCK
16W06	900	H01	1	2+0
16W03	900	H01	2	2+0 FAN OUT TO MEMORY
16W06	901	H01	8	2+1
16W03	901	H01	19	2+1 FAN OUT TO MEMORY
16W03	902	H01	24	2+2 FAN OUT TO MEMORY
16W06	902	H01	28	2+2
16W06	903	H02	1	2+3
16W03	903	H02	2	2+3 FAN OUT TO MEMORY
16W06	904	H02	8	2+4
16W03	904	H02	19	2+4 FAN OUT TO MEMORY
16W03	905	H02	24	2+5 FAN OUT TO MEMORY
16W06	905	H02	28	2+5
16W06	906	H03	1	2+6
16W03	906	H03	2	2+6 FAN OUT TO MEMORY
16W06	907	H03	8	2+7
16W03	907	H03	19	2+7 FAN OUT TO MEMORY
16W03	90	H03	24	2+8 FAN OUT TO MEMORY
16W06	90	H03	28	2+8
16W06	91	H04	1	2+9
16W03	91	H04	2	2+9 FAN OUT TO MEMORY
16W06	92	H04	8	2+10
16W03	92	H04	19	2+10 FAN OUT TO MEMORY
16W03	93	H04	24	2+11 FAN OUT TO MEMORY
16W06	93	H04	28	2+11
16W06	94	H06	1	2+12
16W03	94	H06	2	2+12 FAN OUT TO MEMORY
16W06	95	H06	8	2+13
16W03	95	H06	19	2+13 FAN OUT TO MEMORY
16W03	96	H06	24	2+14 FAN OUT TO MEMORY
16W06	96	H06	28	2+14
16W07	900	H07	1	2+15
16W04	900	H07	2	2+15 FAN OUT TO MEMORY
16W07	901	H07	8	2+16
16W04	901	H07	19	2+16 FAN OUT TO MEMORY
16W04	902	H07	24	2+17 FAN OUT TO MEMORY
16W07	902	H07	28	2+17
16W07	903	H08	1	2+18
16W04	903	H08	2	2+18 FAN OUT TO MEMORY

16W07	904	H08	8	2+19	
16W04	904	H08	19	2+19	FAN OUT TO MEMORY
16W04	905	H08	24	2+20	FAN OUT TO MEMORY
16W07	905	H08	28	2+20	
16W07	906	H09	1	2+21	
16W04	906	H09	2	2+21	FAN OUT TO MEMORY
16W07	907	H09	8	2+22	
16W04	907	H09	19	2+22	FAN OUT TO MEMORY
16W04	908	H09	24	2+23	FAN OUT TO MEMORY
16W07	90	H09	28	2+23	
16W07	91	H11	1	2+24	
16W04	90	H11	2	2+24	FAN OUT TO MEMORY
16W07	92	H11	8	2+25	
16W04	91	H11	19	2+25	FAN OUT TO MEMORY
16W04	92	H11	24	2+26	FAN OUT TO MEMORY
16W07	93	H11	28	2+26	
16W07	94	H12	1	2+27	
16W04	93	H12	2	2+27	FAN OUT TO MEMORY
16W07	95	H12	8	2+28	
16W04	94	H12	19	2+28	FAN OUT TO MEMORY
16W04	95	H12	24	2+29	FAN OUT TO MEMORY
16W07	96	H12	28	2+29	
16W08	900	H13	1	2+30	
16W05	900	H13	2	2+30	FAN OUT TO MEMORY
16W08	901	H13	8	2+31	
16W05	901	H13	19	2+31	FAN OUT TO MEMORY
16W05	902	H13	24	2+32	FAN OUT TO MEMORY
16W08	902	H13	28	2+32	
16W08	903	H14	1	2+33	
16W05	903	H14	2	2+33	FAN OUT TO MEMORY
16W08	904	H14	8	2+34	
16W05	904	H14	19	2+34	FAN OUT TO MEMORY
16W05	905	H14	24	2+35	FAN OUT TO MEMORY
16W08	905	H14	28	2+35	
16W08	906	H16	1	2+36	
16W05	906	H16	2	2+36	FAN OUT TO MEMORY
16W08	907	H16	8	2+37	
16W05	907	H16	19	2+37	FAN OUT TO MEMORY
16W05	908	H16	24	2+38	FAN OUT TO MEMORY
16W08	90	H16	28	2+38	
16W08	91	H17	1	2+39	
16W05	90	H17	2	2+39	FAN OUT TO MEMORY
16W08	92	H17	8	2+40	
16W05	91	H17	19	2+40	FAN OUT TO MEMORY
16W05	92	H17	24	2+41	FAN OUT TO MEMORY
16W08	93	H17	28	2+41	
16W08	94	H18	1	2+42	
16W05	93	H18	2	2+42	FAN OUT TO MEMORY
16W08	95	H18	8	2+43	
16W05	94	H18	19	2+43	FAN OUT TO MEMORY
16W05	95	H18	24	2+44	FAN OUT TO MEMORY
16W08	96	H18	28	2+44	
16W09	900	H19	1	2+45	
16W39	900	H19	2	2+45	FAN OUT TO MEMORY
16W09	901	H19	8	2+46	
16W39	901	H19	19	2+46	FAN OUT TO MEMORY
16W39	902	H19	24	2+47	FAN OUT TO MEMORY
16W09	902	H19	28	2+47	
16W09	903	H21	1	2+48	
16W39	903	H21	2	2+48	FAN OUT TO MEMORY
16W09	904	H21	8	2+49	
16W39	904	H21	19	2+49	FAN OUT TO MEMORY

16W39	905	H21	24	2+50	FAN OUT TO MEMORY		
16W09	905	H21	28	2+50			
16W09	906	H22	1	2+51			
16W39	906	H22	2	2+51	FAN OUT TO MEMORY		
16W09	907	H22	8	2+52			
16W39	907	H22	19	2+52	FAN OUT TO MEMORY		
16W39	90	H22	24	2+53	FAN OUT TO MEMORY		
16W09	90	H22	28	2+53			
16W09	91	H23	1	2+54			
16W39	91	H23	2	2+54	FAN OUT TO MEMORY		
16W09	92	H23	8	2+55			
16W39	92	H23	19	2+55	FAN OUT TO MEMORY		
16W39	93	H23	24	2+56	FAN OUT TO MEMORY		
16W09	93	H23	28	2+56			
16W09	94	H24	1	2+57			
16W39	94	H24	2	2+57	FAN OUT TO MEMORY		
16W09	95	H24	8	2+58			
16W39	95	H24	19	2+58	FAN OUT TO MEMORY		
16W39	96	H24	24	2+59	FAN OUT TO MEMORY		
16W09	96	H24	28	2+59			
16W10	90	I02	4	2+45	DATA TO MEMORY	FAN	OUT
16W10	91	I02	6	2+46	DATA TO MEMORY	FAN	OUT
16W10	92	I02	7	2+47	DATA TO MEMORY	FAN	OUT
16W10	95	I02	22	2+50	DATA TO MEMORY	FAN	OUT
16W10	94	I02	25	2+49	DATA TO MEMORY	FAN	OUT
16W10	93	I02	27	2+48	DATA TO MEMORY	FAN	OUT
16W10	96	I03	4	2+51	DATA TO MEMORY	FAN	OUT
16W10	97	I03	6	2+52	DATA TO MEMORY	FAN	OUT
16W10	98	I03	7	2+53	DATA TO MEMORY	FAN	OUT
16W10	901	I03	22	2+56	DATA TO MEMORY	FAN	OUT
16W10	900	I03	25	2+55	DATA TO MEMORY	FAN	OUT
16W10	99	I03	27	2+54	DATA TO MEMORY	FAN	OUT
16W10	902	I04	4	2+57	DATA TO MEMORY	FAN	OUT
16W10	903	I04	6	2+58	DATA TO MEMORY	FAN	OUT
16W10	904	I04	7	2+59	DATA TO MEMORY	FAN	OUT
16W14	900	I06	3	2+45	FAN OUT TO MEMORY		
16W15	900	I06	5	2+45	FAN OUT TO MEMORY		
16W13	901	I06	7	2+46	FAN OUT TO MEMORY		
16W39	901	I06	9	2+46	FAN OUT TO MEMORY		
16W12	901	I06	11	2+46	FAN OUT TO MEMORY		
16W11	901	I06	13	2+46	FAN OUT TO MEMORY		
16W11	900	I06	18	2+45	FAN OUT TO MEMORY		
16W12	900	I06	20	2+45	FAN OUT TO MEMORY		
16W39	900	I06	22	2+45	FAN OUT TO MEMORY		
16W13	900	I06	24	2+45	FAN OUT TO MEMORY		
16W16	900	I06	26	2+45	FAN OUT TO MEMORY		
16W17	900	I06	28	2+45	FAN OUT TO MEMORY		
16W11	902	I07	3	2+47	FAN OUT TO MEMORY		
16W12	902	I07	5	2+47	FAN OUT TO MEMORY		
16W17	902	I07	7	2+47	FAN OUT TO MEMORY		
16W16	902	I07	9	2+47	FAN OUT TO MEMORY		
16W15	902	I07	11	2+47	FAN OUT TO MEMORY		
16W14	902	I07	13	2+47	FAN OUT TO MEMORY		
16W14	901	I07	18	2+46	FAN OUT TO MEMORY		
16W15	901	I07	20	2+46	FAN OUT TO MEMORY		
16W16	901	I07	22	2+46	FAN OUT TO MEMORY		
16W17	901	I07	24	2+46	FAN OUT TO MEMORY		
16W39	902	I07	26	2+47	FAN OUT TO MEMORY		
16W13	902	I07	28	2+47	FAN OUT TO MEMORY		
16W14	903	I08	3	2+48	FAN OUT TO MEMORY		
16W15	903	I08	5	2+48	FAN OUT TO MEMORY		
16W13	904	I08	7	2+49	FAN OUT TO MEMORY		

16W39	904	I08	9	2*49	FAN	OUT	TO	MEMORY
16W12	904	I08	11	2*49	FAN	OUT	TO	MEMORY
16W11	904	I08	13	2*49	FAN	OUT	TO	MEMORY
16W11	903	I08	18	2*48	FAN	OUT	TO	MEMORY
16W12	903	I08	20	2*48	FAN	OUT	TO	MEMORY
16W39	903	I08	22	2*48	FAN	OUT	TO	MEMORY
16W13	903	I08	24	2*48	FAN	OUT	TO	MEMORY
16W16	903	I08	26	2*48	FAN	OUT	TO	MEMORY
16W17	903	I08	28	2*48	FAN	OUT	TO	MEMORY
16W11	905	I09	3	2*50	FAN	OUT	TO	MEMORY
16W12	905	I09	5	2*50	FAN	OUT	TO	MEMORY
16W17	905	I09	7	2*50	FAN	OUT	TO	MEMORY
16W16	905	I09	9	2*50	FAN	OUT	TO	MEMORY
16W15	905	I09	11	2*50	FAN	OUT	TO	MEMORY
16W14	905	I09	13	2*50	FAN	OUT	TO	MEMORY
16W14	904	I09	18	2*49	FAN	OUT	TO	MEMORY
16W15	904	I09	20	2*49	FAN	OUT	TO	MEMORY
16W16	904	I09	22	2*49	FAN	OUT	TO	MEMORY
16W17	904	I09	24	2*49	FAN	OUT	TO	MEMORY
16W39	905	I09	26	2*50	FAN	OUT	TO	MEMORY
16W13	905	I09	28	2*50	FAN	OUT	TO	MEMORY
16W14	906	I10	3	2*51	FAN	OUT	TO	MEMORY
16W15	906	I10	5	2*51	FAN	OUT	TO	MEMORY
16W13	907	I10	7	2*52	FAN	OUT	TO	MEMORY
16W39	907	I10	9	2*52	FAN	OUT	TO	MEMORY
16W12	907	I10	11	2*52	FAN	OUT	TO	MEMORY
16W11	907	I10	13	2*52	FAN	OUT	TO	MEMORY
16W11	906	I10	18	2*51	FAN	OUT	TO	MEMORY
16W12	906	I10	20	2*51	FAN	OUT	TO	MEMORY
16W39	906	I10	22	2*51	FAN	OUT	TO	MEMORY
16W13	906	I10	24	2*51	FAN	OUT	TO	MEMORY
16W16	906	I10	26	2*51	FAN	OUT	TO	MEMORY
16W17	906	I10	28	2*51	FAN	OUT	TO	MEMORY
16W11	90	I11	3	2*53	FAN	OUT	TO	MEMORY
16W12	90	I11	5	2*53	FAN	OUT	TO	MEMORY
16W17	96	I11	7	2*53	FAN	OUT	TO	MEMORY
16W16	908	I11	9	2*53	FAN	OUT	TO	MEMORY
16W15	90	I11	11	2*53	FAN	OUT	TO	MEMORY
16W14	90	I11	13	2*53	FAN	OUT	TO	MEMORY
16W14	907	I11	18	2*52	FAN	OUT	TO	MEMORY
16W15	907	I11	20	2*52	FAN	OUT	TO	MEMORY
16W16	907	I11	22	2*52	FAN	OUT	TO	MEMORY
16W17	907	I11	24	2*52	FAN	OUT	TO	MEMORY
16W39	90	I11	26	2*53	FAN	OUT	TO	MEMORY
16W13	90	I11	28	2*53	FAN	OUT	TO	MEMORY
16W14	91	I12	3	2*54	FAN	OUT	TO	MEMORY
16W15	91	I12	5	2*54	FAN	OUT	TO	MEMORY
16W13	92	I12	7	2*55	FAN	OUT	TO	MEMORY
16W39	92	I12	9	2*55	FAN	OUT	TO	MEMORY
16W12	92	I12	11	2*55	FAN	OUT	TO	MEMORY
16W11	92	I12	13	2*55	FAN	OUT	TO	MEMORY
16W11	91	I12	18	2*54	FAN	OUT	TO	MEMORY
16W12	91	I12	20	2*54	FAN	OUT	TO	MEMORY
16W39	91	I12	22	2*54	FAN	OUT	TO	MEMORY
16W13	91	I12	24	2*54	FAN	OUT	TO	MEMORY
16W16	90	I12	26	2*54	FAN	OUT	TO	MEMORY
16W17	90	I12	28	2*54	FAN	OUT	TO	MEMORY
16W11	93	I13	3	2*56	FAN	OUT	TO	MEMORY
16W12	93	I13	5	2*56	FAN	OUT	TO	MEMORY
16W17	92	I13	7	2*56	FAN	OUT	TO	MEMORY
16W16	92	I13	9	2*56	FAN	OUT	TO	MEMORY
16W15	93	I13	11	2*56	FAN	OUT	TO	MEMORY

16W14	93	I13	13	2*56	FAN OUT TO MEMORY
16W14	92	I13	18	2*55	FAN OUT TO MEMORY
16W15	92	I13	20	2*55	FAN OUT TO MEMORY
16W16	91	I13	22	2*55	FAN OUT TO MEMORY
16W17	91	I13	24	2*55	FAN OUT TO MEMORY
16W39	93	I13	26	2*56	FAN OUT TO MEMORY
16W13	93	I13	28	2*56	FAN OUT TO MEMORY
16W14	94	I14	3	2*57	FAN OUT TO MEMORY
16W15	94	I14	5	2*57	FAN OUT TO MEMORY
16W13	95	I14	7	2*58	FAN OUT TO MEMORY
16W39	95	I14	9	2*58	FAN OUT TO MEMORY
16W12	95	I14	11	2*58	FAN OUT TO MEMORY
16W11	95	I14	13	2*58	FAN OUT TO MEMORY
16W11	94	I14	18	2*57	FAN OUT TO MEMORY
16W12	94	I14	20	2*57	FAN OUT TO MEMORY
16W39	94	I14	22	2*57	FAN OUT TO MEMORY
16W13	94	I14	24	2*57	FAN OUT TO MEMORY
16W16	93	I14	26	2*57	FAN OUT TO MEMORY
16W17	93	I14	28	2*57	FAN OUT TO MEMORY
16W11	96	I15	3	2*59	FAN OUT TO MEMORY
16W12	96	I15	5	2*59	FAN OUT TO MEMORY
16W17	95	I15	7	2*59	FAN OUT TO MEMORY
16W16	95	I15	9	2*59	FAN OUT TO MEMORY
16W15	96	I15	11	2*59	FAN OUT TO MEMORY
16W14	96	I15	13	2*59	FAN OUT TO MEMORY
16W14	95	I15	18	2*58	FAN OUT TO MEMORY
16W15	95	I15	20	2*58	FAN OUT TO MEMORY
16W16	94	I15	22	2*58	FAN OUT TO MEMORY
16W17	94	I15	24	2*58	FAN OUT TO MEMORY
16W39	96	I15	26	2*59	FAN OUT TO MEMORY
16W13	96	I15	28	2*59	FAN OUT TO MEMORY
16W29	91	J01	5	2*1	
16W29	90	J01	12	2*0	READ DATA ANALOG
16W29	93	J01	19	2*3	
16W29	92	J01	26	2*2	
16W29	95	J02	5	2*5	
16W29	94	J02	12	2*4	
16W29	97	J02	19	2*7	
16W29	96	J02	26	2*6	
16W29	99	J03	5	2*9	
16W29	98	J03	12	2*8	
16W29	901	J03	19	2*11	
16W29	900	J03	26	2*10	READ DATA ANALOG
16W28	904	J04	4		WRITE CONTROL
16W29	903	J04	5		CLOCK
16W28	903	J04	6		GROUP CONTROL
16W28	902	J04	8		POSITION CONTROL
16W29	902	J04	12		REV MARK
16W29	904	J04	26		SECTOR MARK
16W28	905	J04	27		CLOCK CONTROL
16W28	98	J05	4	2*8	
16W28	97	J05	6	2*7	
16W28	96	J05	8	4*6	
16W28	901	J05	23	2*11	
16W28	900	J05	25	2*10	WRITE DATA
16W28	99	J05	27	2*9	
16W28	92	J06	4	2*2	
16W28	91	J06	6	2*1	
16W28	90	J06	8	2*0	WRITE DATA
16W28	95	J06	23	2*5	
16W28	94	J06	25	2*4	
16W28	93	J06	27	2*3	

16W24	906	K01	4	FUNCTION
16W24	904	K01	6	FULL
16W24	903	K01	8	INACTIVE
16W24	902	K01	23	ACTIVE
16W24	907	K01	25	MASTER CLEAR
16W24	905	K01	27	EMPTY
16W24	92	K02	4	2*2 OUTPUT DATA
16W24	91	K02	6	2*1 OUTPUT DATA
16W24	90	K02	8	2*0 OUTPUT DATA
16W24	95	K02	23	2*5 OUTPUT DATA
16W24	94	K02	25	2*4 OUTPUT DATA
16W24	93	K02	27	2*3 OUTPUT DATA
16W24	98	K03	4	2*8 OUTPUT DATA
16W24	97	K03	6	2*7 OUTPUT DATA
16W24	96	K03	8	2*6 OUTPUT DATA
16W24	901	K03	23	2*11 OUTPUT DATA
16W24	900	K03	25	2*10 OUTPUT DATA
16W24	99	K03	27	2*9 OUTPUT DATA
16W26	902	K04	4	ACTIVE
16W26	904	K04	6	FULL
16W26	903	K04	8	INACTIVE
16W26	905	K04	27	EMPTY
16W26	92	K05	4	BIT 2
16W26	91	K05	6	BIT 1
16W26	90	K05	8	BIT 0
16W26	95	K05	23	BIT 5
16W26	94	K05	25	BIT 4
16W26	93	K05	27	BIT 3
16W26	98	K06	4	BIT 8
16W26	97	K06	6	BIT 7
16W26	96	K06	8	BIT 6
16W26	901	K06	23	BIT 11
16W26	900	K06	25	BIT 10
16W26	99	K06	27	BIT 9
16W27	90	L01	5	2*0
16W27	91	L01	7	2*1
16W27	92	L01	10	2*2
16W27	93	L01	21	2*3
16W27	94	L01	24	2*4
16W27	95	L01	26	2*5
16W27	96	L02	5	2*6
16W27	97	L02	7	2*7
16W27	98	L02	10	2*8
16W27	99	L02	21	2*9
16W27	900	L02	24	2*10
16W27	901	L02	26	2*11
16W27	906	L03	5	FUNCTION
16W27	902	L03	7	ACTIVE
16W27	904	L03	10	FULL
16W27	903	L03	21	INACTIVE
16W27	907	L03	24	MASTER CLEAR
16W27	905	L03	26	EMPTY
16W25	903	L07	5	INACTIVE
16W25	904	L07	7	FULL
16W25	96	L08	5	2*6 INPUT DATA
16W25	97	L08	7	2*7 INPUT DATA
16W25	98	L08	10	2*8 INPUT DATA
16W25	99	L08	21	2*9 INPUT DATA
16W25	900	L08	24	2*10 INPUT DATA
16W25	901	L08	26	2*11 INPUT DATA
16W25	90	L09	5	2*0 INPUT DATA
16W25	91	L09	7	2*1 INPUT DATA

16W25	92	L09	10	2*2	INPUT	DATA
16W25	93	L09	21	2*3	INPUT	DATA
16W25	94	L09	24	2*4	INPUT	DATA
16W25	95	L09	26	2*5	INPUT	DATA
16W25	906	L26	1			
16W26	906	L26	3			
16W26	907	L26	13			
16W25	907	L26	15			

1

2

3

4

5

6

7

8

9

10

11

12

13

CHASSIS 30

30W04	90	A01	9	2*0	READ DATA ANALOG
30W04	91	A01	15	2*1	READ DATA ANALOG
30W04	92	A01	25	2*2	READ DATA ANALOG
30W04	93	A02	9	2*3	READ DATA ANALOG
30W04	94	A02	15	2*4	READ DATA ANALOG
30W04	95	A02	25	2*5	READ DATA ANALOG
30W04	96	A03	9	2*6	READ DATA ANALOG
30W04	97	A03	15	2*7	READ DATA ANALOG
30W04	98	A03	25	2*8	READ DATA ANALOG
30W04	99	A04	9	2*9	READ DATA ANALOG
30W04	900	A04	15	2*10	READ DATA ANALOG
30W04	901	A04	25	2*11	READ DATA ANALOG
30W03	905	A17	12		CLOCK
30W03	90	A18	5	2*0	WRITE DATA
30W03	91	A18	7	2*1	WRITE DATA
30W03	92	A18	10	2*2	WRITE DATA
30W03	93	A18	21	2*3	WRITE DATA
30W03	94	A18	24	2*4	WRITE DATA
30W03	95	A18	26	2*5	WRITE DATA
30W03	96	A19	5	2*6	WRITE DATA
30W03	97	A19	7	2*7	WRITE DATA
30W03	98	A19	10	2*8	WRITE DATA
30W03	99	A19	21	2*9	WRITE DATA
30W03	900	A19	24	2*10	WRITE DATA
30W03	901	A19	26	2*11	WRITE DATA
30W03	902	A20	5		POSITION CONTROL
30W03	903	A20	7		GROUP CONTROL
30W03	904	A20	10		WRITE CONTROL
30W04	902	B12	9		REV. MARK
30W04	903	B12	25		CLOCK



CHASSIS 50

50W01	901	A04	1	Y*2
50W01	900	A04	6	Y*1
50W01	99	A04	9	Y*0
50W01	904	A08	1	Y*5
50W01	903	A08	6	Y*4
50W01	902	A08	9	Y*3
50W01	907	A12	1	Y*8
50W01	906	A12	6	Y*7
50W01	905	A12	9	Y*6
50W01	92	A16	1	X*2
50W01	91	A16	6	X*1
50W01	90	A16	9	X*0
50W01	95	A20	1	X*5
50W01	94	A20	6	X*4
50W01	93	A20	9	X*3
50W01	98	A24	1	X*8
50W01	97	A24	6	X*7
50W01	96	A24	9	X*6
50W02	90	A26	1	KEYBOARD BIT 0 (TB2 1)
50W02	91	A26	2	KEYBOARD BIT 1 (TB2 2)
50W02	92	A26	3	KEYBOARD BIT 2 (TB2 3)
50W02	93	A26	4	KEYBOARD BIT 3 (TB2 4)
50W02	94	A26	5	KEYBOARD BIT 4 (TB2 5)
50W02	95	A26	6	KEYBOARD BIT 5 (TB2 6)
50W02	96	A26	7	KEY DOWN (TB2 8)
50W02	97	A26	8	KEY UP (TB2 7)
50W02	98	B21	1	UNBLANK LEFT CONSOLE 0
50W02	99	B21	6	UNBLANK RIGHT CONSOLE 0
50W02	901	B22	1	MED. SIZE
50W02	900	B22	6	SMALL SIZE
50W01	908	B22	9	FOCUS AND ASTIGMATISM
50W02	905	B23	1	HORIZ. PUSH ANALOG
50W02	906	B23	9	HORIZ. PULL ANALOG
50W02	903	B24	1	VERT. PUSH ANALOG
50W02	904	B24	7	VERT. PULL ANALOG



CHASSIS 81

81W00	906	C09	3	FUNCTION	
81W00	907	C09	13	MASTER CLEAR	
81W01	907	C09	15	MASTER CLEAR	
81W02	907	C09	16	CLOCK (1 MC)	
81W01	904	C10	1	FULL	
81W00	904	C10	3	FULL	
81W00	905	C10	13	EMPTY	
81W01	902	C10	14	ACTIVE	
81W01	905	C10	15	EMPTY	
81W00	902	C10	16	ACTIVE	
81W00	903	C10	26	INACTIVE	
81W01	903	C10	28	INACTIVE	
81W01	901	C11	1	DATA BIT	2*11
81W00	901	C11	3	DATA BIT	2*11
81W00	900	C11	13	DATA BIT	2*10
81W01	99	C11	14	DATA BIT	2*9
81W01	900	C11	15	DATA BIT	2*10
81W00	99	C11	16	DATA BIT	2*9
81W00	98	C11	26	DATA BIT	2*8
81W01	98	C11	28	DATA BIT	2*8
81W01	97	C12	1	DATA BIT	2*7
81W00	97	C12	3	DATA BIT	2*7
81W00	96	C12	13	DATA BIT	2*6
81W01	95	C12	14	DATA BIT	2*5
81W01	96	C12	15	DATA BIT	2*6
81W00	95	C12	16	DATA BIT	2*5
81W00	94	C12	26	DATA BIT	2*4
81W01	94	C12	28	DATA BIT	2*4
81W02	99	D09	3	DATA BIT	2*9
81W02	900	D09	5	DATA BIT	2*10
81W02	901	D09	7	DATA BIT	2*11
81W03	96	D09	8	DATA BIT	2*6
81W03	97	D09	9	DATA BIT	2*7
81W03	98	D09	10	DATA BIT	2*8
81W03	99	D09	21	DATA BIT	2*9
81W03	900	D09	22	DATA BIT	2*10
81W03	901	D09	23	DATA BIT	2*11
81W02	96	D09	24	DATA BIT	2*6
81W02	97	D09	26	DATA BIT	2*7
81W02	98	D09	28	DATA BIT	2*8
81W02	93	D10	3	DATA BIT	2*3
81W02	94	D10	5	DATA BIT	2*4
81W02	95	D10	7	DATA BIT	2*5
81W03	90	D10	8	DATA BIT	2*0
81W03	91	D10	9	DATA BIT	2*1
81W03	92	D10	10	DATA BIT	2*2
81W03	93	D10	21	DATA BIT	2*3
81W03	94	D10	22	DATA BIT	2*4
81W03	95	D10	23	DATA BIT	2*5
81W02	90	D10	24	DATA BIT	2*0
81W02	91	D10	26	DATA BIT	2*1
81W02	92	D10	28	DATA BIT	2*2
81W02	903	D11	3	INACTIVE	
81W02	904	D11	5	FULL	
81W02	905	D11	7	EMPTY	
81W03	903	D11	21	INACTIVE	
81W03	904	D11	22	FULL	
81W03	905	D11	23	EMPTY	
81W03	907	D11	26	CLOCK (1 MC)	
81W03	906	D11	28	CLOCK (10 MC)	

81W01	93	D12	1	DATA BIT 2*3
81W00	93	D12	3	DATA BIT 2*3
81W00	92	D12	13	DATA BIT 2*2
81W01	91	D12	14	DATA BIT 2*1
81W01	92	D12	15	DATA BIT 2*2
81W00	91	D12	16	DATA BIT 2*1
81W00	90	D12	26	DATA BIT 2*0
81W01	90	D12	28	DATA BIT 2*0
81W01	906	E09	1	FUNCTION
81W02	906	E12	12	CLOCK (10 MC)

CHASSIS 82

82W01	906	C09	1	FUNCTION
82W00	906	C09	3	FUNCTION
82W02	99	D09	3	DATA BIT 2*9
82W02	900	D09	5	DATA BIT 2*10
82W02	901	D09	7	DATA BIT 2*11
82W03	96	D09	8	DATA BIT 2*6
82W03	97	D09	9	DATA BIT 2*7
82W03	98	D09	10	DATA BIT 2*8
82W00	907	C09	13	MASTER CLEAR
82W01	907	C09	15	MASTER CLEAR
82W02	907	C09	16	CLOCK (1 MC)
82W03	99	D09	21	DATA BIT 2*9
82W03	900	D09	22	DATA BIT 2*10
82W03	901	D09	23	DATA BIT 2*11
82W02	96	D09	24	DATA BIT 2*6
82W02	97	D09	26	DATA BIT 2*7
82W02	98	D09	28	DATA BIT 2*8
82W01	904	C10	1	FULL
82W00	904	C10	3	FULL
82W02	93	D10	3	DATA BIT 2*3
82W02	94	D10	5	DATA BIT 2*4
82W02	95	D10	7	DATA BIT 2*5
82W03	90	D10	8	DATA BIT 2*0
82W03	91	D10	9	DATA BIT 2*1
82W03	92	D10	10	DATA BIT 2*2
82W00	905	C10	13	EMPTY
82W01	902	C10	14	ACTIVE
82W01	905	C10	15	EMPTY
82W00	902	C10	16	ACTIVE
82W03	93	D10	21	DATA BIT 2*3
82W03	94	D10	22	DATA BIT 2*4
82W03	95	D10	23	DATA BIT 2*5
82W02	90	D10	24	DATA BIT 2*0
82W00	903	C10	26	INACTIVE
82W02	91	D10	26	DATA BIT 2*1
82W01	903	C10	28	INACTIVE
82W02	92	D10	28	DATA BIT 2*2
82W01	901	C11	1	DATA BIT 2*11
82W00	901	C11	3	DATA BIT 2*11
82W02	903	D11	3	INACTIVE
82W02	904	D11	5	FULL
82W02	905	D11	7	EMPTY
82W00	900	C11	13	DATA BIT 2*10
82W01	99	C11	14	DATA BIT 2*9
82W01	900	C11	15	DATA BIT 2*10
82W00	99	C11	16	DATA BIT 2*9
82W03	903	D11	21	INACTIVE
82W03	904	D11	22	FULL
82W03	905	D11	23	EMPTY
82W00	98	C11	26	DATA BIT 2*8
82W03	907	D11	26	CLOCK (1 MC)
82W01	98	C11	28	DATA BIT 2*8
82W03	906	D11	28	CLOCK (10 MC)
82W01	93	D12	1	DATA BIT 2*3
82W01	97	C12	1	DATA BIT 2*7
82W00	93	D12	3	DATA BIT 2*3
82W00	97	C12	3	DATA BIT 2*7
82W02	906	E12	12	CLOCK (10 MC)
82W00	92	D12	13	DATA BIT 2*2
82W00	96	C12	13	DATA BIT 2*6

82W01	91	D12	14	DATA	BIT	2*1
82W01	95	C12	14	DATA	BIT	2*5
82W01	92	D12	15	DATA	BIT	2*2
82W01	96	C12	15	DATA	BIT	2*6
82W00	91	D12	16	DATA	BIT	2*1
82W00	95	C12	16	DATA	BIT	2*5
82W00	90	D12	26	DATA	BIT	2*0
82W00	94	C12	26	DATA	BIT	2*4
82W01	90	D12	28	DATA	BIT	2*0
82W01	94	C12	28	DATA	BIT	2*4

COMMENT SHEET

CONTROL DATA 6601-H/6613-A Central Computer
Wire Lists
Customer Engineering Manual
Pub. No. 60148900

FROM: NAME: _____

BUSINESS
ADDRESS: _____

THESE COMMENTS REFER TO REV. _____ OF THIS MANUAL.

COMMENTS: (DESCRIBE ERRORS, SUGGESTED ADDITIONS OR
DELETIONS, ETC. INCLUDE PAGE NUMBER.)

CUT ALONG LINE

FORM CA 231 REV. 1-66

NO POSTAGE STAMP NECESSARY IF MAILED IN U.S.A.
FOLD ON DOTTED LINES AND STAPLE

STAPLE

STAPLE

FOLD

FOLD

FIRST CLASS
 PERMIT NO. 8241
 MINNEAPOLIS, MINN.

BUSINESS REPLY MAIL
 NO POSTAGE STAMP NECESSARY IF MAILED IN U.S.A.

POSTAGE WILL BE PAID BY
CONTROL DATA CORPORATION
8100 34TH AVENUE SOUTH
MINNEAPOLIS, MINNESOTA 55440

ATTN: TECHNICAL PUBLICATIONS DEPT.
PLANT TWO



CUT ALONG LINE

FOLD

FOLD

STAPLE

STAPLE

CONTROL DATA

CORPORATION

8100 34th AVE. SO., MINNEAPOLIS, MINN. 55440

PRINTED IN U.S.A.